





OCB





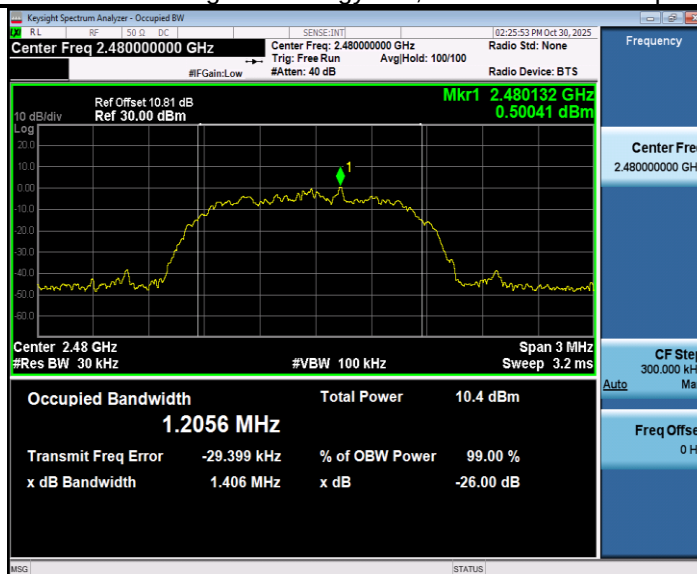
2DH5 Ant1 2402



2DH5 Ant1 2441



2DH5 Ant1 2480



3DH5 Ant1 2402



3DH5 Ant1 2441



3DH5 Ant1 2480

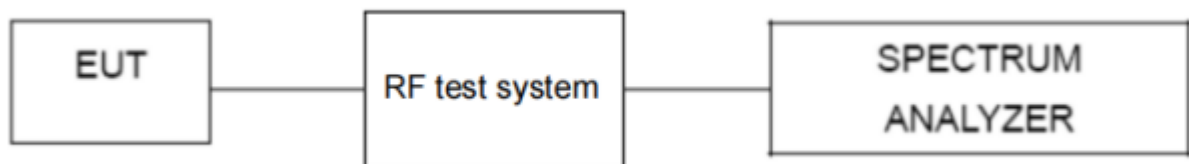




8. MAXIMUM PEAK OUTPUT POWER

Test Requirement:	FCC Part15 C Section 15.247 (b)(1)
Test Method:	ANSI C63.10:2020
Limit:	GFSK: 30dBm $\pi/4$ -DQPSK & 8-DPSK: 20.97dBm

8.1 BLOCK DIAGRAM OF TEST SETUP



8.2 LIMIT

For frequency hopping systems operating in the 2400-2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725-5850 MHz band: 1 watt.
For all other frequency hopping systems in the 2400-2483.5 MHz band: 0.125 watts.

8.3 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 2MHz. VBW = 6MHz. Sweep = auto; Detector Function = Peak.
3. Keep the EUT in transmitting at lowest, medium and highest channel individually. Record the max value.

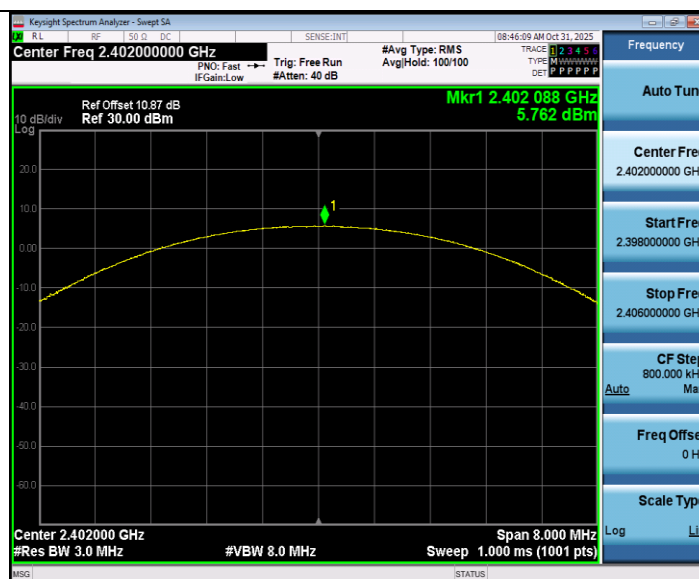
8.4 DEVIATION FROM STANDARD

No deviation.



8.5 TEST RESULT

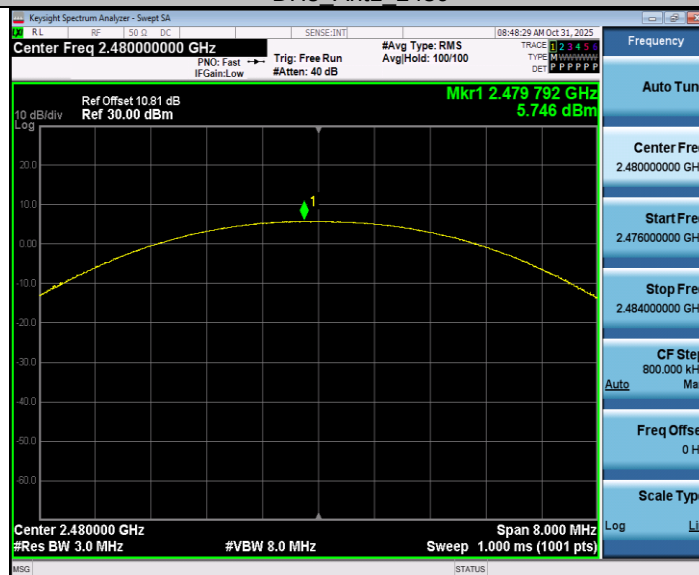
Modulation	Packet	Test channel	Peak output power (dBm)	Limit (dBm)	Result
GFSK	1-DH5	Lowest	5.76	30.00	Pass
		Middle	5.01		
		Highest	5.75		
$\pi/4$ -DQPSK	2-DH5	Lowest	6.48	20.97	Pass
		Middle	6.26		
		Highest	7.11		
8-DPSK	3-DH5	Lowest	6.92	20.97	Pass
		Middle	6.65		
		Highest	7.50		



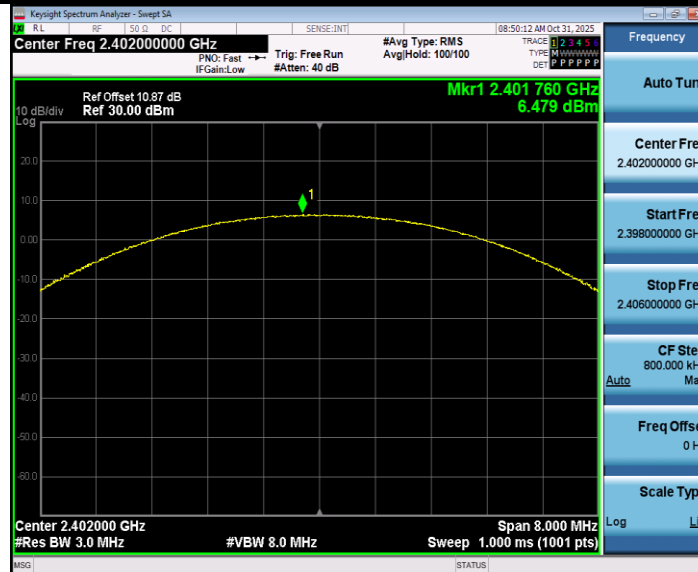
DH5_Ant1_2441



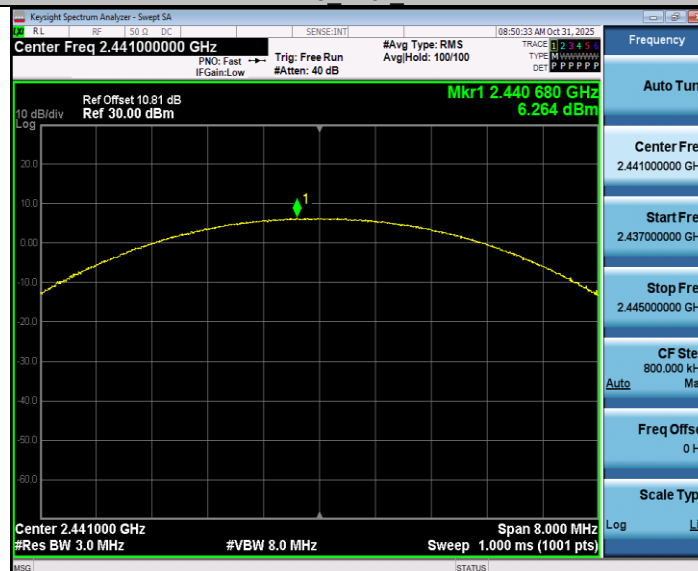
DH5_Ant1_2480



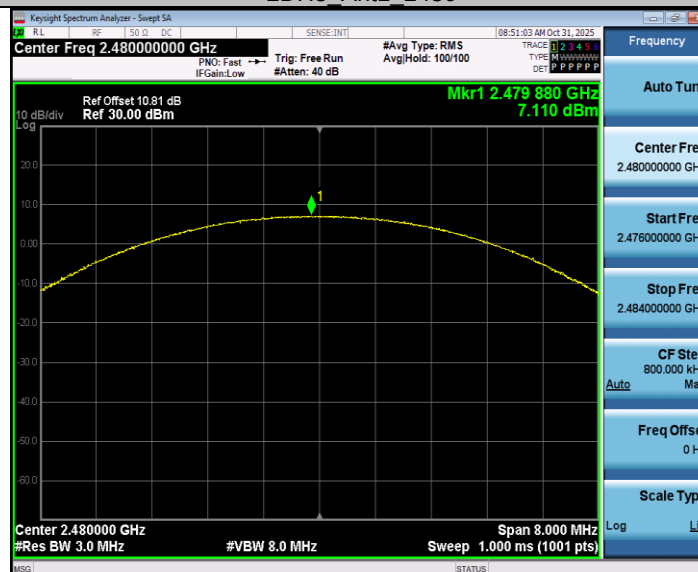
2DH5_Ant1_2402



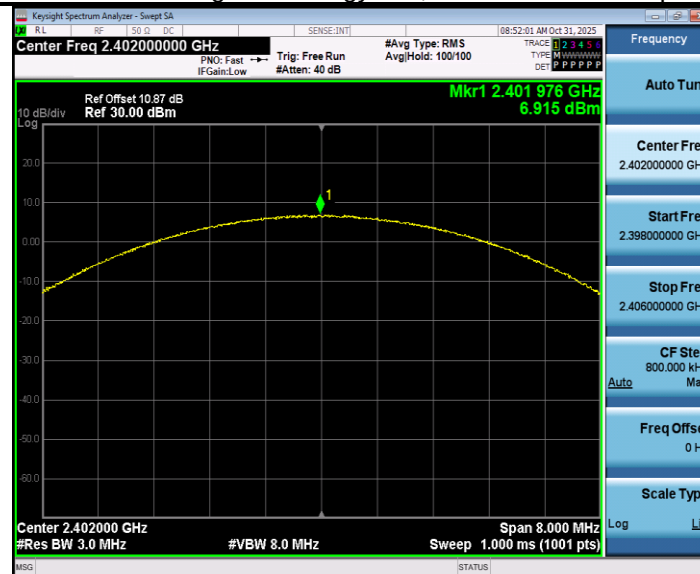
2DH5 Ant1 2441



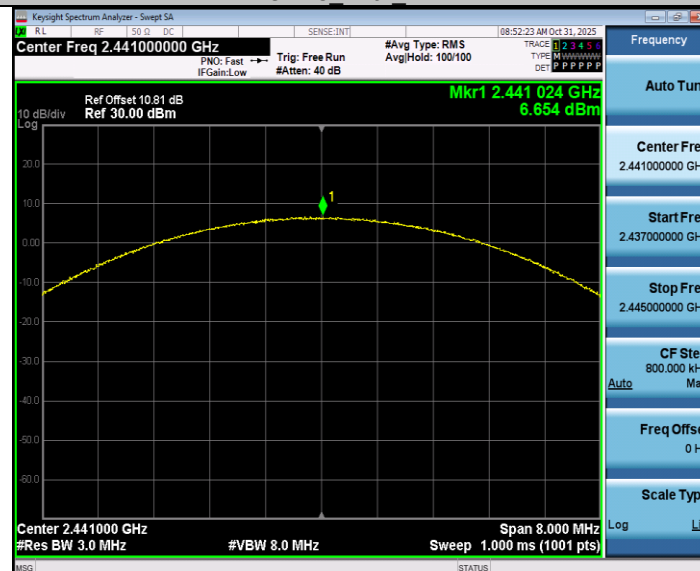
2DH5 Ant1 2480



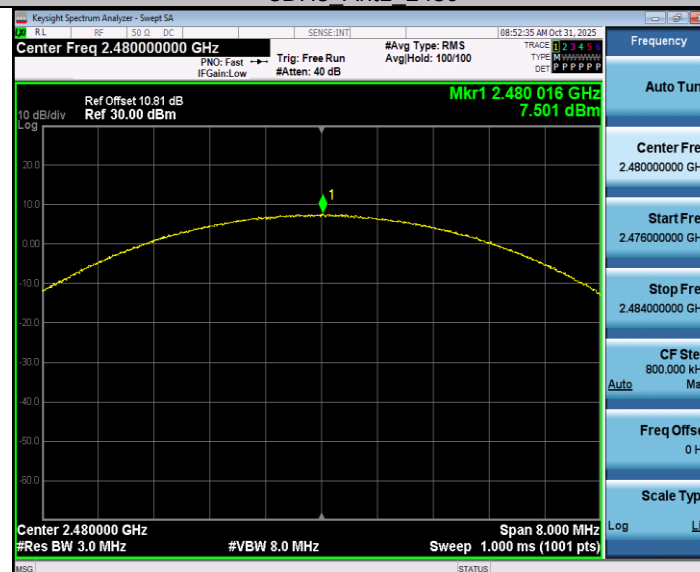
3DH5 Ant1 2402



3DH5 Ant1 2441



3DH5 Ant1 2480

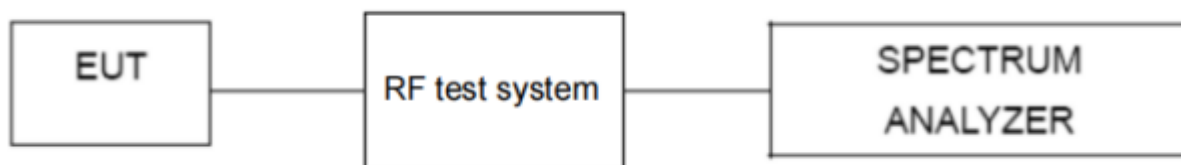




9. HOPPING CHANNEL SEPARATION

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2020
Receiver setup:	RBW = 30kHz, VBW = 100kHz, Detector = peak
Limit:	GFSK: 20dB Bandwidth $\pi/4$ -DQPSK & 8DSK: 0.025MHz or 2/3 of the 20dB Bandwidth (whichever is greater)

9.1 TEST SETUP



9.2 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 30kHz. VBW = 100kHz , Span = 3.0MHz. Sweep = auto;
Detector Function = Peak; Trace = Max hold.
3. Allow the trace to stabilize. Use the marker-delta function to determine the separation between the peaks of the adjacent channels. The limit is specified in one of the subparagraphs of this Section Submit this plot.

9.3 DEVIATION FROM STANDARD

No deviation.



9.4 TEST RESULT

TestMode	Antenna	Freq(MHz)	Result[MHz]	Limit[MHz]	Verdict
DH5	Ant1	Hop	0.992	≥ 0.690	PASS
2DH5	Ant1	Hop	0.998	≥ 0.918	PASS
3DH5	Ant1	Hop	1.066	≥ 0.880	PASS



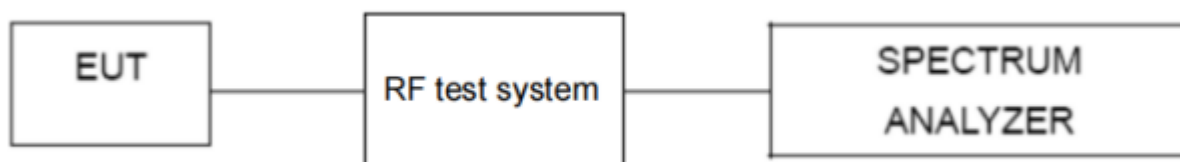




10. NUMBER OF HOPPING FREQUENCY

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2020
Receiver setup:	RBW = 100 kHz, VBW = 300 kHz Frequency range = 2400MHz - 2483.5MHz Detector = peak
Limit:	$P_{\max-pk} \leq 1W$, $N_{ch} \geq 75$ Channels $P_{\max-pk} \leq 0.125W$, $N_{ch} \geq 15$ Channels

10.1 TEST SETUP



10.2 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 100kHz. VBW = 300kHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. It may prove necessary to break the span up to sections. in order to clearly show all of the hopping frequencies. The limit is specified in one of the subparagraphs of this Section.
4. Set the spectrum analyzer: Start Frequency = 2.4GHz, Stop Frequency = 2.4835GHz. Sweep=auto;

10.3 DEVIATION FROM STANDARD

No deviation.

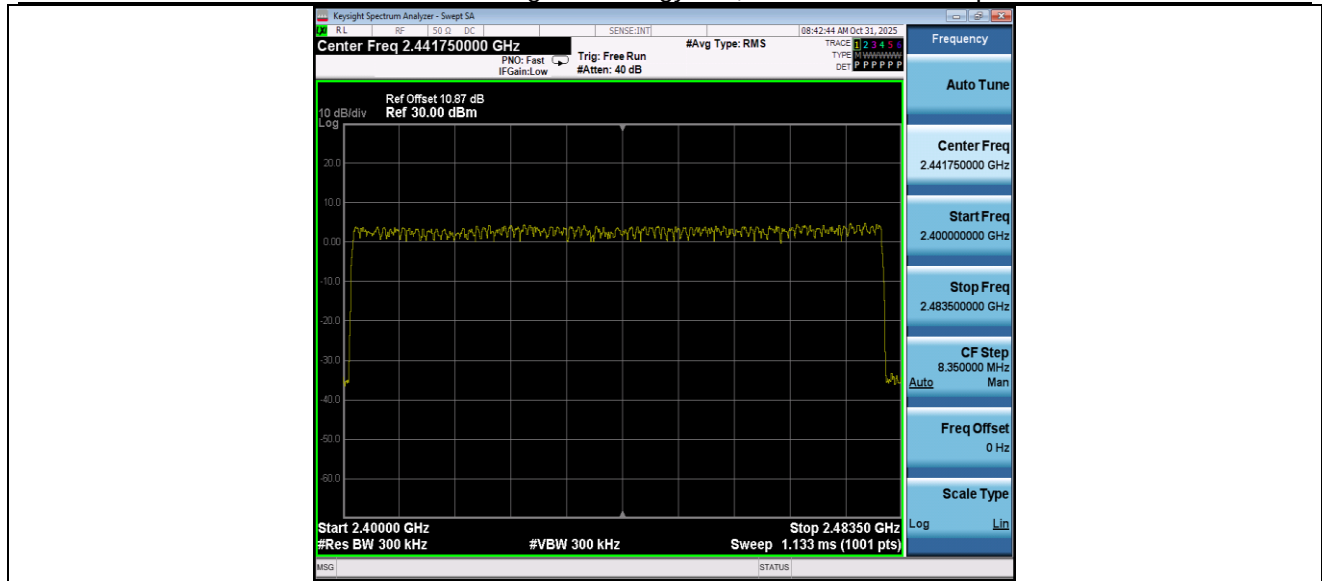


10.4 TEST RESULT

Modulation	Packet	Hopping Number	Limit	Result
GFSK	1-DH5	79	≥ 75	Pass
$\pi/4$ -DQPSK	2-DH5	79	≥ 15	Pass
8-DPSK	3-DH5	79	≥ 15	Pass

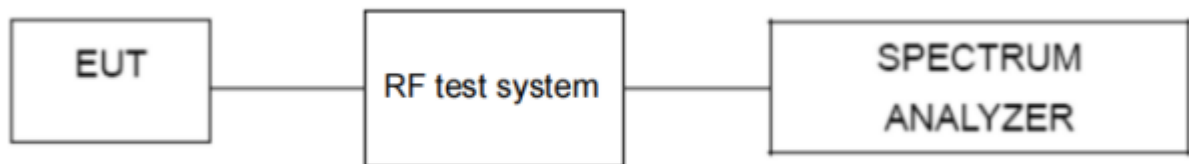
Test Plots:
79 Channels in total
DH5_Ant1_Hop





**11. DWELL TIME**

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2020
Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak
Limit:	0.4 Second

11.1 TEST SETUP**11.2 TEST PROCEDURE**

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0 Hz;
3. Set RBW = 1MHz and VBW = 3MHz. Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g.. data rate. modulation format. etc.). repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

11.3 DEVIATION FROM STANDARD

No deviation.



11.4 TEST RESULT

GFSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	DH1	121.92	400	Pass
2441MHz	DH3	261.92	400	Pass
2441MHz	DH5	307.73	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

Test channel: as blow

CH:2441MHz time slot= $0.381(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 121.92 \text{ ms}$ CH:2441MHz time slot= $1.637(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 261.92 \text{ ms}$ CH:2441MHz time slot= $2.885(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 307.73 \text{ ms}$ $\pi/4$ -DQPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	2DH1	123.52	400	Pass
2441MHz	2DH3	262.24	400	Pass
2441MHz	2DH5	307.95	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

Test channel: as blow

CH:2441MHz time slot= $0.386(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 123.52 \text{ ms}$ CH:2441MHz time slot= $1.639(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 262.24 \text{ ms}$ CH:2441MHz time slot= $2.887(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 307.95 \text{ ms}$

8-DPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	3DH1	123.52	400	Pass
2441MHz	3DH3	262.24	400	Pass
2441MHz	3DH5	308.05	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

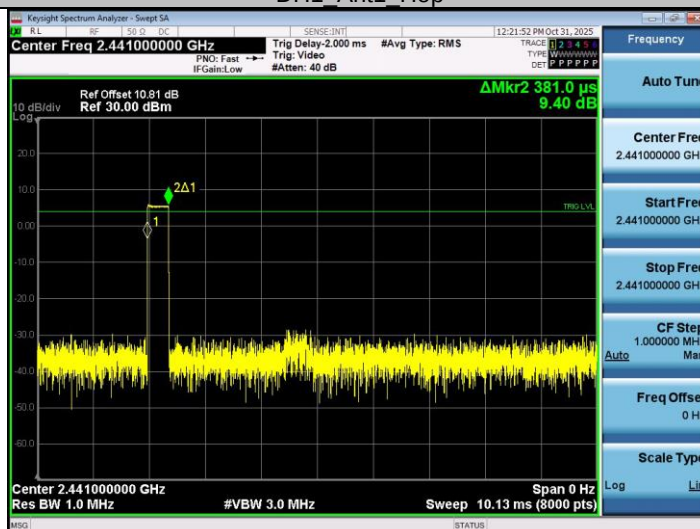
Test channel: as blow

CH:2441MHz time slot= $0.386(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 123.52 \text{ ms}$ CH:2441MHz time slot= $1.638(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 262.08 \text{ ms}$ CH:2441MHz time slot= $2.888(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 308.05 \text{ ms}$

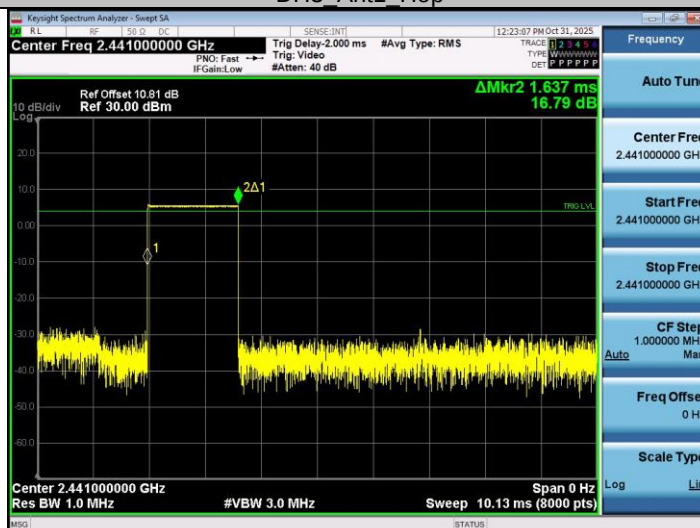


Test Plots

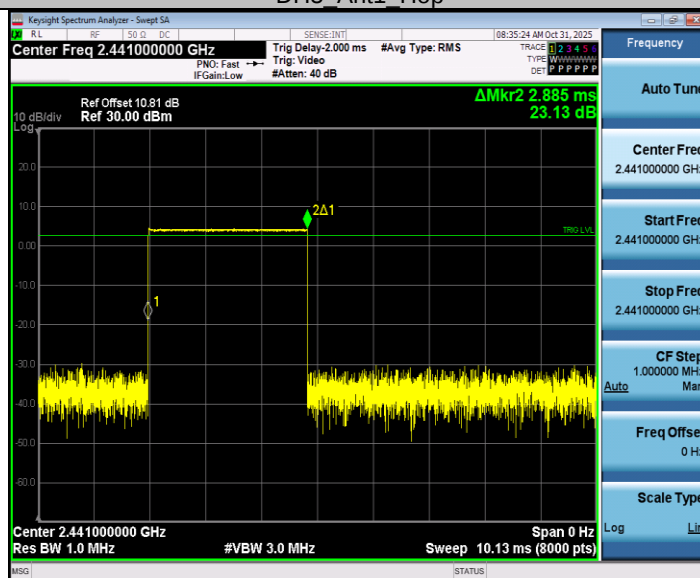
DH1_Ant1_Hop



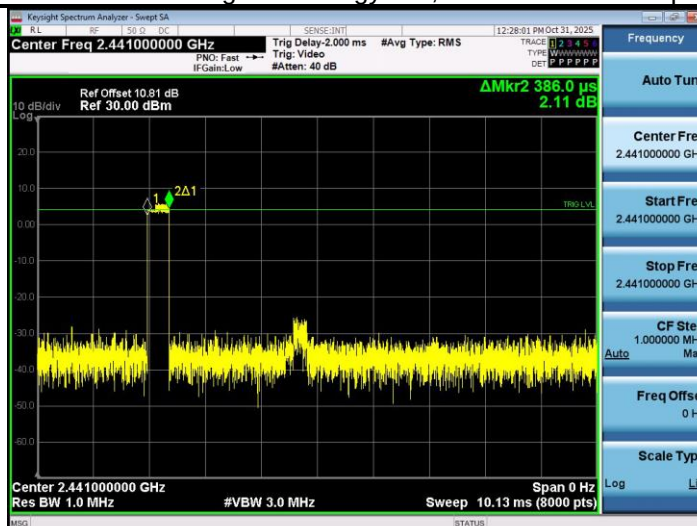
DH3_Ant1_Hop



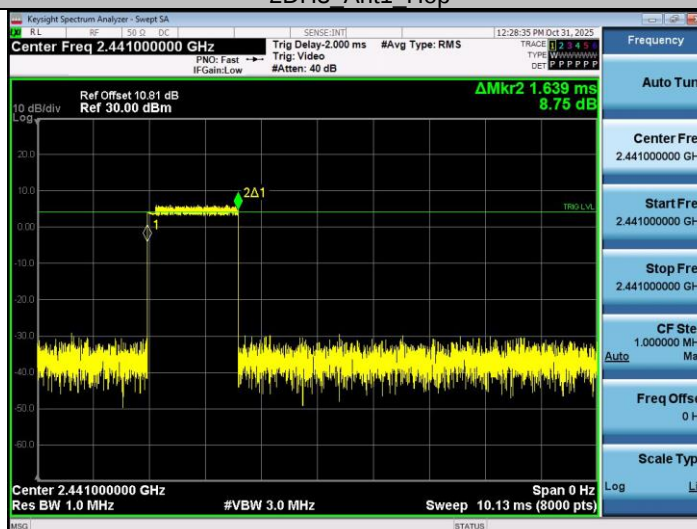
DH5_Ant1_Hop



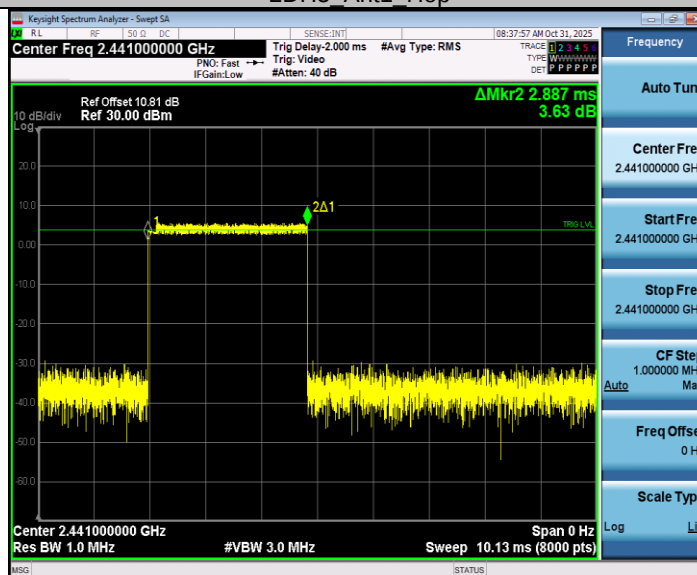
2DH1_Ant1_Hop



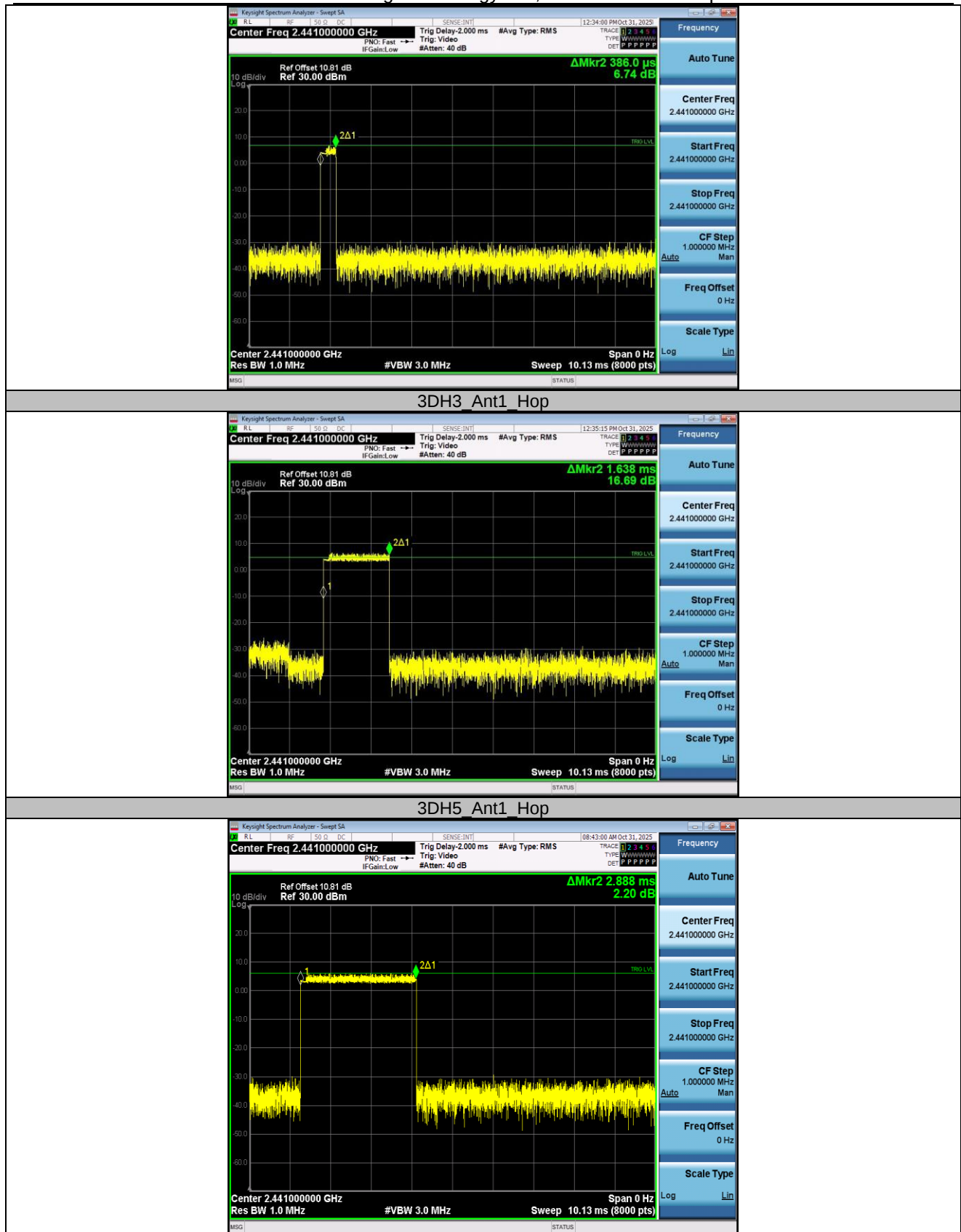
2DH3 Ant1 Hop



2DH5 Ant1 Hop



3DH1 Ant1 Hop



**12. ANTENNA REQUIREMENT**

Standard requirement:	FCC Part15 C Section 15.203 /247(c)
15.203 requirement: An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.	
15.247(c) (1)(i) requirement: (i) Systems operating in the 2400-2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.	
EUT Antenna:	
The antenna is FPC Antenna, the best case gain of the antennas is 3.5dBi, reference to the appendix II for details.	



13. TEST SETUP PHOTO

Reference to the appendix I for details.

14. EUT CONSTRUCTIONAL DETAILS

Reference to the appendix II for details.

***** END OF REPORT *****