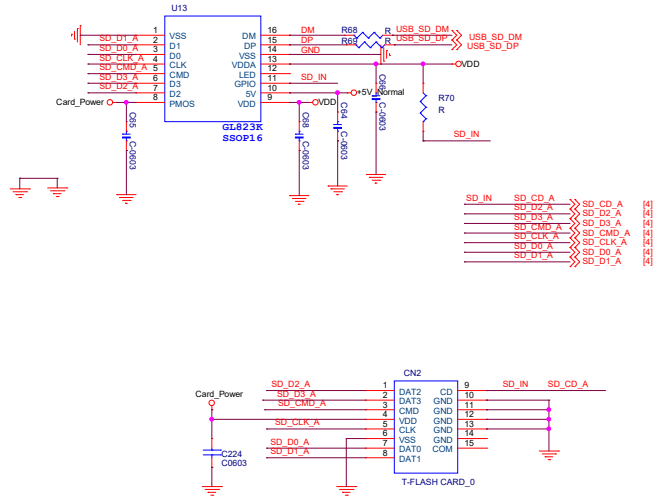
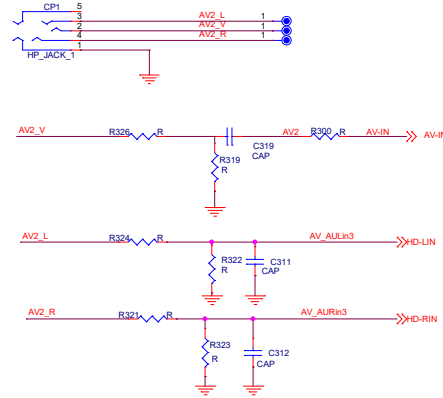
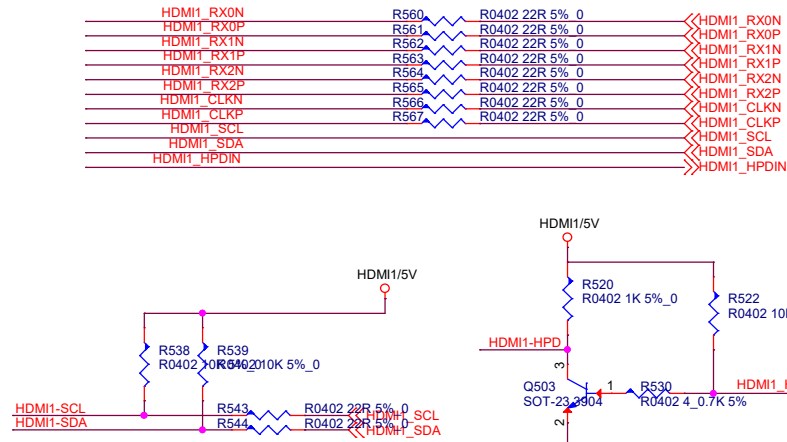
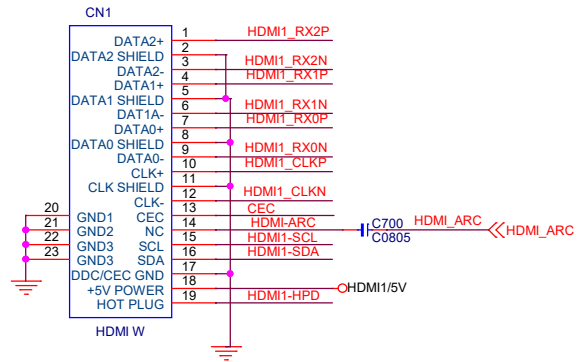




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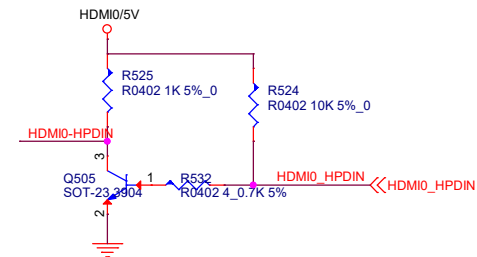
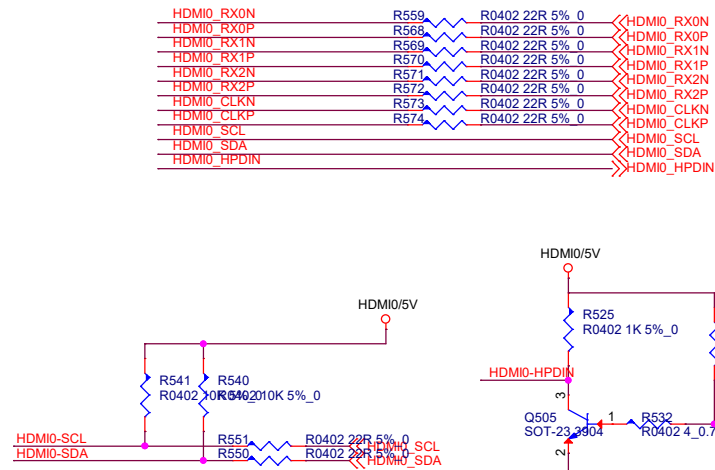
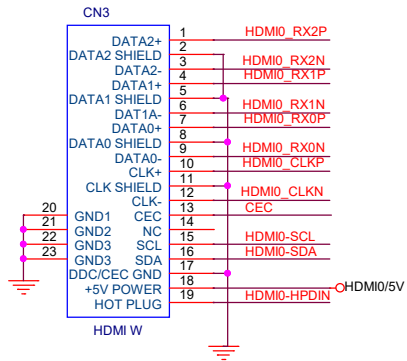
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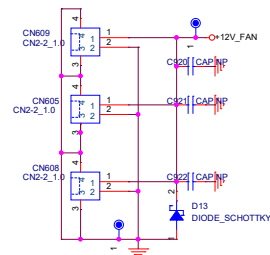
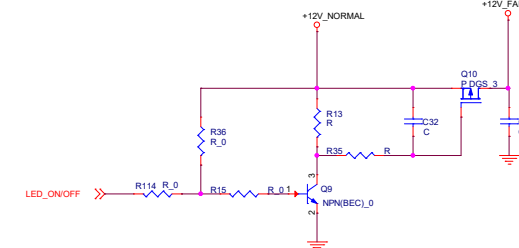
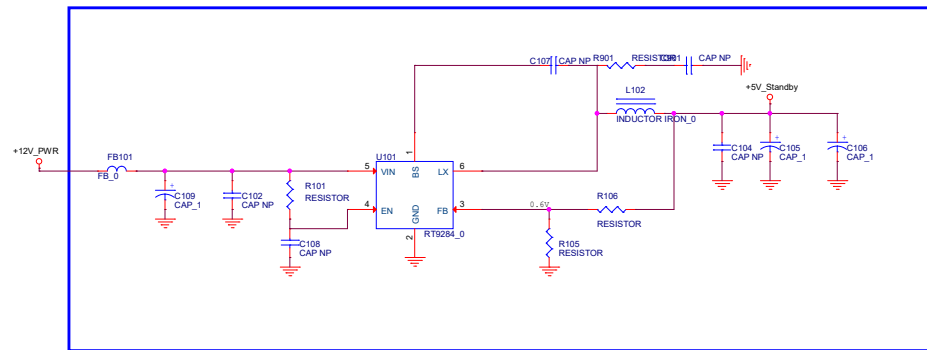
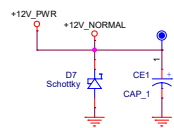


## CEC & ARC

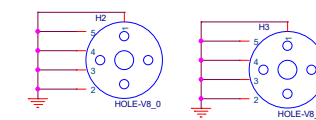
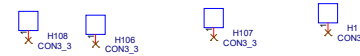
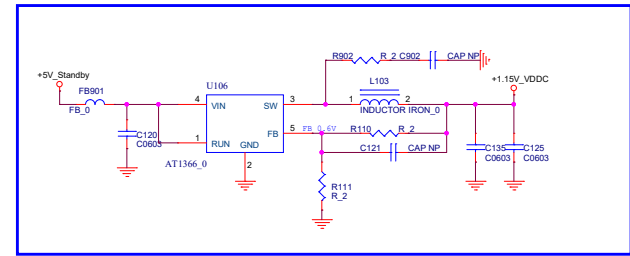
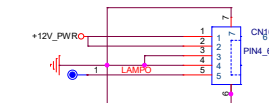
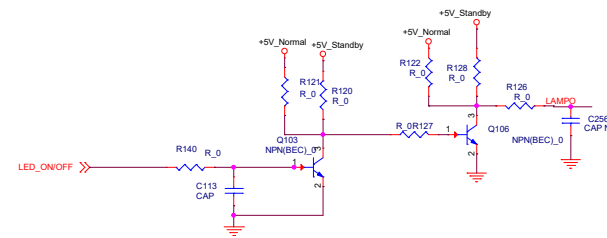
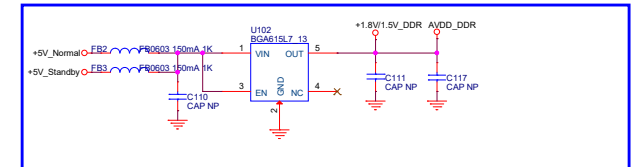
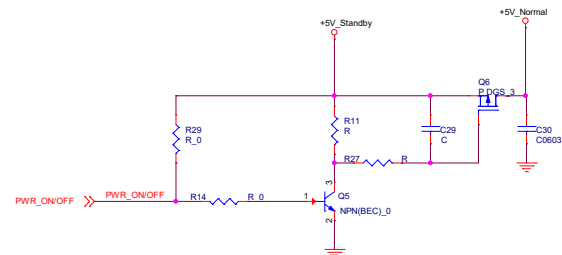
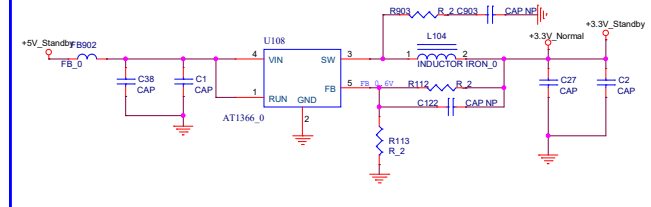


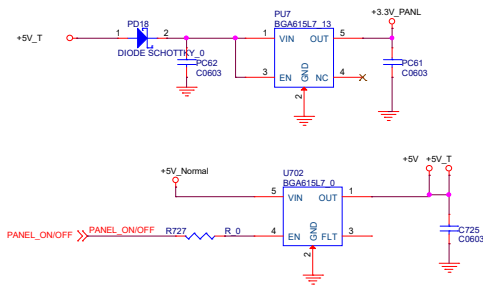
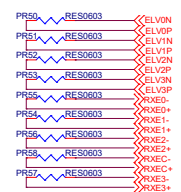
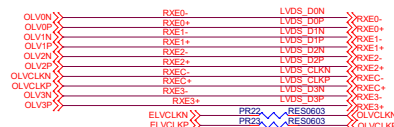
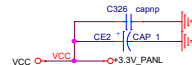
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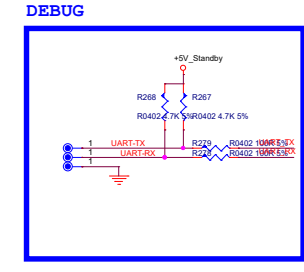
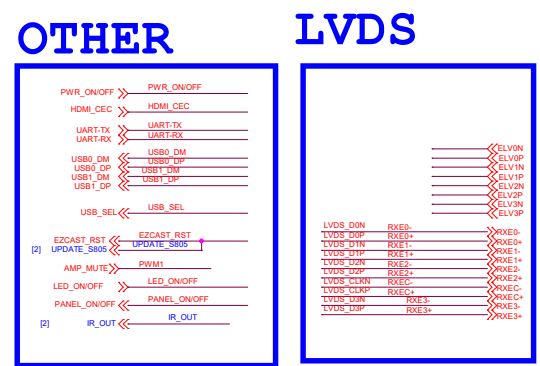
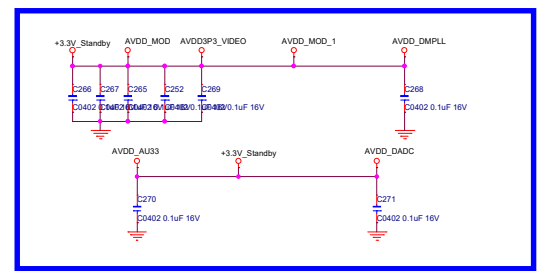
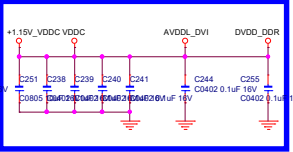
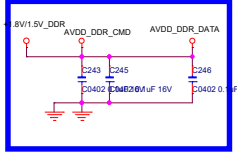
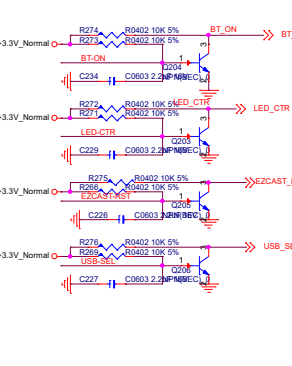
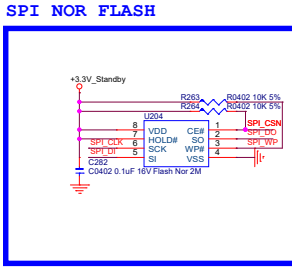
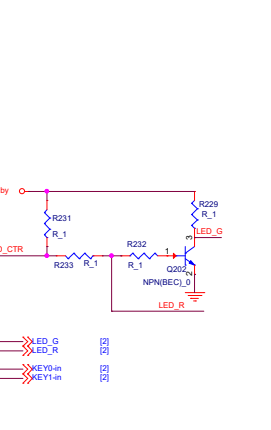
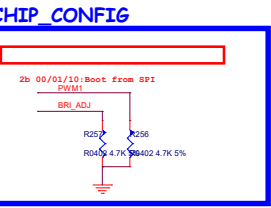
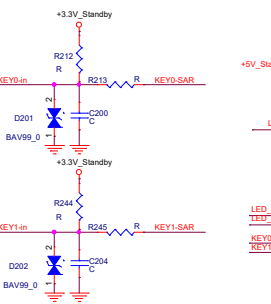
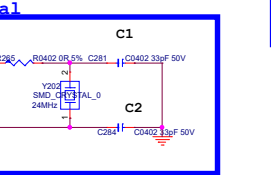
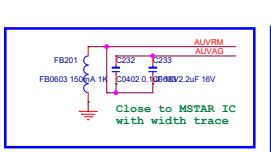
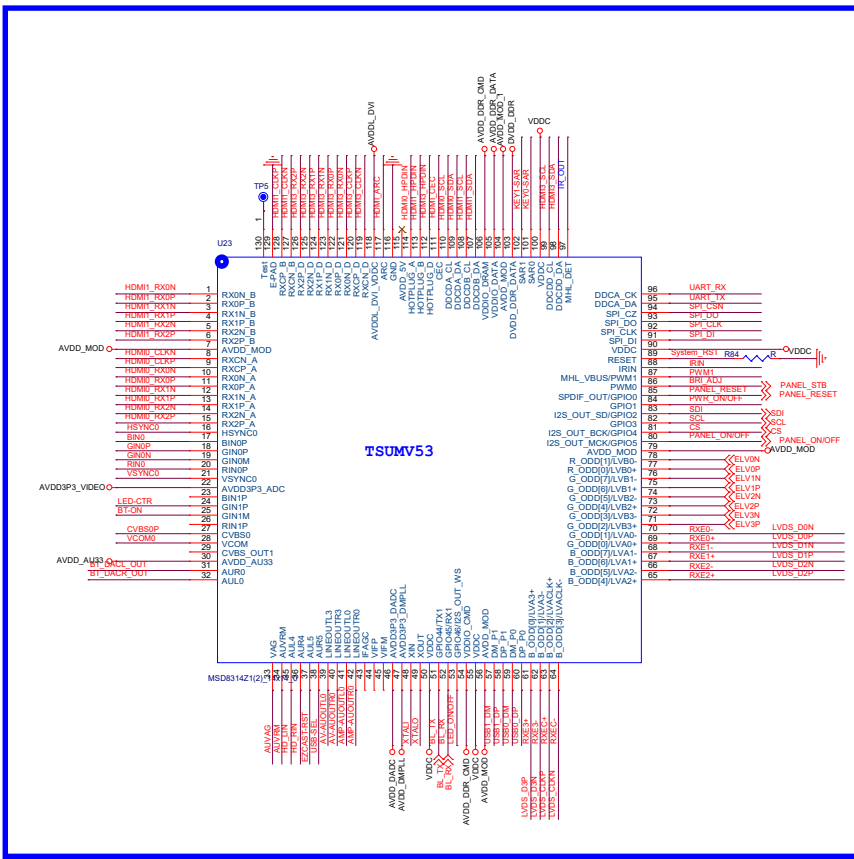
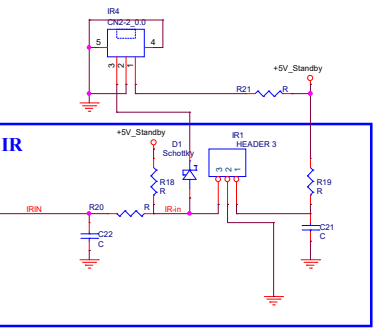
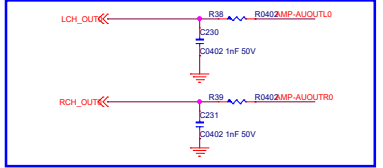
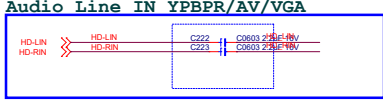
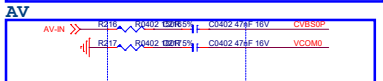
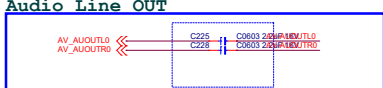
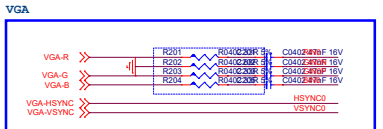
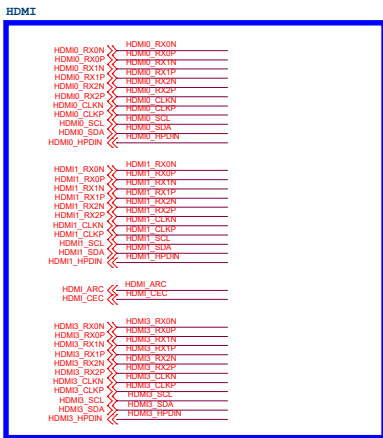


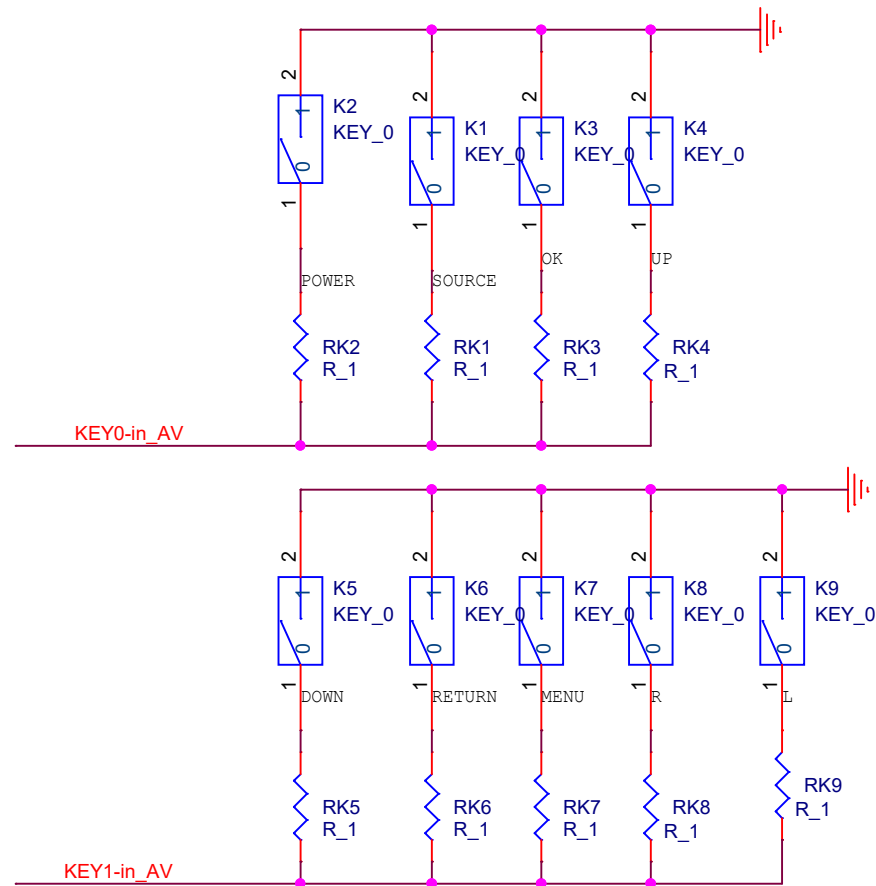
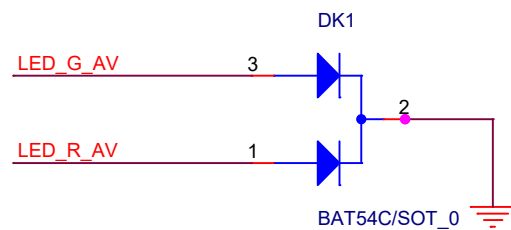
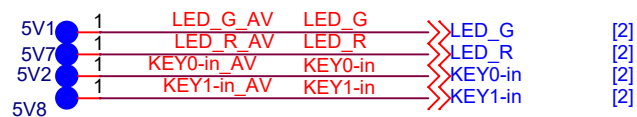


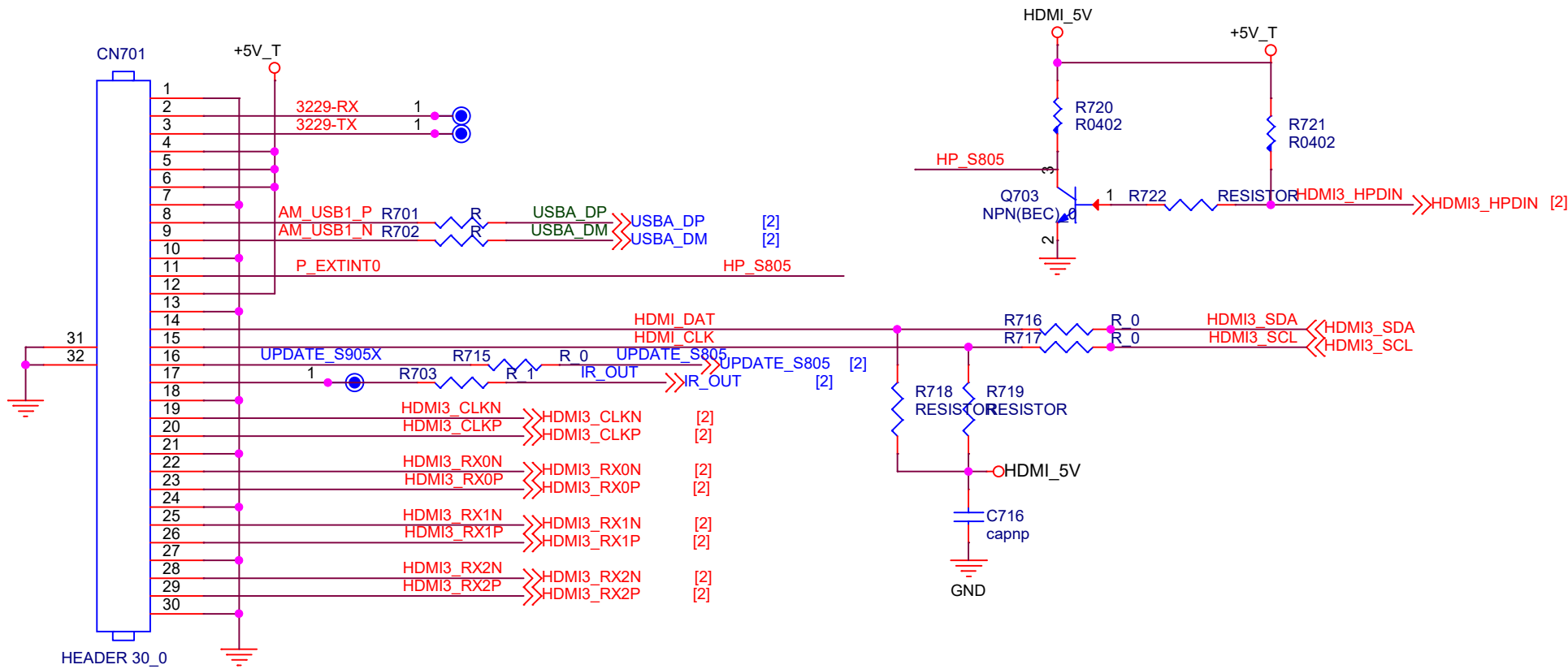
3.3V 320mA Max(When ATV)

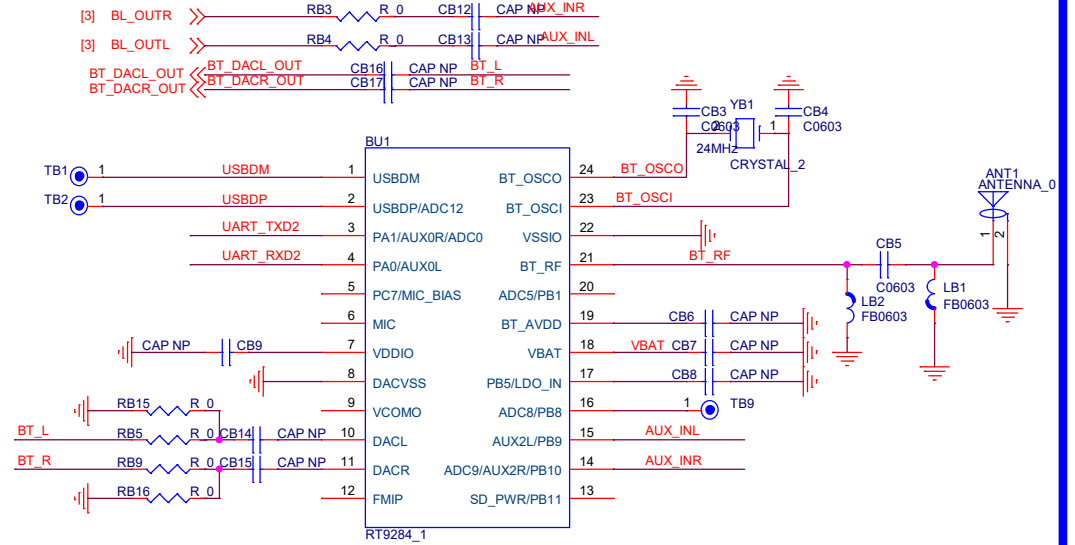
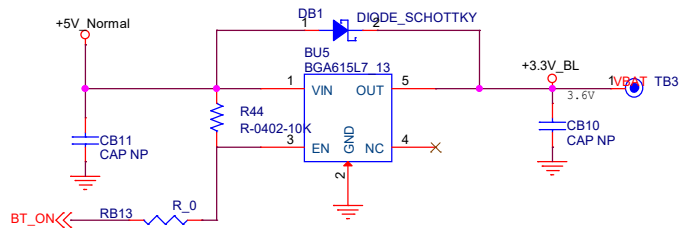
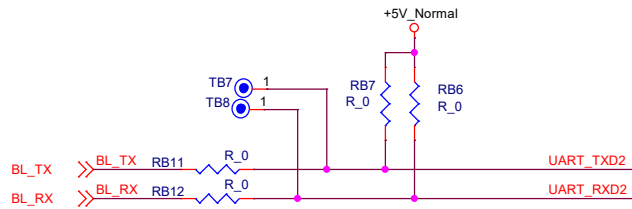
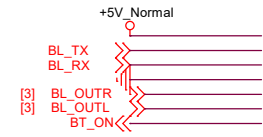












U32I  
DSP\_RK3036-G  
BGA236 12R00X12R00X1R28

## I2S

GPIO1\_A0/I2S\_MCLK\_d  
GPIO1\_A1/I2S\_SCLK\_d  
GPIO1\_A2/I2S\_LRCK\_RX/PWM0\_d  
GPIO1\_A3/I2S\_LRCK\_TX\_d  
GPIO1\_A5/I2S\_SDI\_d  
GPIO1\_A4/I2S\_SDO0\_d  
GPIO2\_D6/I2S\_SDO1\_d  
GPIO2\_D5/I2S\_SDO2\_d  
GPIO2\_D4/I2S\_SDO3\_d

D17  
C17  
E15  
D16  
D15  
B17  
C16  
C15  
B16

WIFI\_PWR\_ON << WIFI\_PWR\_ON

## SDMMC1/I2S1

GPIO0\_B3/SDMMC1\_D0/I2S1\_LRCK\_RX\_u  
GPIO0\_B4/SDMMC1\_D1/I2S1\_LRCK\_TX\_u  
GPIO0\_B5/SDMMC1\_D2/I2S1\_SDI\_u  
GPIO0\_B6/SDMMC1\_D3/I2S1\_SCLK\_u  
GPIO0\_B1/SDMMC1\_CLK0/I2S1\_MCLK\_u  
GPIO0\_B0/SDMMC1\_CMD/I2S1\_SDO\_u

F17  
G16  
G17  
H15  
F16  
G15

U32F  
DSP\_RK3036-G  
BGA236 12R00X12R00X1R28

## MAC

GPIO2\_C3/MAC\_TXD0\_d  
GPIO2\_C2/MAC\_TXD1\_d  
GPIO2\_C1/MAC\_RXD0\_d  
GPIO2\_C0/MAC\_RXD1\_d  
GPIO2\_D1/MAC\_MDC\_d  
GPIO2\_B7/MAC\_RXER\_d  
GPIO2\_B6/MAC\_CLK\_IN/MAC\_CLK\_OUT\_d  
GPIO2\_B5/MAC\_TXEN\_d  
GPIO2\_B4/MAC\_MDIO\_d  
GPIO2\_B2/MAC\_CRD\_d

U11  
T11  
R10  
T12  
U12  
U13  
R11  
R13  
T13  
U14

## I2C/PWM

GPIO0\_A0/PWM1/I2C0\_SCL\_u  
GPIO0\_A1/PWM2/I2C0\_SDA\_u

U15  
T15

GPIO0\_A2/I2C1\_SCL\_u  
GPIO0\_A3/I2C1\_SDA\_u

R14  
R15

GPIO2\_C5/I2C2\_SCL\_u  
GPIO2\_C4/I2C2\_SDA\_u

T16  
U16

## SPDIF

GPIO0\_D4/SPDIF\_d

N13

## GPIO

GPIO2\_A7\_d  
GPIO2\_B0\_u  
GPIO2\_B1\_d  
GPIO2\_B3\_d  
GPIO0\_D2\_d

N2  
M2  
L3  
N1  
E16

<< Recover

GPIO0\_D3/IR\_d

E17

<< IR

GPIO0\_C4/DRIVE\_VBUS\_d

T14

## UART

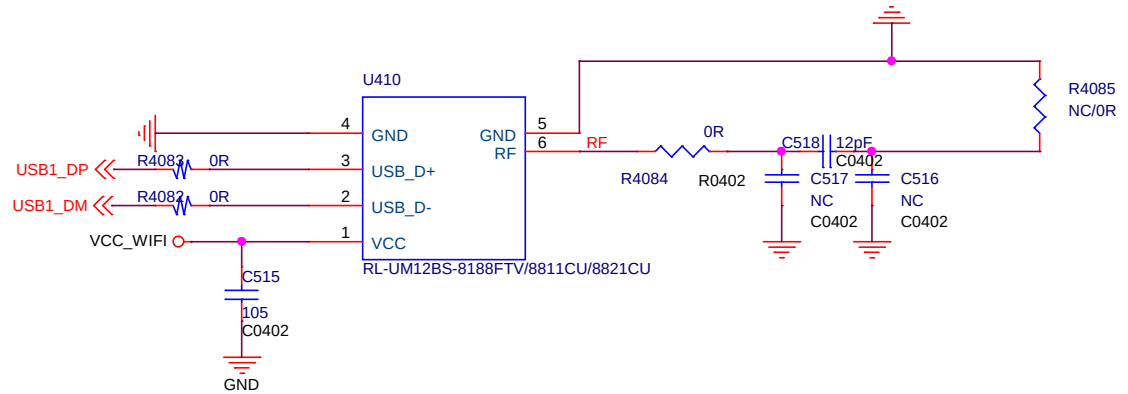
GPIO0\_C0/UART0\_TX\_d  
GPIO0\_C1/UART0\_RX\_u  
GPIO0\_C2/UART0\_RTSN\_u  
GPIO0\_C3/UART0\_CTSN\_u

B15  
C14  
A17  
A16

<< UART0\_TX  
<< UART0\_RX

GPIO2\_C6/UART1\_RX\_u  
GPIO2\_C7/UART1\_TX\_d

P1  
P2



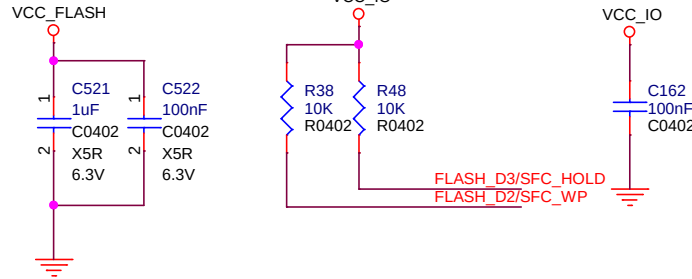
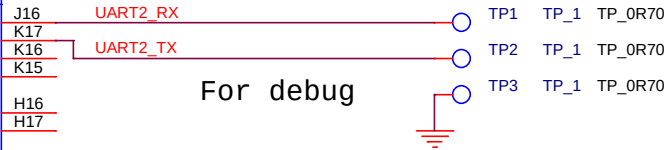
U32H  
DSP\_RK3036-G  
BGA236\_12R00X12R00X1R28

## SDMMC0

GPIO1\_C2/SDMMC0\_D0/UART2\_RX\_u  
GPIO1\_C3/SDMMC0\_D1/UART2\_TX\_u  
GPIO1\_C4/SDMMC0\_D2/JTAG\_TCK\_u  
GPIO1\_C5/SDMMC0\_D3/JTAG\_TMS\_u

GPIO1\_C0/SDMMC0\_CLKO\_d  
GPIO1\_B7/SDMMC0\_CMD\_u

GPIO1\_C1/SDMMC0\_DET\_u



U32G  
DSP\_RK3036-G  
BGA236\_12R00X12R00X1R28

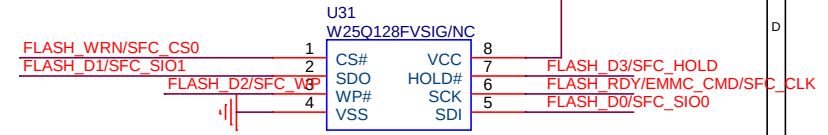
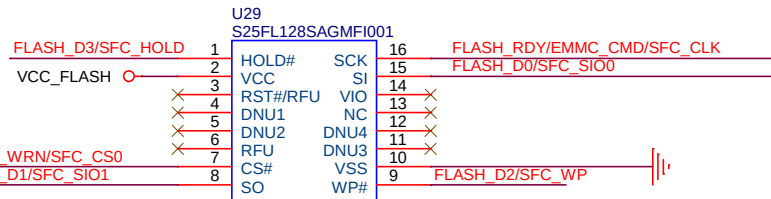
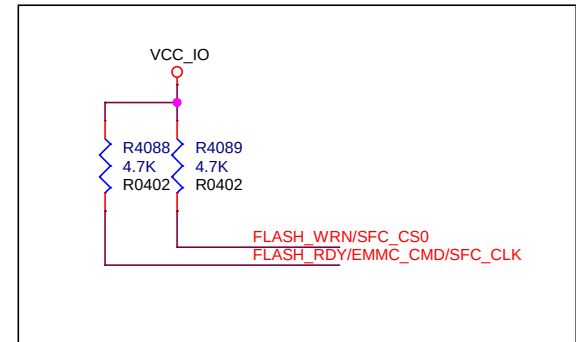
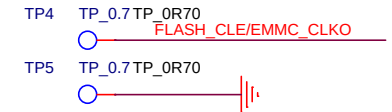
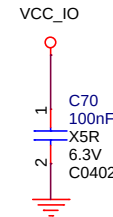
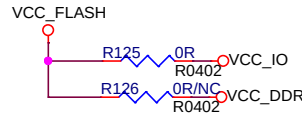
## Nand Flash/emmc/SFC

GPIO1\_D0/FLASH\_D0/EMMC\_D0/SFC\_SIO0\_u  
GPIO1\_D1/FLASH\_D1/EMMC\_D1/SFC\_SIO1\_u  
GPIO1\_D2/FLASH\_D2/EMMC\_D2/SFC\_SIO2\_u  
GPIO1\_D3/FLASH\_D3/EMMC\_D3/SFC\_SIO3\_u  
GPIO1\_D4/FLASH\_D4/EMMC\_D4/SPI\_RXD\_u  
GPIO1\_D5/FLASH\_D5/EMMC\_D5/SPI\_TXD\_u  
GPIO1\_D6/FLASH\_D6/EMMC\_D6/SPI\_CSN0\_u  
GPIO1\_D7/FLASH\_D7/EMMC\_D7/SPI\_CSN1\_u

GPIO2\_A0/FLASH\_ALE/SPI\_CLK\_d  
GPIO2\_A1/FLASH\_CLE/EMMC\_CLK\_d  
GPIO2\_A2/FLASH\_WRN/SFC\_CSN0\_u  
GPIO2\_A3/FLASH\_RDN/SFC\_CSN1\_u  
GPIO2\_A4/FLASH\_RDY/EMMC\_CMD/SFC\_CLK\_u  
GPIO2\_A6/FLASH\_CS0\_u

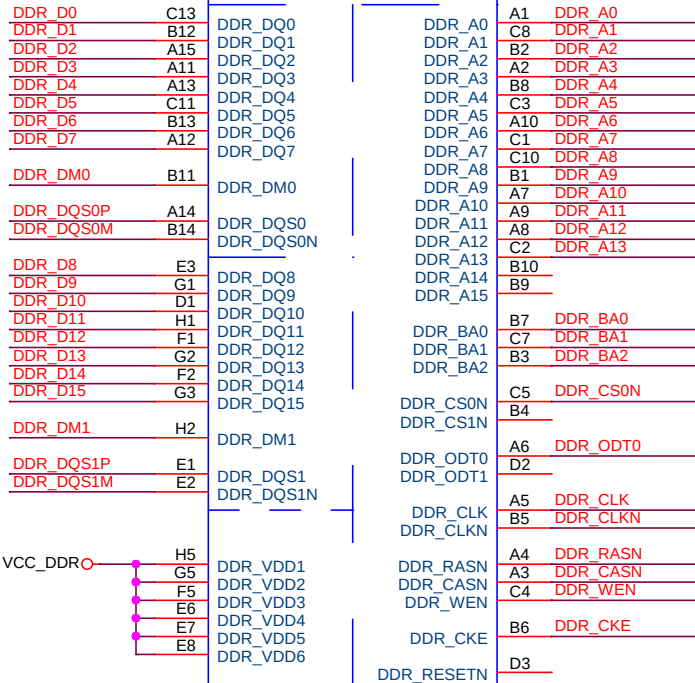
U17 FLASH\_D0/SFC\_SIO0  
P15 FLASH\_D1/SFC\_SIO1  
T17 FLASH\_D2/SFC\_WP  
R16 FLASH\_D3/SFC\_HOLD

N16  
N17 FLASH\_CLE/EMMC\_CLKO  
M16 FLASH\_WRN/SFC\_CS0  
M17  
L15 FLASH\_RDY/EMMC\_CMD/SFC\_CLK  
L16

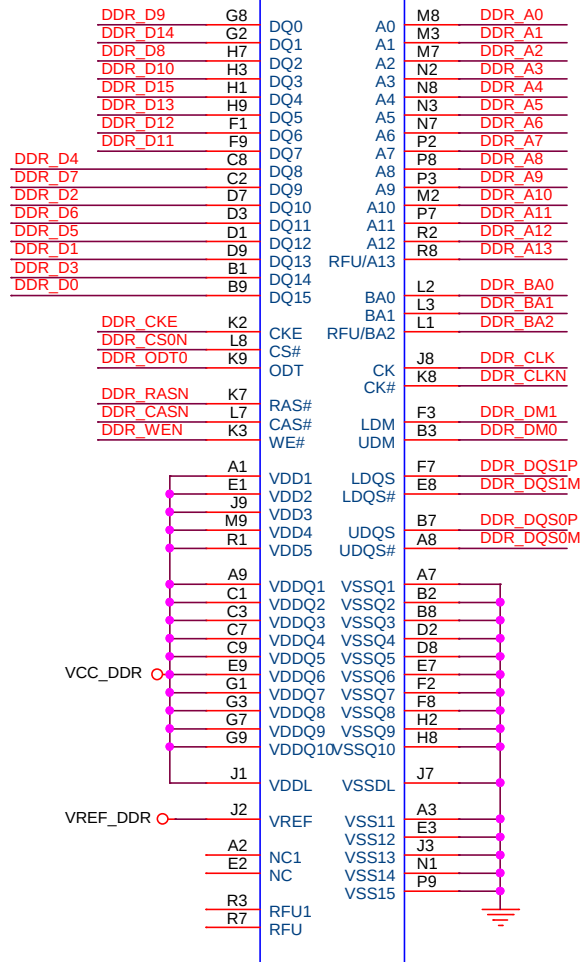


U32A  
DSP\_RK3036-G  
BGA236 12R00X12R00X1R28

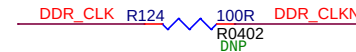
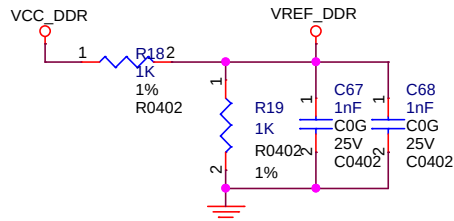
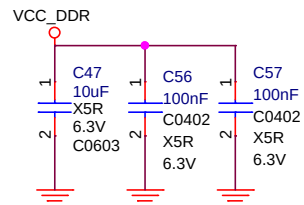
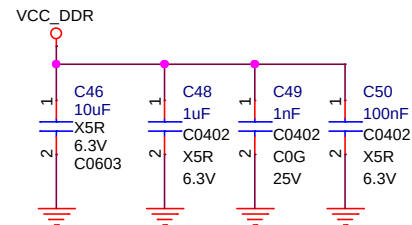
## Memory

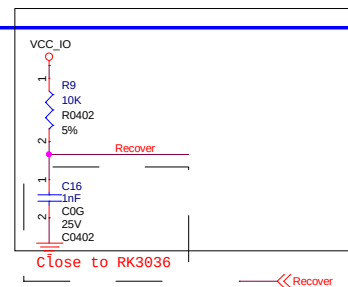
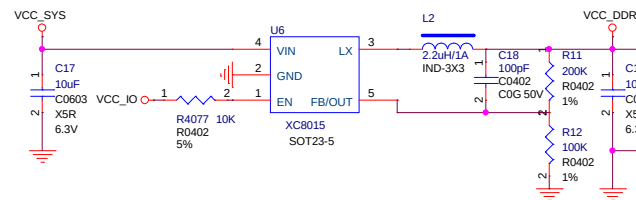
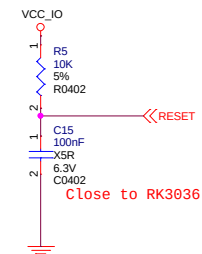
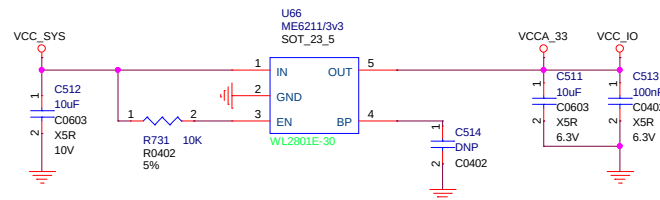


U27  
DDRII-1Gb



## DDR FILTER

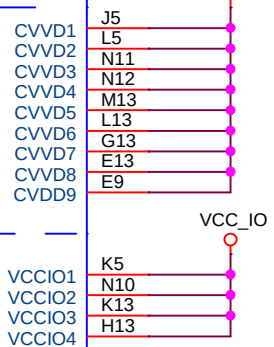




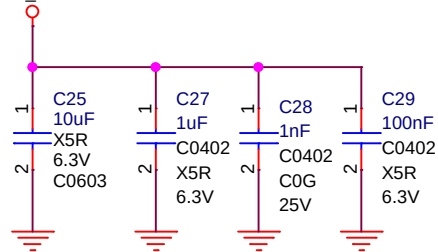
Recovery key

U32J  
DSP\_RK3036-G  
BGA236 12R00X12R00X1R28

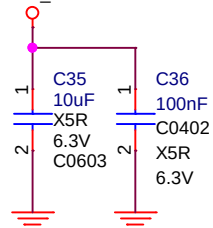
### Power



VDD\_CPU

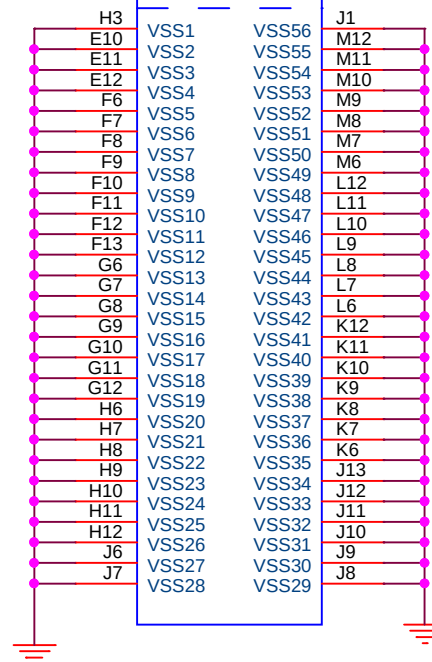


VCC\_IO



U32K  
DSP\_RK3036-G  
BGA236 12R00X12R00X1R28

### GND



福州瑞芯微电子有限公司

Title: RK3036 Power

File: RK3036-G Dongle

REV:0.1

Create Date: Tuesday, March 24, 2015

Page Num:5

Modify Date: Wednesday, August 18, 2021

Page Total:12

