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Band	Channel	Cell Power	Test Item	Measured Value	EIRP
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;5;CP-OFDM 16QAM*Inner_Full:PC2_SUM	22.11	28.41
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;6;CP-OFDM 16QAM*Edge_1RB_Left:PC2_SUM	21.01	27.31
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;7;CP-OFDM 16QAM*Edge_1RB_Right:PC2_SUM	21.13	27.43
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;8;CP-OFDM 16QAM*Outer_Full:PC2_SUM	21.39	27.69
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;9;CP-OFDM 64QAM*Edge_1RB_Left:PC2_SUM	20.79	27.09
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;10;CP-OFDM 64QAM*Edge_1RB_Right:PC2_SUM	21.03	27.33
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;11;CP-OFDM 64QAM*Outer_Full:PC2_SUM	20.66	26.96
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;12;CP-OFDM 256QAM*Edge_1RB_Left:PC2_SUM	16.83	23.13
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;13;CP-OFDM 256QAM*Edge_1RB_Right:PC2_SUM	17.2	23.5
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;14;CP-OFDM 256QAM*Outer_Full:PC2_SUM	17.49	23.79
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;1;CP-OFDM QPSK*Inner_Full:PC2_SUM	22.76	29.06
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;2;CP-OFDM QPSK*Edge_1RB_Left:PC2_SUM	20.43	26.73
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;3;CP-OFDM QPSK*Edge_1RB_Right:PC2_SUM	19.99	26.29
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;4;CP-OFDM QPSK*Outer_Full:PC2_SUM	21.45	27.75
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;5;CP-OFDM 16QAM*Inner_Full:PC2_SUM	22.32	28.62
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;6;CP-OFDM 16QAM*Edge_1RB_Left:PC2_SUM	21.38	27.68
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;7;CP-OFDM 16QAM*Edge_1RB_Right:PC2_SUM	20.95	27.25
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;8;CP-OFDM 16QAM*Outer_Full:PC2_SUM	21.47	27.77
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;9;CP-OFDM 64QAM*Edge_1RB_Left:PC2_SUM	20.98	27.28
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;10;CP-OFDM 64QAM*Edge_1RB_Right:PC2_SUM	20.54	26.84
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;11;CP-OFDM 64QAM*Outer_Full:PC2_SUM	20.93	27.23
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;12;CP-OFDM 256QAM*Edge_1RB_Left:PC2_SUM	17.27	23.57
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;13;CP-OFDM 256QAM*Edge_1RB_Right:PC2_SUM	16.85	23.15
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;14;CP-OFDM 256QAM*Outer_Full:PC2_SUM	17.62	23.92
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;1;CP-OFDM QPSK*Inner_Full:PC2_SUM	21.98	28.28
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;2;CP-OFDM QPSK*Edge_1RB_Left:PC2_SUM	20.3	26.6
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;3;CP-OFDM QPSK*Edge_1RB_Right:PC2_SUM	18.63	24.93
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;4;CP-OFDM QPSK*Outer_Full:PC2_SUM	20.7	27
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;5;CP-OFDM 16QAM*Inner_Full:PC2_SUM	21.52	27.82
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;6;CP-OFDM 16QAM*Edge_1RB_Left:PC2_SUM	21.21	27.51
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;7;CP-OFDM 16QAM*Edge_1RB_Right:PC2_SUM	19.51	25.81
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;8;CP-OFDM 16QAM*Outer_Full:PC2_SUM	20.72	27.02
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;9;CP-OFDM 64QAM*Edge_1RB_Left:PC2_SUM	20.92	27.22
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;10;CP-OFDM 64QAM*Edge_1RB_Right:PC2_SUM	19.24	25.54
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;11;CP-OFDM 64QAM*Outer_Full:PC2_SUM	20.17	26.47
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;12;CP-OFDM 256QAM*Edge_1RB_Left:PC2_SUM	17.22	23.52
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;13;CP-OFDM 256QAM*Edge_1RB_Right:PC2_SUM	15.41	21.71
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;14;CP-OFDM 256QAM*Outer_Full:PC2_SUM	16.87	23.17

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Band	Channel	Cell Power	Test Item	Measured Value	EIRP
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	21.51	27.79
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.72	26
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	19.8	26.08
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	19.95	26.23
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21	27.28
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.65	26.83
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.83	26.91
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:8.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	19.96	26.24
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:9.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.26	26.54
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.33	26.61
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.46	25.74
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.61	22.89
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.73	23.01
n78	647000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 647000:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.26	22.54
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	21.68	27.96
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.88	26.1
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	19.98	26.2
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.11	26.3
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.25	27.53
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.91	27.19
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.99	27.27
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.12	26.4
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.44	26.72
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.52	26.8
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.62	25.9
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.65	22.93
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.75	23.03
n78	650000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 650000:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.38	22.66
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	21.91	28.19
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	20.3	26.58
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	20.21	26.49
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.47	26.75
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.53	27.81
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	21.18	27.46
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	21.24	27.52
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.5	26.78
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.82	27.1
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.89	27.17
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.99	26.27
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.91	23.19
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.99	23.27
n78	653000	-57	6.2D.2 UE maximum output power reduction:n78 10MHz 30kHz 653000:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.62	22.9
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	21.95	28.23
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.91	26.19
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	19.99	26.27
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.38	26.66
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.53	27.81
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.89	27.17
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.95	27.23
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.37	26.65
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.4	26.68
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.48	26.76
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.87	26.15
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.72	23
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.79	23.07
n78	647168	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 647168:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.51	22.79
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	22.06	28.34
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.95	26.23
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	20.1	26.38
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.44	26.72
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.58	27.86
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.91	27.19
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	21.03	27.31
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.44	26.72
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.43	26.71
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.58	26.86
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.95	26.23
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.7	22.98
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.82	23.1
n78	650000	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 650000:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.54	22.82
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	22.28	28.56
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	20.28	26.56
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	20.28	26.56
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.71	26.99
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.83	28.11
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	21.11	27.39
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	21.12	27.4
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.72	27
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.77	27.05
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.79	27.07
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	20.2	26.48
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	17.07	23.35
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	17.07	23.35
n78	652832	-55	6.2D.2 UE maximum output power reduction:n78 15MHz 30kHz 652832:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.87	23.15
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	22.04	28.32
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.69	25.97
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	19.71	25.99
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.46	26.74
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.56	27.84
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.58	26.86
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.58	26.86
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.48	26.76
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:9.CP-OFDM 64QAM*Edge_1RB_Left-PC2_SUM	20.25	26.53
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:10.CP-OFDM 64QAM*Edge_1RB_Right-PC2_SUM	20.28	26.56
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:11.CP-OFDM 64QAM*Outer_Full-PC2_SUM	19.96	26.24
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:12.CP-OFDM 256QAM*Edge_1RB_Left-PC2_SUM	16.51	22.79
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:13.CP-OFDM 256QAM*Edge_1RB_Right-PC2_SUM	16.56	22.84
n78	647334	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 647334:14.CP-OFDM 256QAM*Outer_Full-PC2_SUM	16.59	22.87
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:1.CP-OFDM QPSK*Inner_Full-PC2_SUM	21.16	28.44
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:2.CP-OFDM QPSK*Edge_1RB_Left-PC2_SUM	19.22	26
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:3.CP-OFDM QPSK*Edge_1RB_Right-PC2_SUM	19.9	26.18
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:4.CP-OFDM QPSK*Outer_Full-PC2_SUM	20.56	26.84
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:5.CP-OFDM 16QAM*Inner_Full-PC2_SUM	21.64	27.92
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:6.CP-OFDM 16QAM*Edge_1RB_Left-PC2_SUM	20.58	26.86
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:7.CP-OFDM 16QAM*Edge_1RB_Right-PC2_SUM	20.75	27.03
n78	650000	-54	6.2D.2 UE maximum output power reduction:n78 20MHz 30kHz 650000:8.CP-OFDM 16QAM*Outer_Full-PC2_SUM	20.58	26.86

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