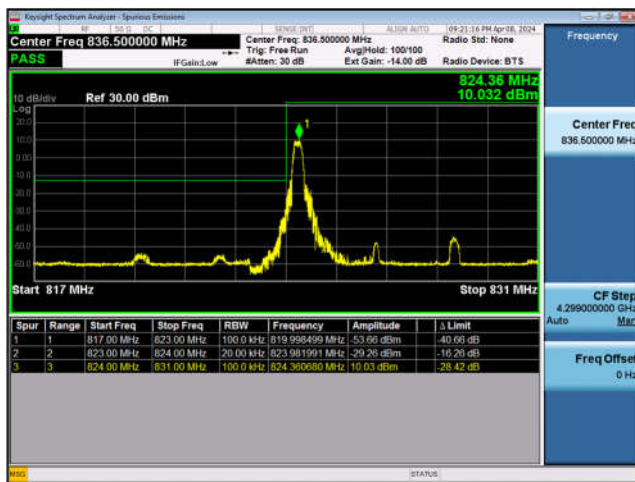
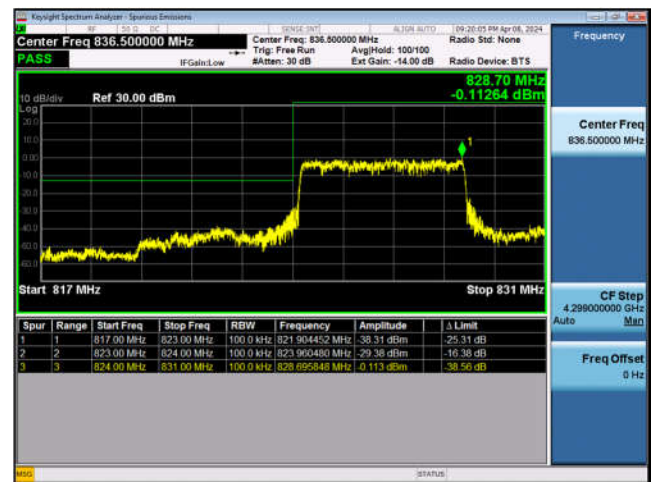


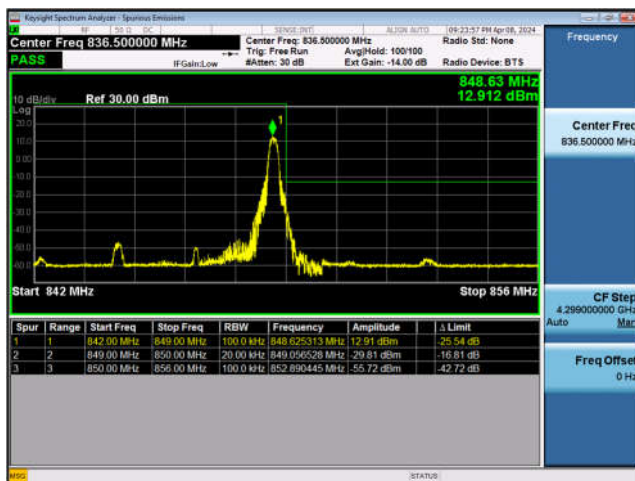
N5_5MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N5_5MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



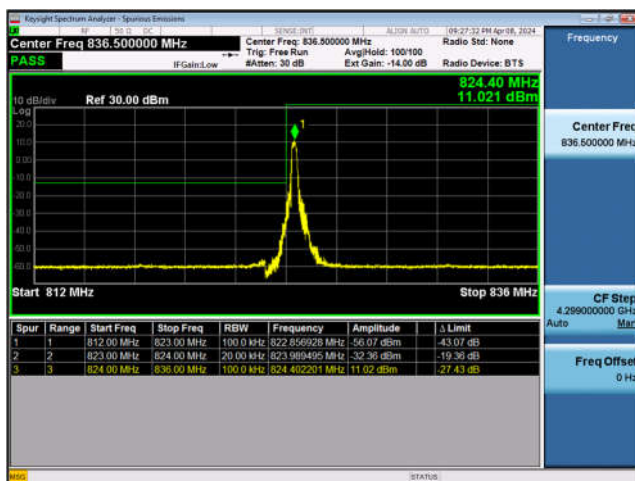
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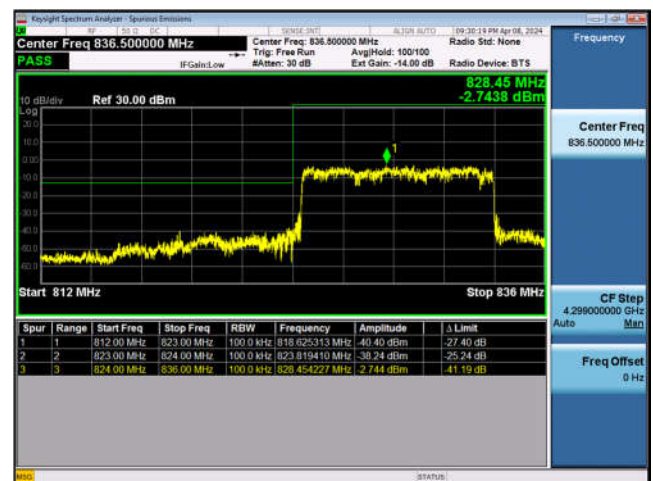
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Outer_Full_High_CH



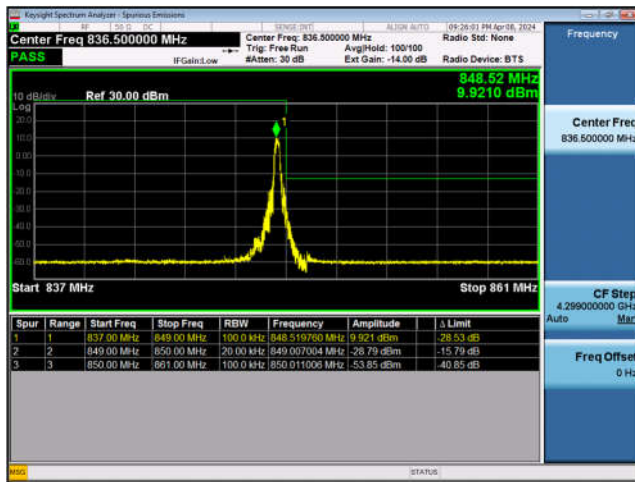
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Outer_Full_Low_CH



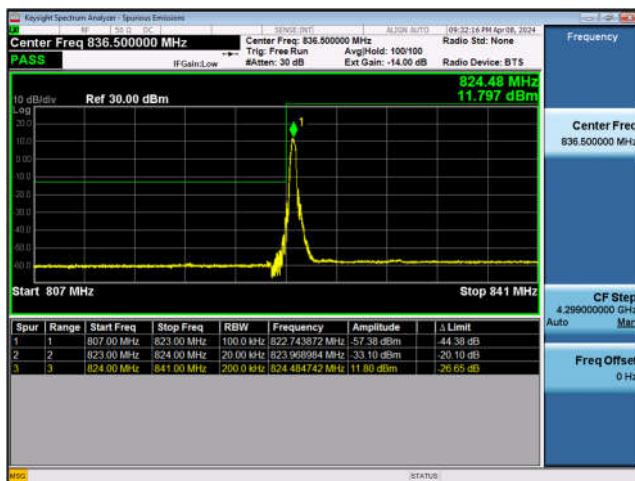
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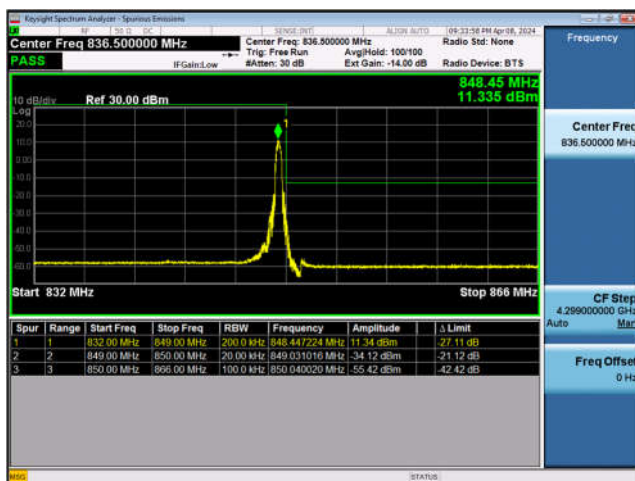
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N5_15MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



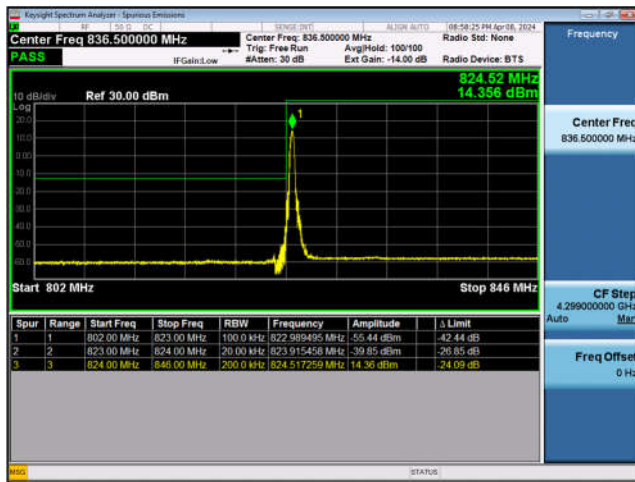
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N5_15MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



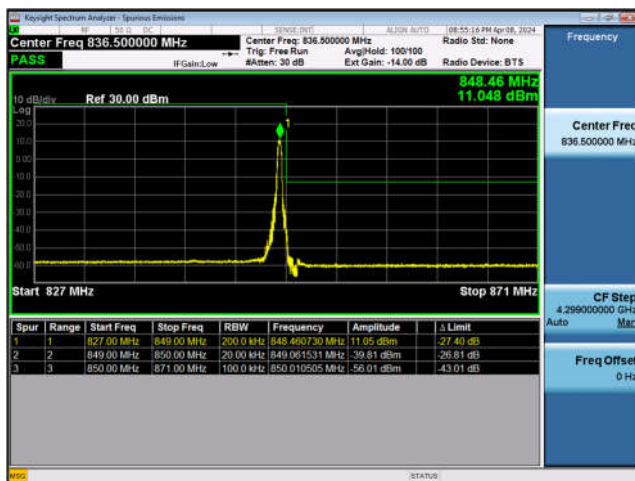
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N5_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



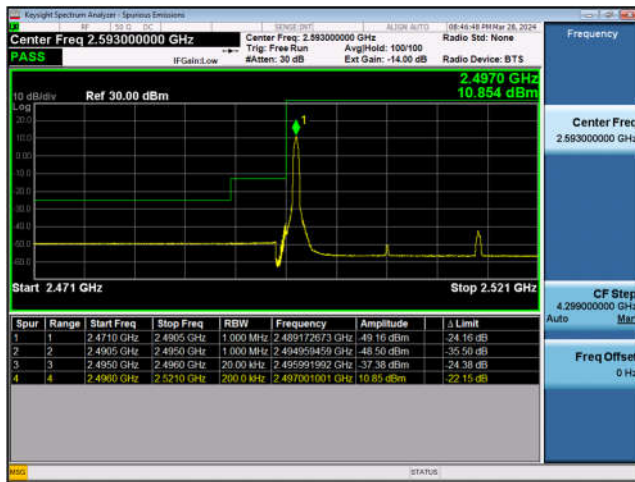
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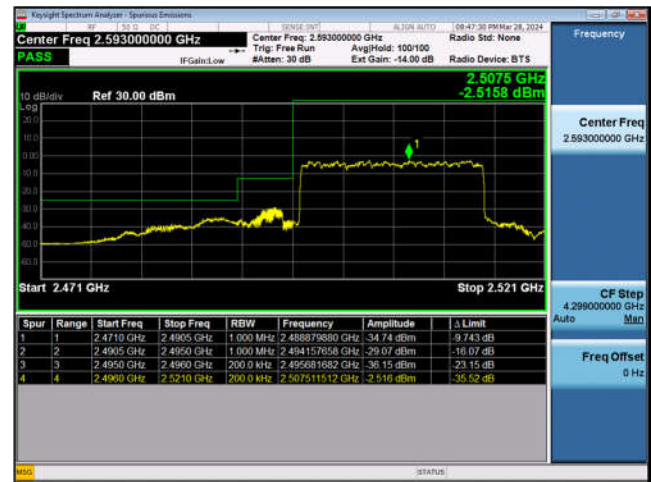
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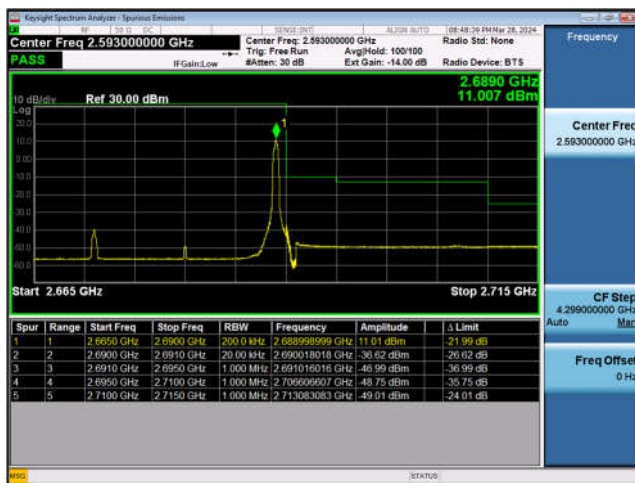
N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



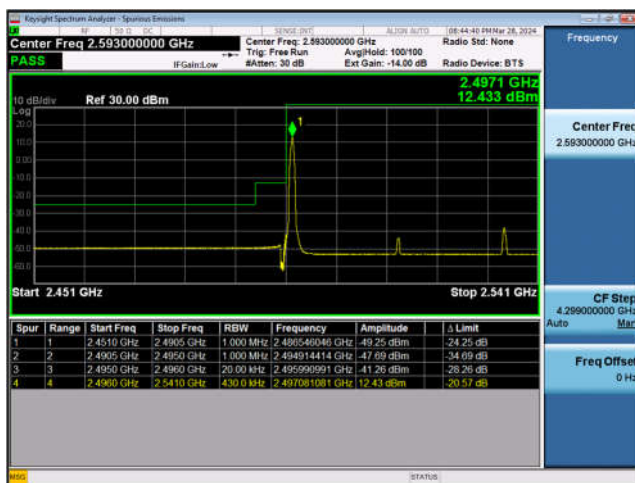
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N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



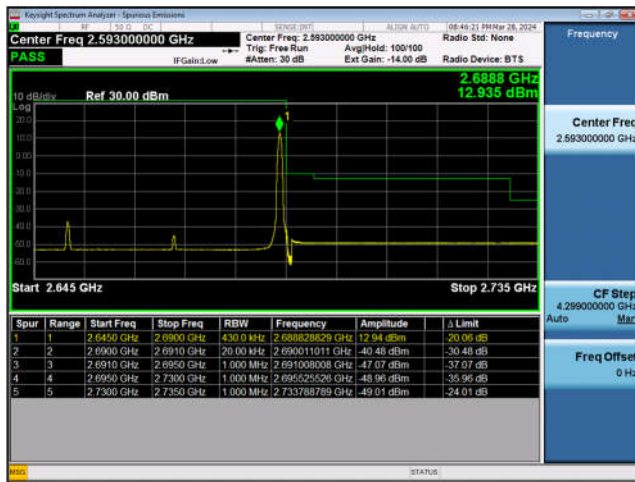
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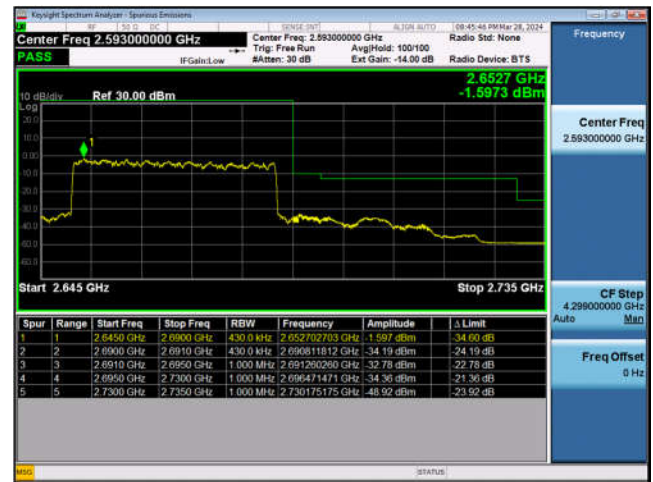
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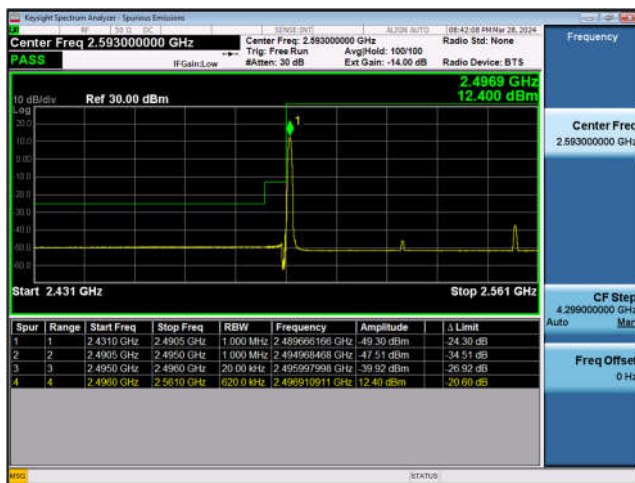
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1RB_Right_High_CH



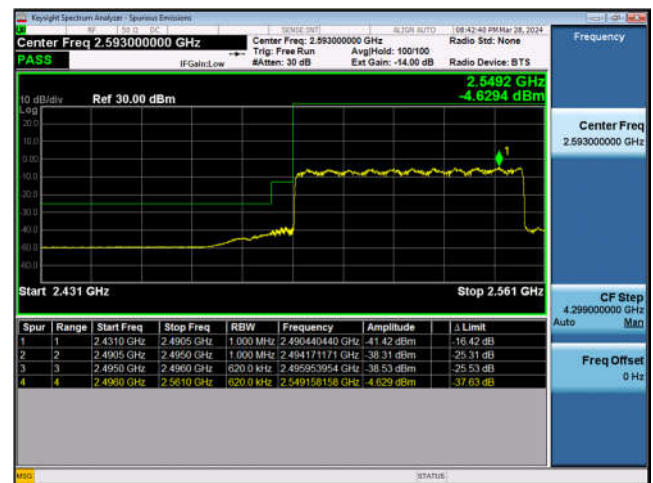
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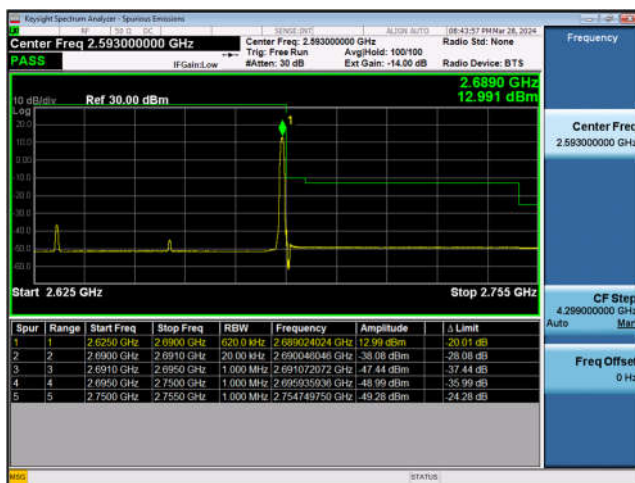
N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



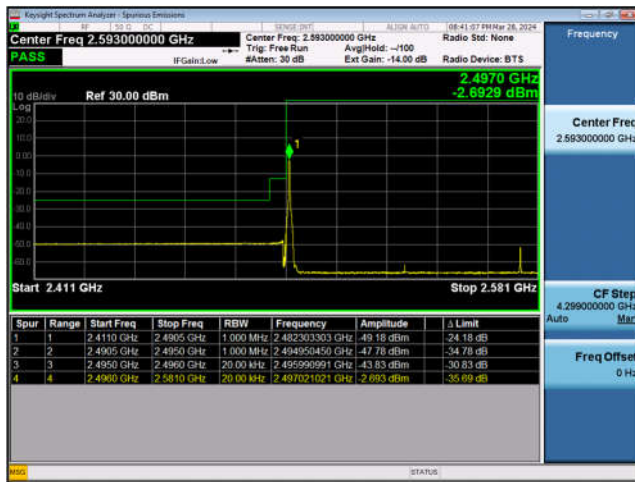
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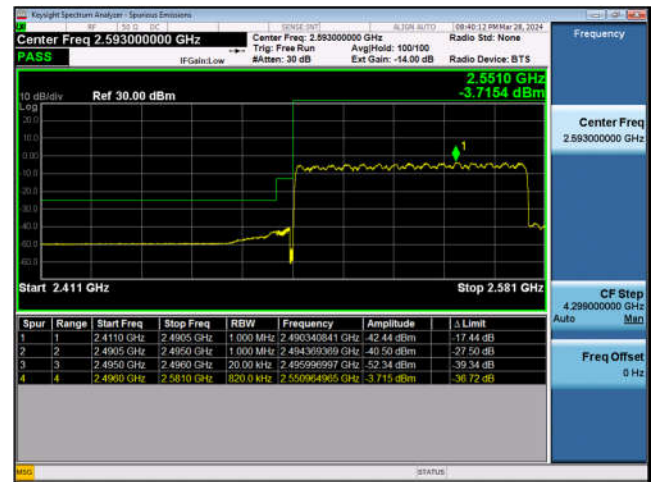
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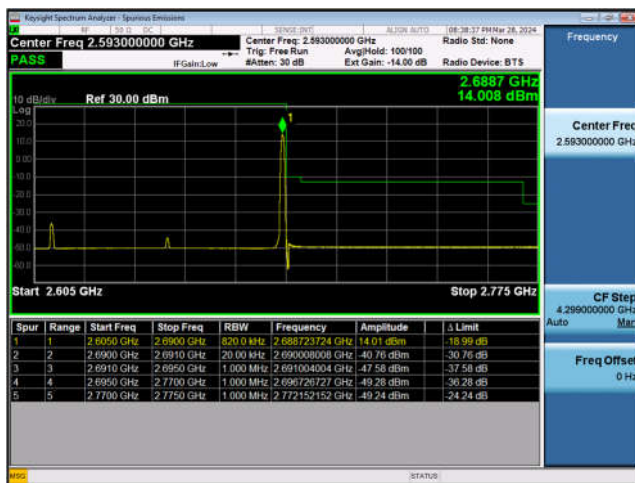
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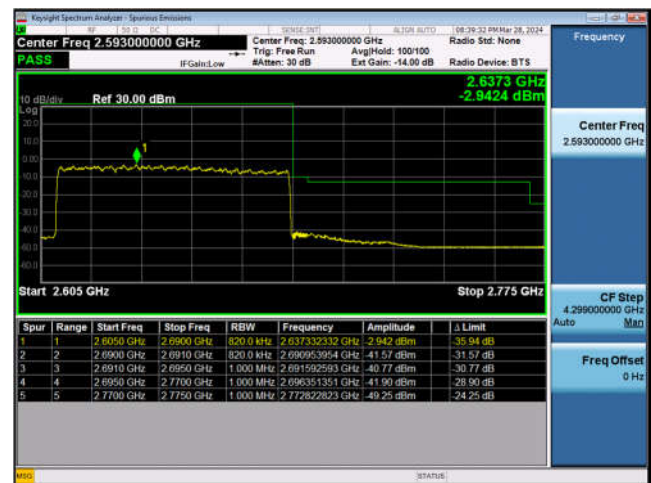
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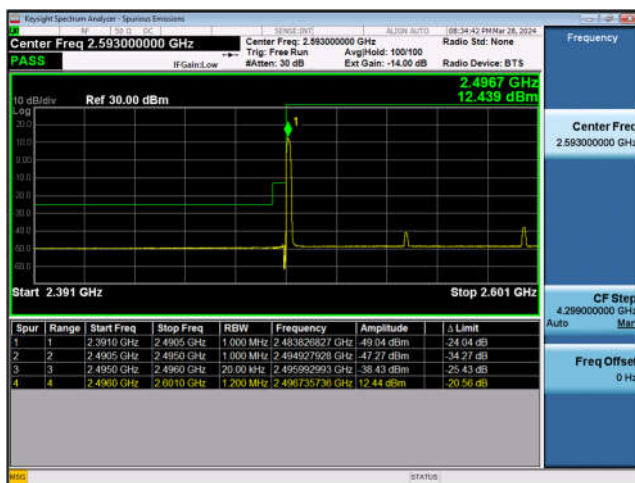
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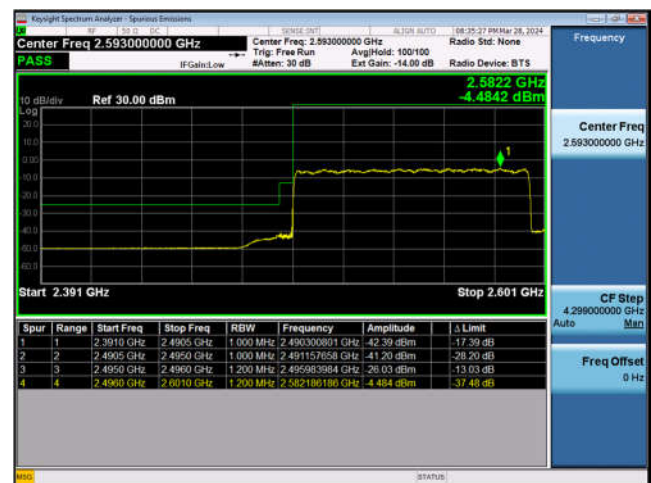
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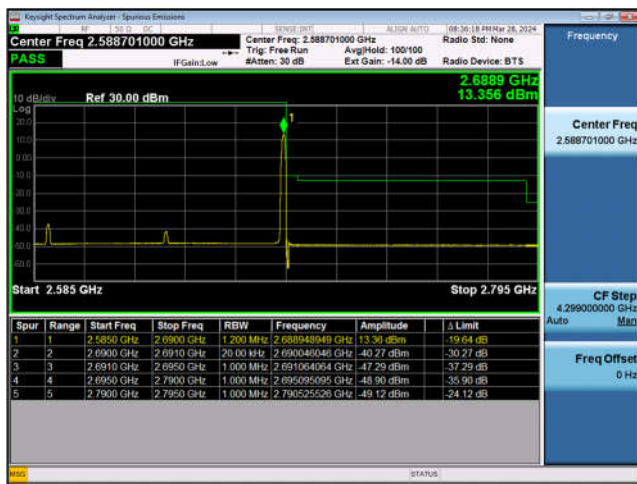
N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
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N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



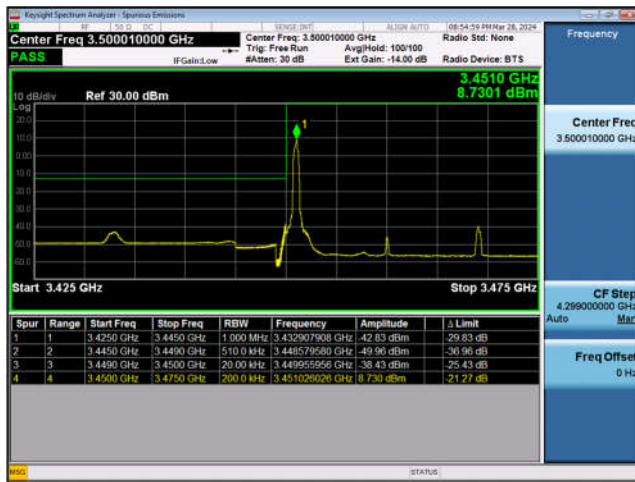
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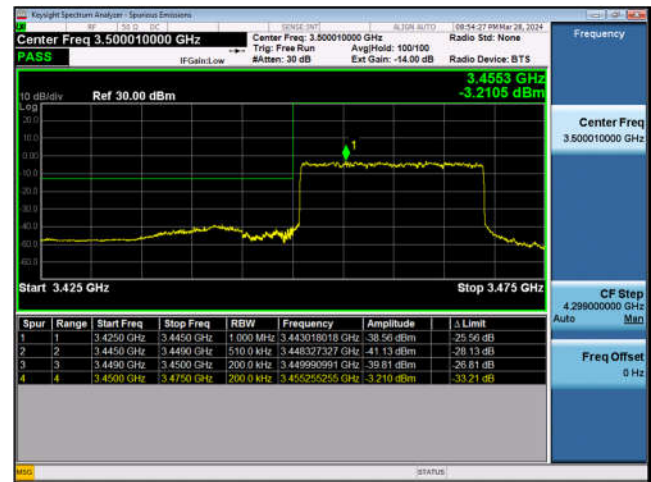
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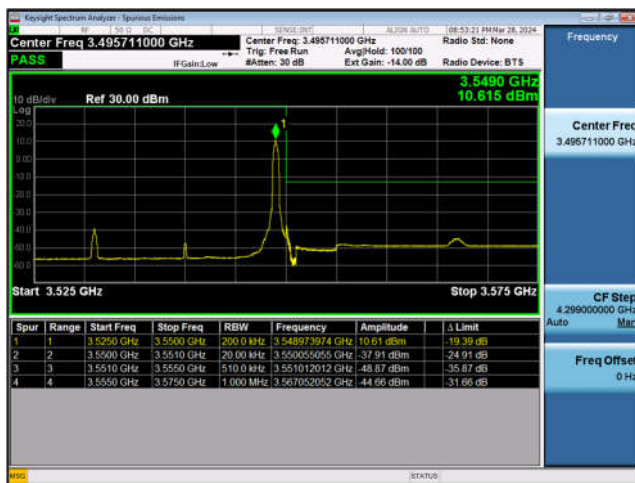
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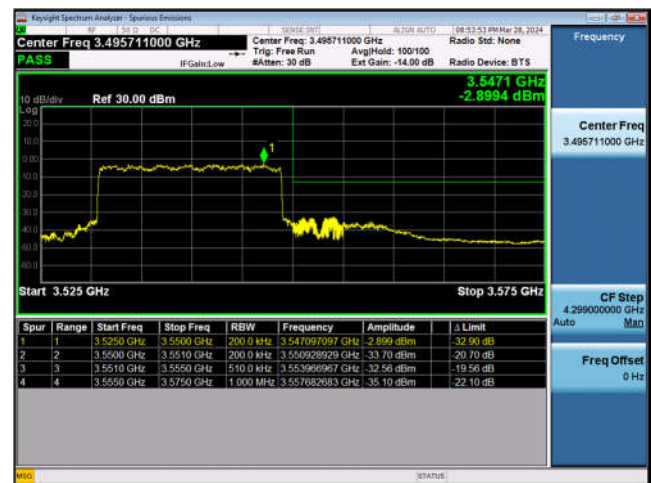
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Outer_Full_Low_CH



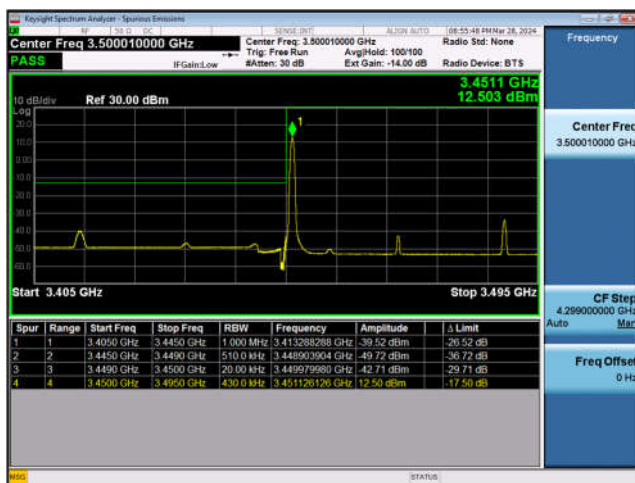
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1RB_Right_High_CH



N77_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



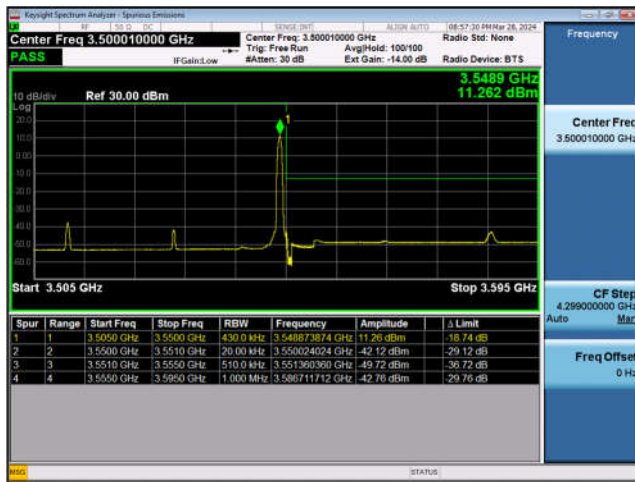
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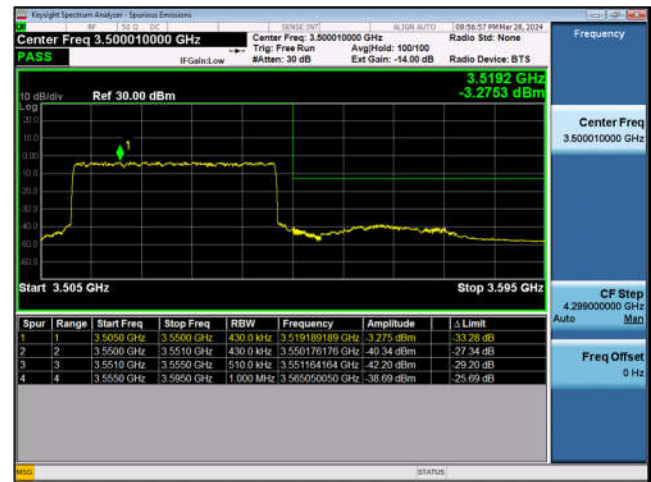
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Outer_Full_Low_CH



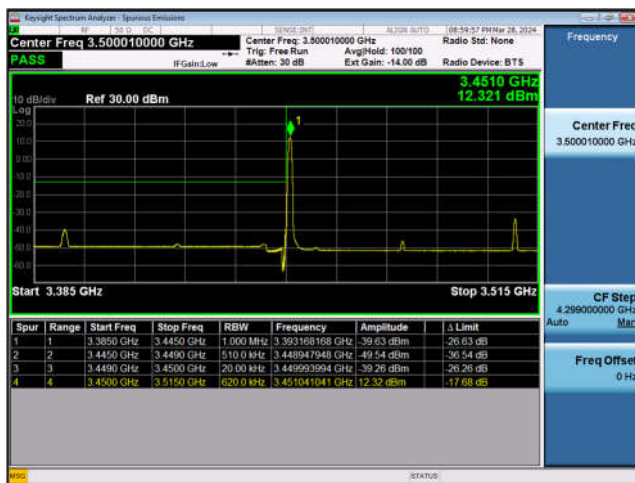
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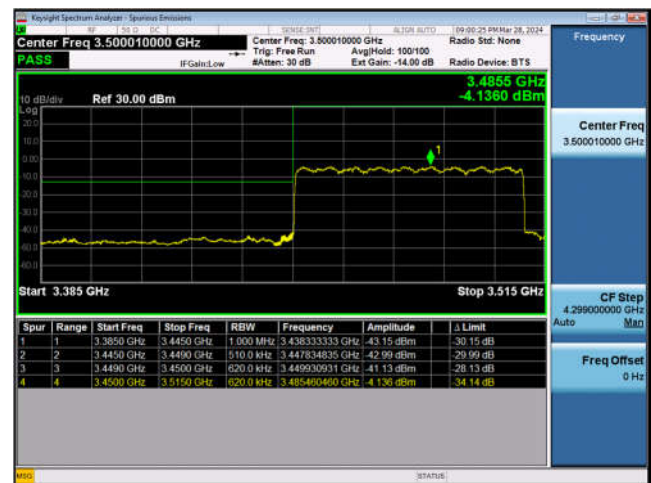
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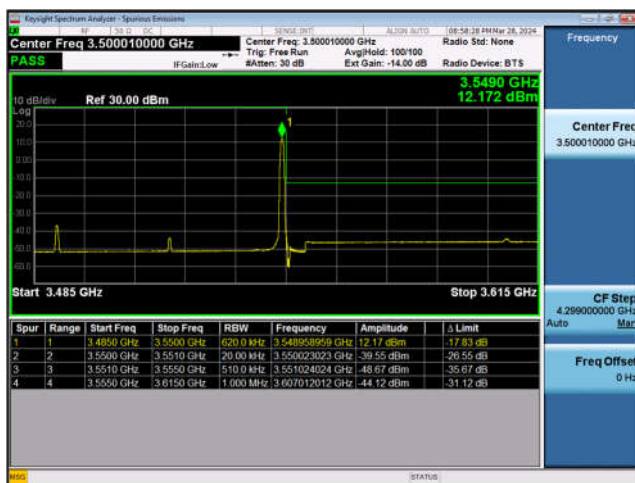
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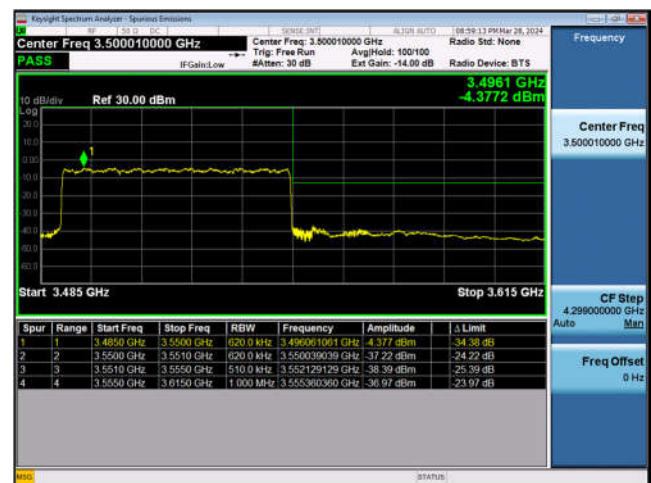
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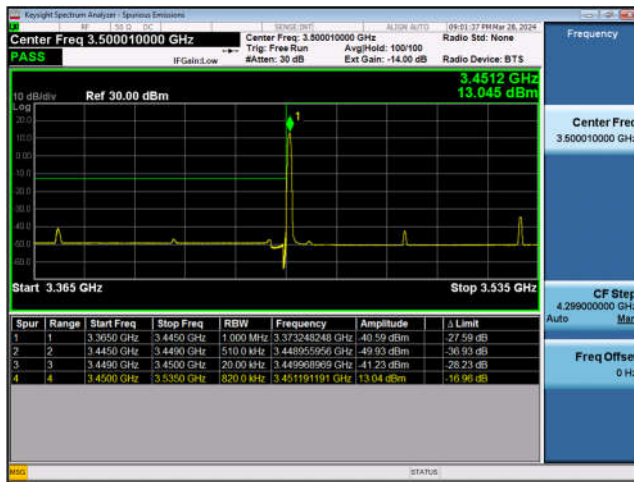
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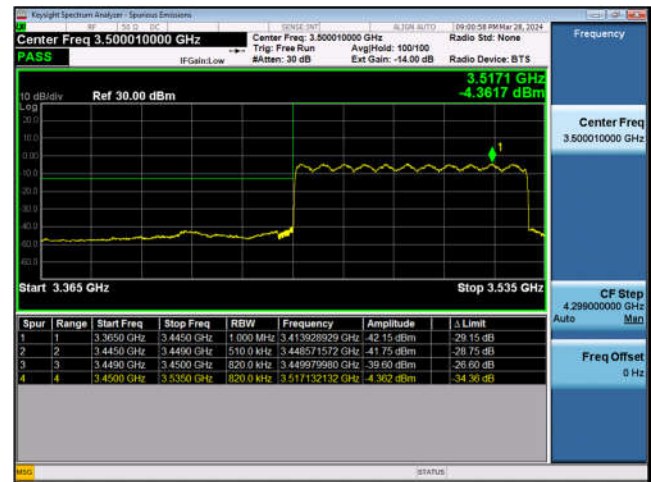
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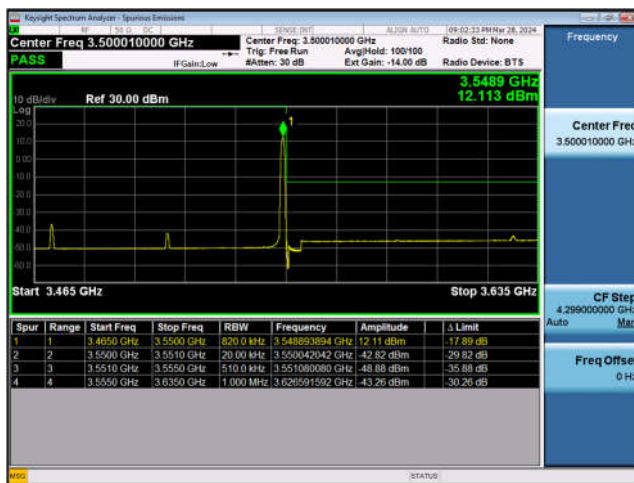
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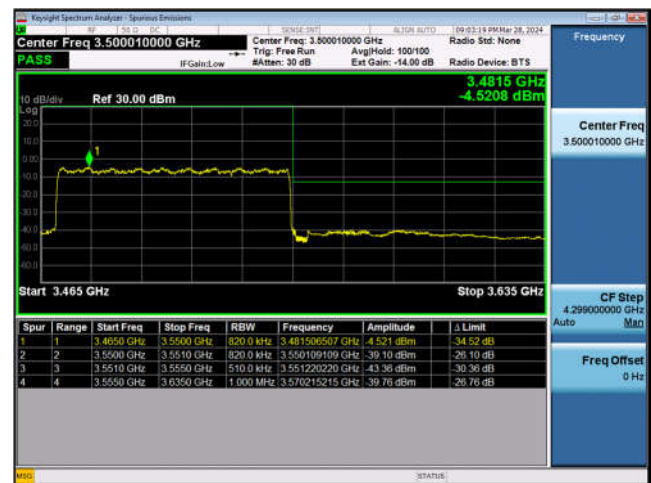
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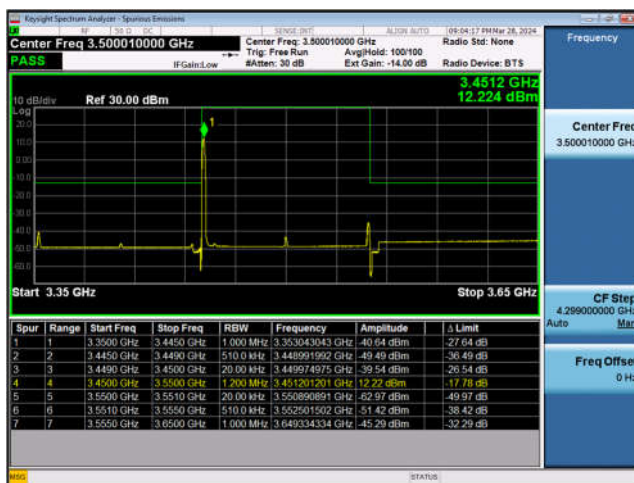
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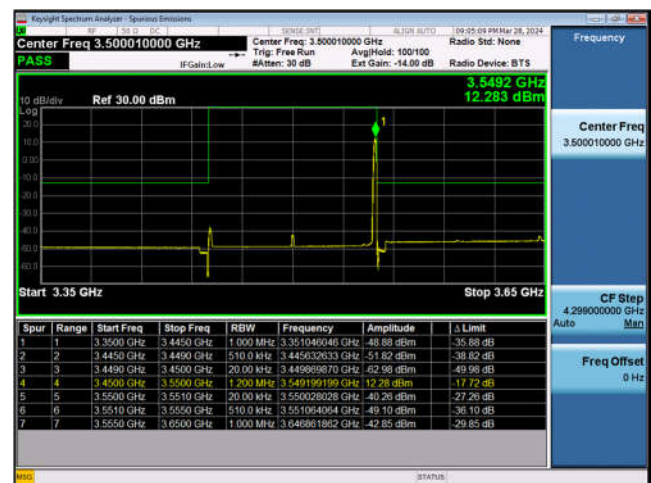
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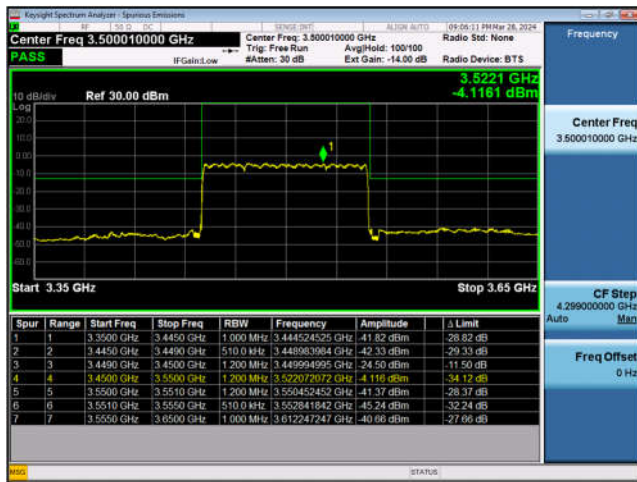
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1RB_Left_Mid_CH



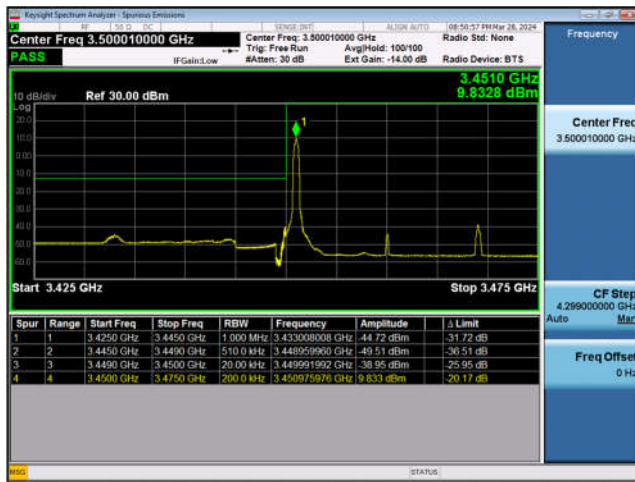
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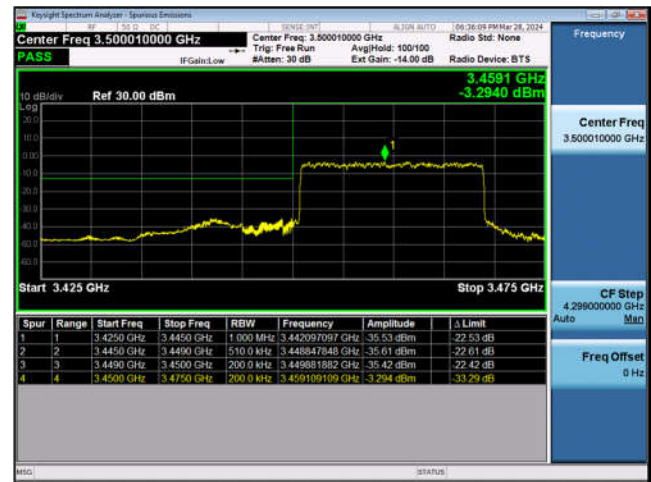
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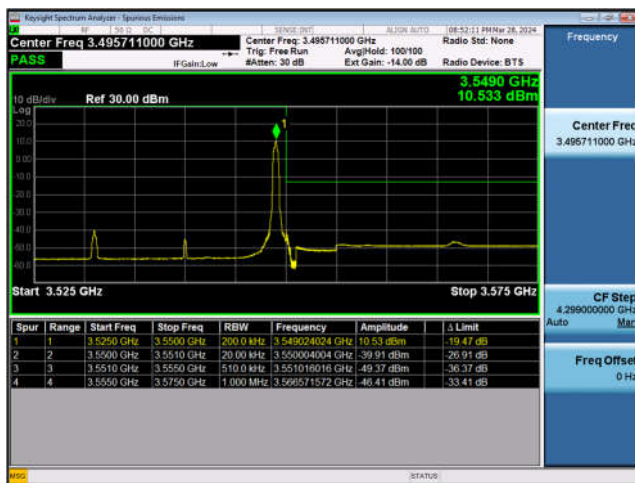
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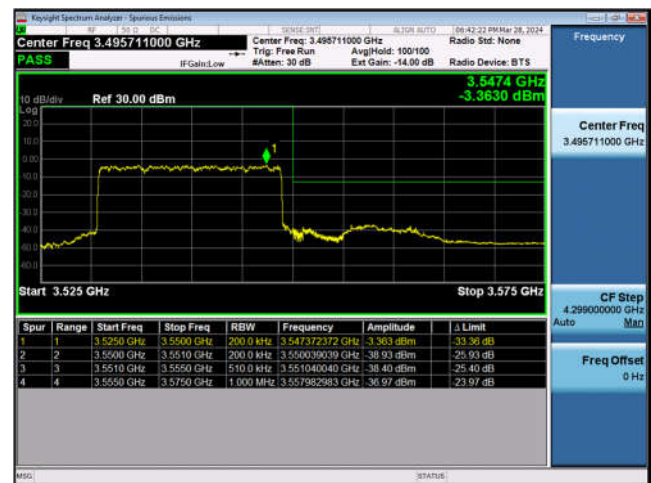
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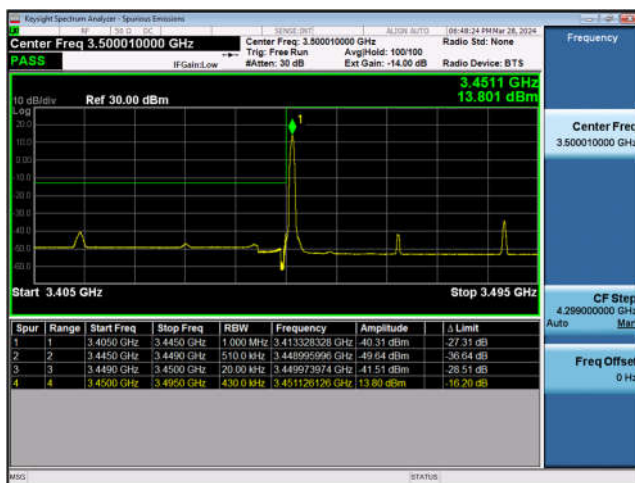
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N78_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



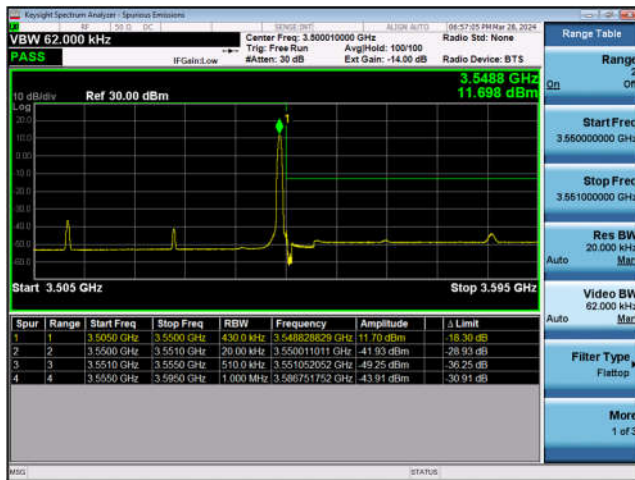
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Outer_Full_Low_CH



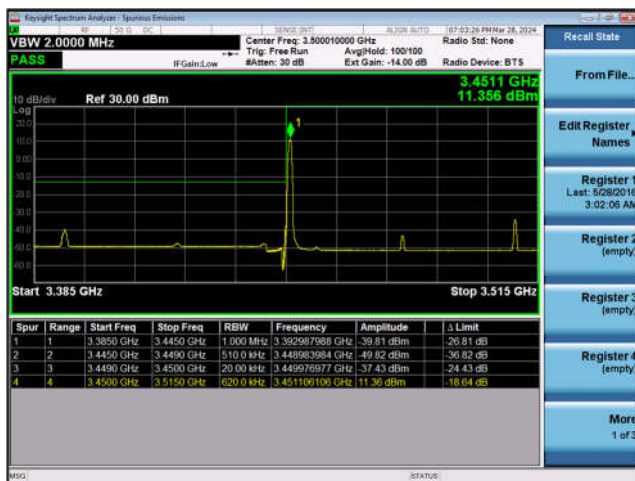
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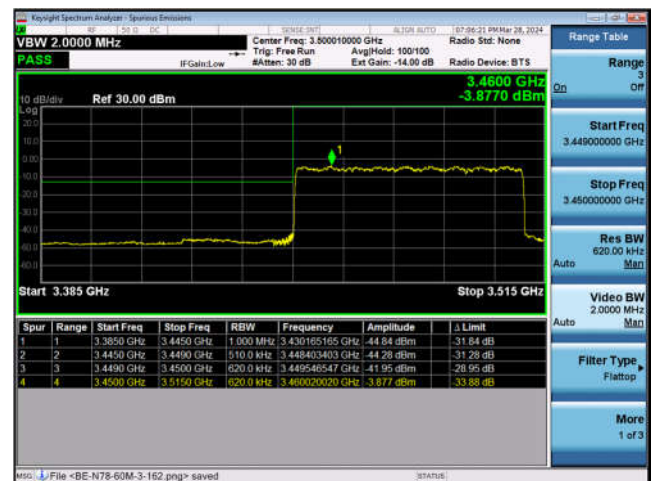
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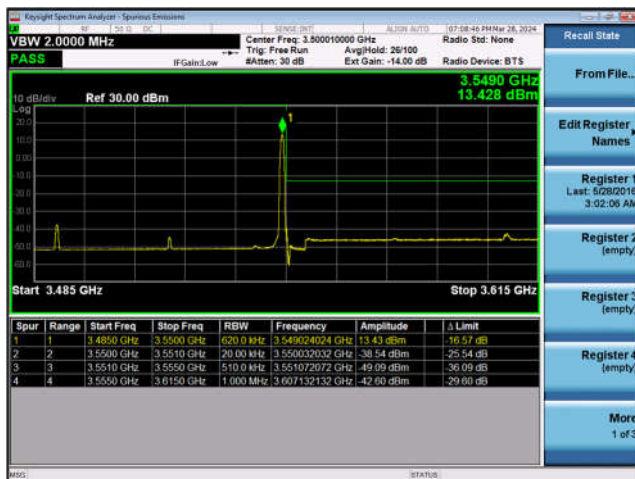
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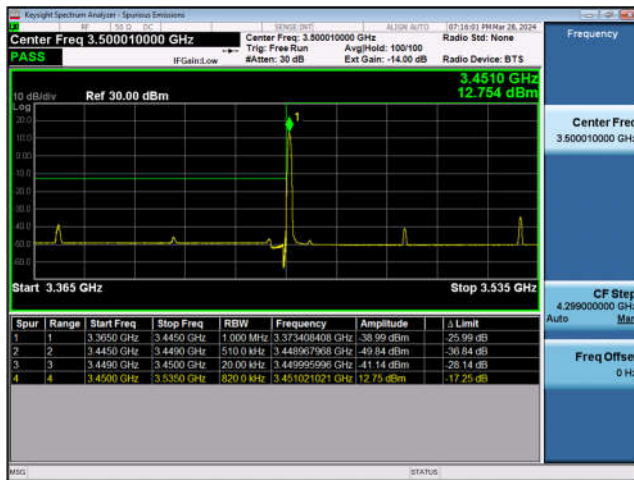
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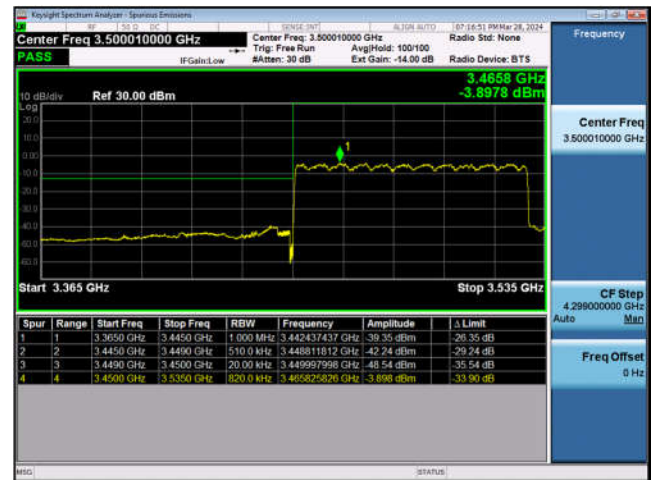
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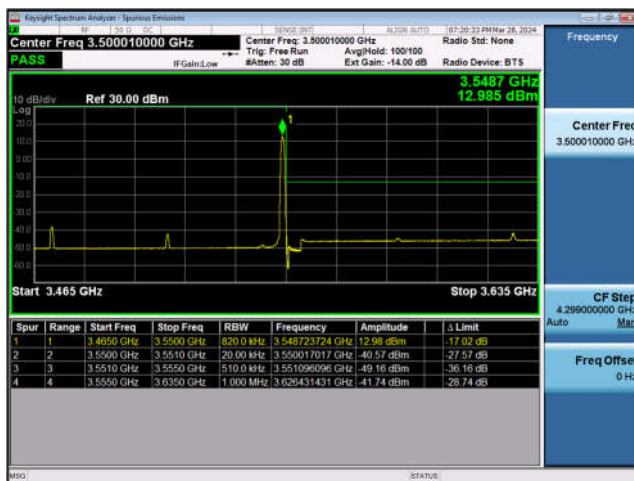
N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



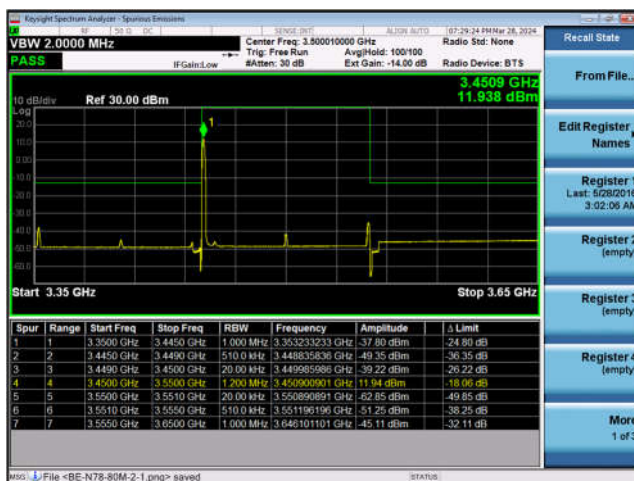
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1RB_Right_High_CH



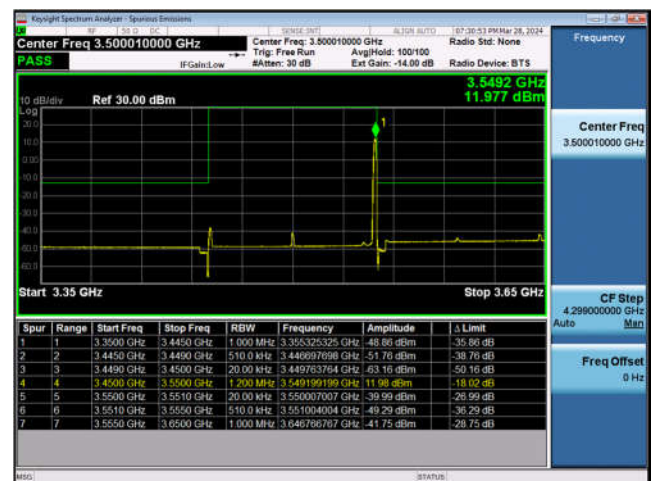
N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Mid_CH



N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_Mid_CH



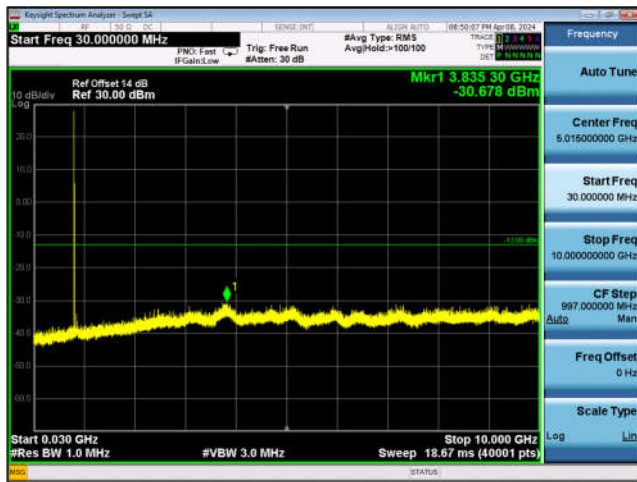
N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_ Outer_Full_Mid_CH_CH



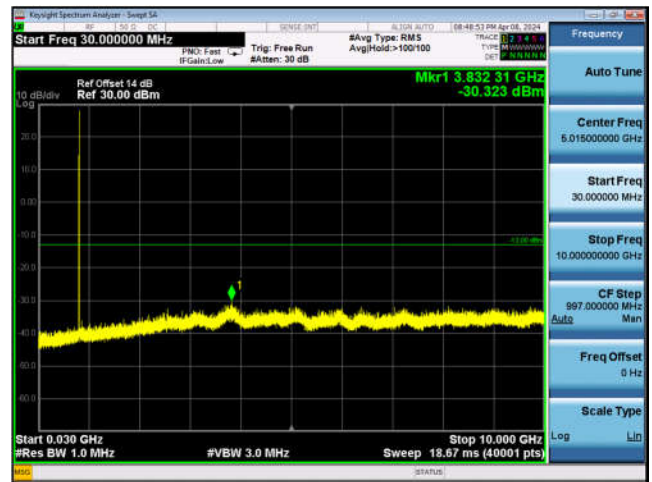
Conducted Spurious Emissions

NR Band	SCS (kHz)	Bandwidth (MHz)	Freq (MHz)	Modulation	RB Configuration	Result	Verdict
n5	15	20	834.0	DFT-s-OFDM BPSK	1@1	see graph	PASS
n5	15	20	836.5	DFT-s-OFDM BPSK	1@1	see graph	PASS
n5	15	20	839.0	DFT-s-OFDM BPSK	1@1	see graph	PASS
n41	30	100	2546.01	DFT-s-OFDM BPSK	1@1	see graph	PASS
n41	30	100	2592.99	DFT-s-OFDM BPSK	1@1	see graph	PASS
n41	30	100	2640.00	DFT-s-OFDM BPSK	1@1	see graph	PASS
n77	30	80	3490.02	DFT-s-OFDM BPSK	1@1	see graph	PASS
n77	30	80	3500.01	DFT-s-OFDM BPSK	1@1	see graph	PASS
n77	30	80	3510.00	DFT-s-OFDM BPSK	1@1	see graph	PASS
n77	30	100	3500.01	DFT-s-OFDM BPSK	1@1	see graph	PASS
n78	30	80	3490.02	DFT-s-OFDM BPSK	1@1	see graph	PASS
n78	30	80	3500.01	DFT-s-OFDM BPSK	1@1	see graph	PASS
n78	30	80	3510.00	DFT-s-OFDM BPSK	1@1	see graph	PASS
n78	30	100	3500.01	DFT-s-OFDM BPSK	1@1	see graph	PASS

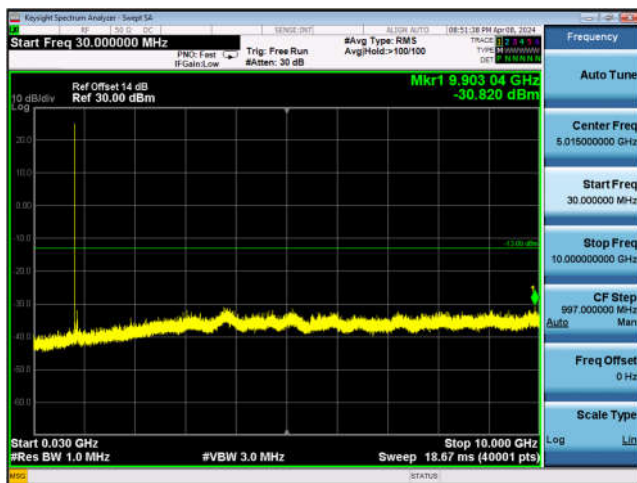
N5_20MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Low_CH



N5_20MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Mid_CH



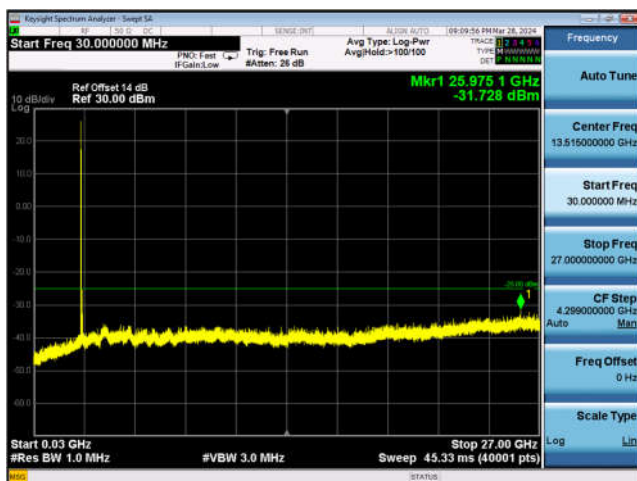
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Inner_1RB_Left_High_CH



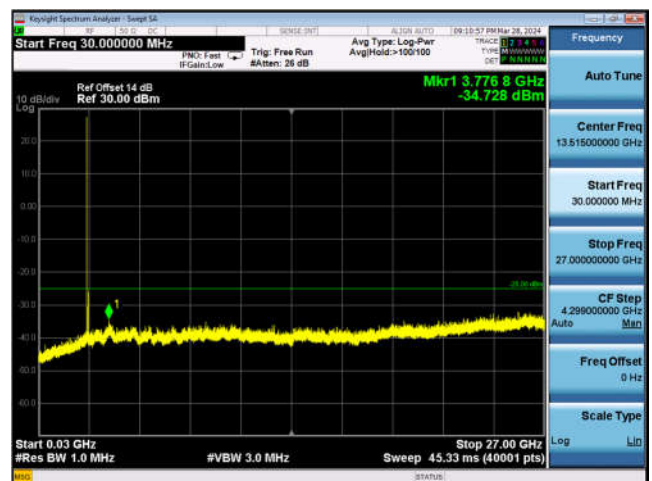
N41_100MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Low_CH



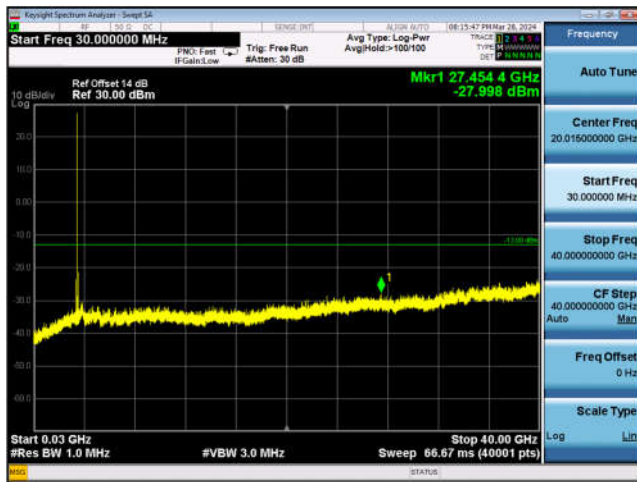
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Inner_1RB_Left_Mid_CH



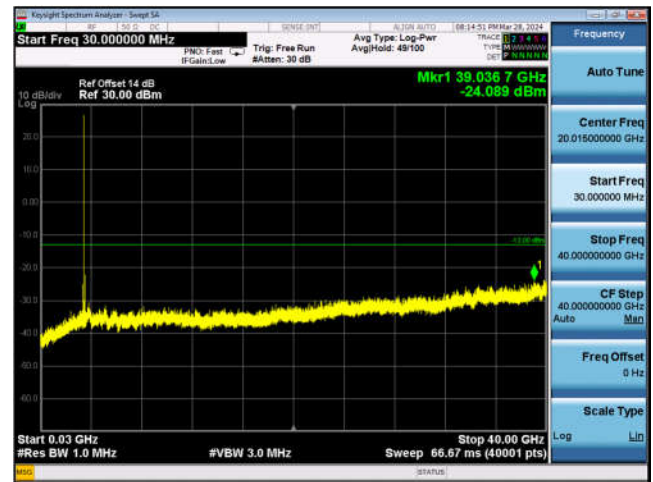
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Inner_1RB_Left_High_CH



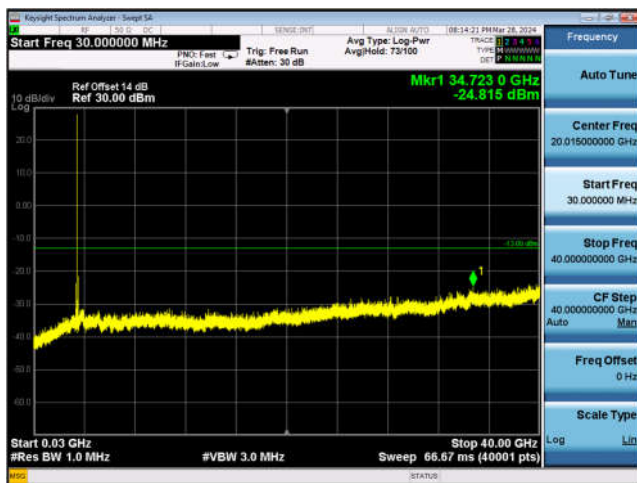
N77_80MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Low_CH



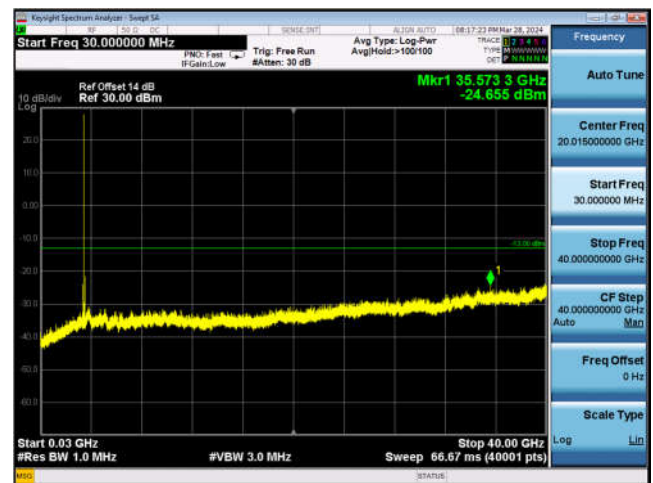
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Inner_1RB_Left_Mid_CH



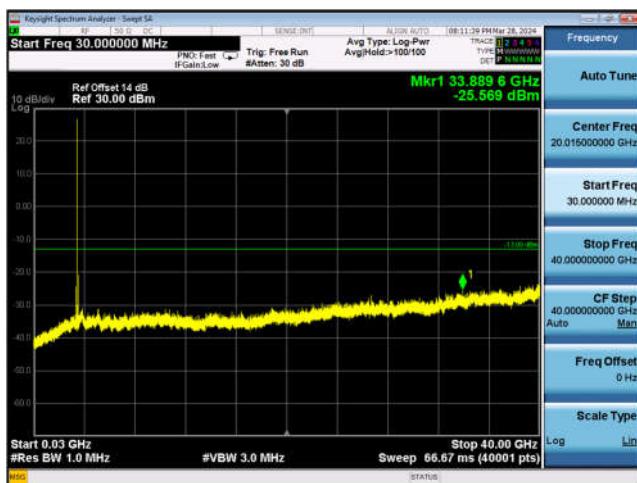
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Inner_1RB_Left_High_CH



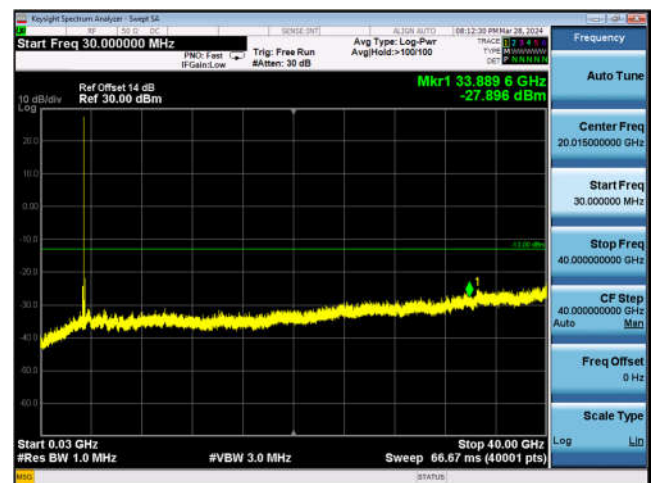
N77_100MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Mid_CH



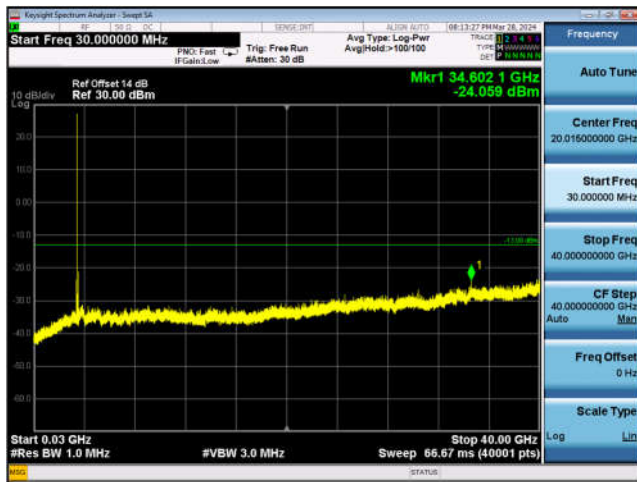
N78_80MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Low_CH



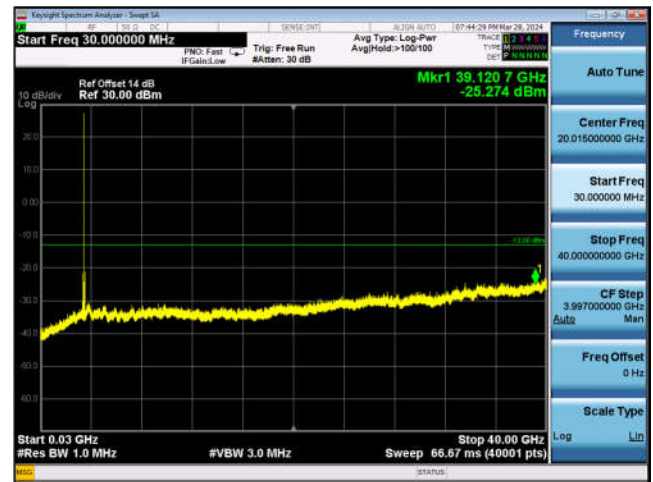
N78_80MHz_DFT-s-OFDM_BPSK_
Inner_1RB_Left_Mid_CH



N78_80MHz_DFT-s-OFDM_BPSK_ Inner_1RB_Left_High_CH



N78_100MHz_DFT-s-OFDM_BPSK_ Inner_1RB_Left_Mid_CH



** END OF REPORT **