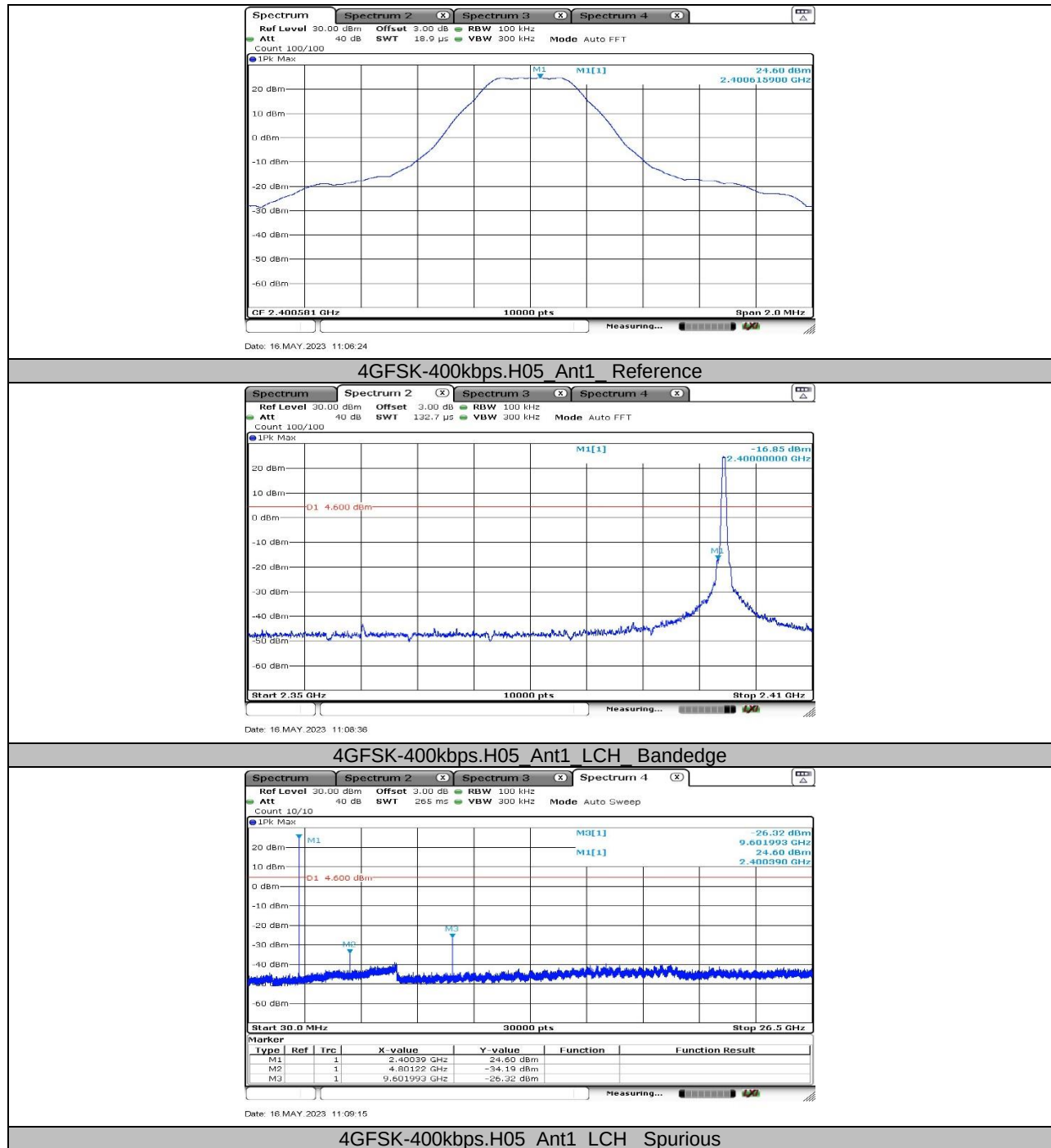
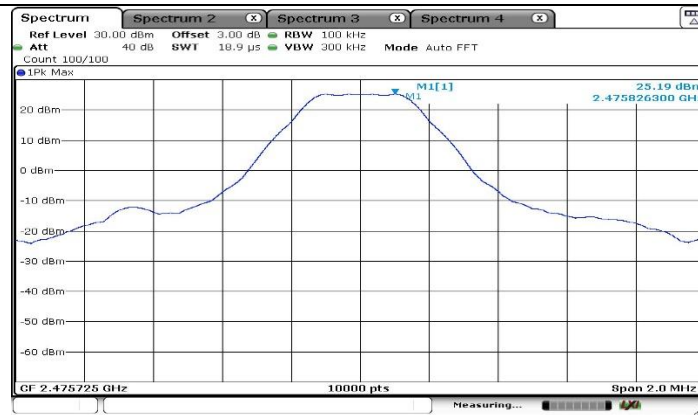


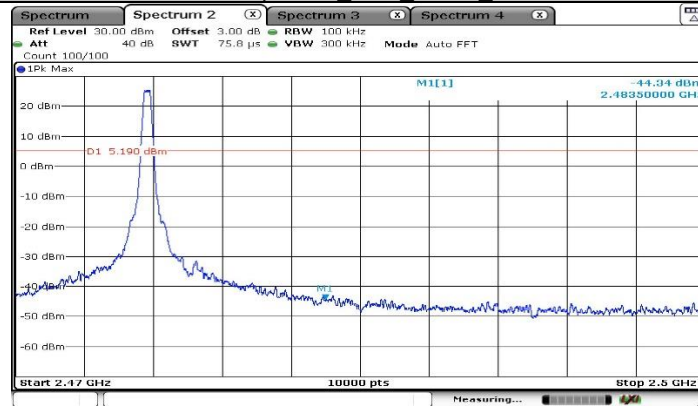
18.6.2. Test Graphs





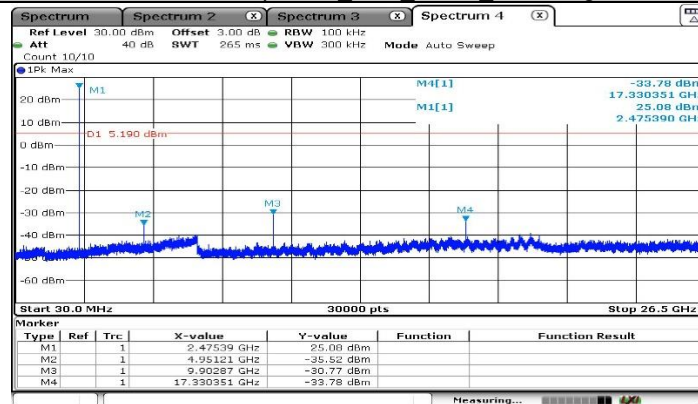
Date: 16 MAY 2023 11:23:30

4GFSK-400kbps.H05_Ant1_HCH_Reference



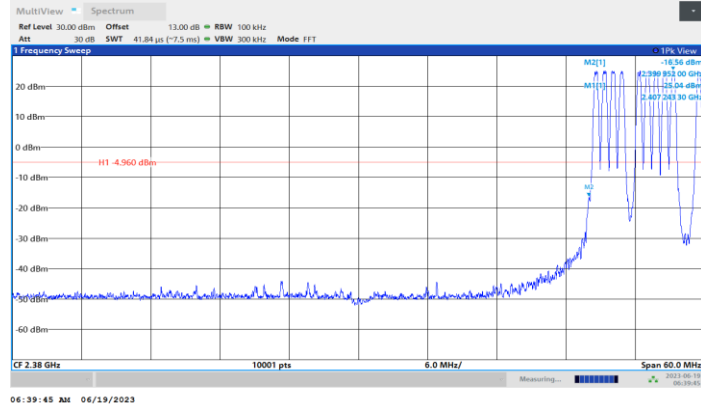
Date: 16 MAY 2023 11:24:11

4GFSK-400kbps.H05_Ant1_HCH_Bandedge

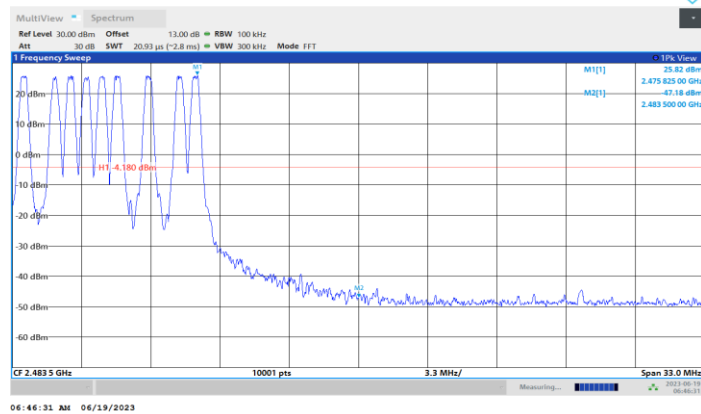


Date: 16 MAY 2023 11:25:16

4GFSK-400kbps.H05_Ant1_HCH_Spurious



4GFSK-400kbps.H05_Ant1_LCH_Bandedge Hopping on



4GFSK-400kbps.H05_Ant1_HCH_Bandedge Hopping on

18.7. Appendix G8: Duty Cycle**18.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
4GFSK-400kbps.H05	222.77	499.45	0.4460	44.60	3.51	0.0044	0.01

Note:

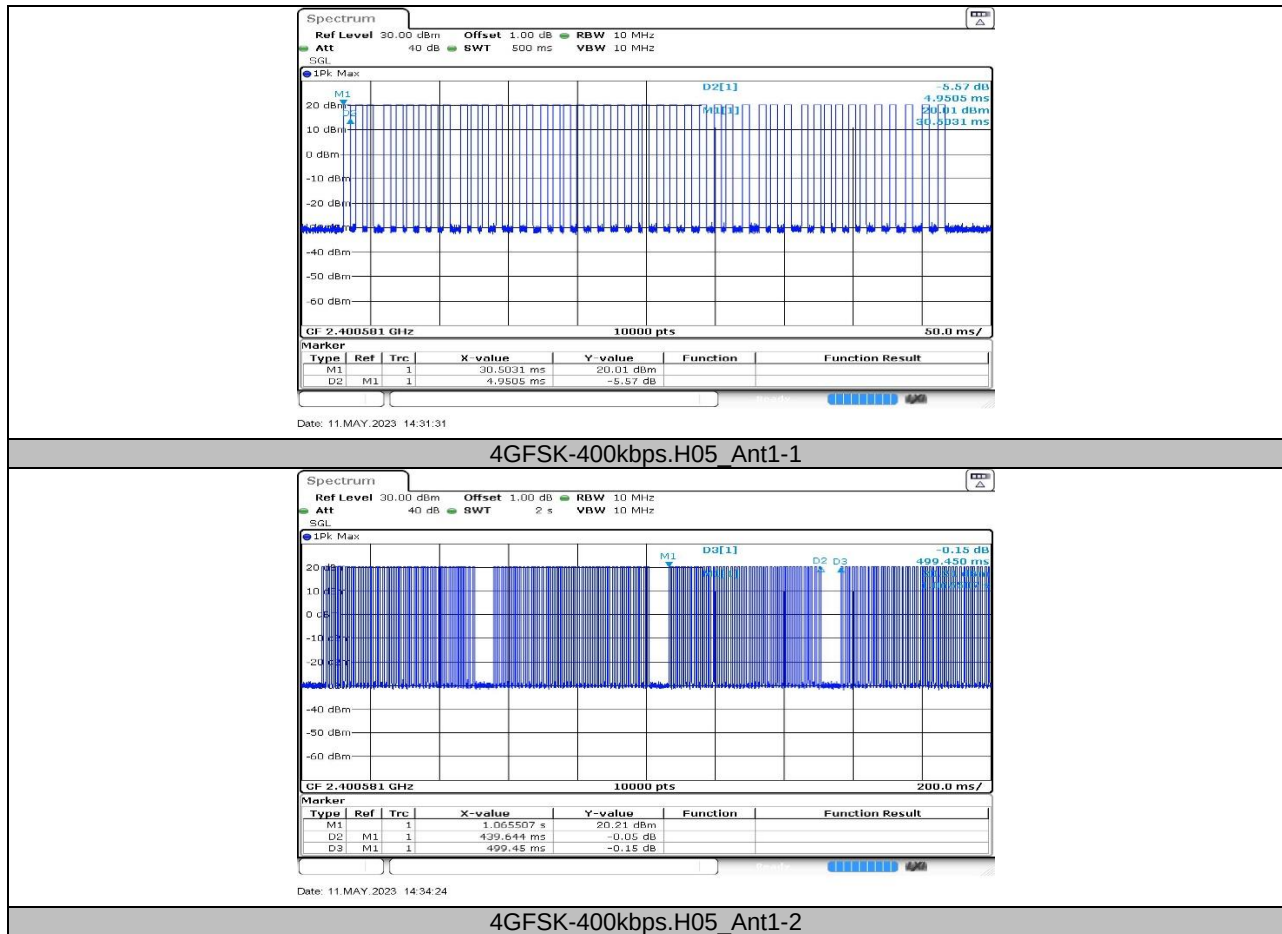
Duty Cycle Correction Factor= $10\log(1/x)$.

Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next HCHer value should be used.

18.7.2. Test Graphs



END OF REPORT