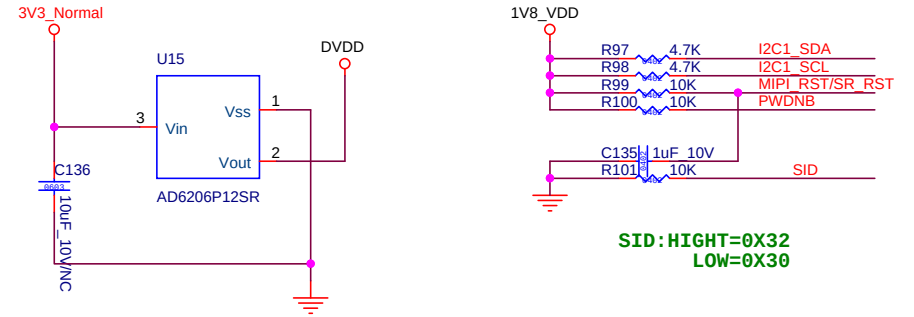
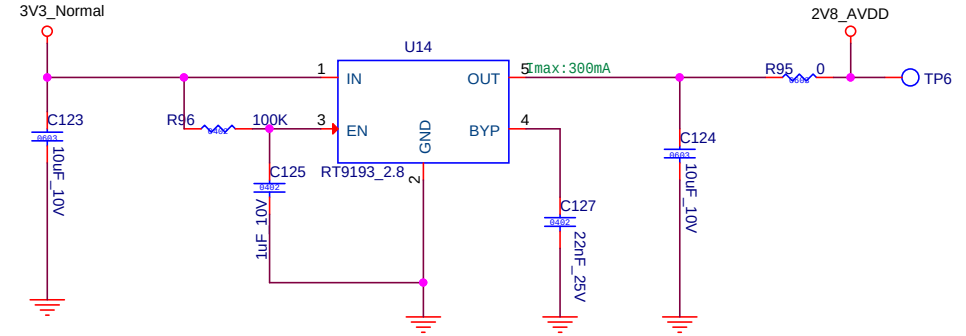
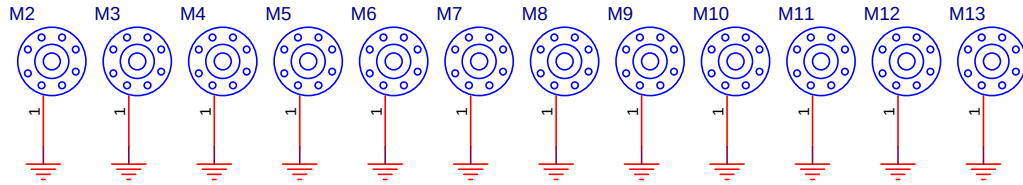
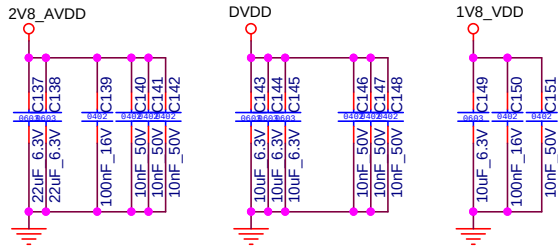
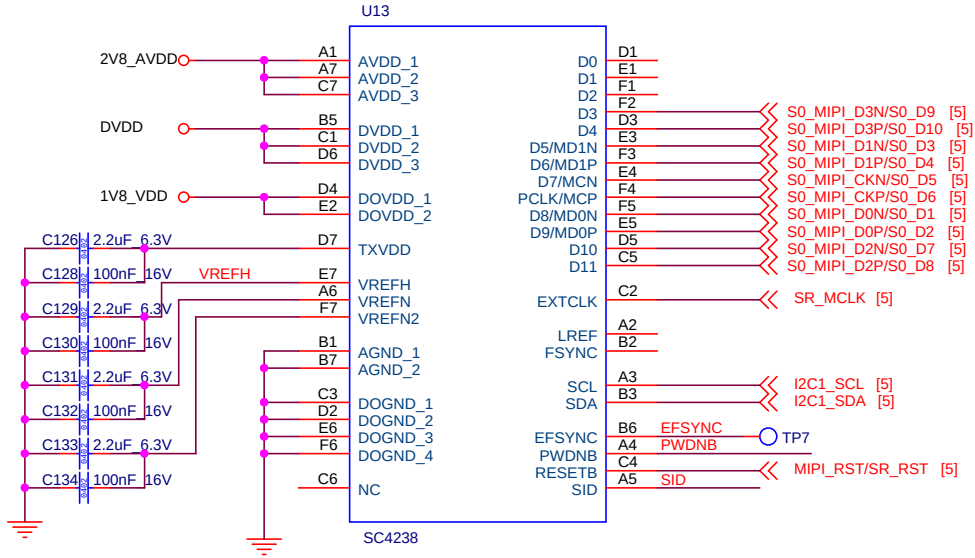
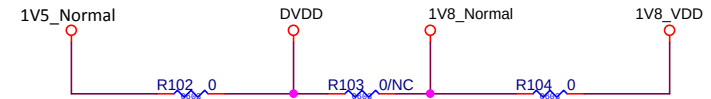


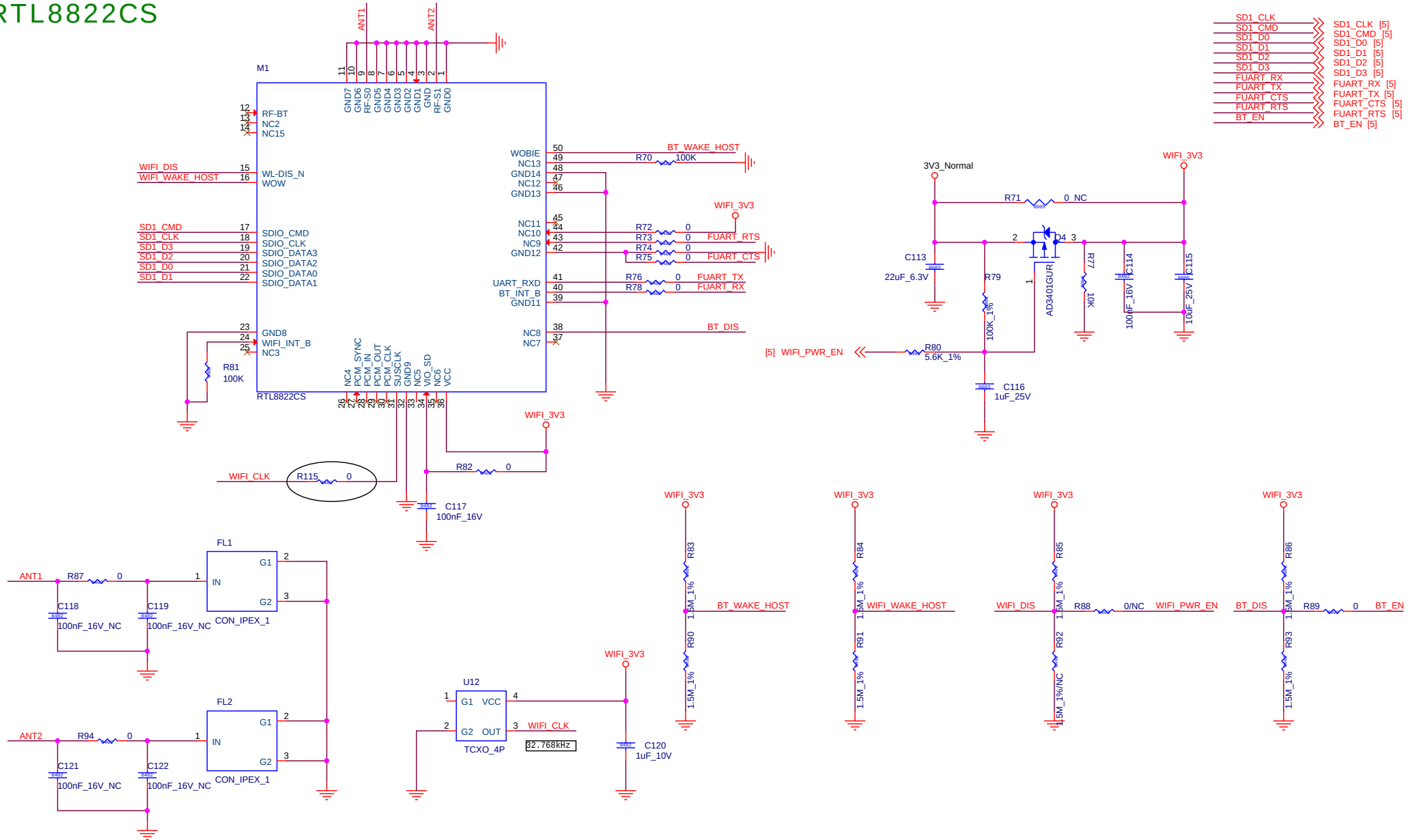
SC4238



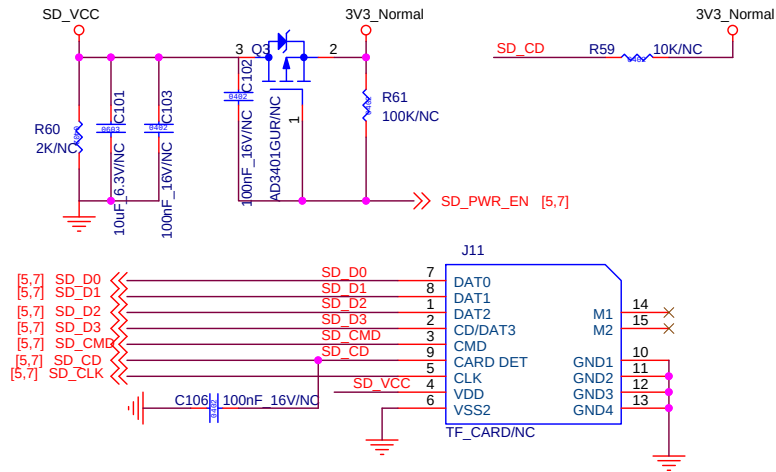
SID: HIGHT=0X32
LOW=0X30



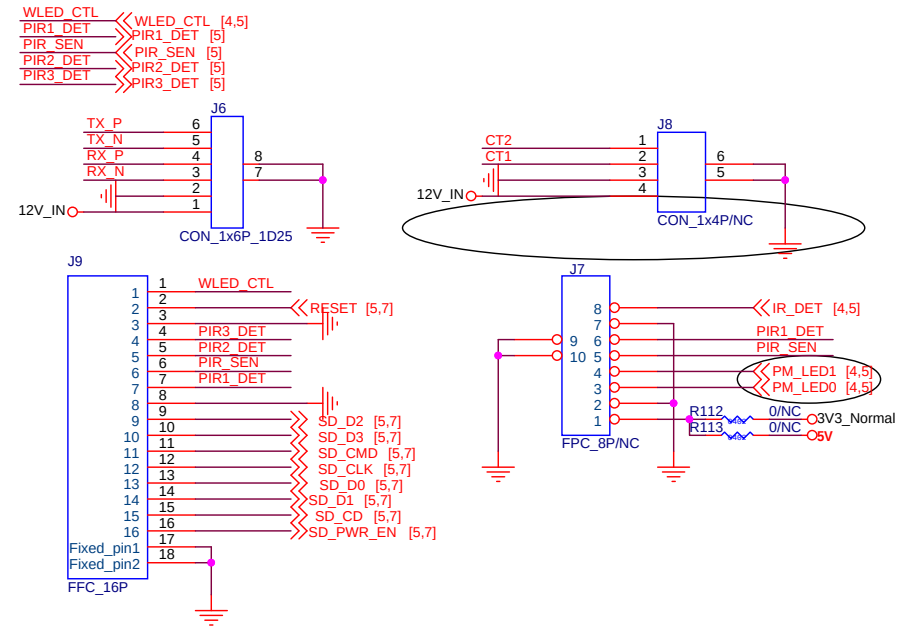
RTL8822CS



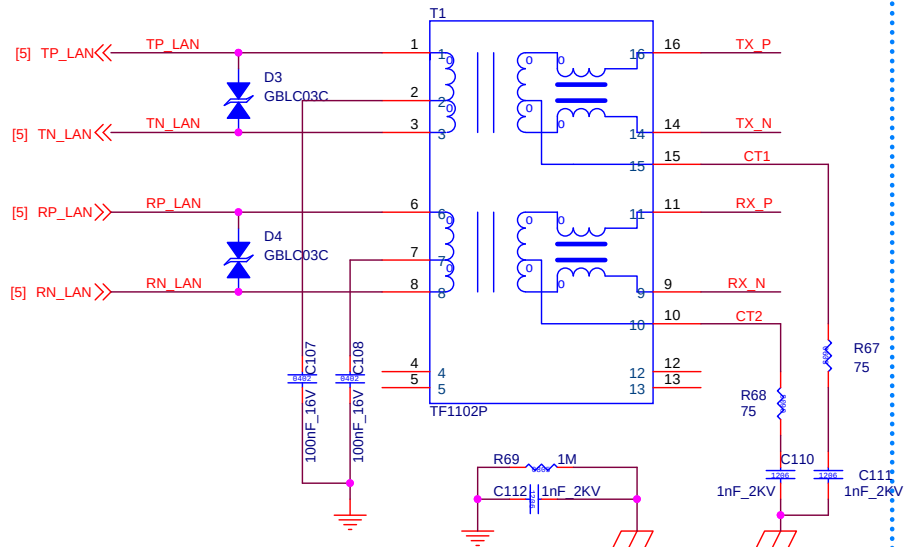
SD Card



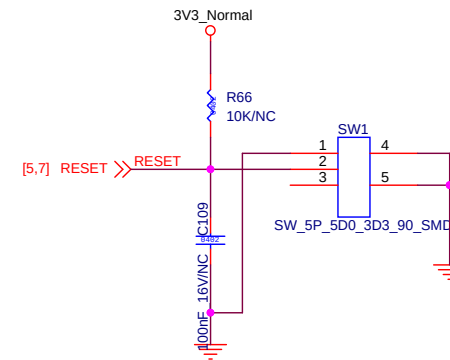
Interface



ETH

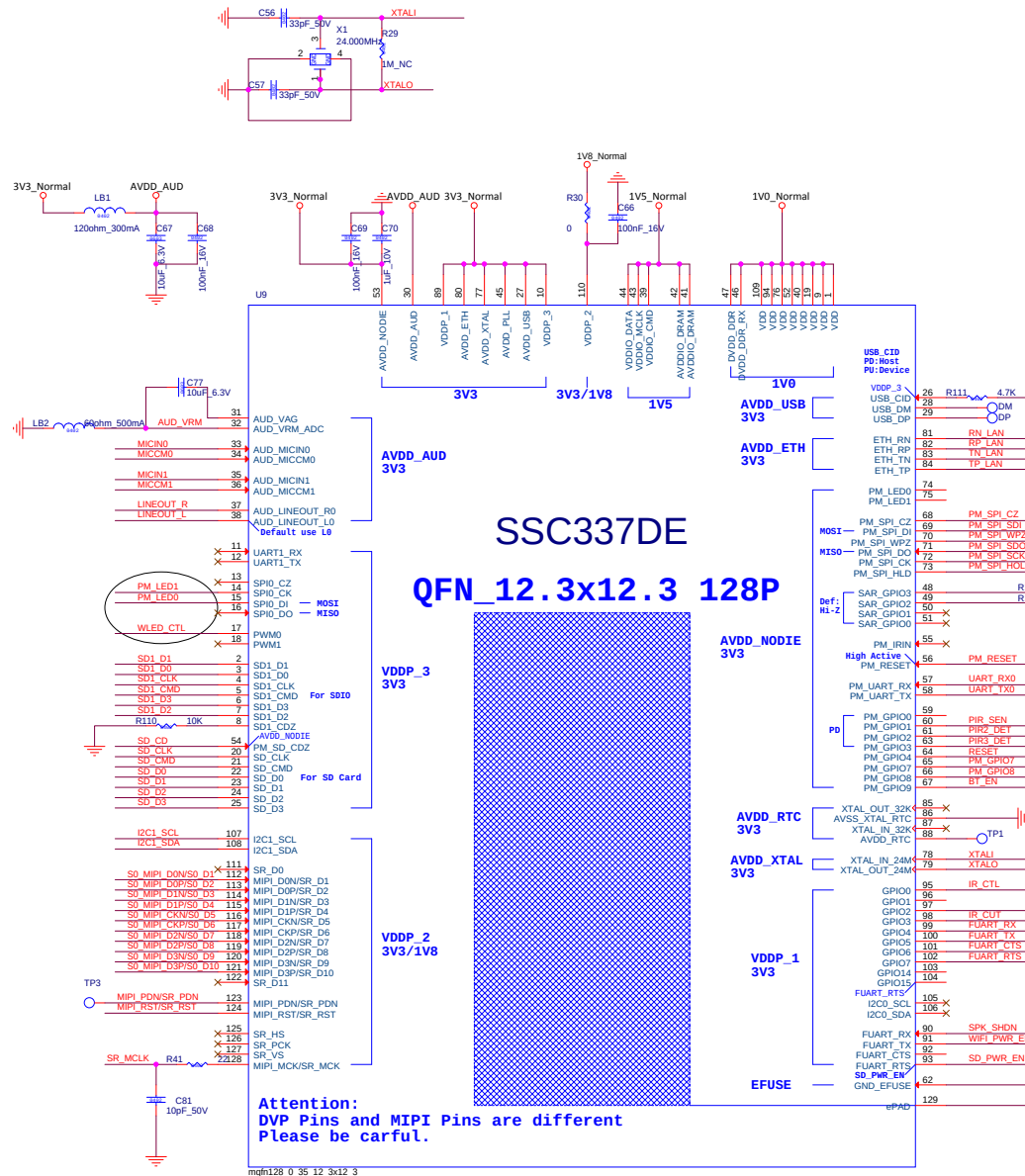


Reset

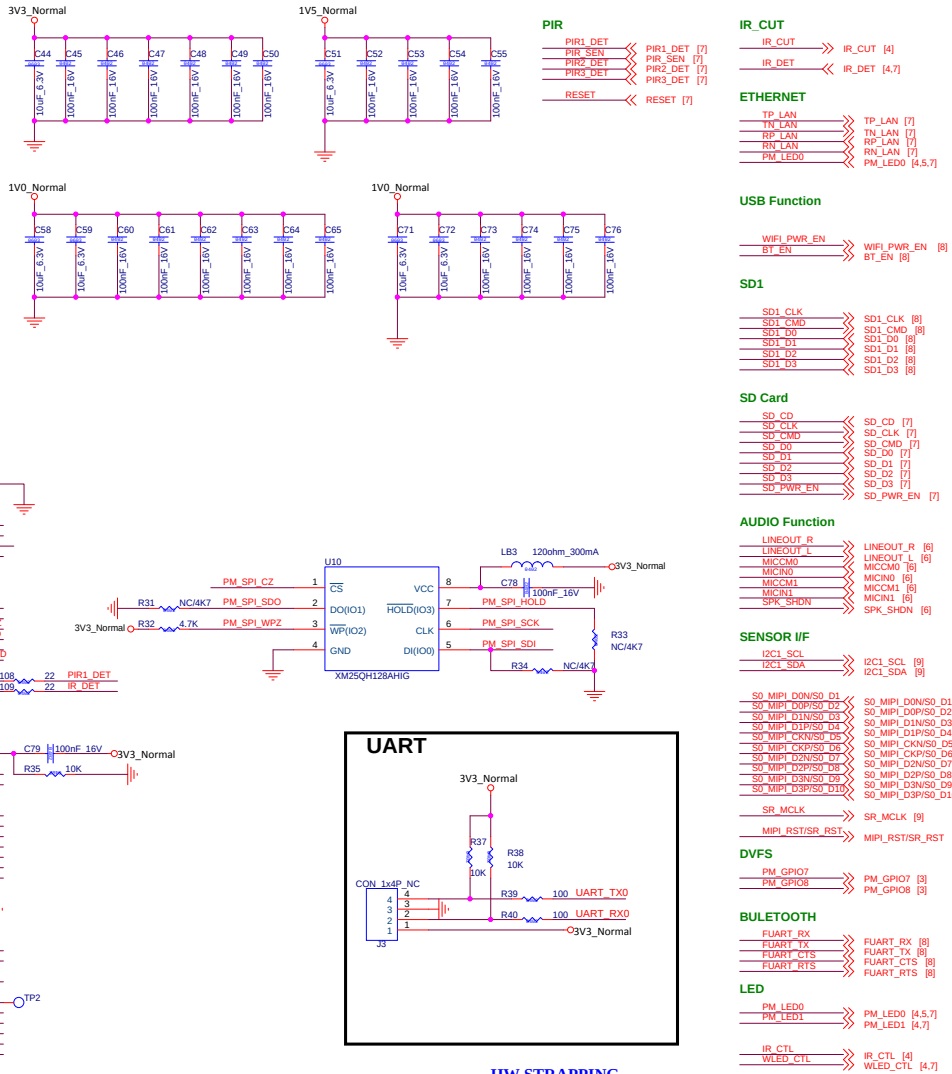


SSC337DE

Clock 24MHz For Core/20PF



mgh128_0_35_12_3x12_3



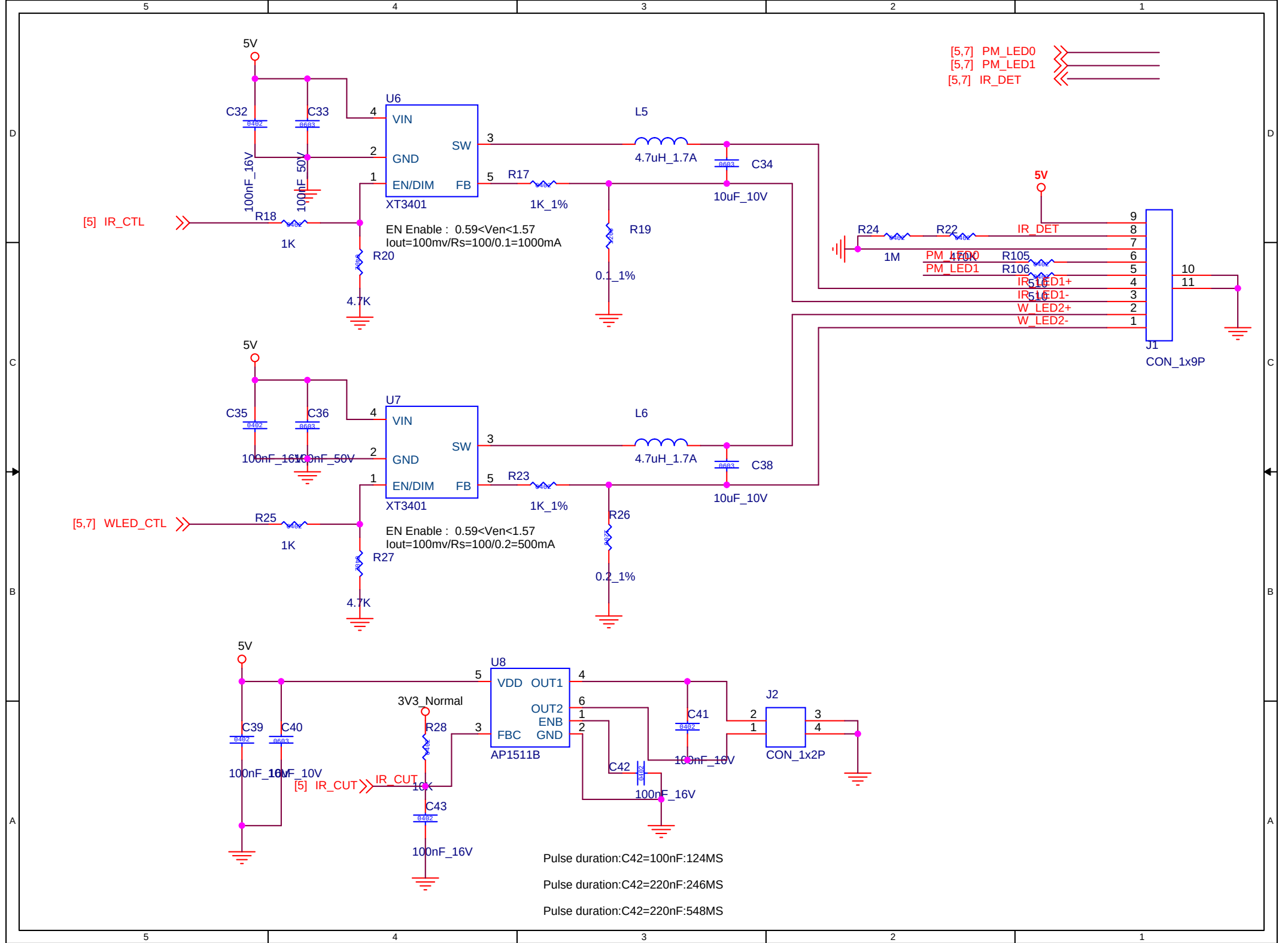
HW STRAPPING

Boot Information (ROM - Boot Mode Selection)

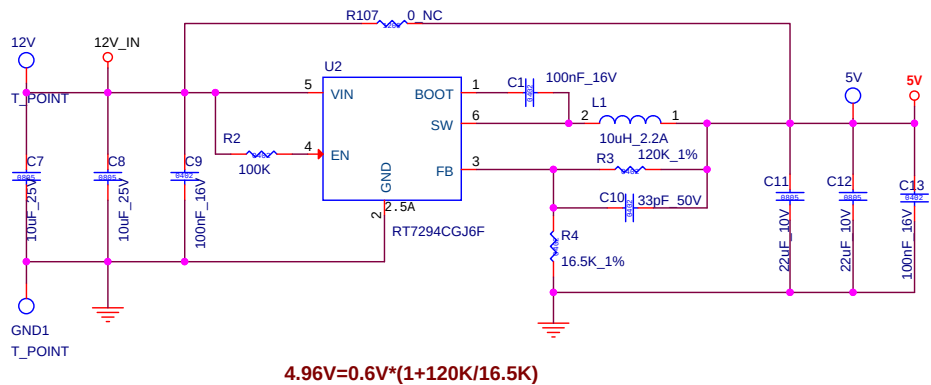
ROM boot up Path	PM_SPI_HLD	PM_SPI_DO	PM_SPI_W_PZ	PM_SPI_DI	PM_SPI_CLK	ROM Boot Storage Selection	Upgrade Path
	M[4]	M[3]	M[2]	M[1]	M[0]		
Stroge Mode (SD Boot first)	1	0			0	Skip SD Card, run SPI NAND Flash (With ECC)	SD
		0			1	Outer storage is SPI NAND Flash (With ECC)	SD
		1			0	Outer storage is SPI NOR Flash	SD
		1			1	Skip SD Card, run SPI NOR Flash	SD
USB Device Mode in High speed	0					Support USB upgrade	USB
Secure Mode				0	1	Secure boot	
Boot Mode			0			Normal boot	
			1			SPI NOR boot	
IC Default Status	1	1	1	1	0	ROM boot -> SPI NOR Flash	

Note 1 : Our IC only support the SPI-NAND Flash with ECC.

Note 2 : Skip SD Card means Rom Code don't detect SD Card that it can start faster.

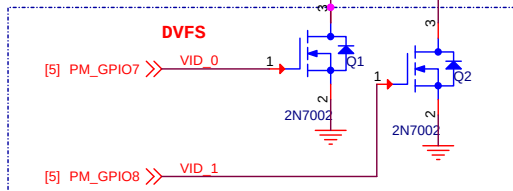
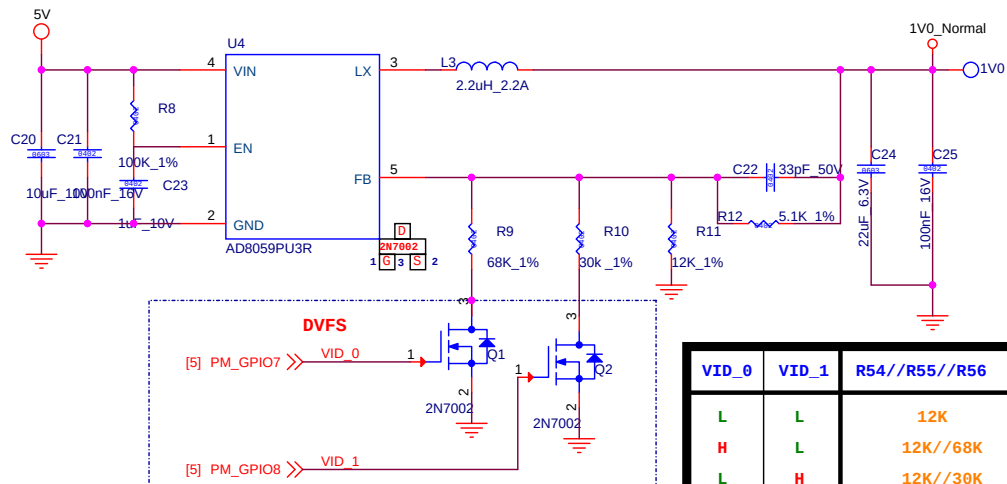


5V



$$4.96V = 0.6V * (1 + 120K / 16.5K)$$

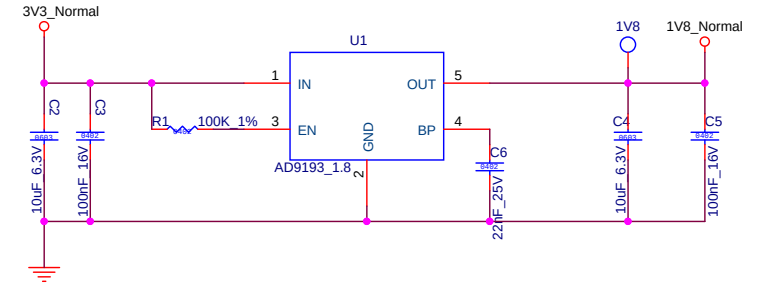
1V0_Normal



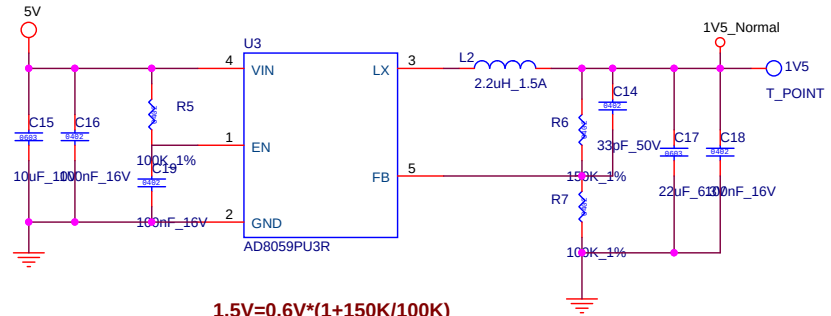
VID_0	VID_1	R54//R55//R56	1V0_VDD
L	L	12K	0.855V
H	L	12K//68K	0.900V
L	H	12K//30K	0.957V
H	H	12K//30K//68K	1.002V

Default :
VID 0 : H
VID 1 : H

1V8_Normal

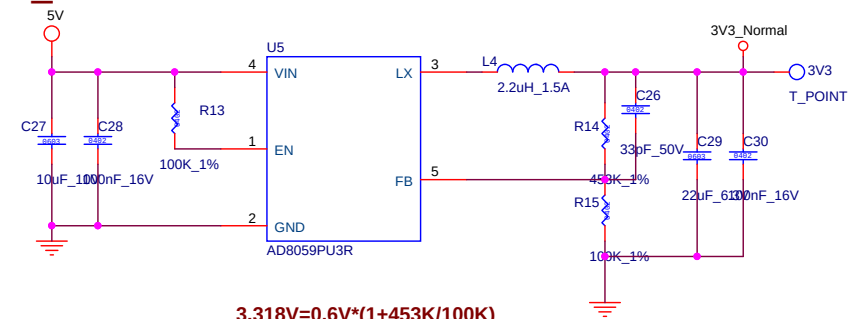


1V5_Normal

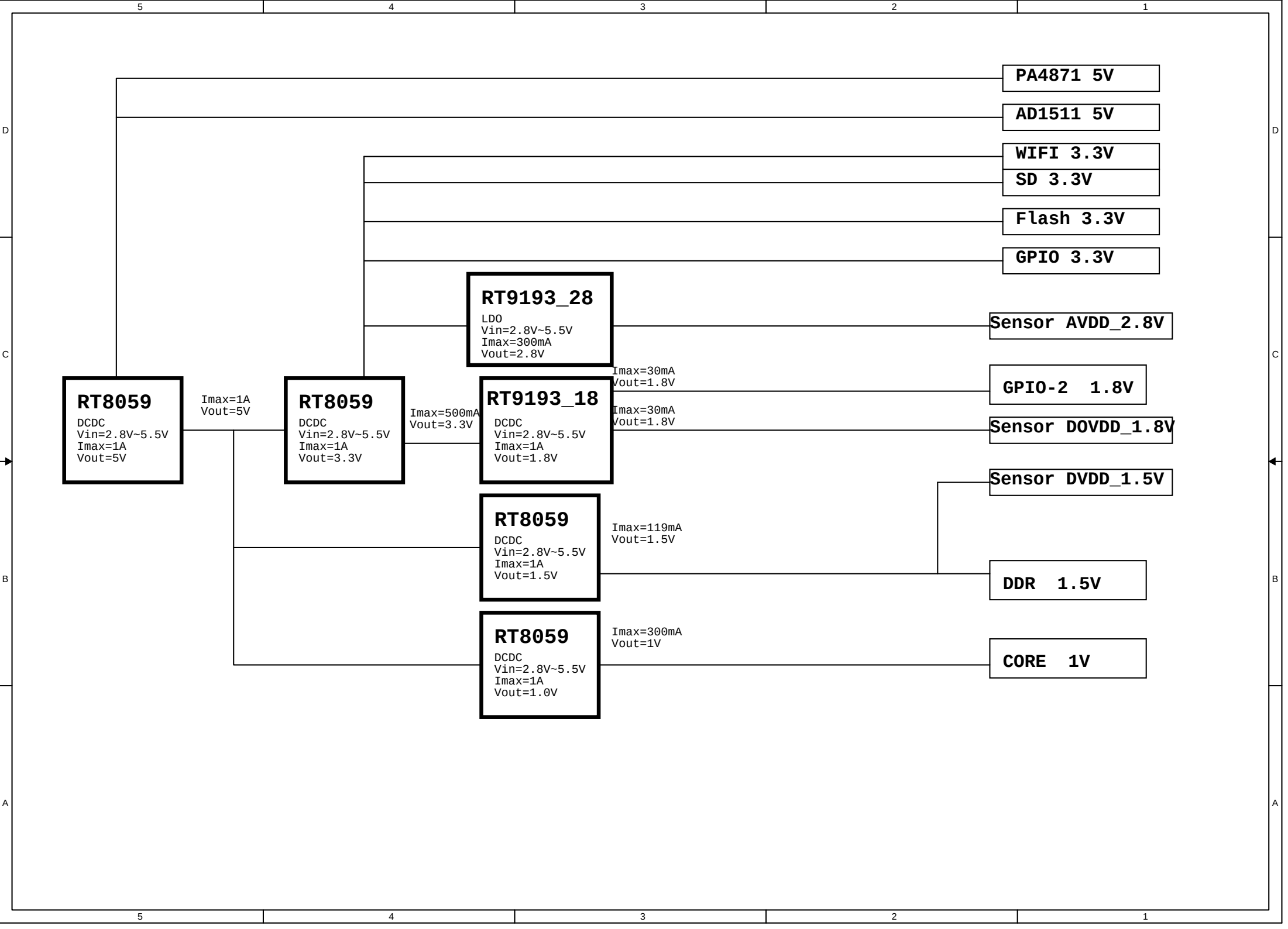


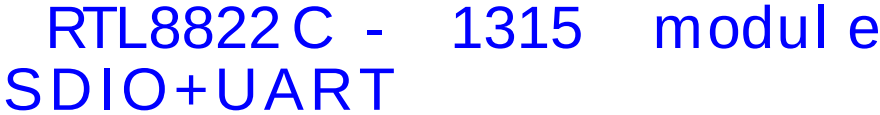
$$1.5V = 0.6V * (1 + 150K / 100K)$$

3V3_Normal

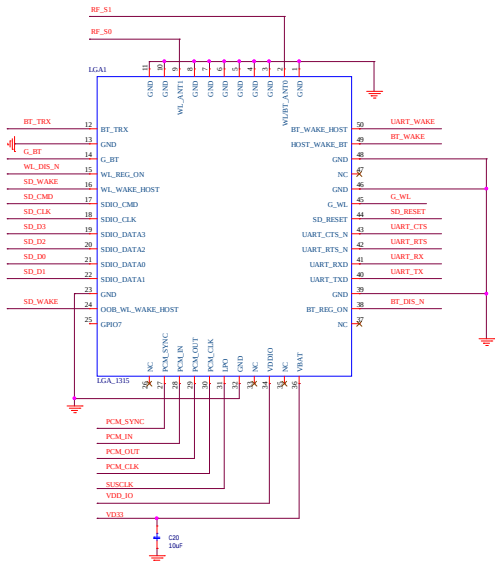


$$3.318V = 0.6V * (1 + 453K / 100K)$$





Pin Out (Based Module Side)



Note :

GPIO0~7 must have test pad or connect to pin output