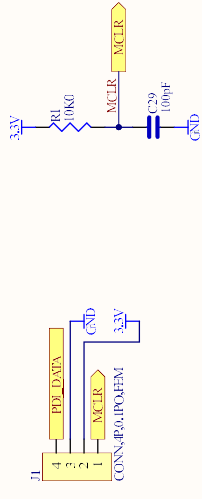
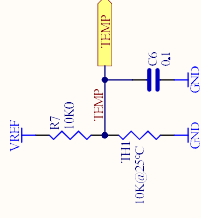


PROGRAMMATION

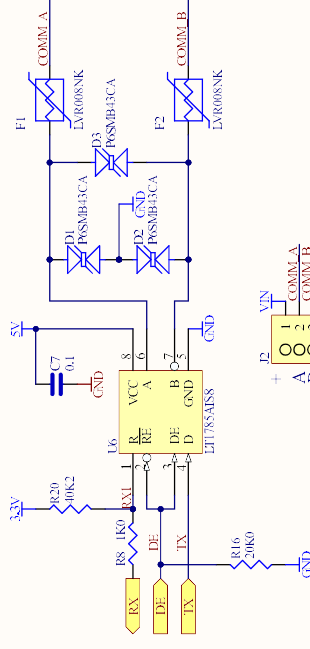
RESET



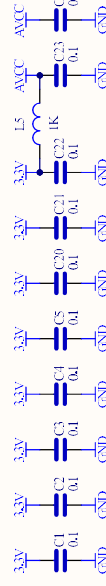
LECTURE TEMPÉRATURE



COMMUNICATION RS-485



DÉCOUPLAGE U1



Titre: **PCB124 - Alimentation microes 3P**

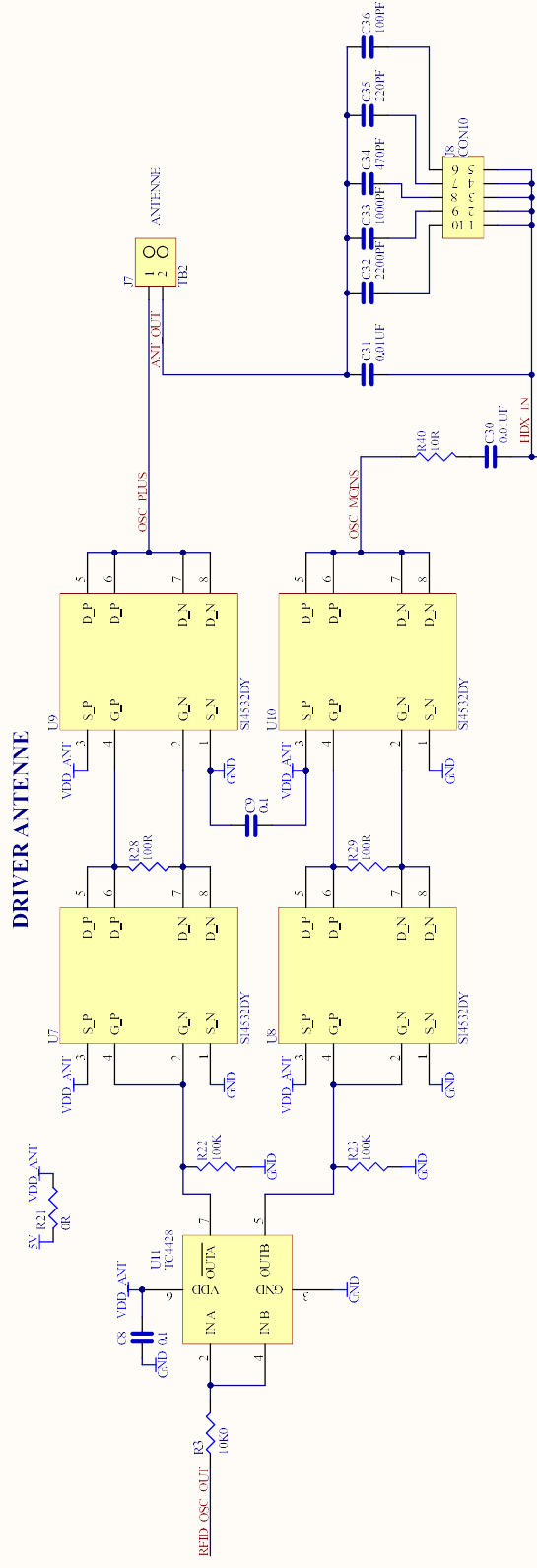
Size: B Number: CPU+COMM Revision: 6

Date: 2023/05/02 Time: 16:17:55 Sheet 2 of 7

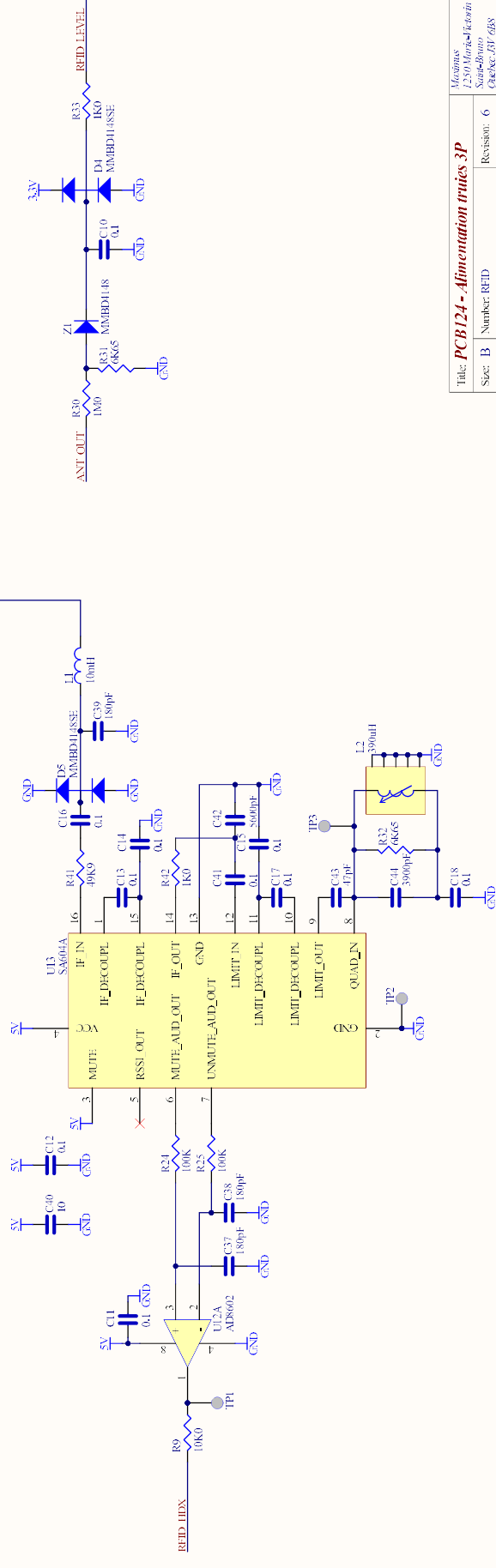


Macbmas
1250 Maréchal Lefebvre
Saint-Benoit
Guebwiller, 68100





LECTURE HDX



LECTURE TENSION ANTENNE

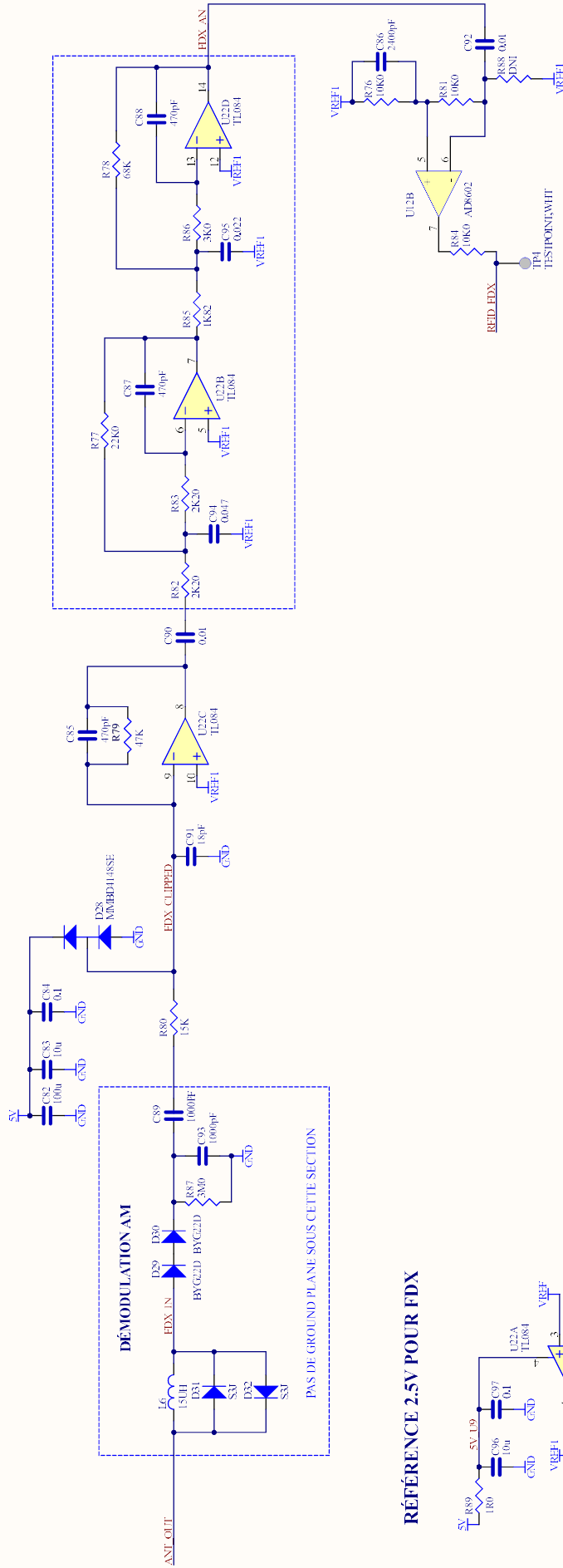
Title: PCB124 - Alimentation truces 3P		<i>Maximus</i> <i>12 rue des Lices, 67400</i> <i>Quœbeq, BF 688</i>		 MAXIMUS
Size: B	Number: REFD	Revision: 6		
Date: 2022.08.02	Time: 16:17:55	Sheet 5 of 7		
File: REFDSC11				

LECTURE FDX

ÉCRÉTAGE

AMPLI

FILTRE PASSE-BAS 4È ORDRE

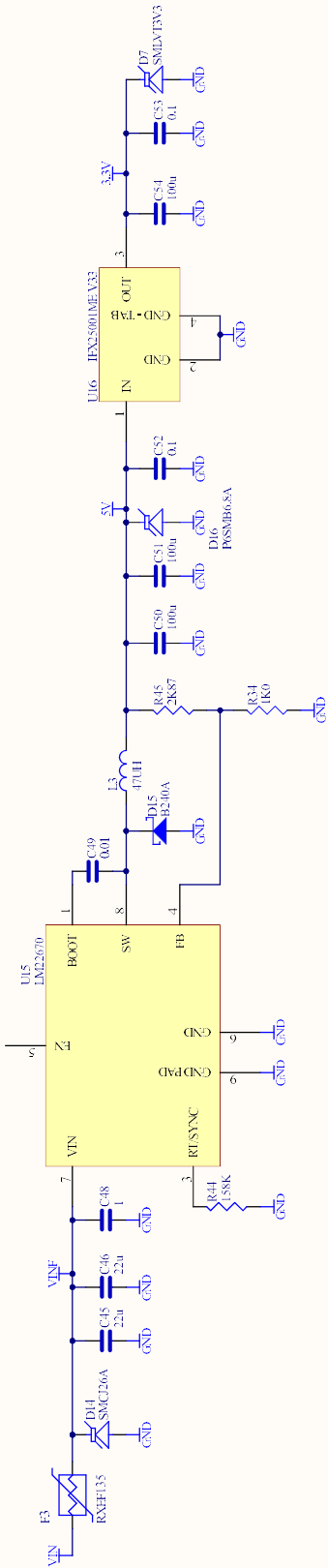


RÉFÉRENCE 2.5V POUR FDX

Titre: PCB124 - Alimentation trices 3P		Macomas 1250 Marka Suda-Bono Geeber-3P edg	
Size: B	Number: REFD	Revision: 6	
Date: 2023-05-02	Time: 16:17:56	Sheet 6 of 7	
File: REFD-AN.SCH			

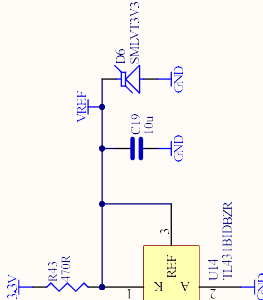


ALIMENTATION 5V

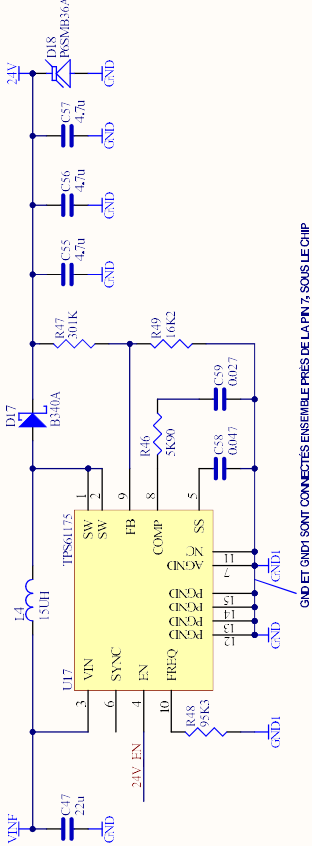


ALIMENTATION 3.3V

RÉFÉRENCE 2.5V A/D

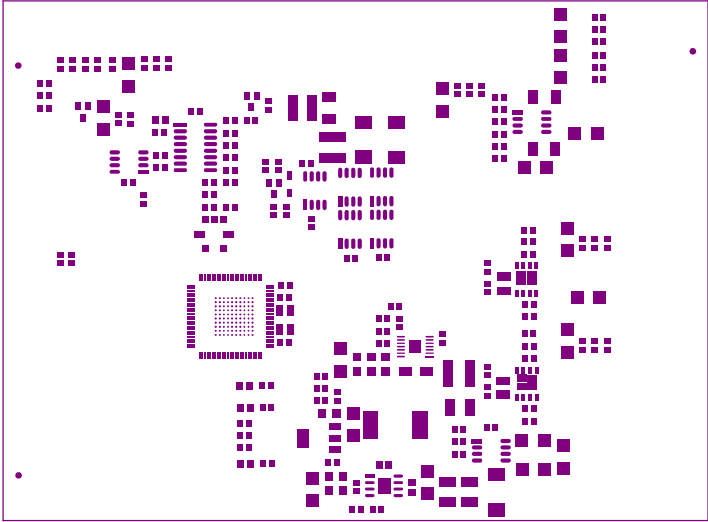


ALIMENTATION 24V MOTEUR



GND ET GND1 SONT CONNECTES ENSEMBLE PRES DE LA PIN 7, SOUS LE CHIP

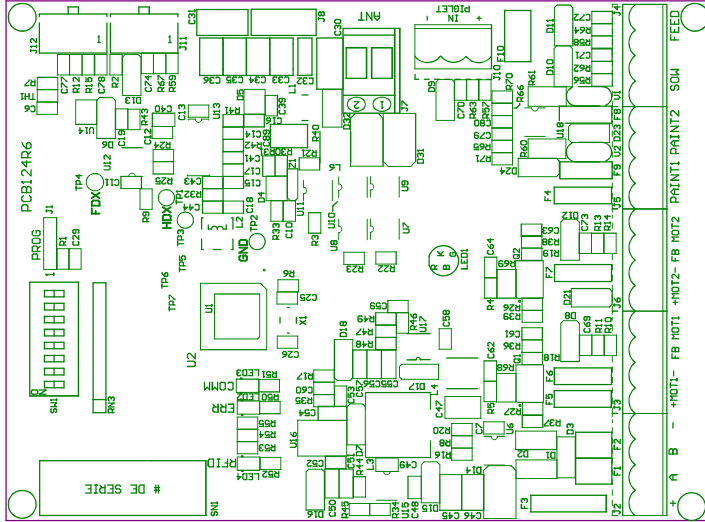




- NOTE:**
- 1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
 - 2. Over all thickness after plating: 0.0625 $\pm$ 0.007
 - 3. All dimensions are imperial unless specified.
 - 4. Soldermask to be non-conductive WHITE epoxy.
 - 5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
 - 6. The supplier must provide date code (week-year) and his logo on either side.
 - 7. The finished line width must be within $\pm$ 10% of the original artwork.
 - 8. Panelize the circuit (V-grooves) to optimize board usage.
 - 9. Add 10mm border all around with 3 fiducials
 - 10. All dimensions must be performed
 - 11. Blue solder mask

LAYER PRINTED	
MECHANICAL :	1 , 4
TOP PASTE	

MAXIMUS
CURRENT
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



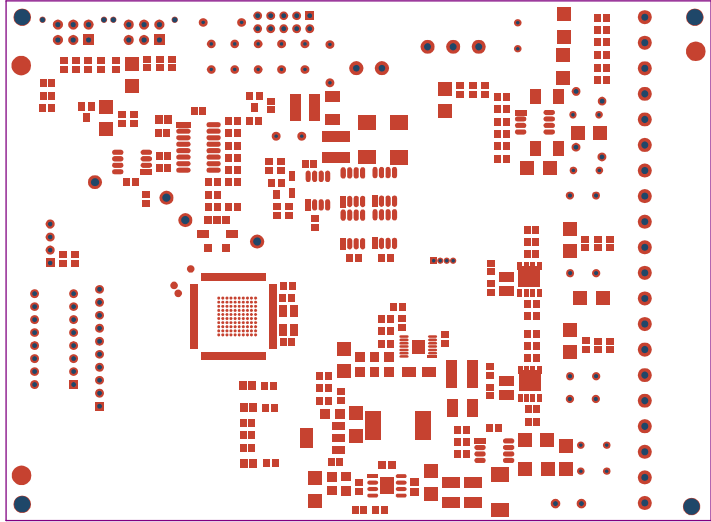
NOTE:

1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All dimensions are imperial unless specified.
4. Sikt screening to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The manufacturer logo must include the date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. All dimensions must be performed
11. Blue solder mask

LAYER PRINTED
TOP OVERLAY

MECHANICAL :
1
1

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



NOTE:

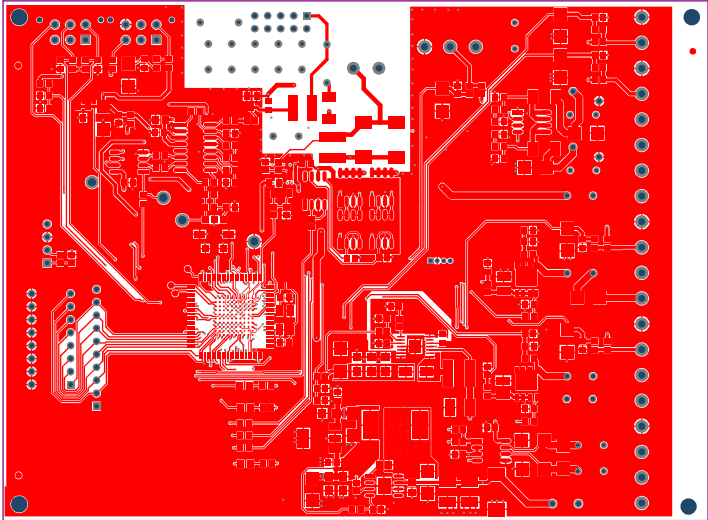
1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All plated surfaces must be imperial unless specified.
4. Soldermask to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. All plated surfaces must be performed
11. Blue solder mask

LAYER PRINTED

MECHANICAL :
1 , 4

TOP SOLDER

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



NOTE:

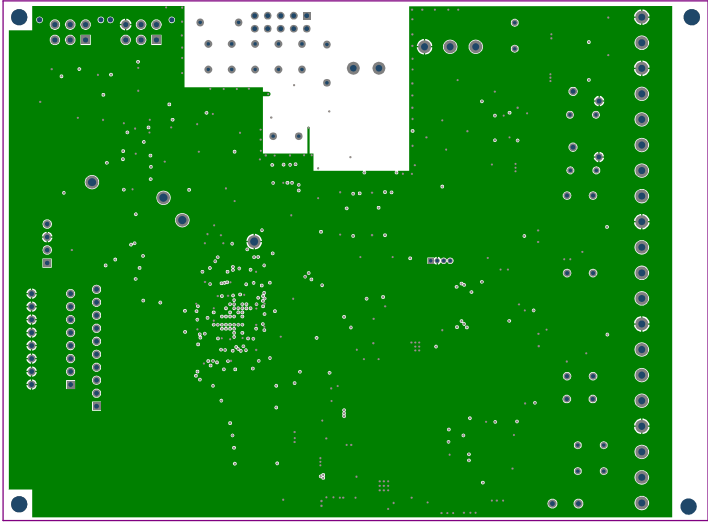
1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. Solder mask to be imperial unless specified.
4. Silkscreening to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. The circuit must be performed
11. Blue solder mask

LAYER PRINTED

TOP LAYER

MECHANICAL :
1 , 4

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



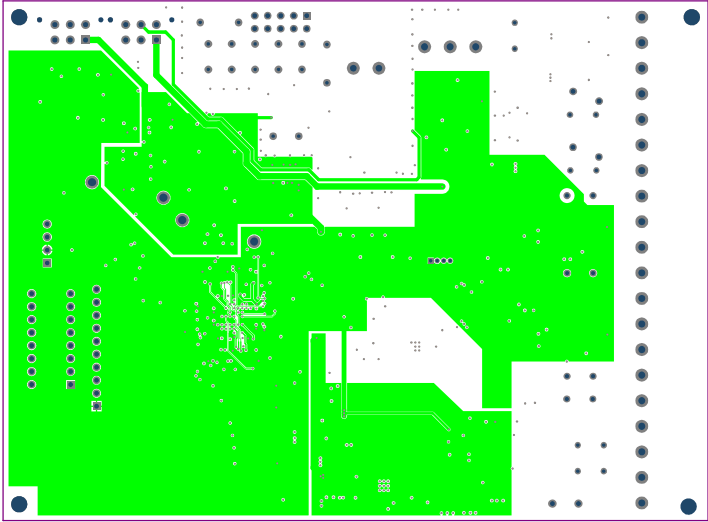
NOTE:

1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All plated surfaces must be imperial unless specified.
4. Soldermasking to be non-conductive, WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide the date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. Silver solder mask must be performed
11. Blue solder mask

LAYER PRINTED

MID LAYER 1
MECHANICAL : 1 , 4

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISÉ BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



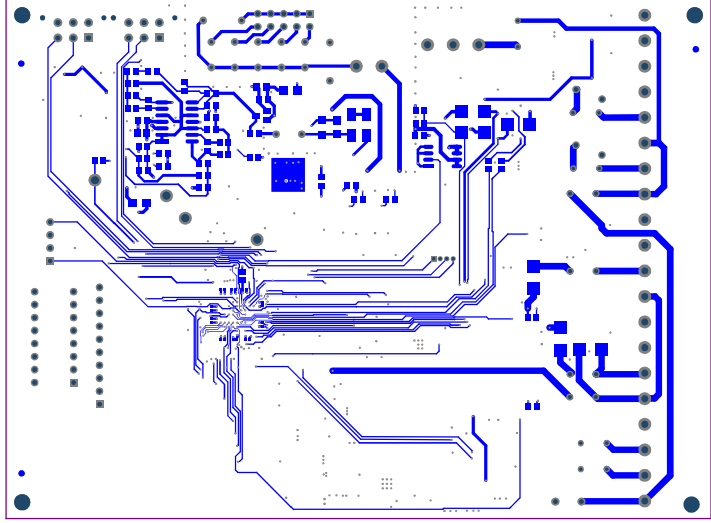
NOTE:

1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All plated surfaces must be imperial unless specified.
4. Soldermask to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide the date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. Silver solder mask must be performed
11. Blue solder mask

LAYER PRINTED

MID LAYER : 2
MECHANICAL : 1

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISÉ BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



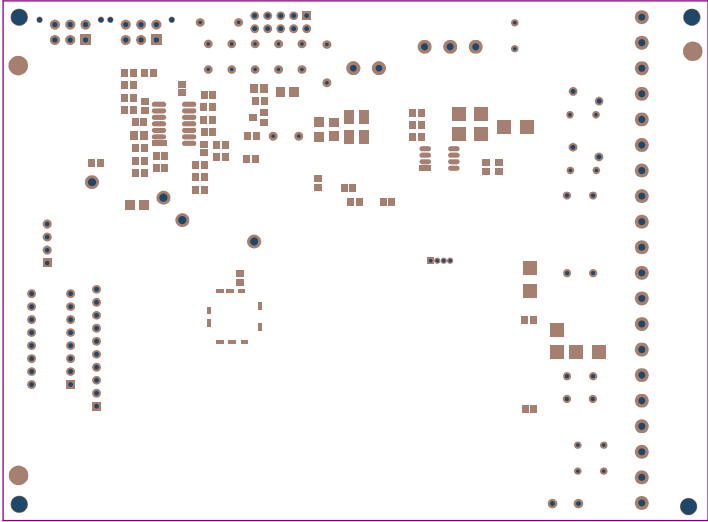
NOTE:

1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All plated surfaces must be imperial unless specified.
4. Sikscreening to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide a date code (week-year) and his loop on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. The solder mask must be performed
11. Blue solder mask

LAYER PRINTED

MECHANICAL :
1
BOTTOM LAYER

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



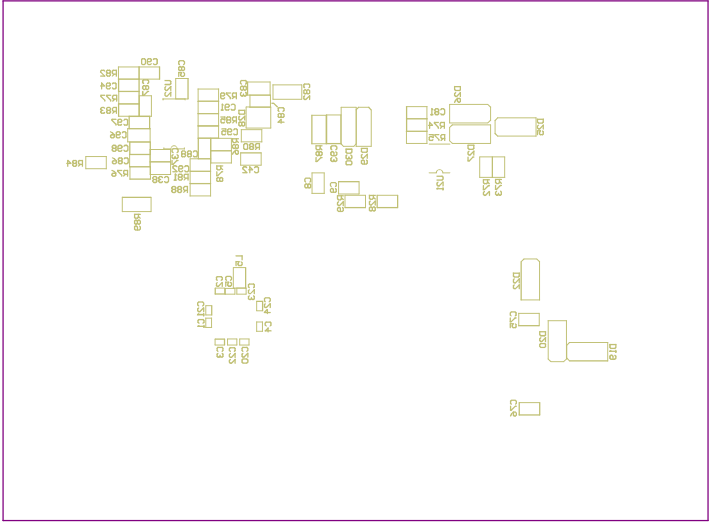
- NOTE:**
1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
 2. Over all thickness after plating: 0.0625 $\pm$ 0.007
 3. All plated surfaces must be imperial unless specified.
 4. Sikscreening to be non-conductive WHITE epoxy.
 5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
 6. The supplier must provide the date code (week-year) and his logo on either side.
 7. The finished line width must be within $\pm$ 10% of the original artwork.
 8. Panelize the circuit (V-grooves) to optimize board usage.
 9. Add 10mm border all around with 3 fiducials
 10. The copper mask must be performed
 11. Blue solder mask

LAYER PRINTED
MECHANICAL : 1 , 4
BOTTOM SOLDER

MAXIMUS
CURRENT
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB

NOTE:

1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
2. Over all thickness after plating: 0.0625 $\pm$ 0.007
3. All dimensions are imperial unless specified.
4. Sirk screening to be non-conductive WHITE epoxy.
5. Board must meet UL rating of 94V-0 and UL rating must appear on P.C.B. side unless the manufacturer logo is UL acknowledged.
6. The supplier must provide the date code (week-year) and his logo on either side.
7. The finished line width must be within $\pm$ 10% of the original artwork.
8. Panelize the circuit (V-grooves) to optimize board usage.
9. Add 10mm border all around with 3 fiducials
10. All plated through holes must be performed
11. Blue solder mask

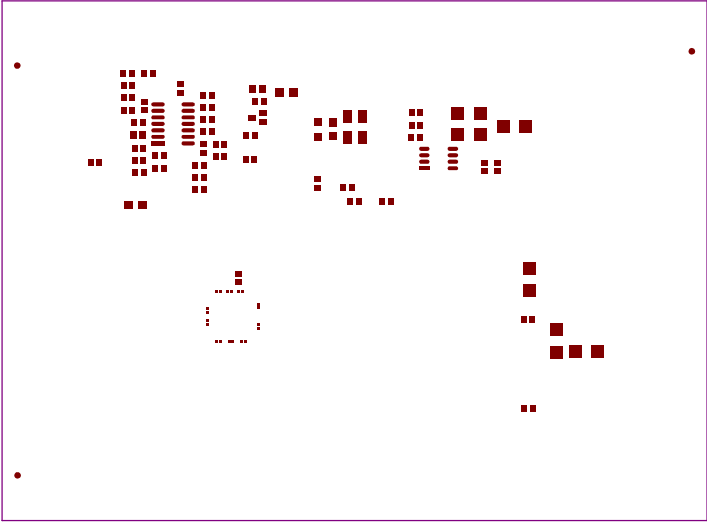


LAYER PRINTED

MECHANICAL :
1 , 4

BOTTOM OVERLAY

MAXIMUS
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANIERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB



- NOTE:**
1. 4 Layers, ENIG finish, FR-4, 1 oz. copper on each layer
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 6. The supplier must provide date code (week-year) and his logo on either side.
 7. The finished line width must be within $\pm$ 10% of the original artwork.
 8. Panelize the circuit (V-grooves) to optimize board usage.
 9. Add 10mm border all around with 3 fiducials
 10. All dimensions must be performed
 11. Blue solder mask

LAYER PRINTED
MECHANICAL : 1 , 4
BOTTOM PASTE

MAXIMUS
CURRENT
PROJECT : MAXIMUS
DESIGNER : ANDRE SPILMANN, ing.
REVISED BY : GUY SANTERRE, ing.
DATE : 2022-05-02
NAME : PCB124R6.PCB

