

1 Internal photographs

1.1 Main device under test (EUT/DUT)



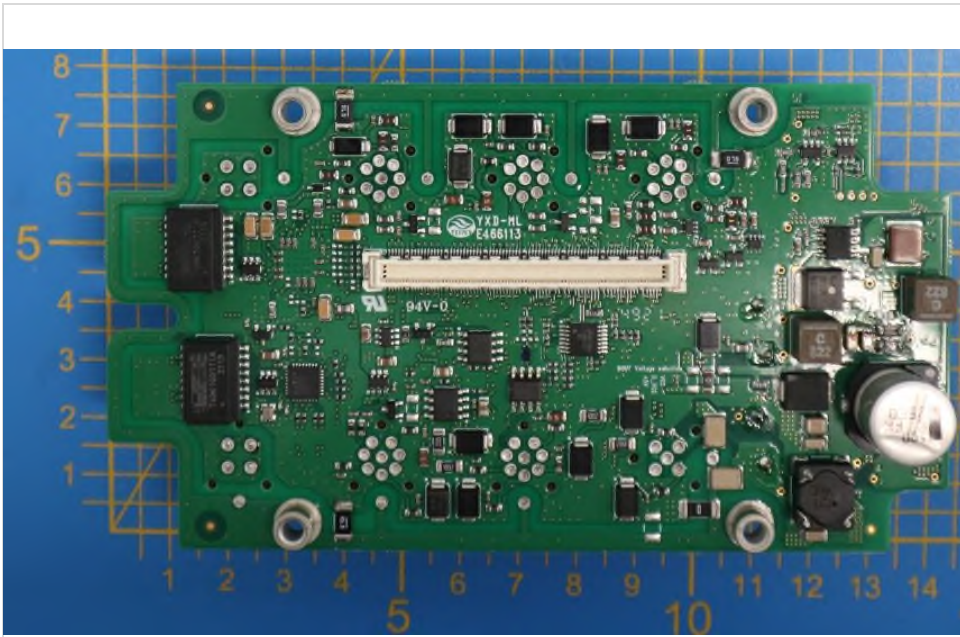
Photograph 1: EUT with open enclosure



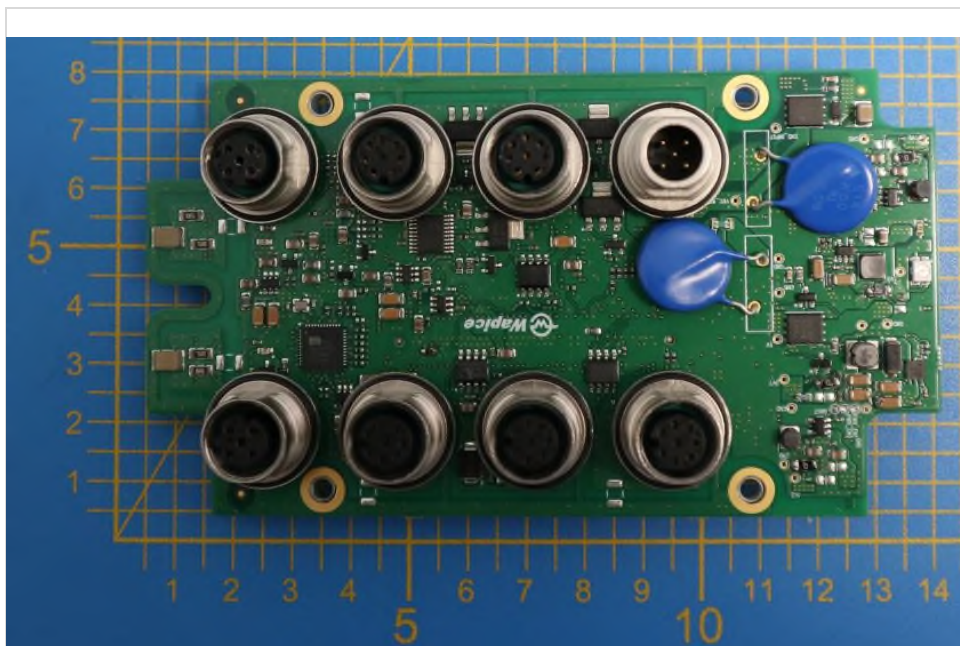
Photograph 2: top side of enclosure with mounted PCBs inside view



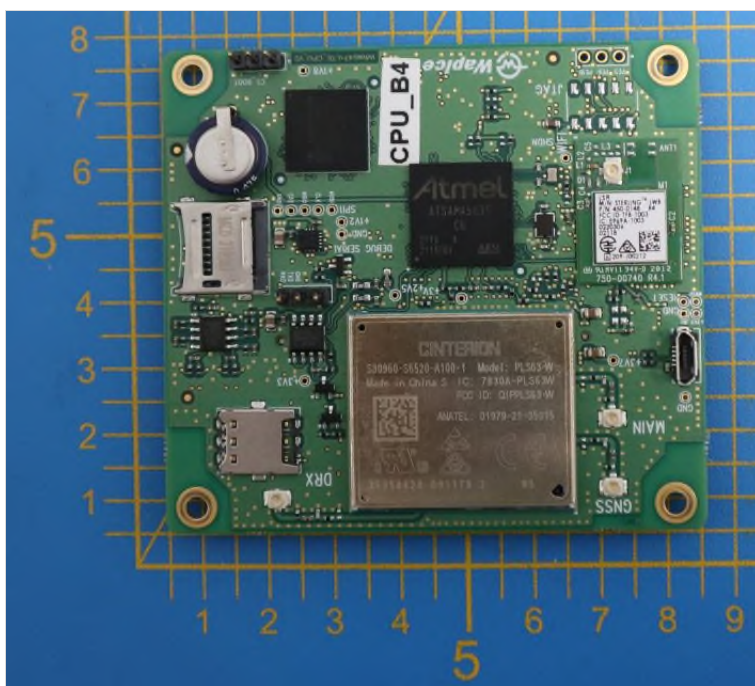
Photograph 3: bottom side of enclosure with antenna connectors inside view



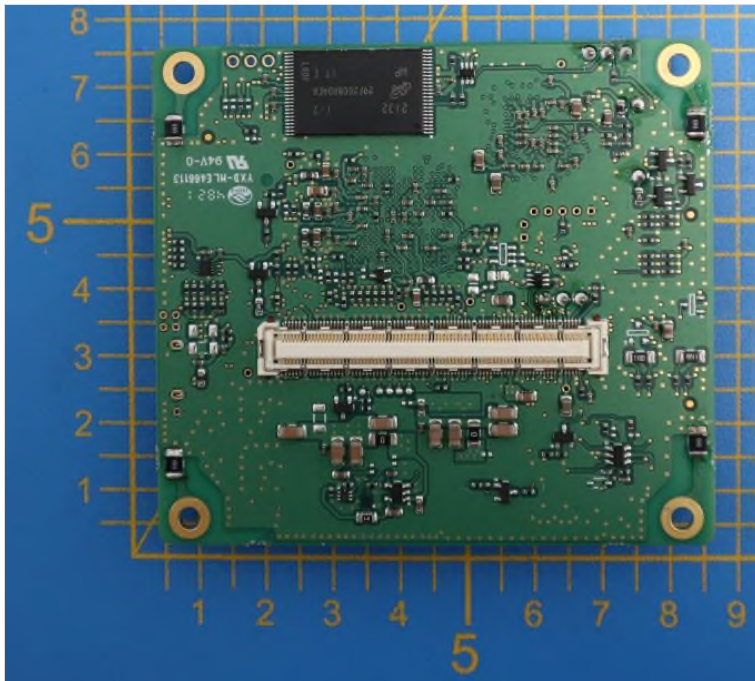
Photograph 4: top side of connector PCB



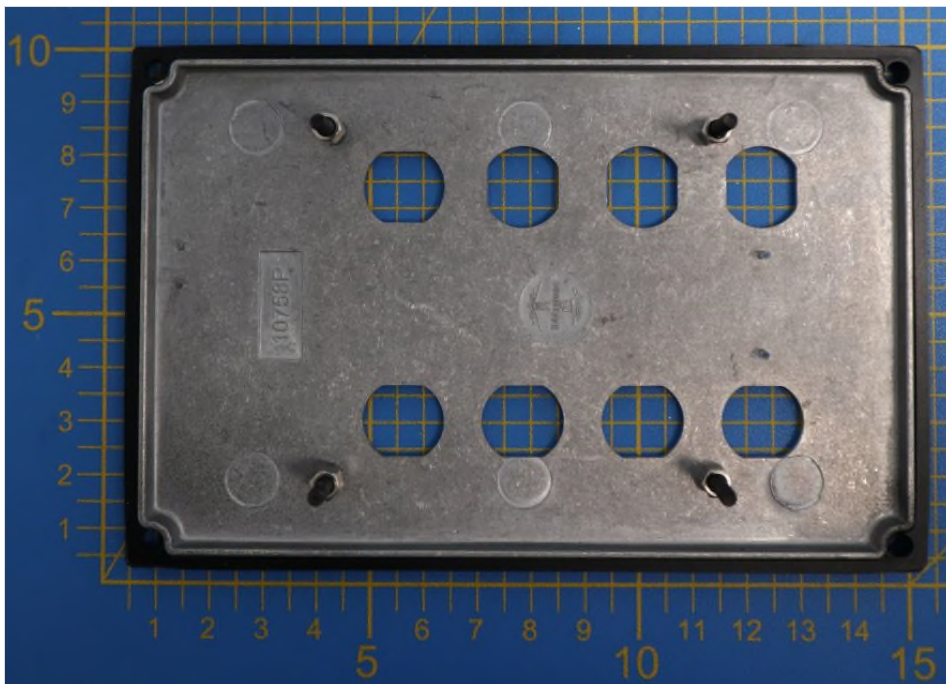
Photograph 5: bottom side of connector PCB



Photograph 6: top side of LTE CPU PCB



Photograph 7: bottom side of LTE CPU PCB



Photograph 8: top side of enclosure inside view

End Of Annex 2