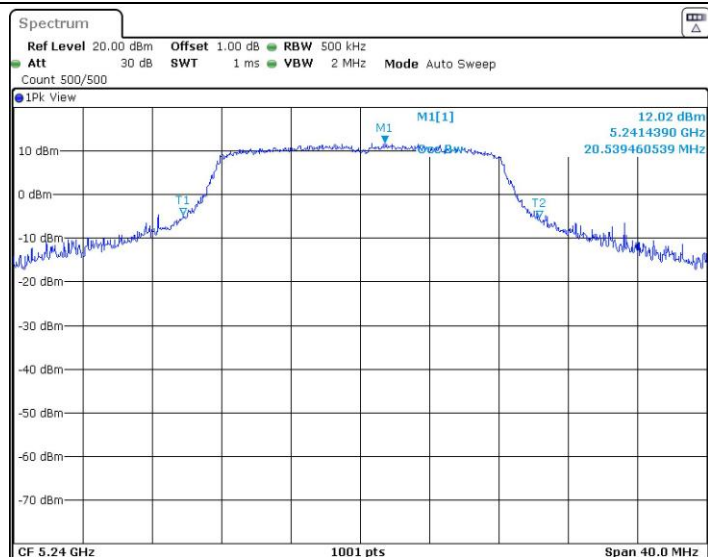
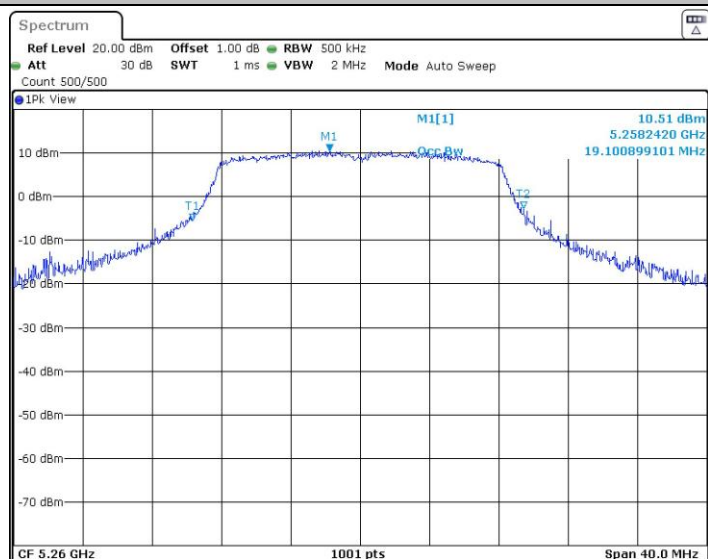
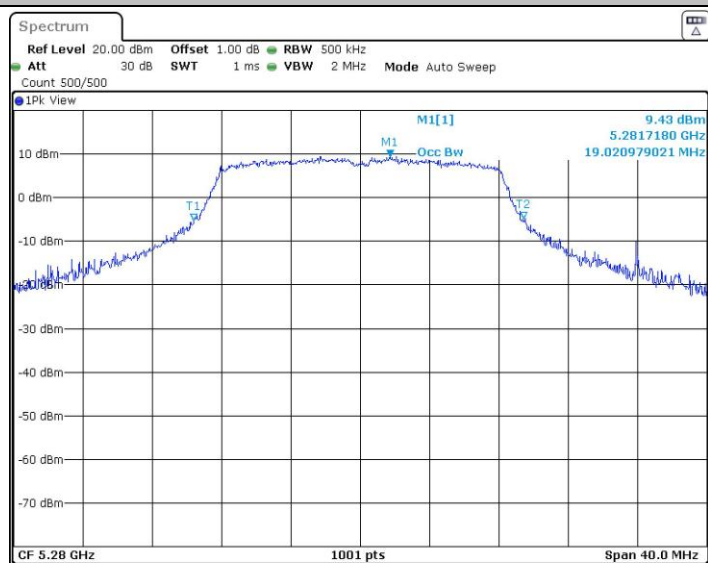




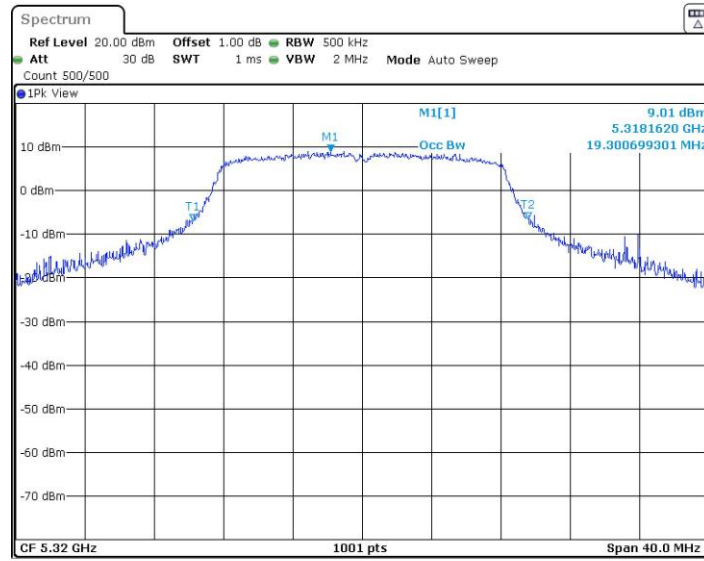
11A_Ant1_5240



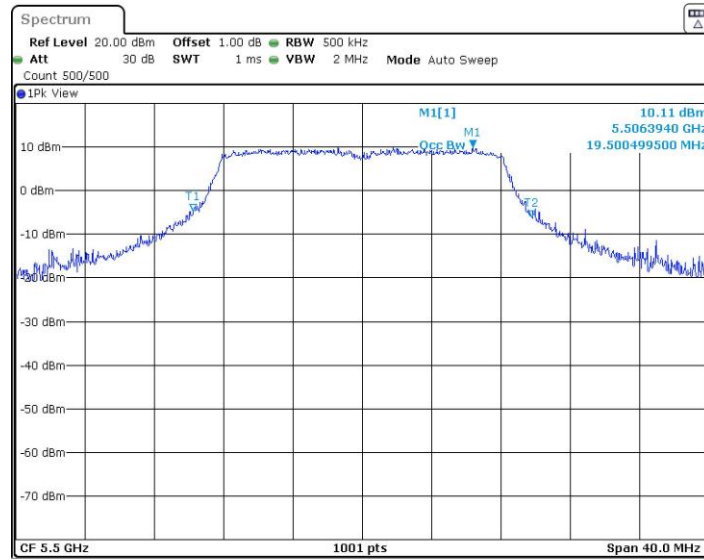
11A_Ant1_5260



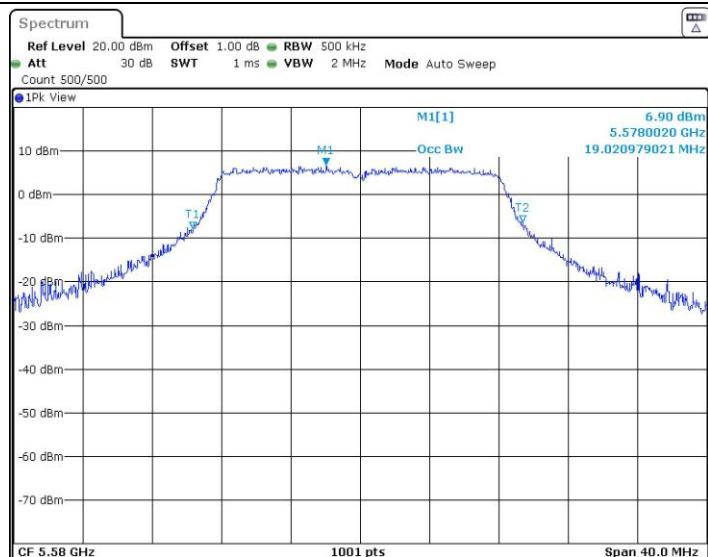
11A_Ant1_5280



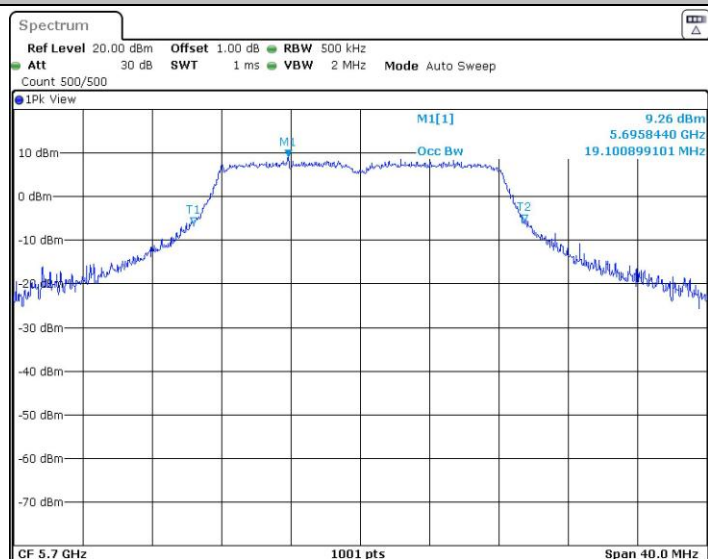
11A_Ant1_5320



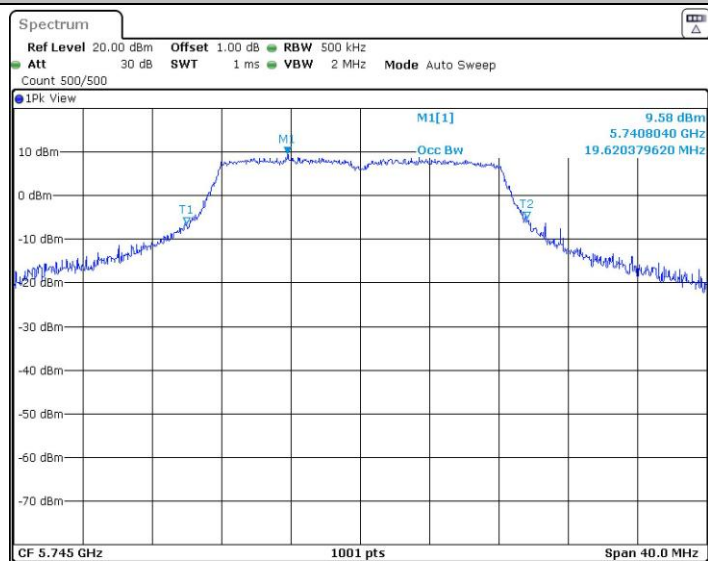
11A_Ant1_5500



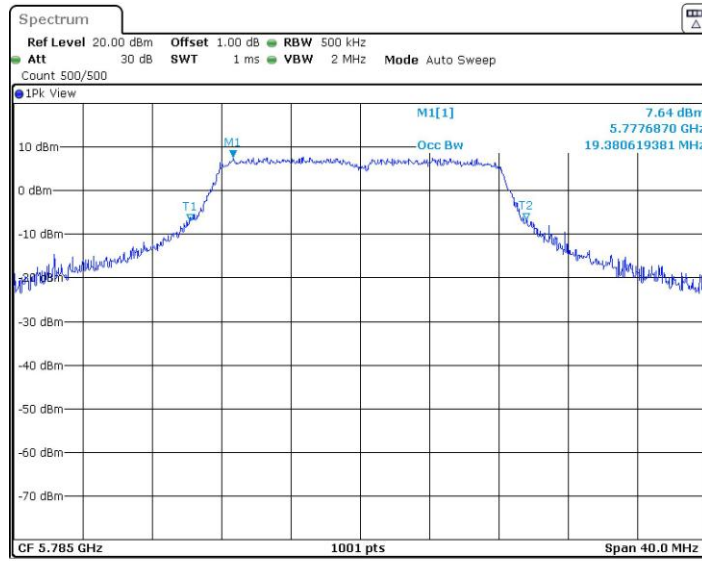
11A_Ant1_5580



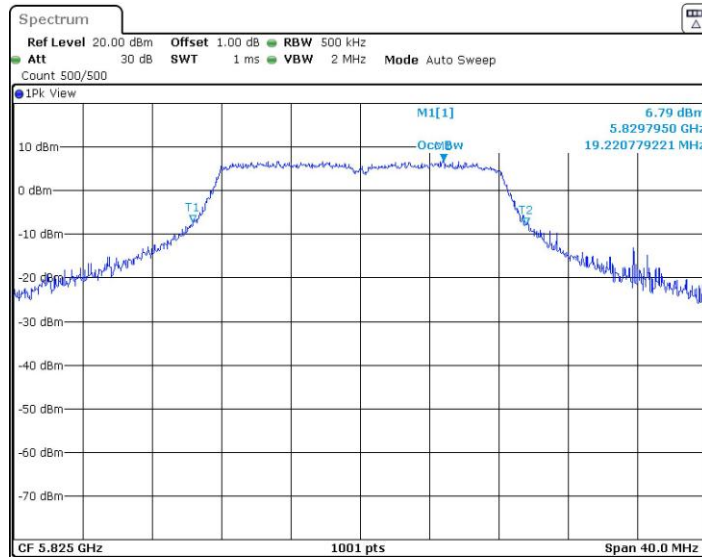
11A_Ant1_5700



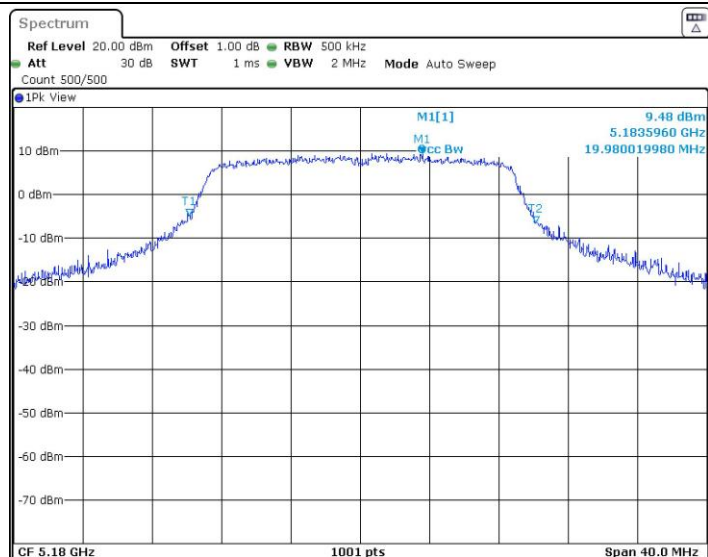
11A_Ant1_5745



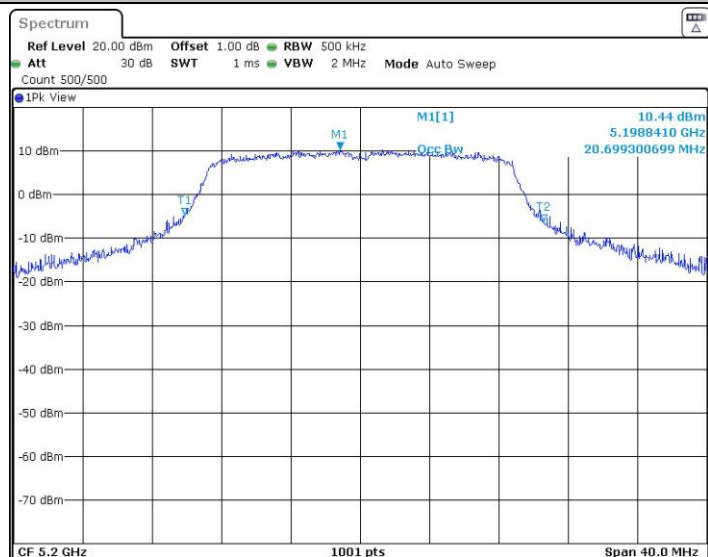
11A_Ant1_5785



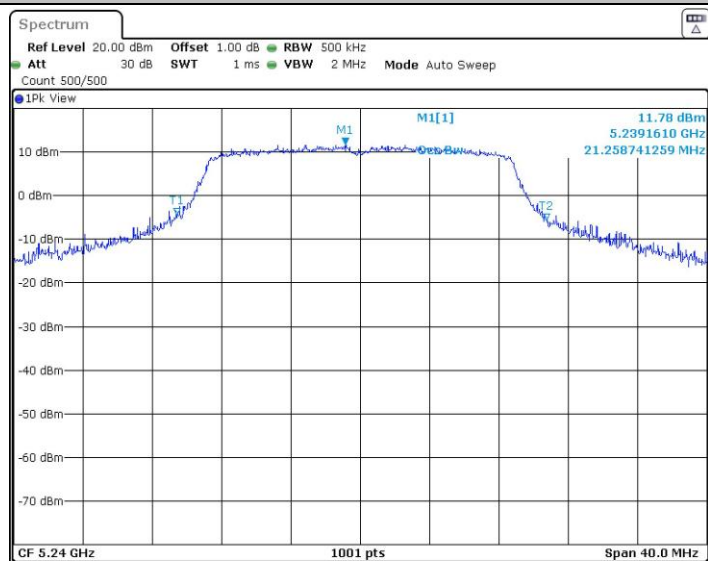
11A_Ant1_5825



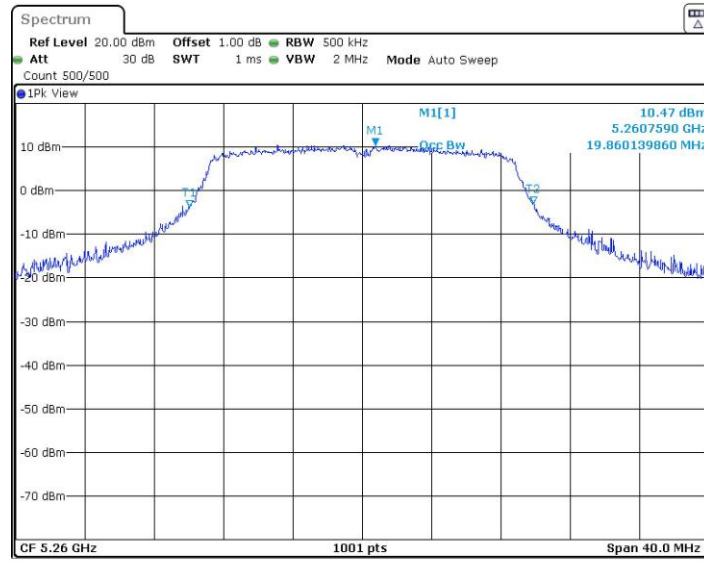
11N20SISO_Ant1_5180



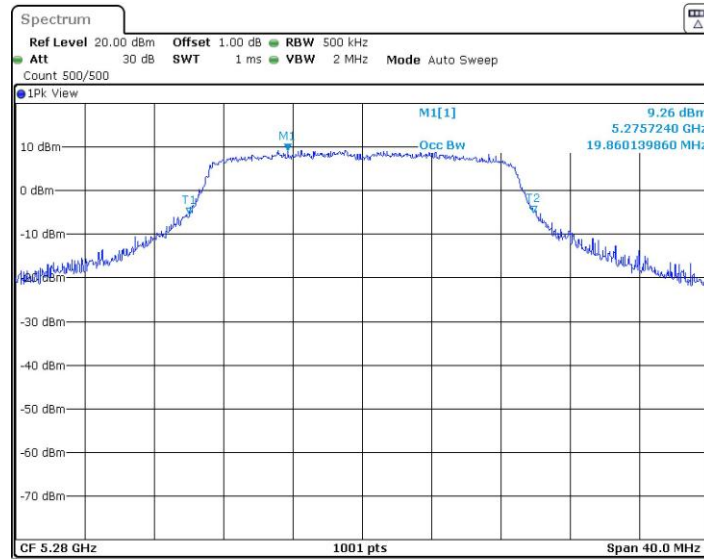
11N20SISO_Ant1_5200



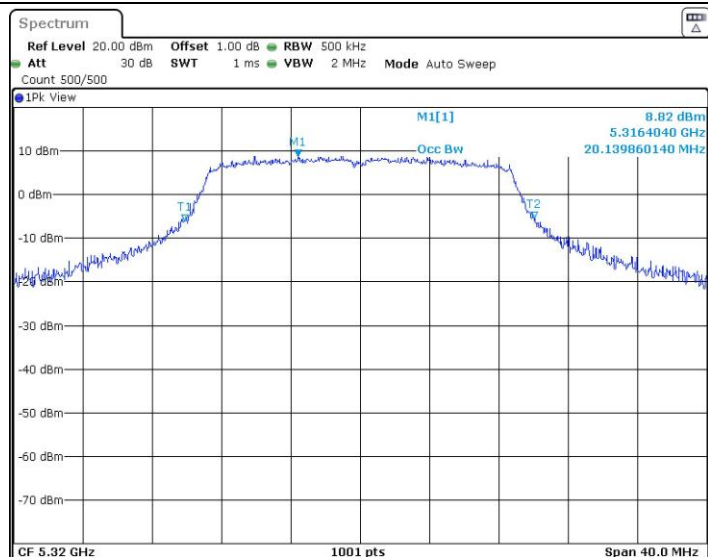
11N20SISO_Ant1_5240



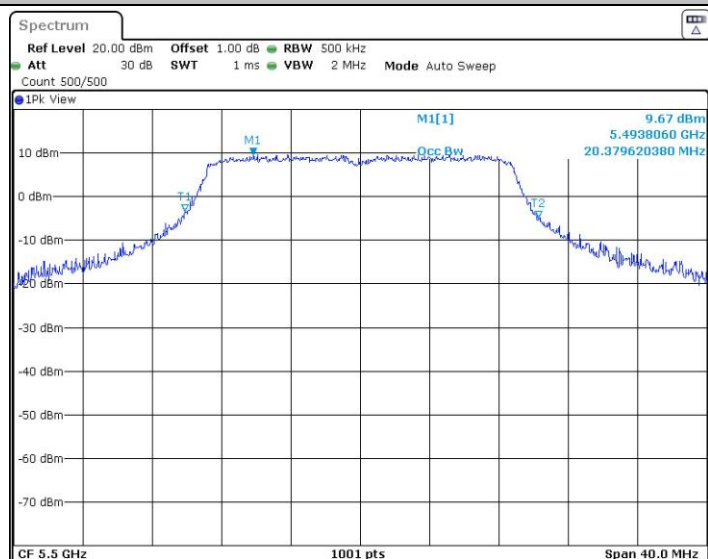
11N20SISO_Ant1_5260



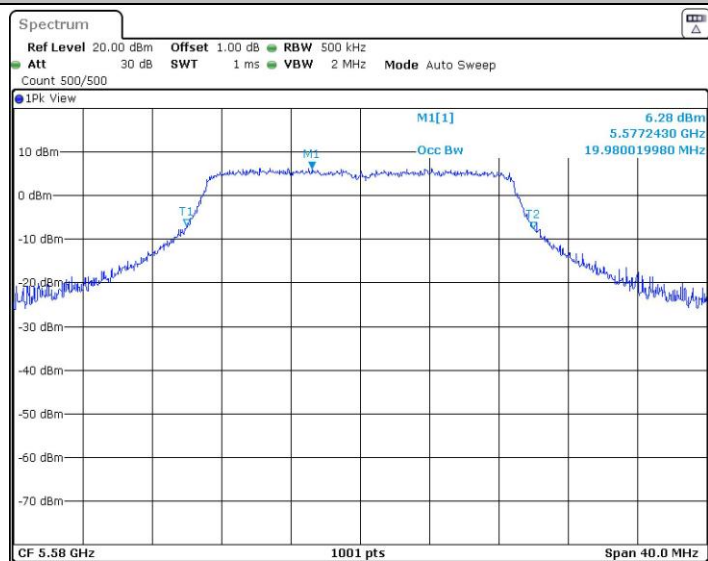
11N20SISO_Ant1_5280



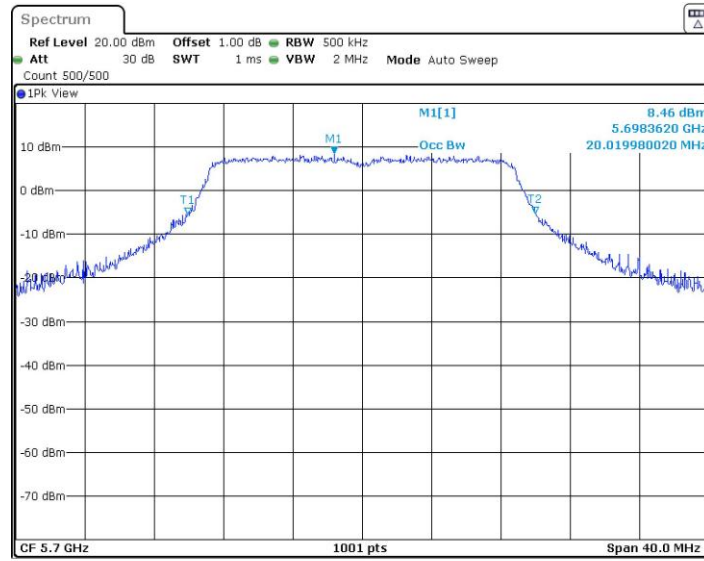
11N20SISO_Ant1_5320



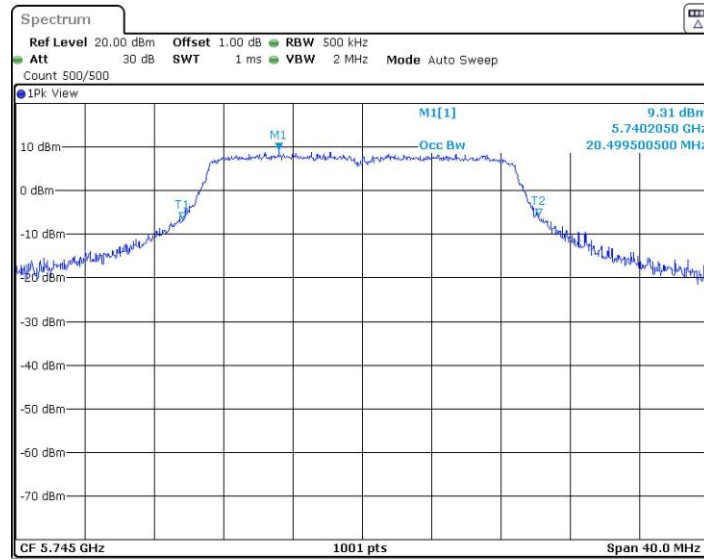
11N20SISO_Ant1_5500



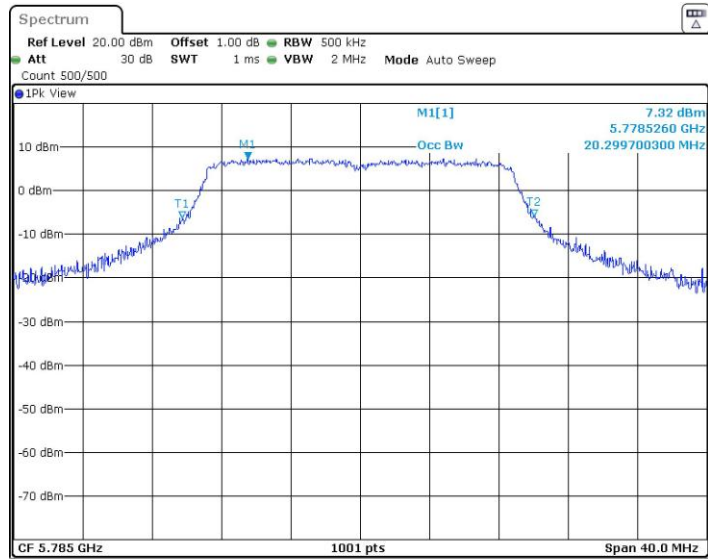
11N20SISO_Ant1_5580



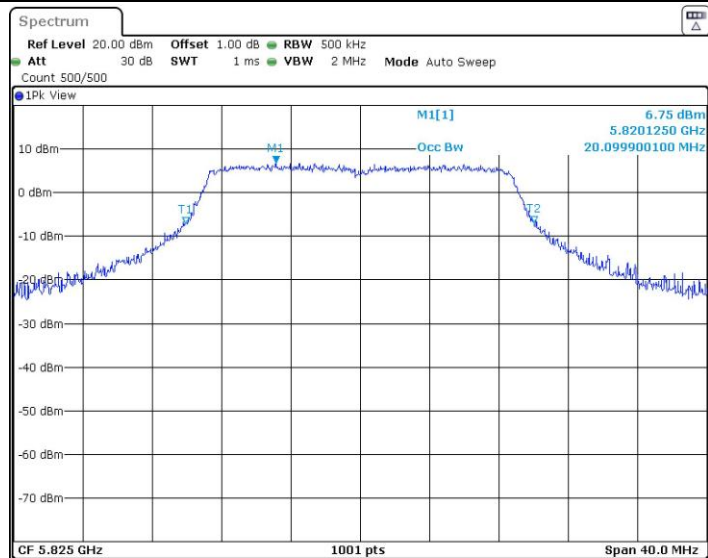
11N20SISO_Ant1_5700



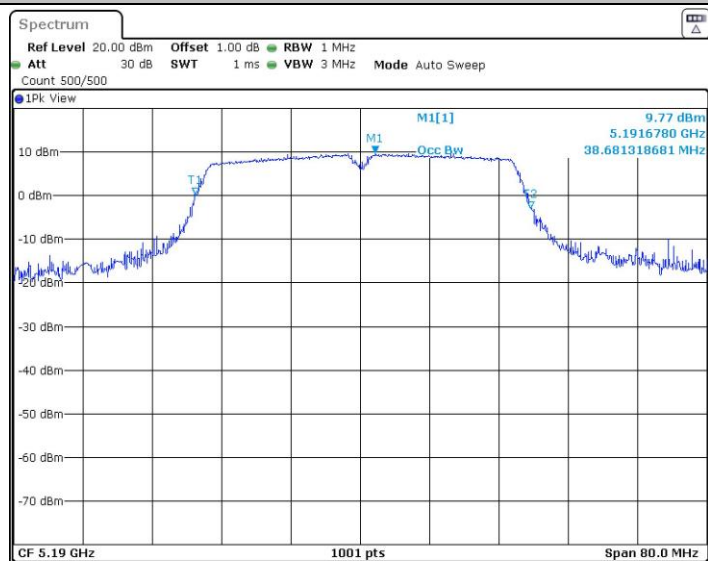
11N20SISO_Ant1_5745



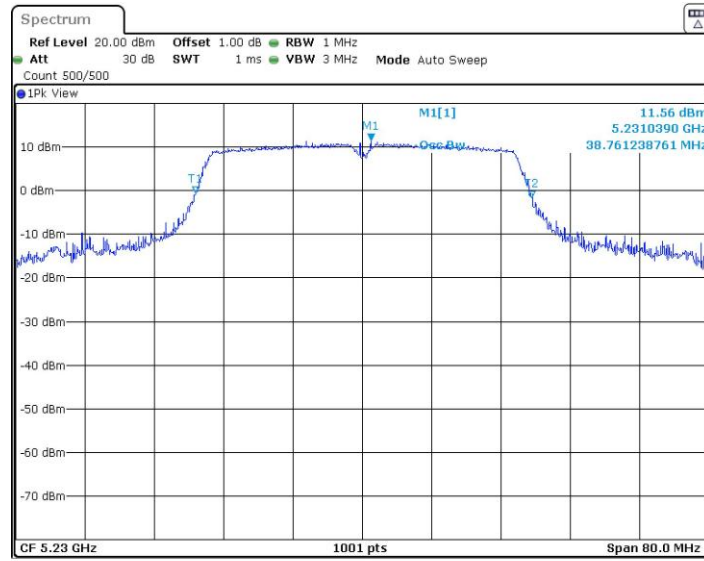
11N20SISO_Ant1_5785



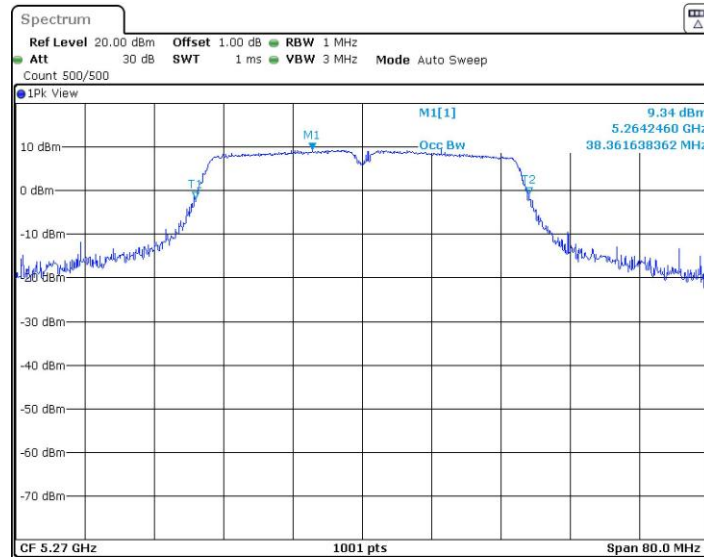
11N20SISO_Ant1_5825



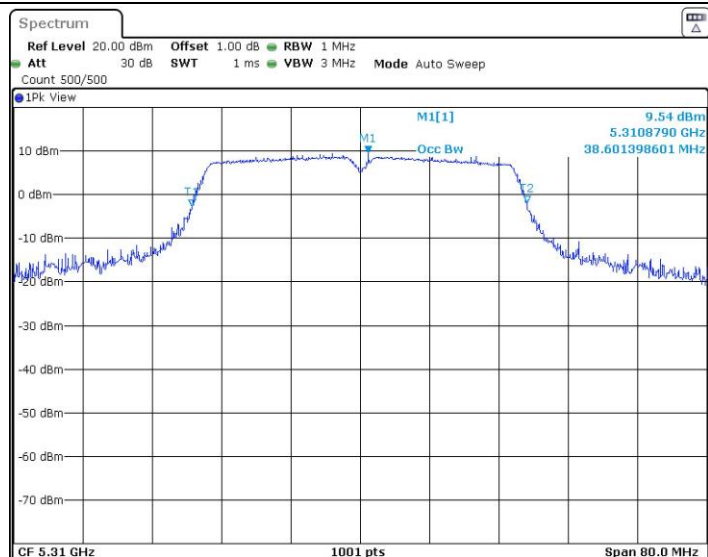
11N40SISO_Ant1_5190



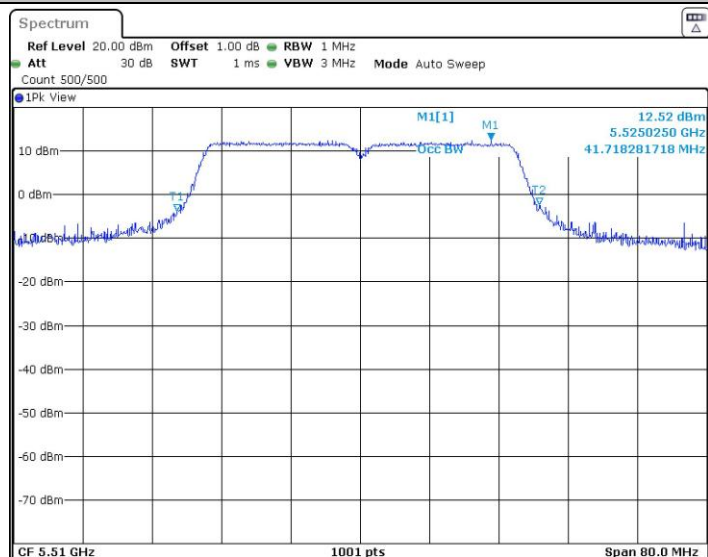
11N40SISO_Ant1_5230



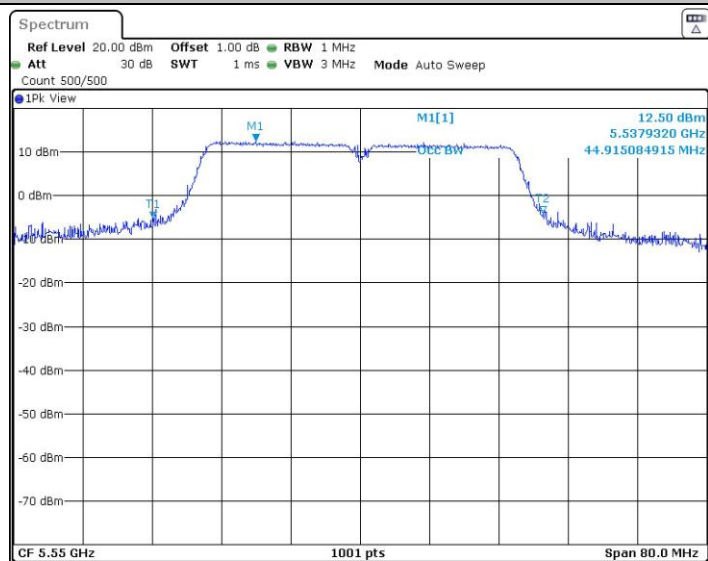
11N40SISO_Ant1_5270



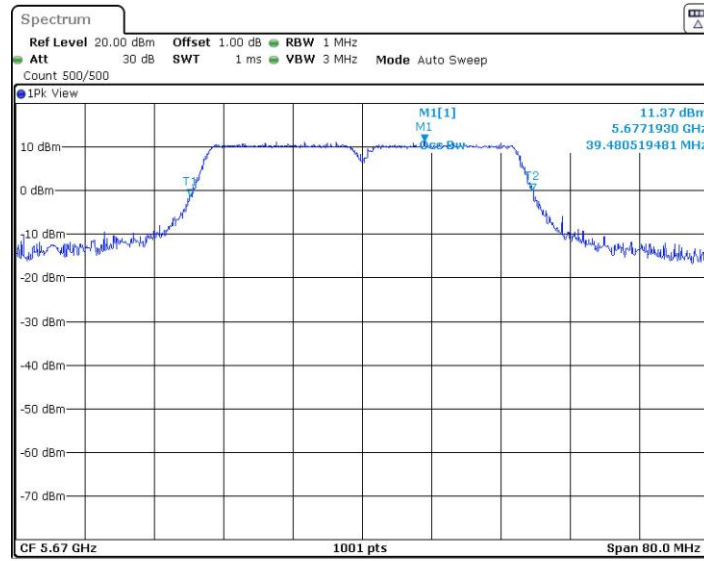
11N40SISO_Ant1_5310



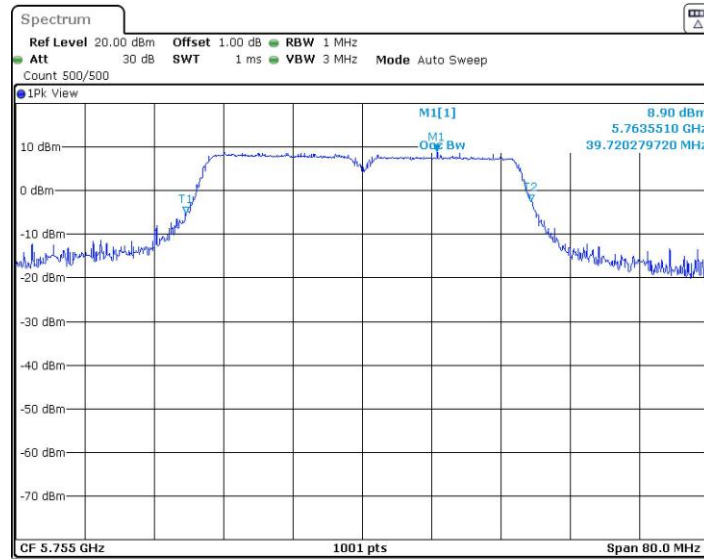
11N40SISO_Ant1_5510



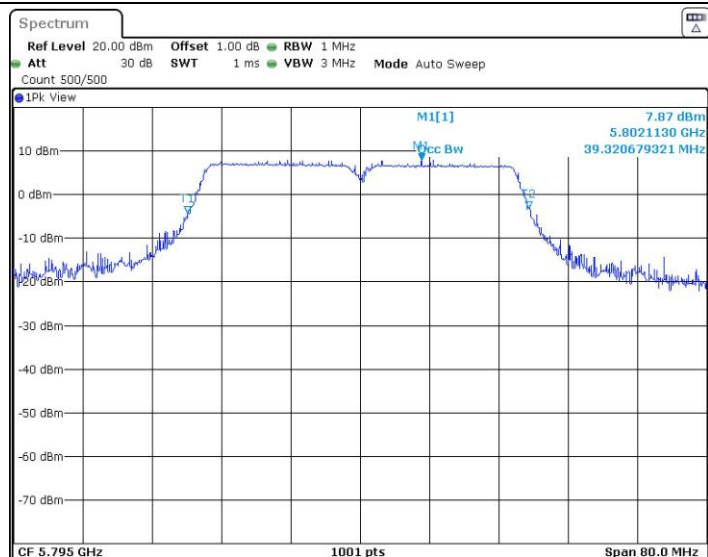
11N40SISO_Ant1_5550



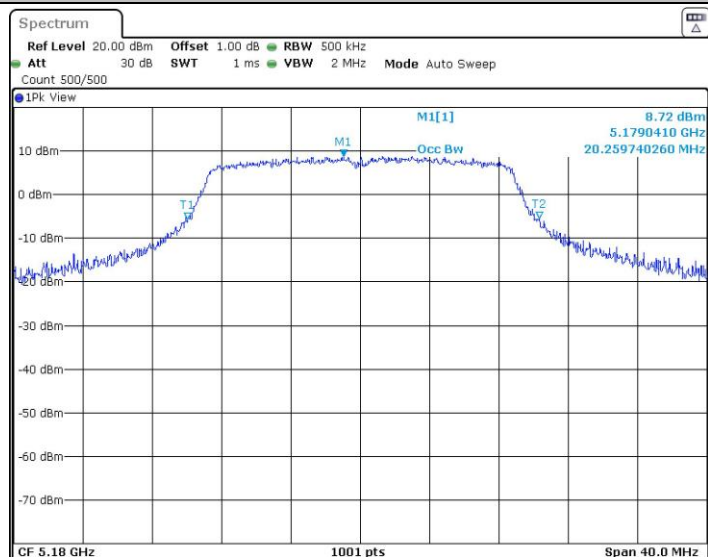
11N40SISO_Ant1_5670



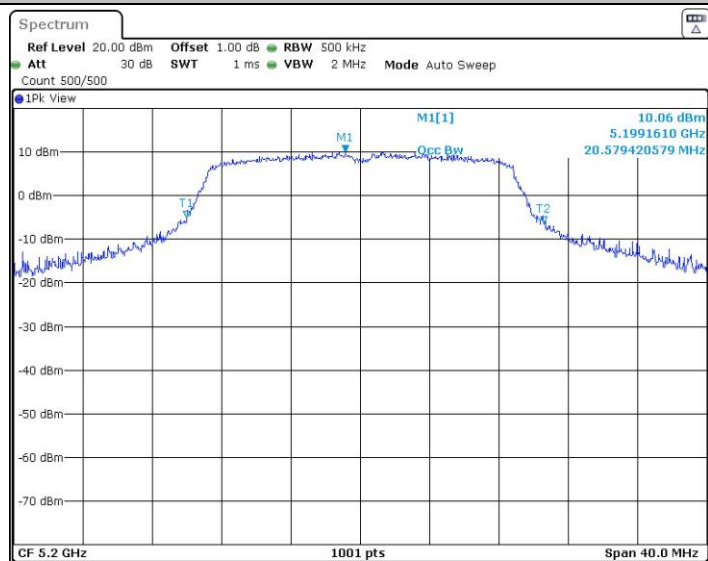
11N40SISO_Ant1_5755



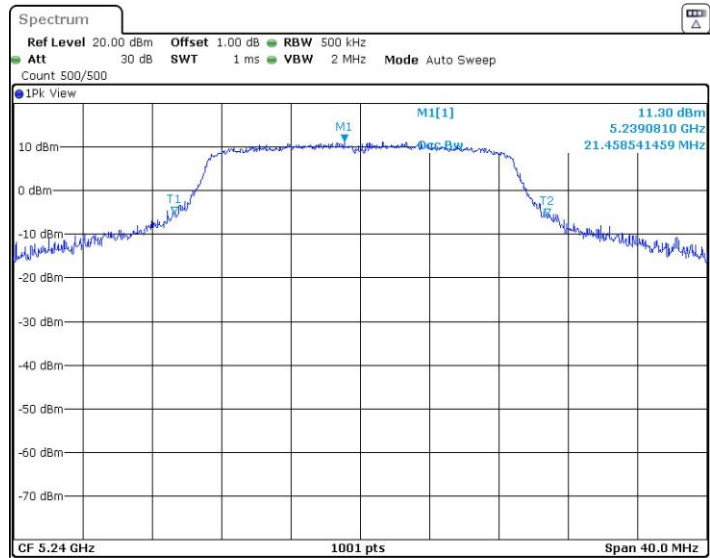
11N40SISO_Ant1_5795



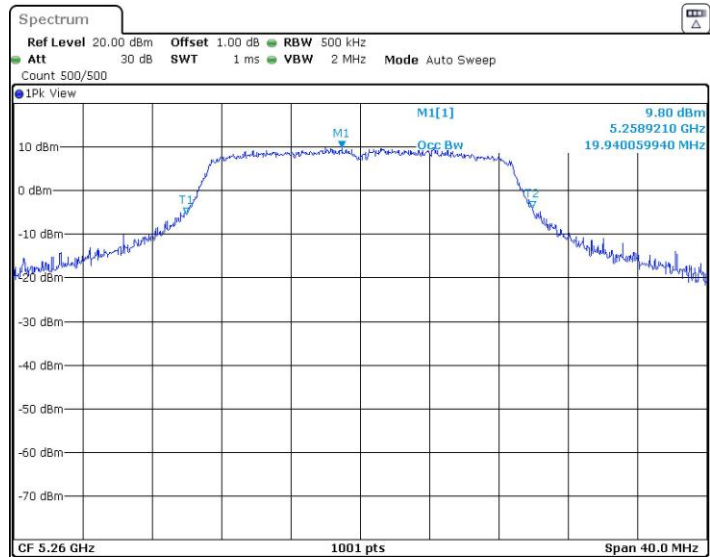
11AC20SISO_Ant1_5180



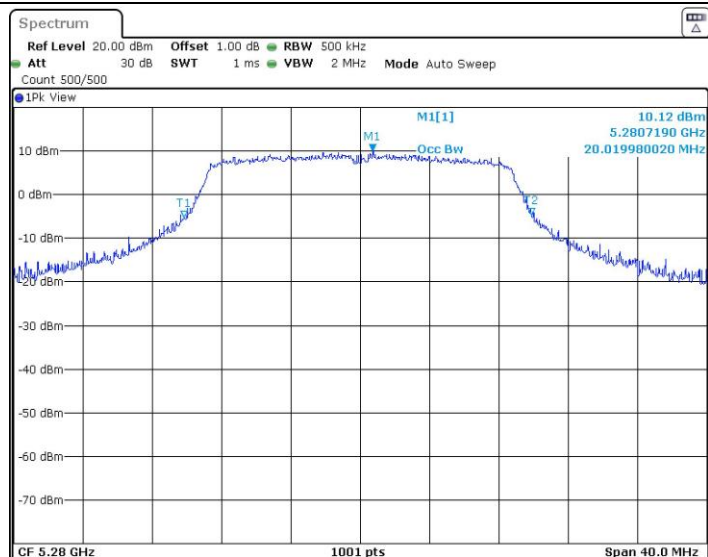
11AC20SISO_Ant1_5200



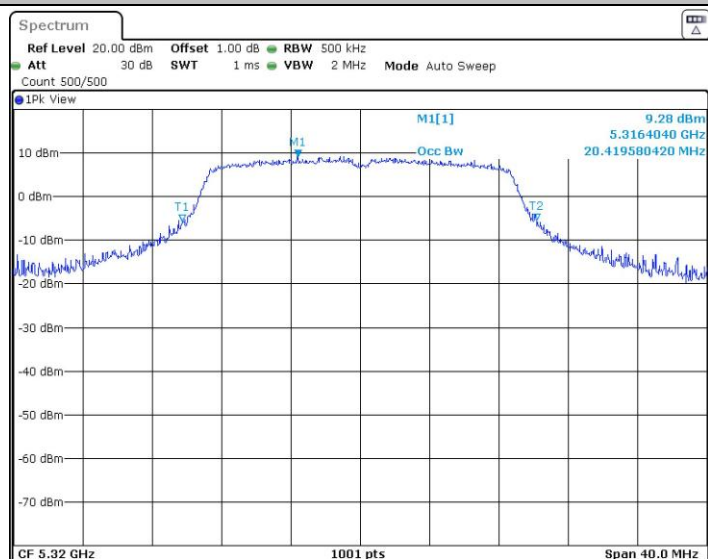
11AC20SISO_Ant1_5240



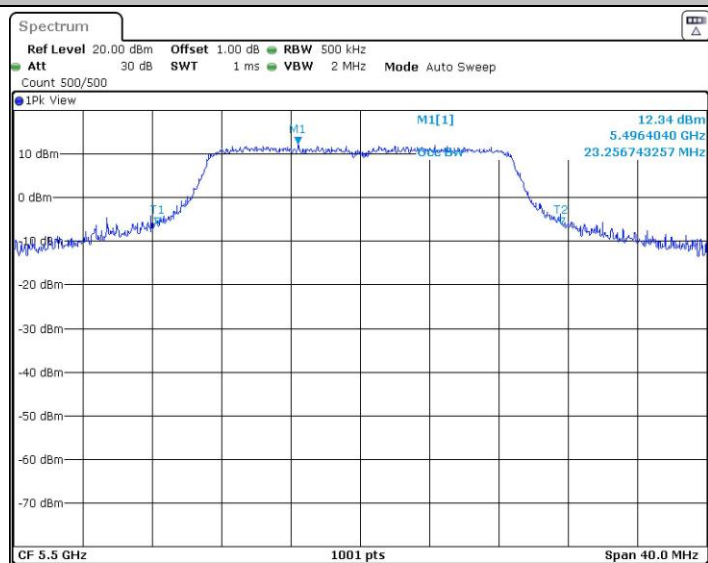
11AC20SISO_Ant1_5260



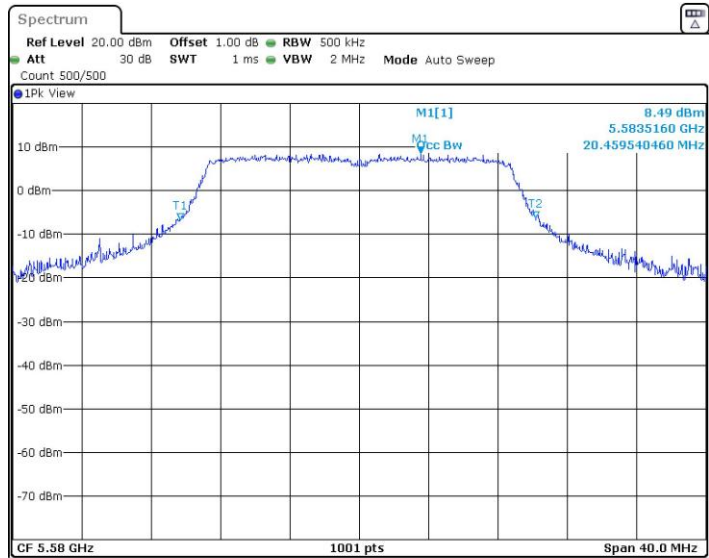
11AC20SISO_Ant1_5280



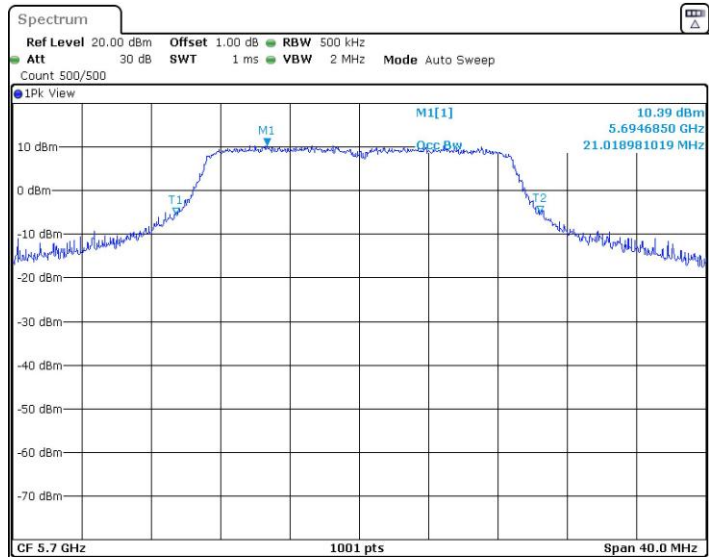
11AC20SISO_Ant1_5320



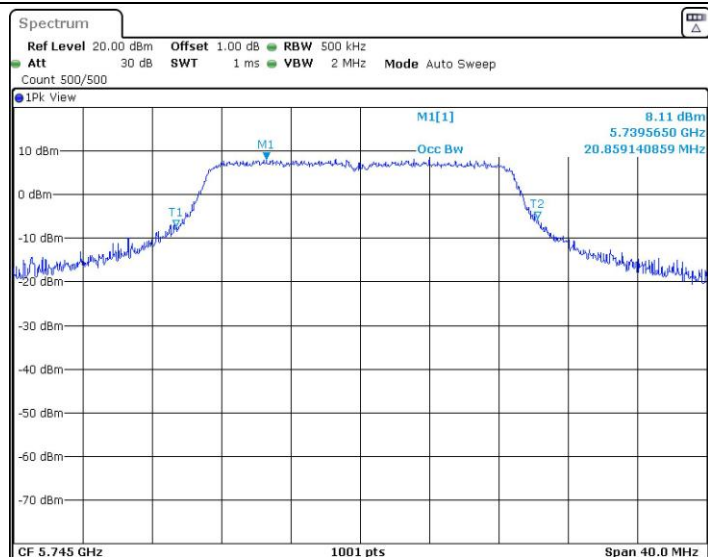
11AC20SISO_Ant1_5500



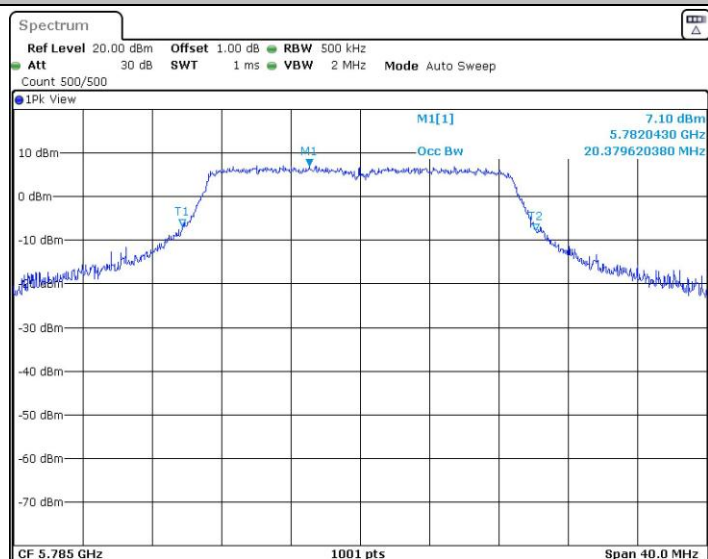
11AC20SISO_Ant1_5580



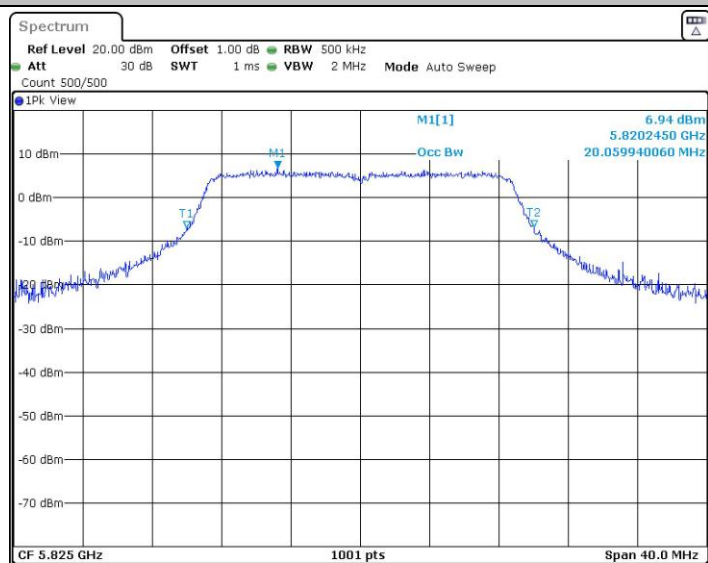
11AC20SISO_Ant1_5700



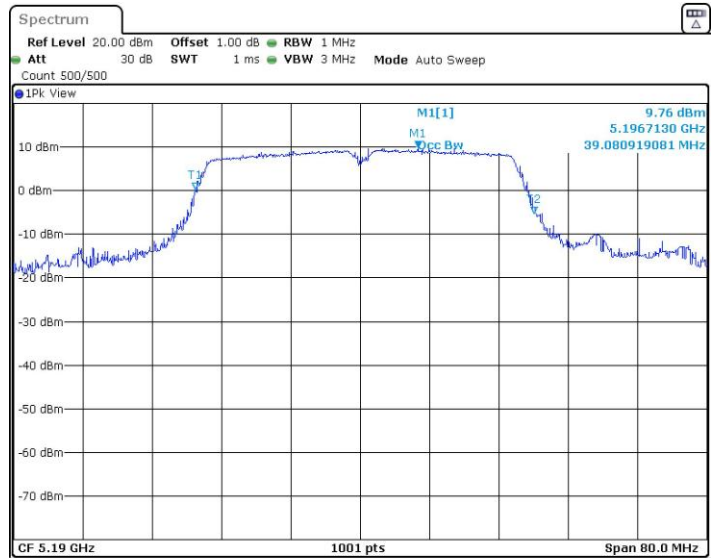
11AC20SISO_Ant1_5745



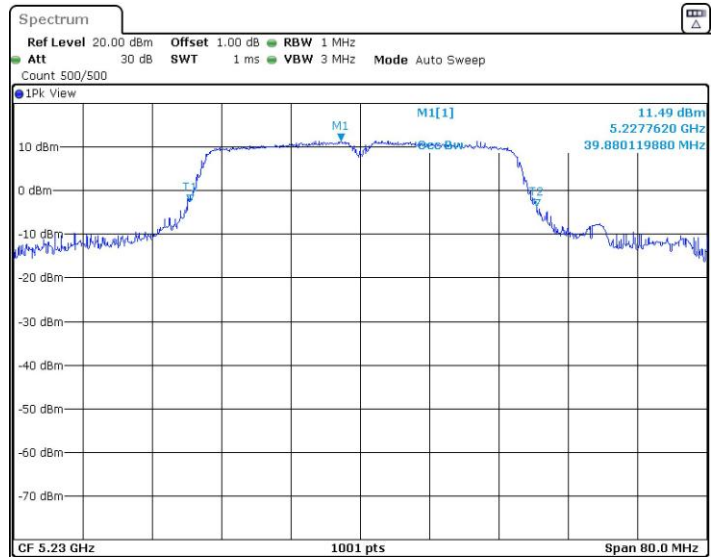
11AC20SISO_Ant1_5785



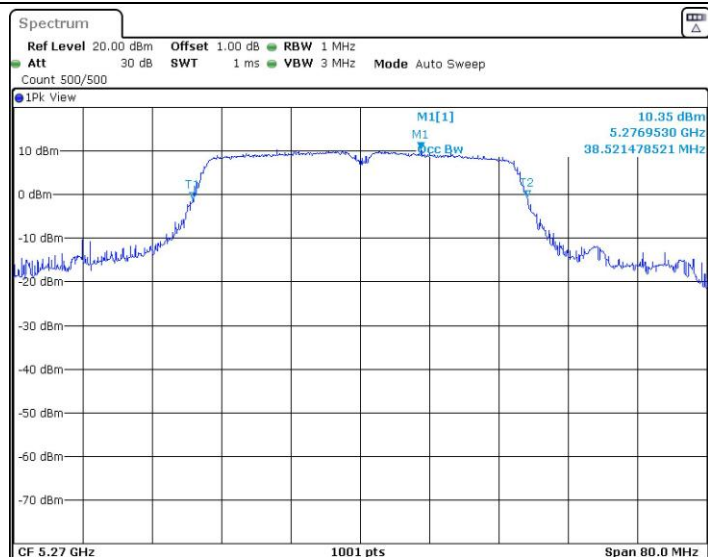
11AC20SISO_Ant1_5825



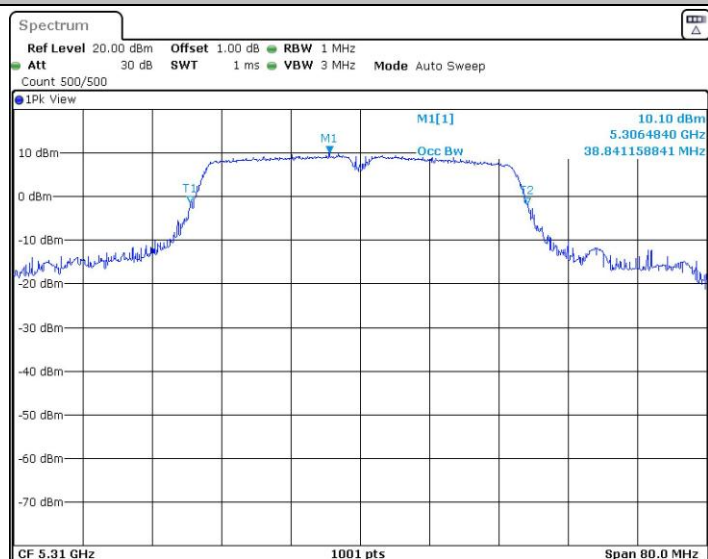
11AC40SISO_Ant1_5190



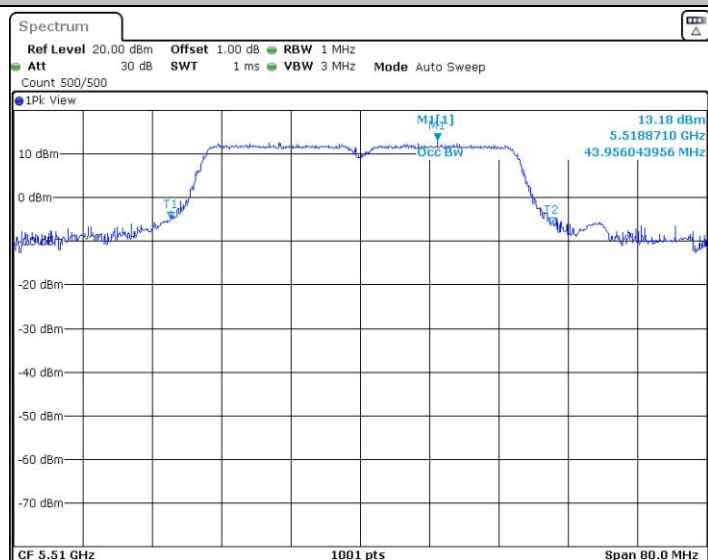
11AC40SISO_Ant1_5230

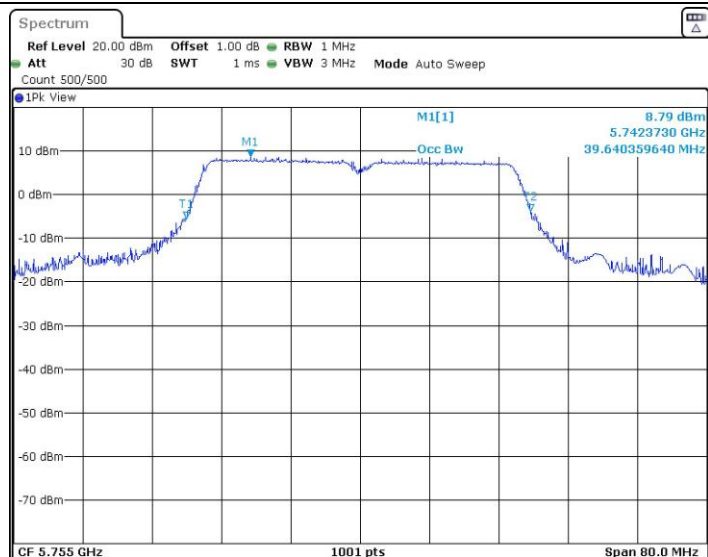


11AC40SISO_Ant1_5270

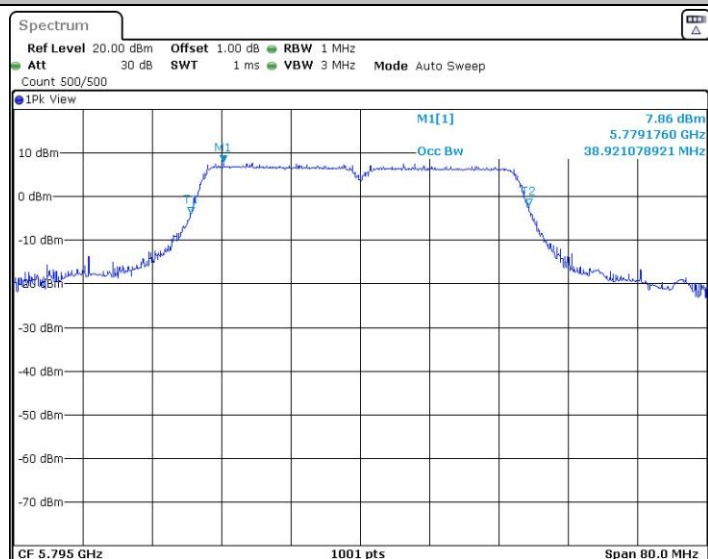


11AC40SISO_Ant1_5310

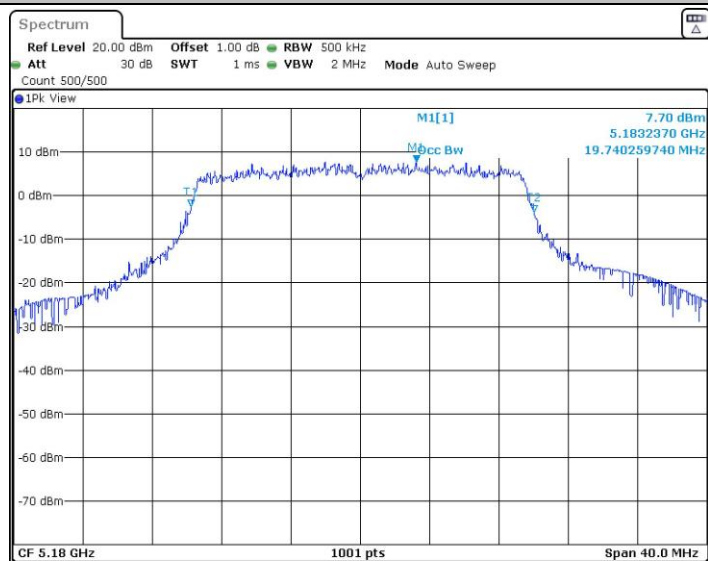




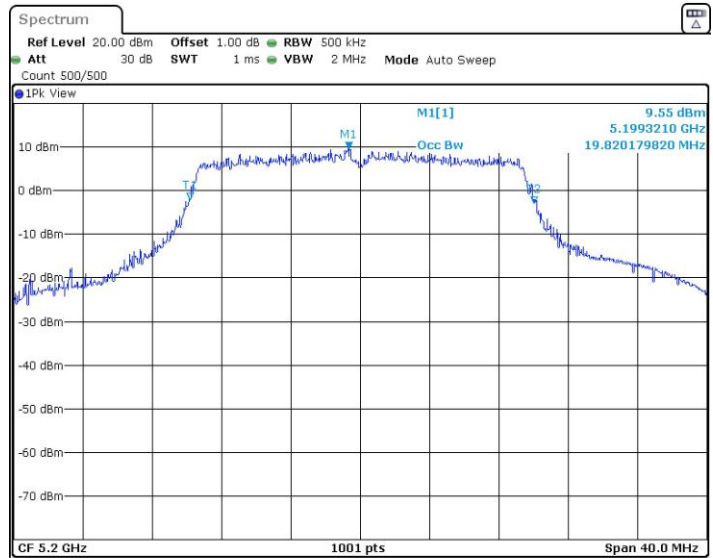
11AC40SISO_Ant1_5755



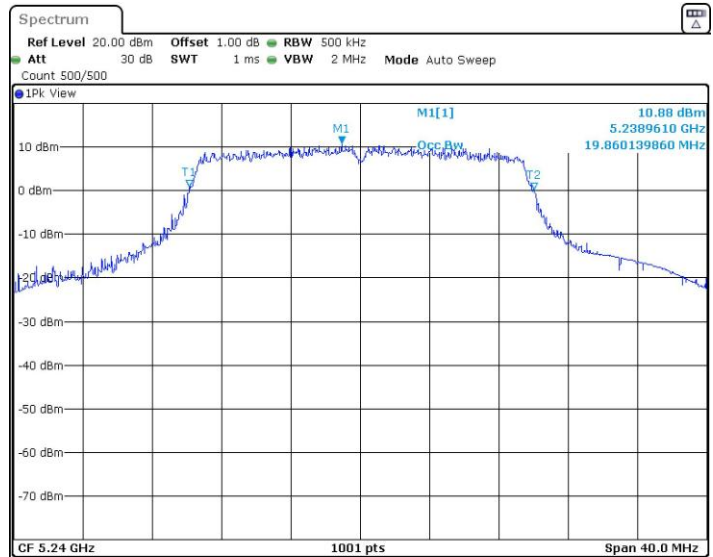
11AC40SISO_Ant1_5795



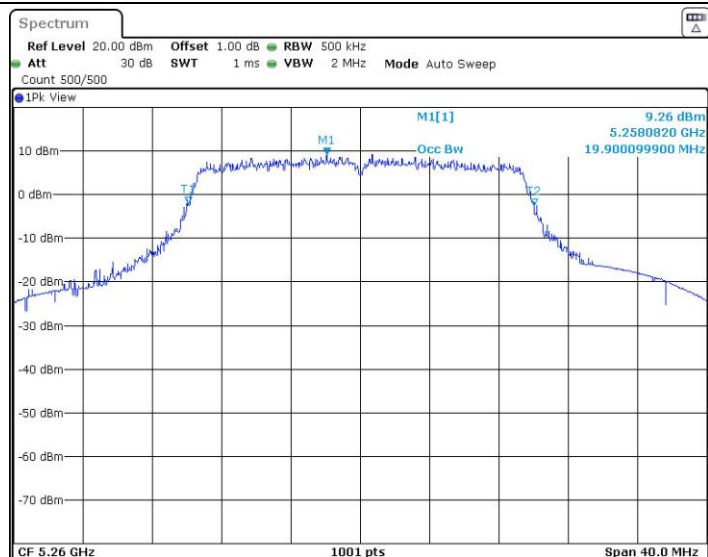
11AX20SISO_Ant1_5180



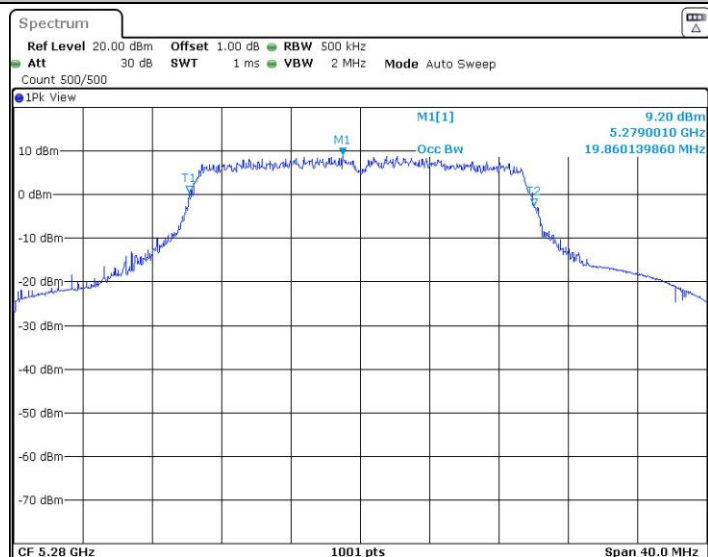
11AX20SISO_Ant1_5200



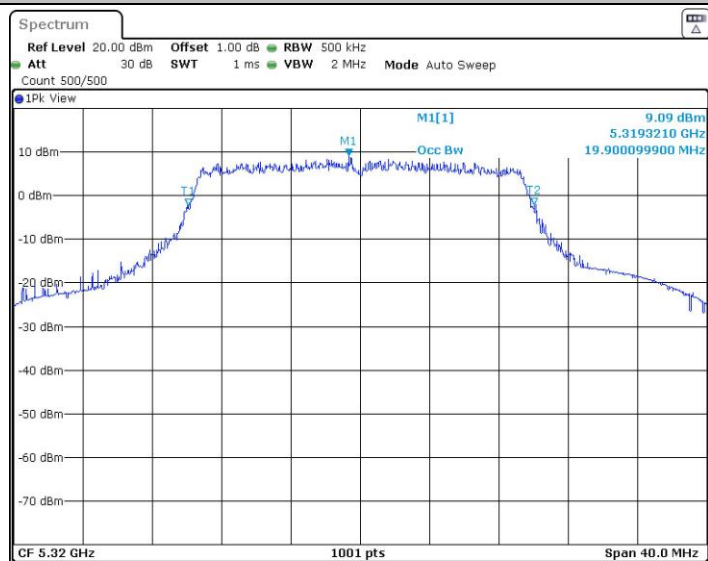
11AX20SISO_Ant1_5240



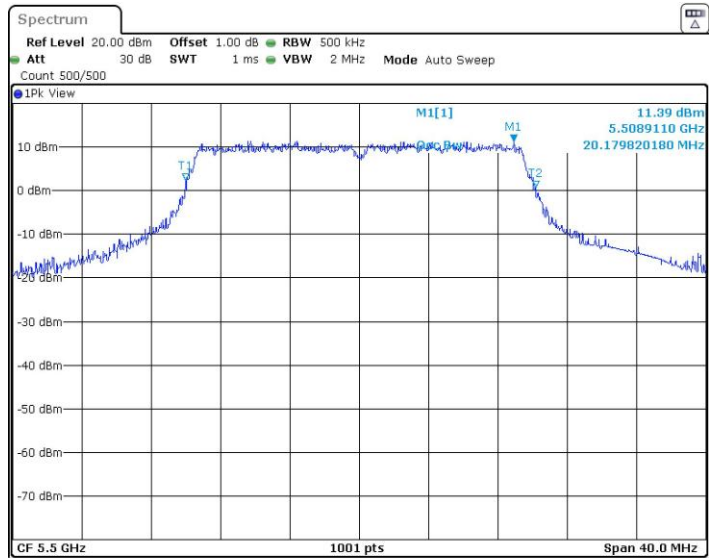
11AX20SISO_Ant1_5260



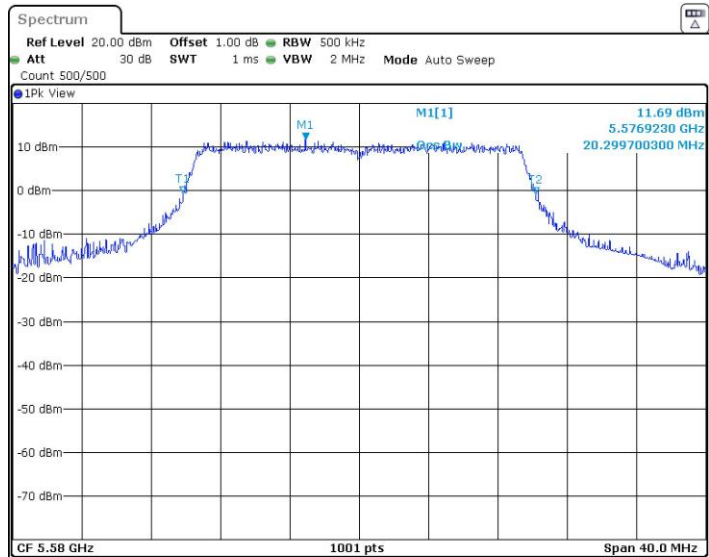
11AX20SISO_Ant1_5280



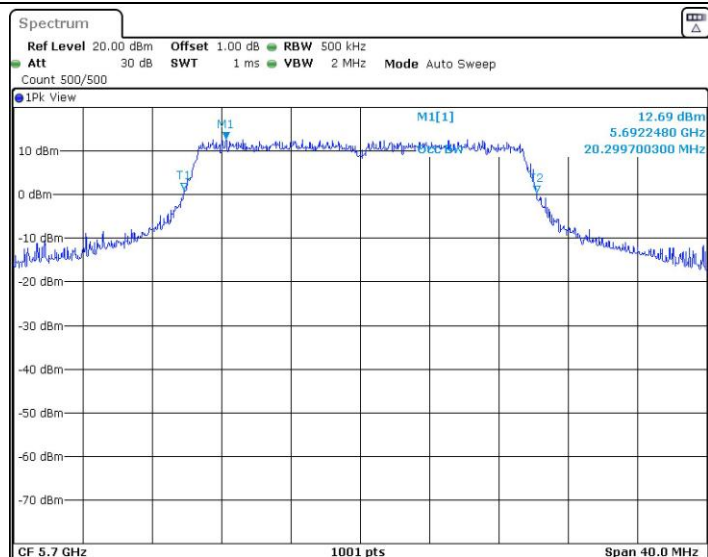
11AX20SISO_Ant1_5320



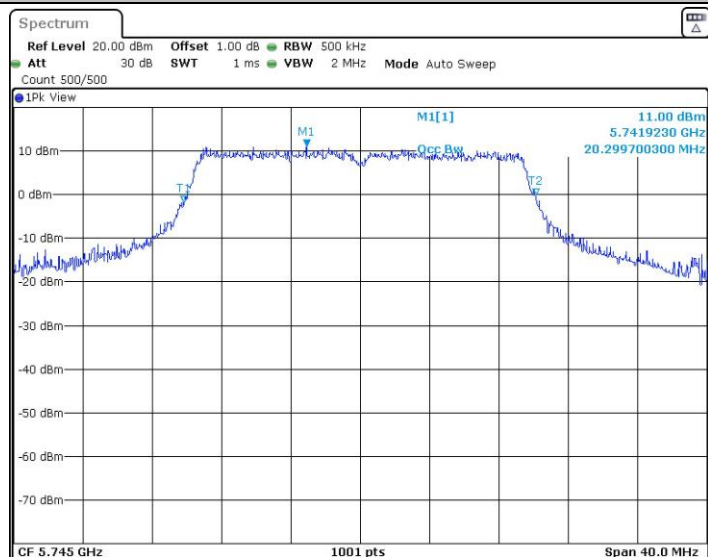
11AX20SISO_Ant1_5500



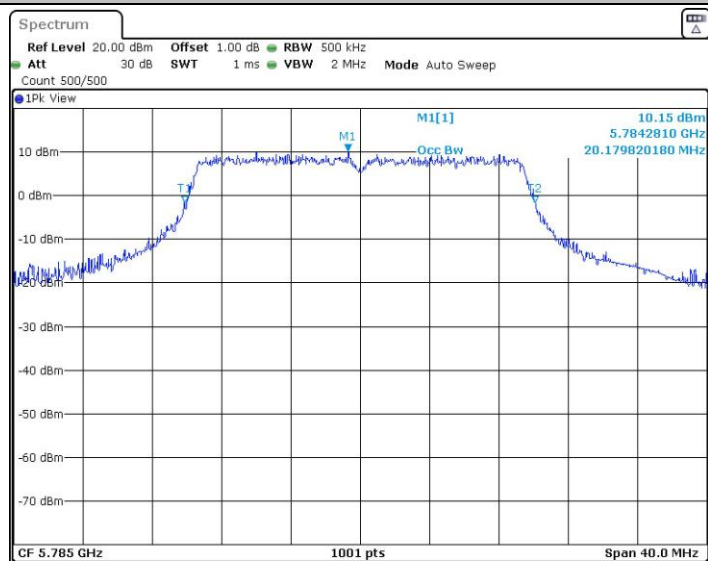
11AX20SISO_Ant1_5580



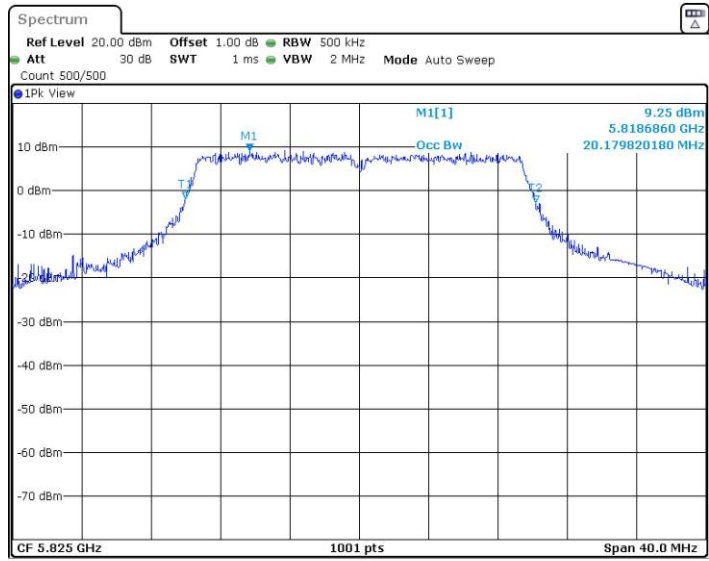
11AX20SISO_Ant1_5700



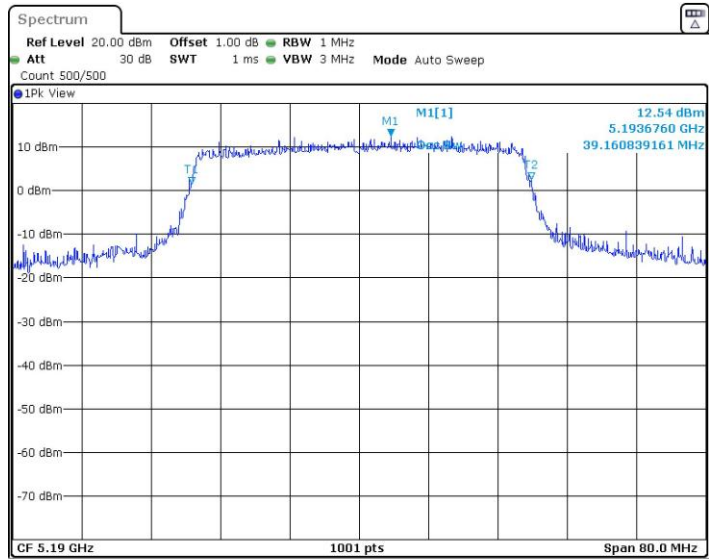
11AX20SISO_Ant1_5745



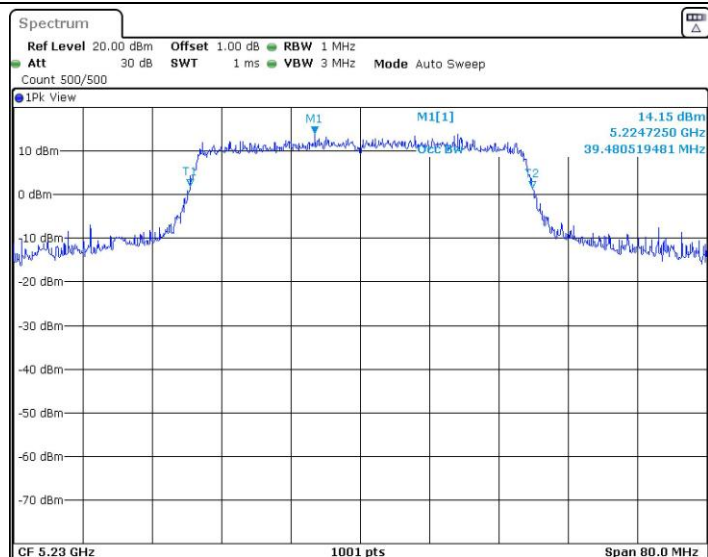
11AX20SISO_Ant1_5785



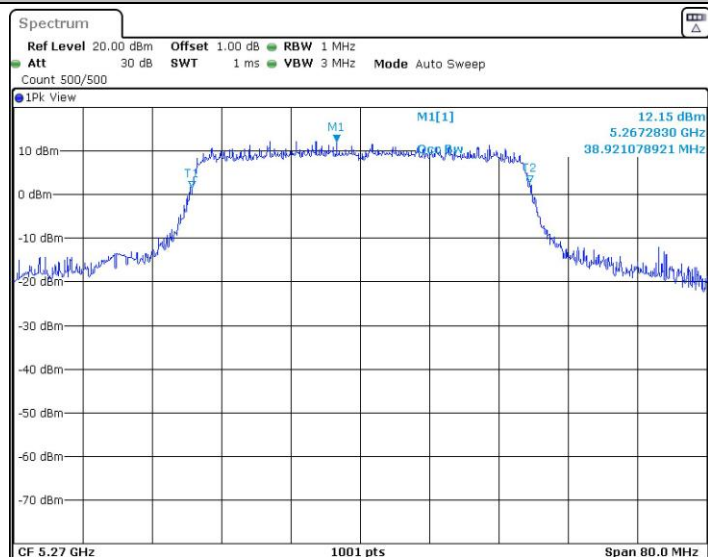
11AX20SISO_Ant1_5825



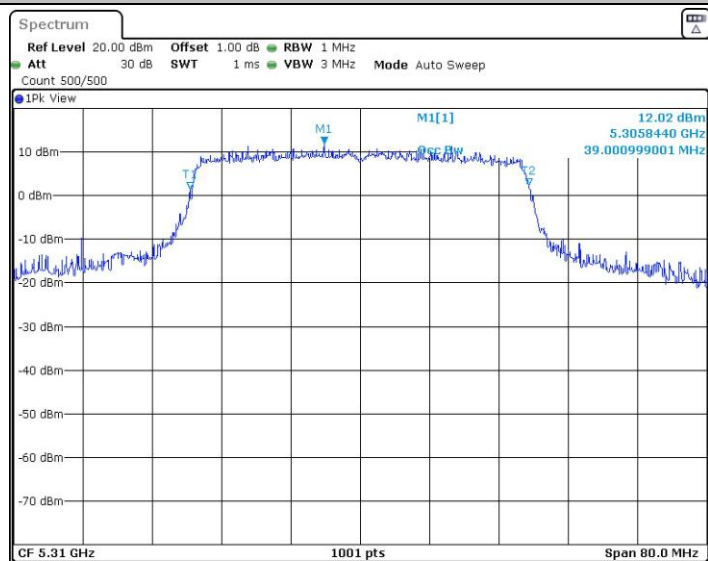
11AX40SISO_Ant1_5190



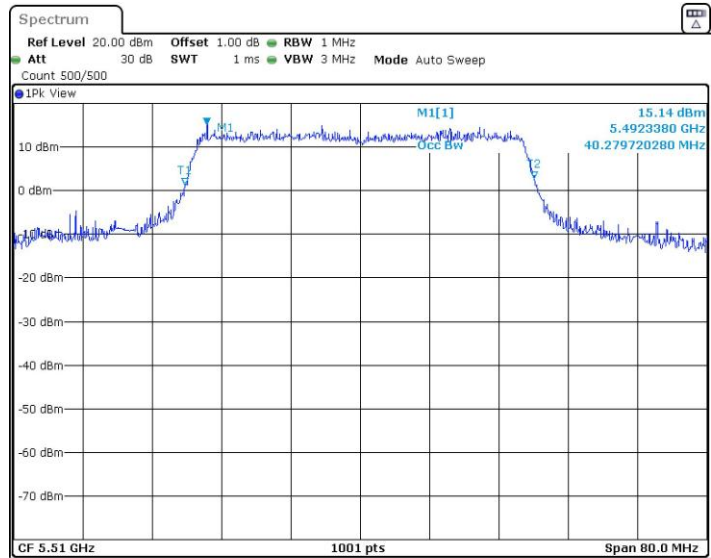
11AX40SISO_Ant1_5230



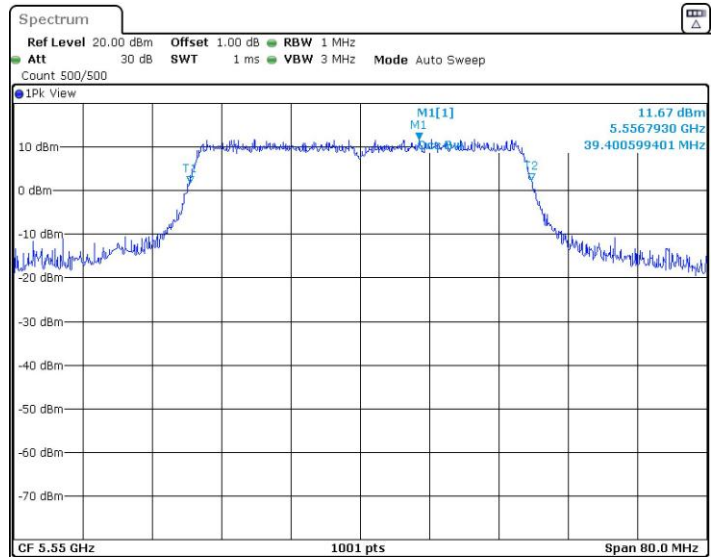
11AX40SISO_Ant1_5270



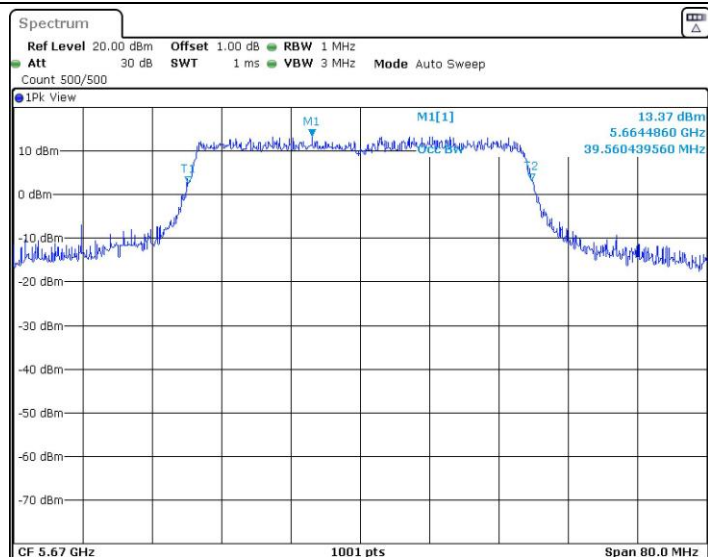
11AX40SISO_Ant1_5310



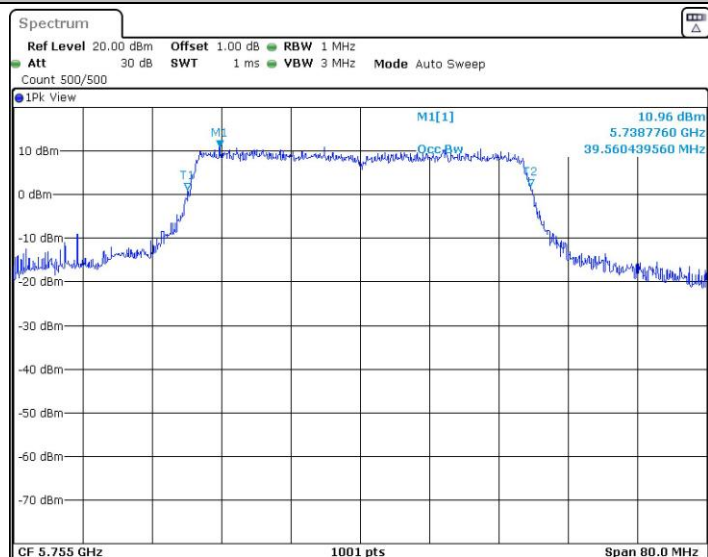
11AX40SISO_Ant1_5510



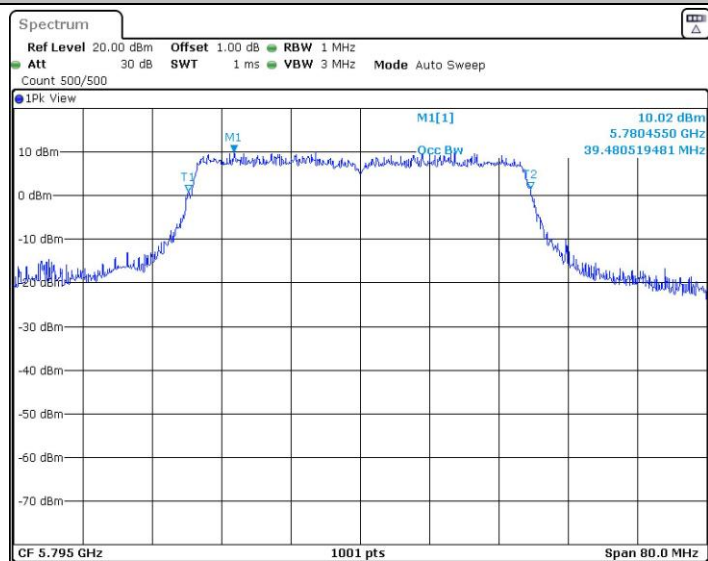
11AX40SISO_Ant1_5550



11AX40SISO_Ant1_5670



11AX40SISO_Ant1_5755



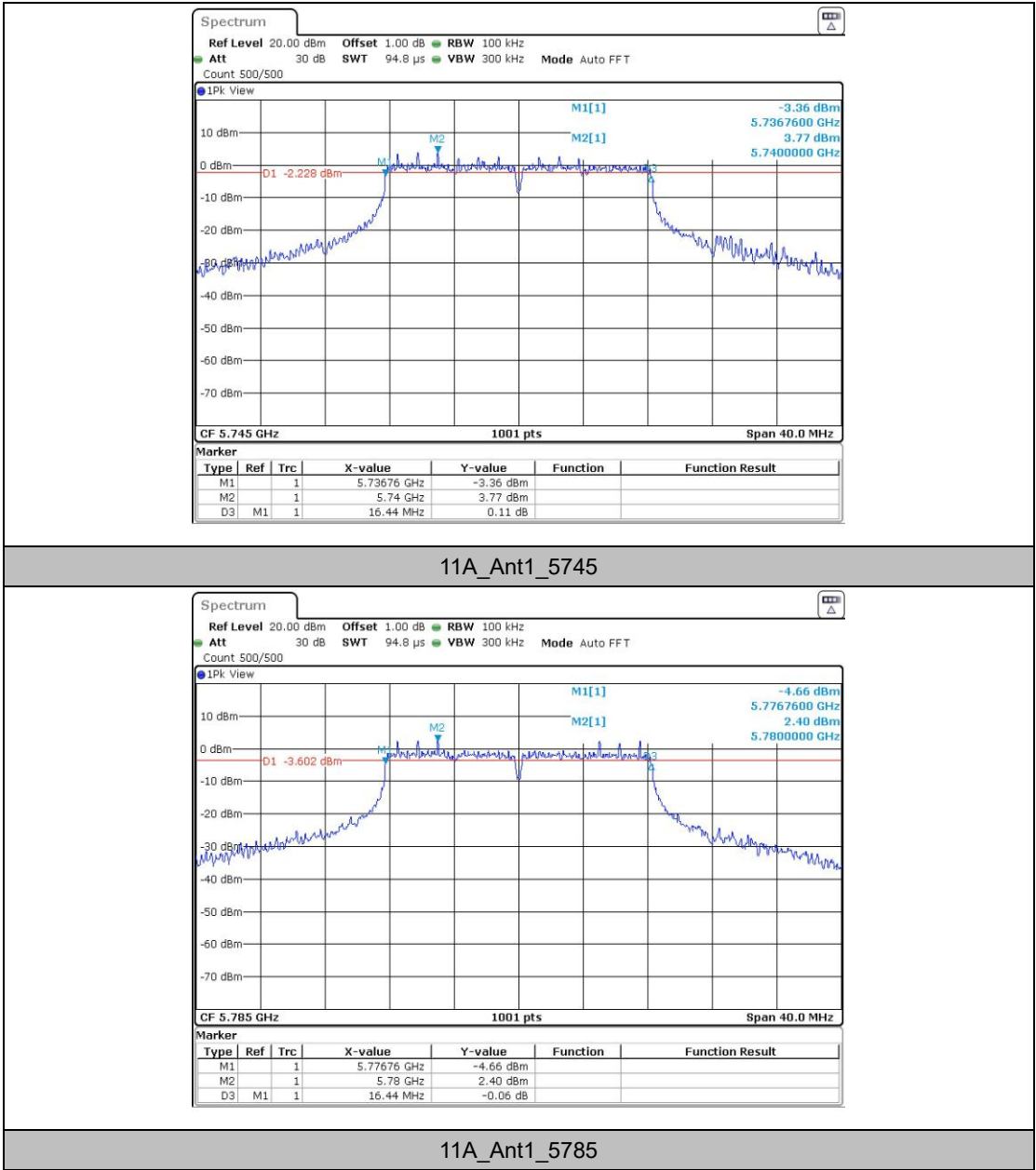
11AX40SISO_Ant1_5795

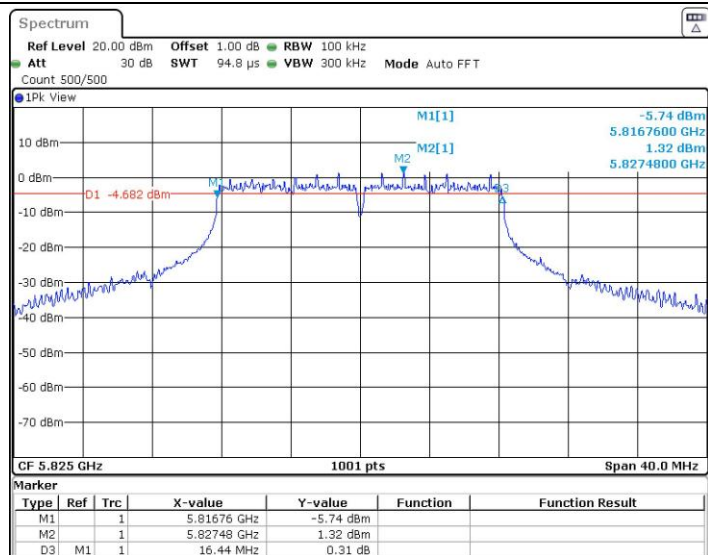
Appendix A3: Min emission bandwidth

Test Result

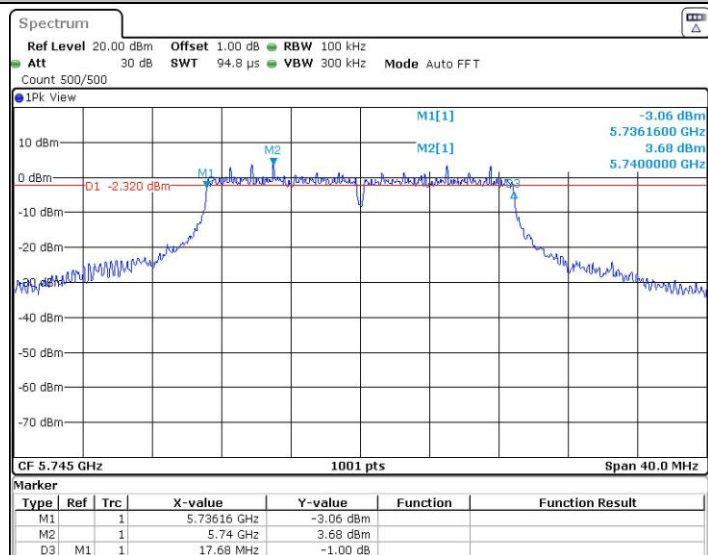
TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.440	5736.760	5753.200	0.5	PASS
		5785	16.440	5776.760	5793.200	0.5	PASS
		5825	16.440	5816.760	5833.200	0.5	PASS
11N20SISO	Ant1	5745	17.680	5736.160	5753.840	0.5	PASS
		5785	17.680	5776.160	5793.840	0.5	PASS
		5825	17.680	5816.160	5833.840	0.5	PASS
11N40SISO	Ant1	5755	36.480	5736.760	5773.240	0.5	PASS
		5795	36.480	5776.760	5813.240	0.5	PASS
11AC20SISO	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
		5785	17.680	5776.160	5793.840	0.5	PASS
		5825	17.680	5816.160	5833.840	0.5	PASS
11AC40SISO	Ant1	5755	36.480	5736.760	5773.240	0.5	PASS
		5795	36.480	5776.760	5813.240	0.5	PASS
11AX20SISO	Ant1	5745	19.120	5735.440	5754.560	0.5	PASS
		5785	19.080	5775.440	5794.520	0.5	PASS
		5825	18.960	5815.480	5834.440	0.5	PASS
11AX40SISO	Ant1	5755	38.240	5735.880	5774.120	0.5	PASS
		5795	38.240	5775.880	5814.120	0.5	PASS

Test Graphs

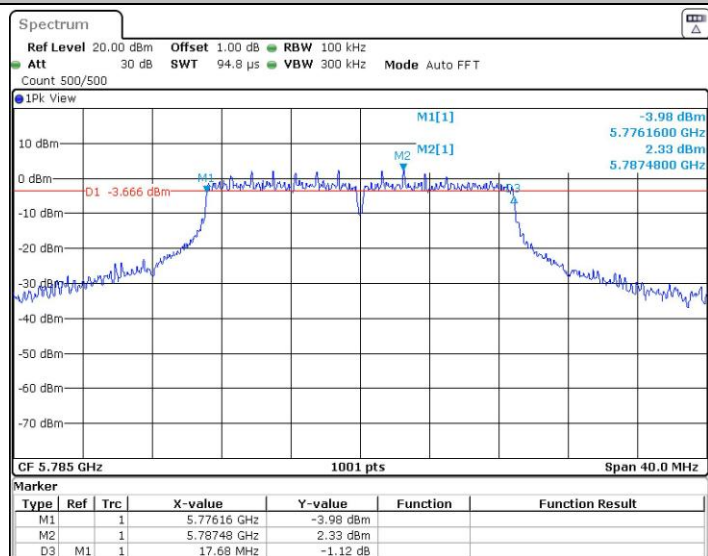




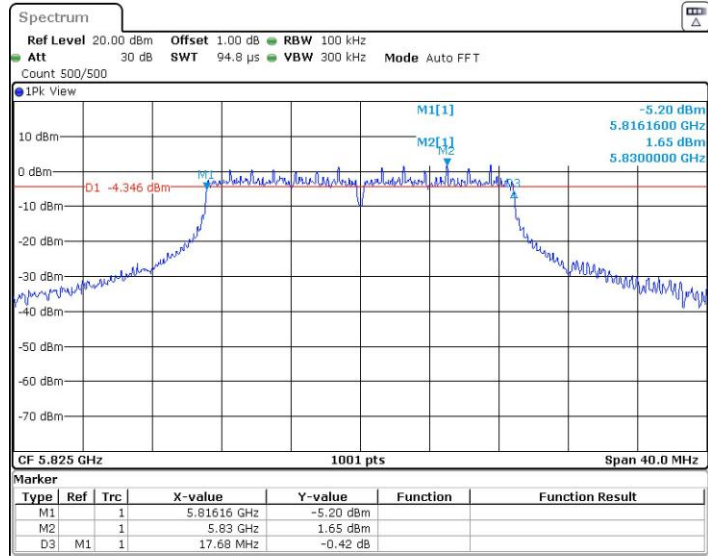
11A_Ant1_5825



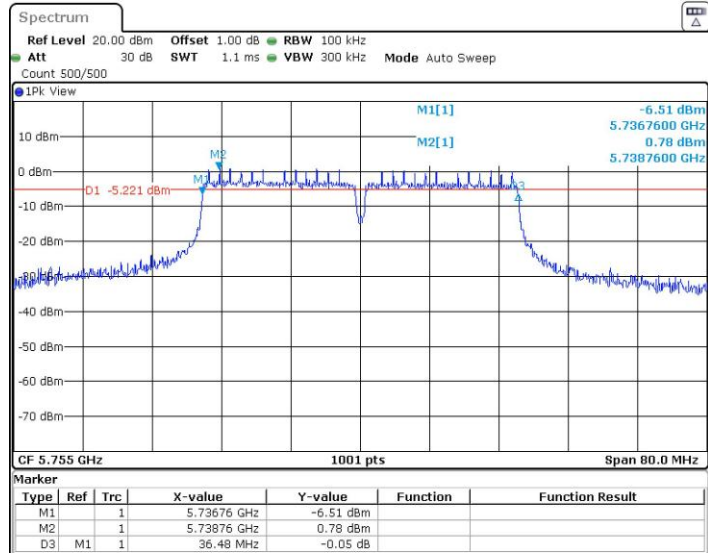
11N20SISO_Ant1_5745



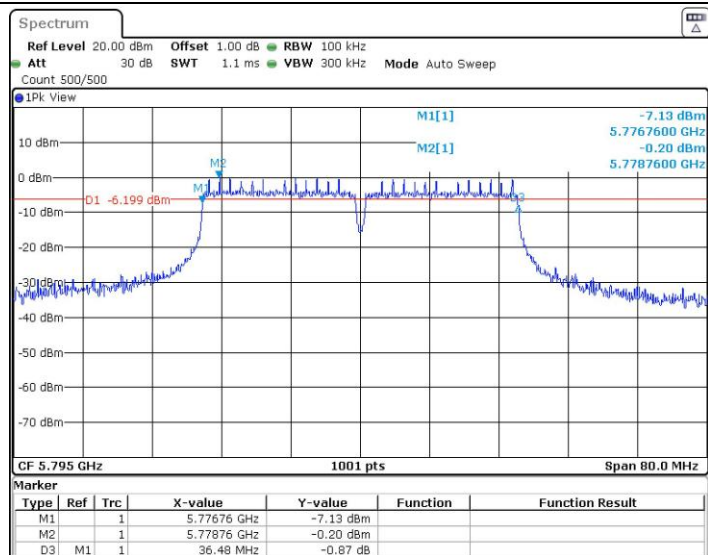
11N20SISO_Ant1_5785



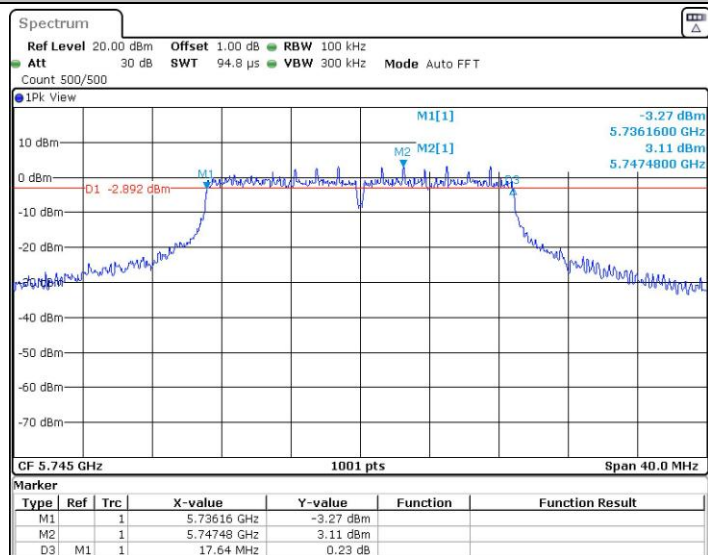
11N20SISO_Ant1_5825



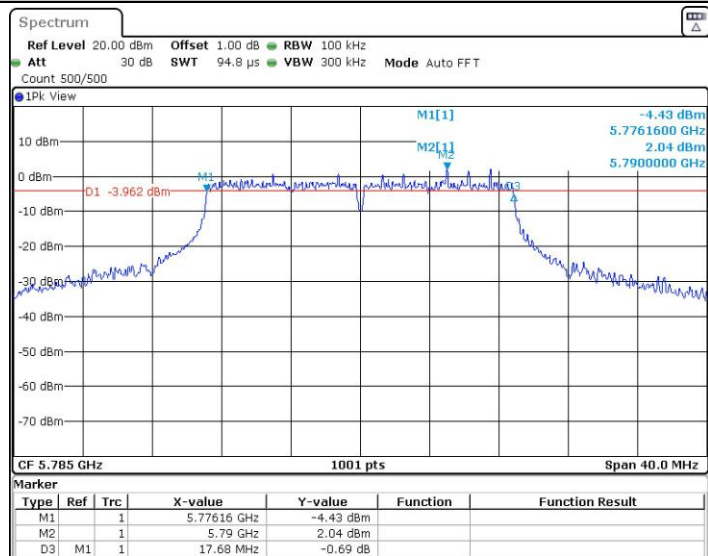
11N40SISO_Ant1_5755



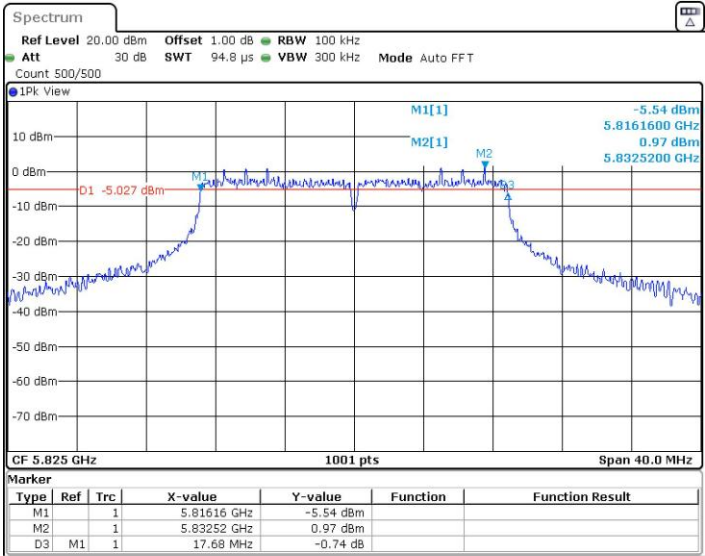
11N40SISO_Ant1_5795



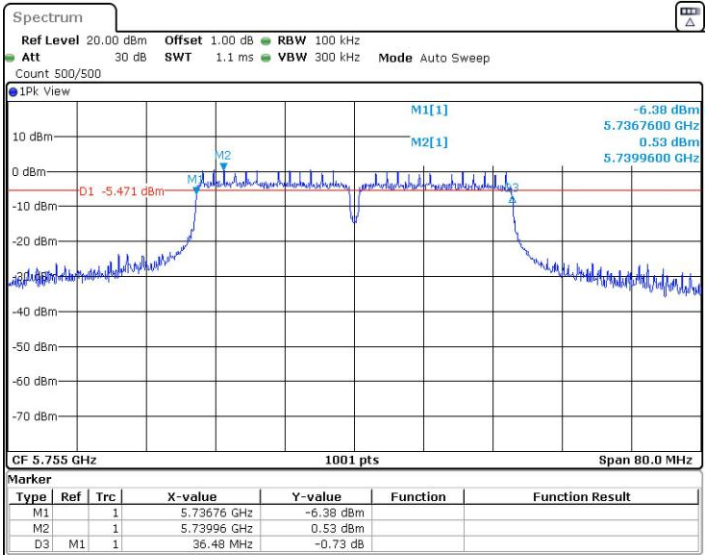
11AC20SISO_Ant1_5745



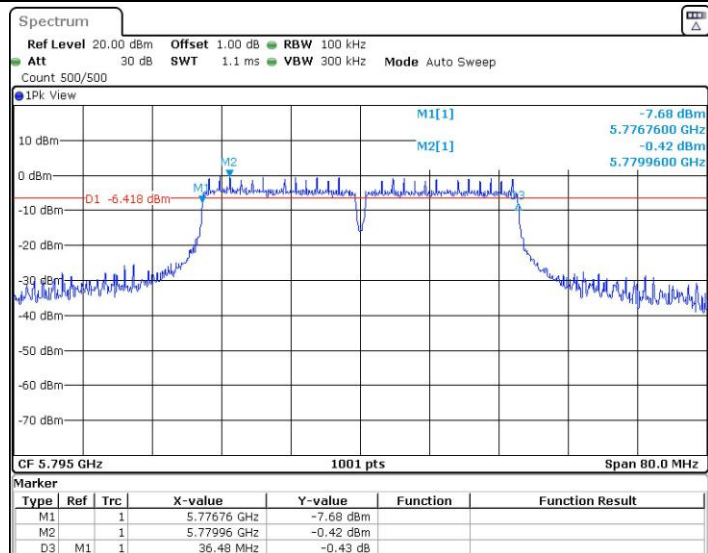
11AC20SISO_Ant1_5785



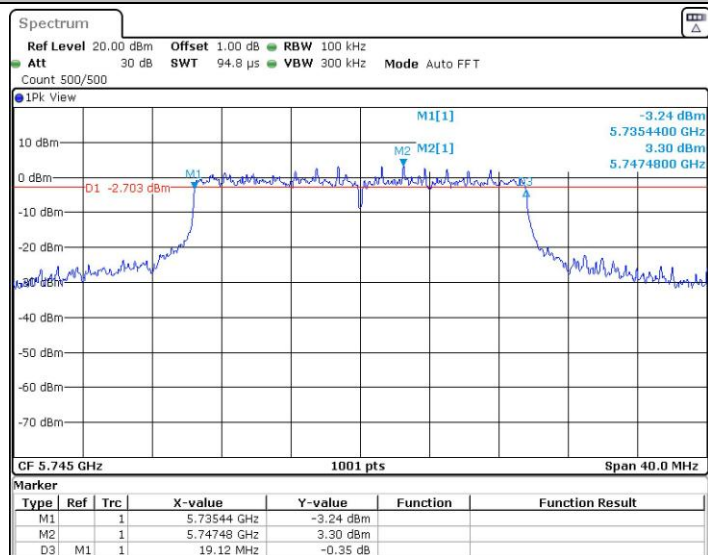
11AC20SISO_Ant1_5825



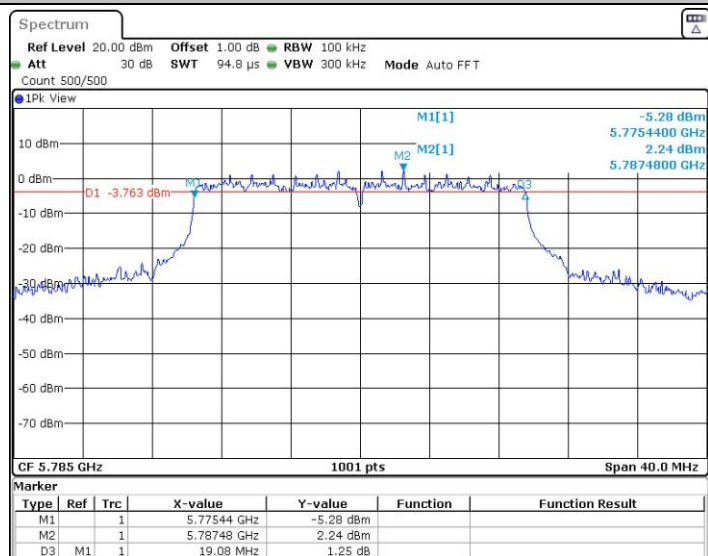
11AC40SISO_Ant1_5755



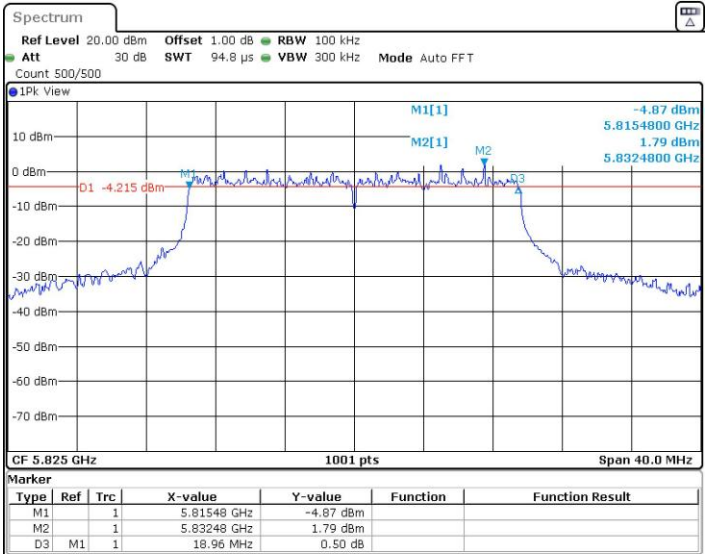
11AC40SISO_Ant1_5795



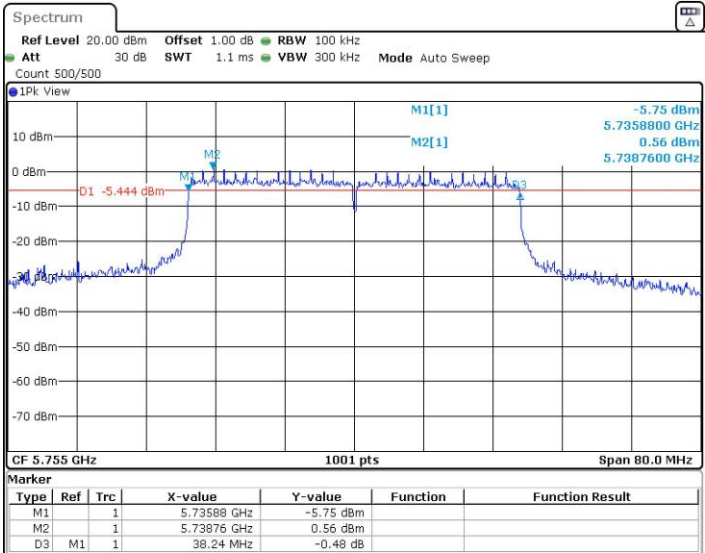
11AX20SISO_Ant1_5745



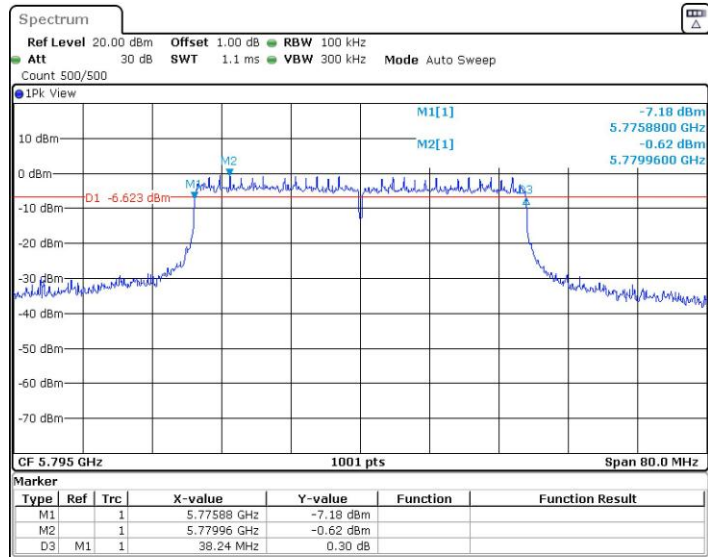
11AX20SISO_Ant1_5785



11AX20SISO_Ant1_5825



11AX40SISO_Ant1_5755



11AX40SISO_Ant1_5795

Appendix B1: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	12.12	<=23.98	PASS
		5200	13.48	<=23.98	PASS
		5240	14.29	<=23.98	PASS
		5260	12.84	<=23.98	PASS
		5280	12.66	<=23.98	PASS
		5320	12.14	<=23.98	PASS
		5500	10.67	<=23.98	PASS
		5580	7.74	<=23.98	PASS
		5700	9.21	<=23.98	PASS
		5745	15.04	<=30	PASS
		5785	14.01	<=30	PASS
		5825	12.92	<=30	PASS
11N20SISO	Ant1	5180	12.46	<=23.98	PASS
		5200	13.27	<=23.98	PASS
		5240	14.15	<=23.98	PASS
		5260	12.84	<=23.98	PASS
		5280	12.54	<=23.98	PASS
		5320	12.30	<=23.98	PASS
		5500	10.48	<=23.98	PASS
		5580	7.53	<=23.98	PASS
		5700	8.72	<=23.98	PASS
		5745	15.23	<=30	PASS
		5785	13.94	<=30	PASS
		5825	13.82	<=30	PASS
11N40SISO	Ant1	5190	10.13	<=23.98	PASS
		5230	12.31	<=23.98	PASS
		5270	10.50	<=23.98	PASS
		5310	10.08	<=23.98	PASS
		5510	8.72	<=23.98	PASS
		5550	8.32	<=23.98	PASS
		5670	7.29	<=23.98	PASS
		5755	14.07	<=30	PASS
		5795	13.70	<=30	PASS
11AC20SISO	Ant1	5180	11.97	<=23.98	PASS
		5200	12.61	<=23.98	PASS
		5240	13.71	<=23.98	PASS
		5260	12.60	<=23.98	PASS
		5280	12.50	<=23.98	PASS

		5320	12.17	<=23.98	PASS
		5500	10.11	<=23.98	PASS
		5580	7.19	<=23.98	PASS
		5700	8.78	<=23.98	PASS
		5745	14.61	<=30	PASS
		5785	13.53	<=30	PASS
		5825	12.65	<=30	PASS
11AC40SISO	Ant1	5190	10.31	<=23.98	PASS
		5230	11.76	<=23.98	PASS
		5270	9.40	<=23.98	PASS
		5310	9.75	<=23.98	PASS
		5510	8.06	<=23.98	PASS
		5550	8.29	<=23.98	PASS
		5670	6.46	<=23.98	PASS
		5755	14.24	<=30	PASS
		5795	13.23	<=30	PASS
11AX20SISO	Ant1	5180	12.40	<=23.98	PASS
		5200	13.27	<=23.98	PASS
		5240	14.09	<=23.98	PASS
		5260	12.78	<=23.98	PASS
		5280	12.47	<=23.98	PASS
		5320	12.20	<=23.98	PASS
		5500	10.59	<=23.98	PASS
		5580	7.26	<=23.98	PASS
		5700	8.84	<=23.98	PASS
		5745	14.78	<=30	PASS
		5785	13.87	<=30	PASS
		5825	13.20	<=30	PASS
11AX40SISO	Ant1	5190	8.50	<=23.98	PASS
		5230	8.45	<=23.98	PASS
		5270	9.42	<=23.98	PASS
		5310	7.45	<=23.98	PASS
		5510	8.27	<=23.98	PASS
		5550	8.33	<=23.98	PASS
		5670	7.10	<=23.98	PASS
		5755	14.36	<=30	PASS
		5795	14.15	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.