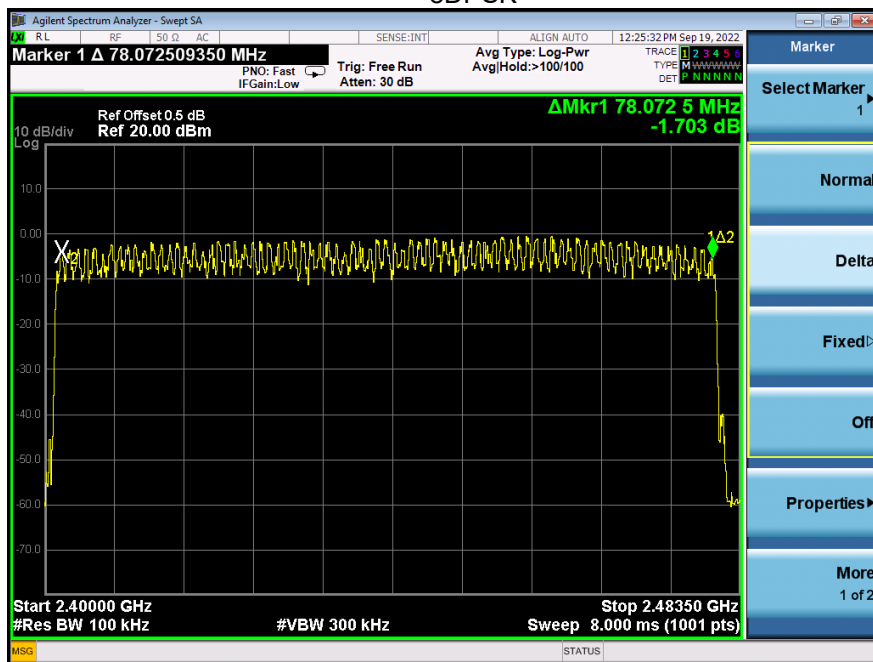


8DPSK



14. Dwell Time

14.1 Block Diagram Of Test Setup



14.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

14.3 Test Procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0. Centred on a hopping channel;
3. Set RBW = 1MHz and VBW = 3MHz. Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

14.4 Test Result

DH5 Packet permit maximum 1600 / 79 / 6 hops per second in each channel (5 time slots RX, 1 time slot TX).

DH3 Packet permit maximum 1600 / 79 / 4 hops per second in each channel (3 time slots RX, 1 time slot TX).

DH1 Packet permit maximum 1600 / 79 / 2 hops per second in each channel (1 time slot RX, 1 time slot TX). So, the Dwell Time can be calculated as follows:

DH5: $1600/79/6 \times 0.4 \times 79 \times (\text{MkrDelta})/1000$

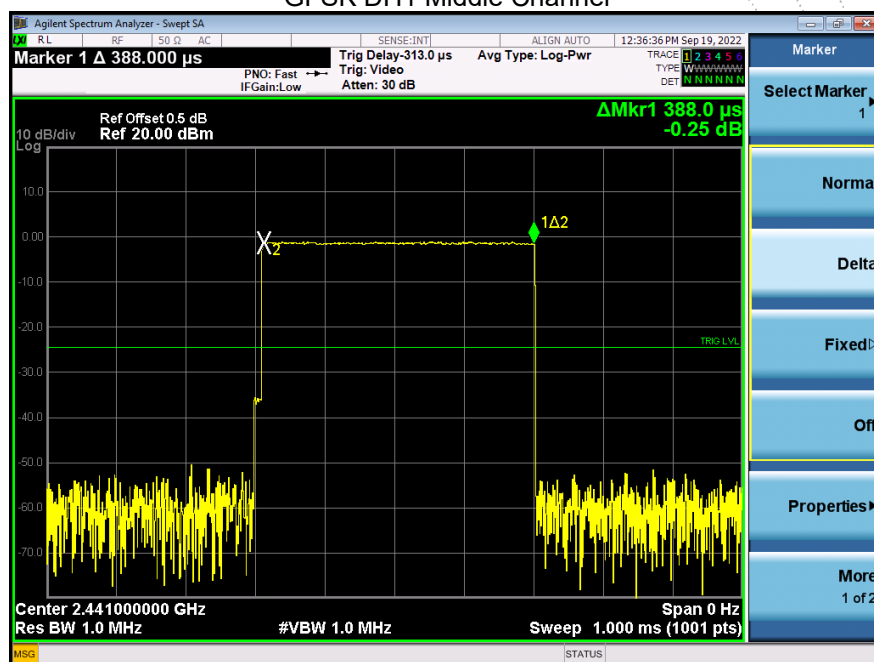
DH3: $1600/79/4 \times 0.4 \times 79 \times (\text{MkrDelta})/1000$

DH1: $1600/79/2 \times 0.4 \times 79 \times (\text{MkrDelta})/1000$

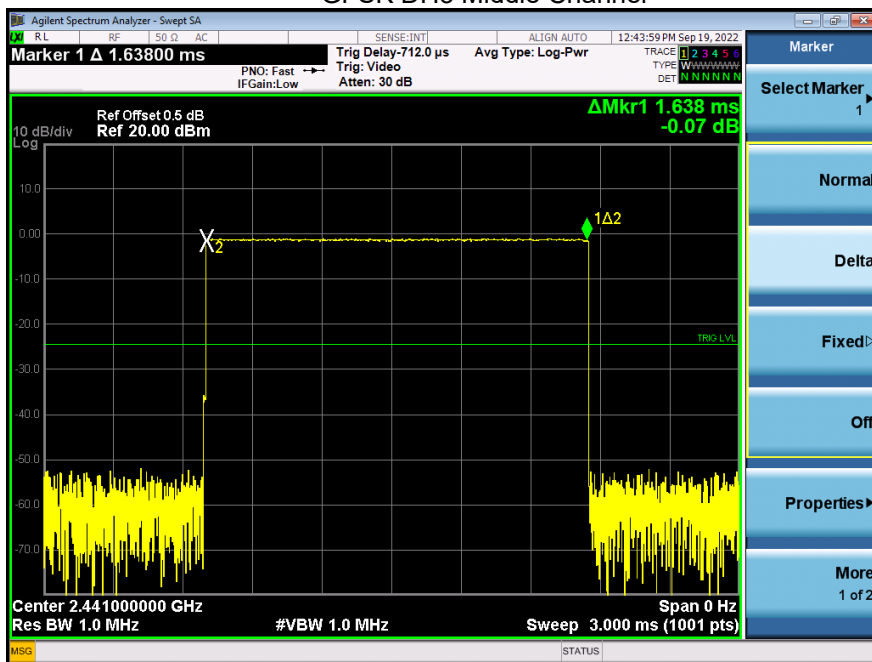
Remark: Mkr Delta is once pulse time.

Modulation	Channel Data	Packet	pulse time(ms)	Dwell Time(s)	Limits(s)
GFSK	Middle	DH1	0.388	0.124	0.4
		DH3	1.638	0.262	0.4
		DH5	2.885	0.308	0.4
$\pi/4$ DQPSK	Middle	2DH1	0.382	0.122	0.4
		2DH3	1.632	0.261	0.4
		2DH5	2.870	0.306	0.4
8DPSK	Middle	3DH1	0.380	0.122	0.4
		3DH3	1.626	0.260	0.4
		3DH5	2.865	0.306	0.4

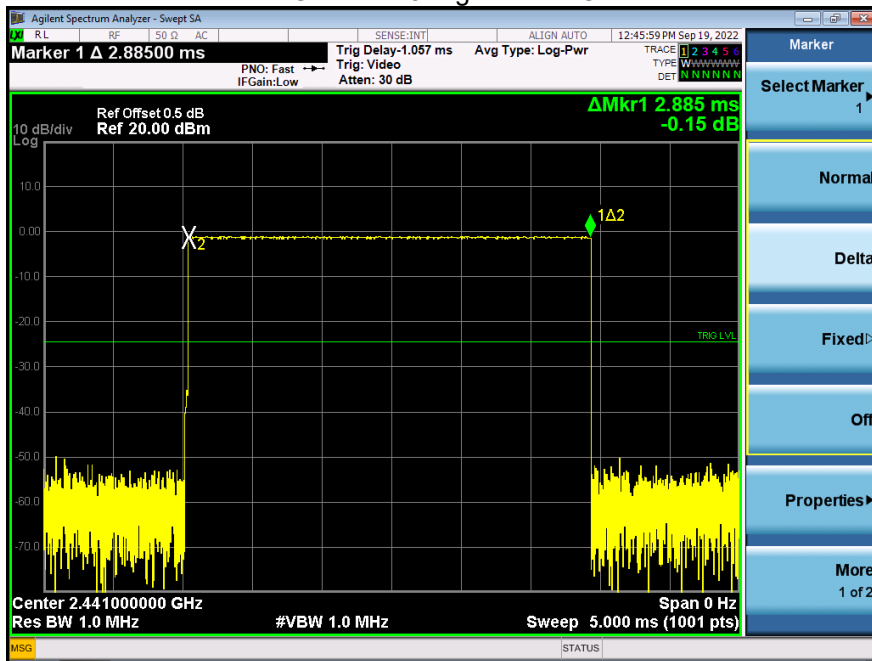
Test Plots
GFSK DH1 Middle Channel

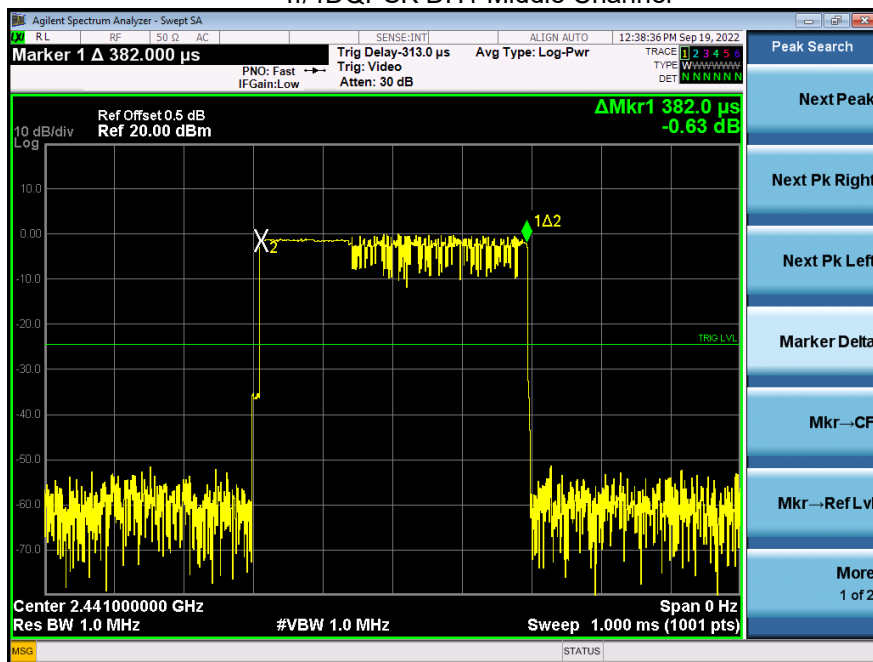
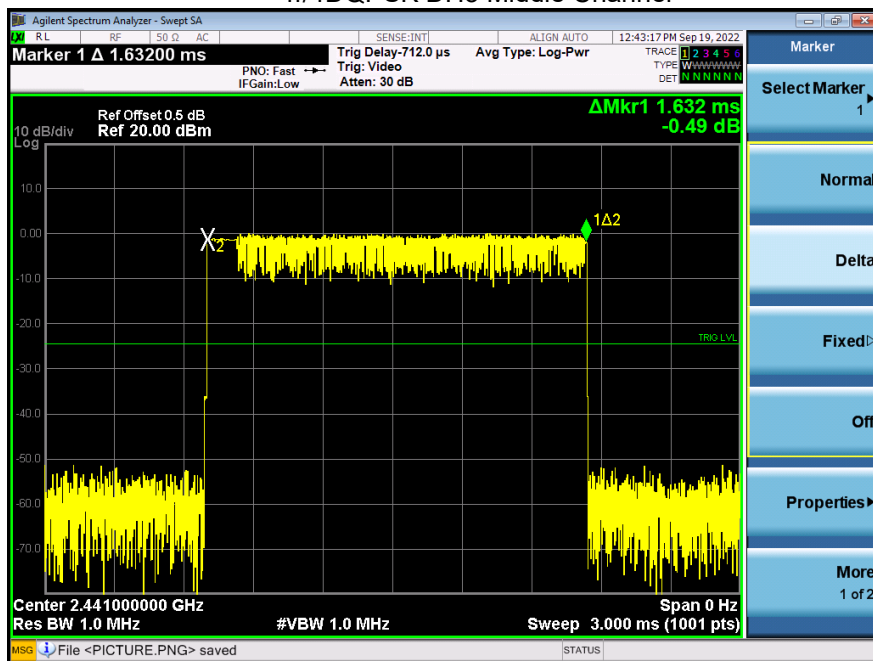


GFSK DH3 Middle Channel

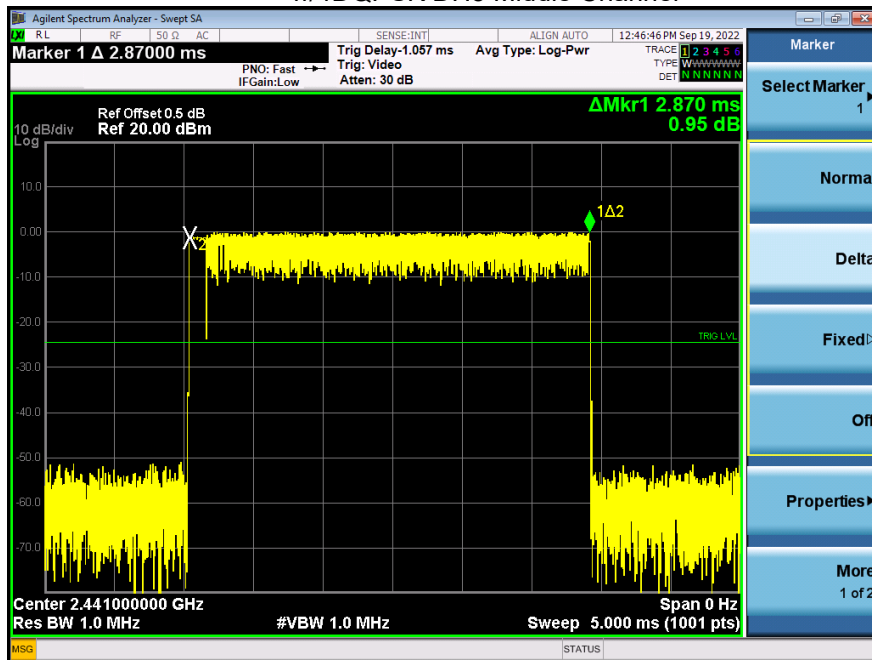


GFSK DH5 High Middle Channel

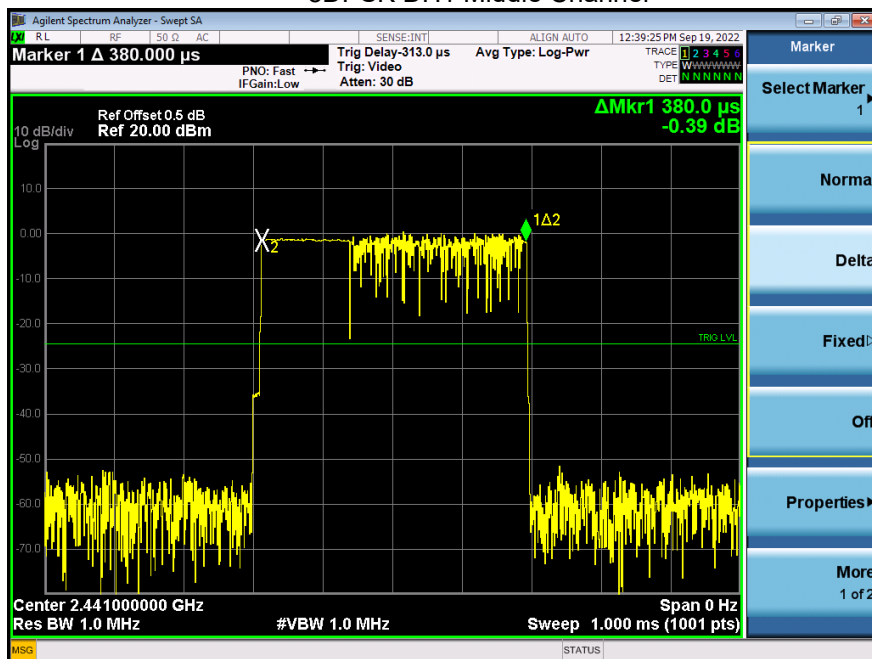


$\pi/4$ DQPSK DH1 Middle Channel

 $\pi/4$ DQPSK DH3 Middle Channel


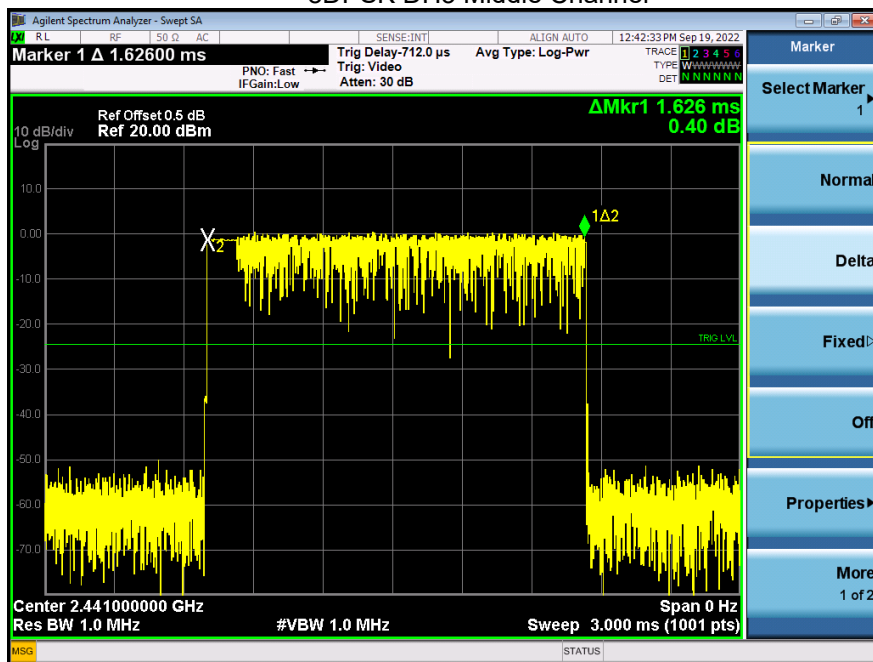
$\pi/4$ DQPSK DH5 Middle Channel



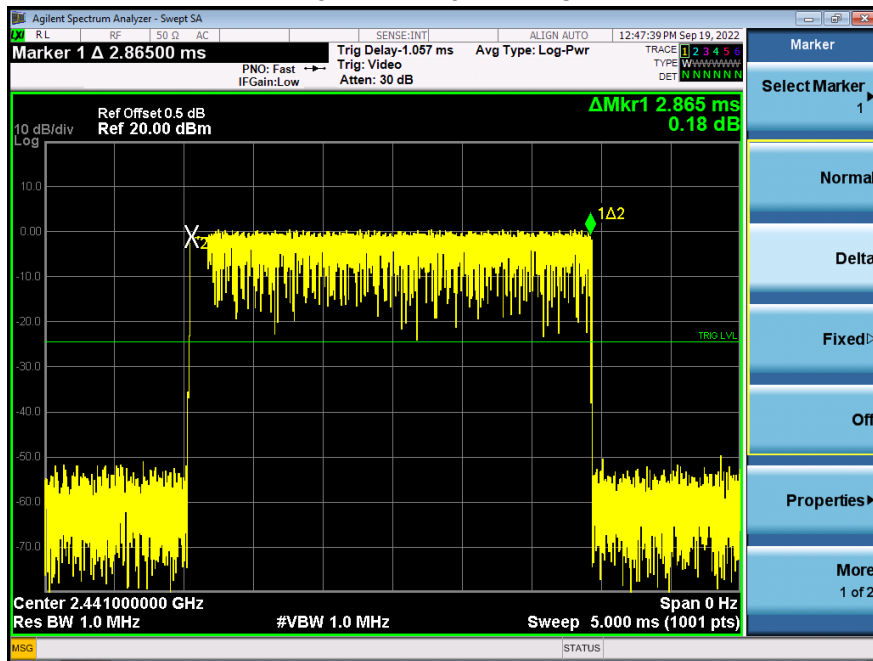
8DPSK DH1 Middle Channel



8DPSK DH3 Middle Channel



8DPSK DH5 Middle Channel



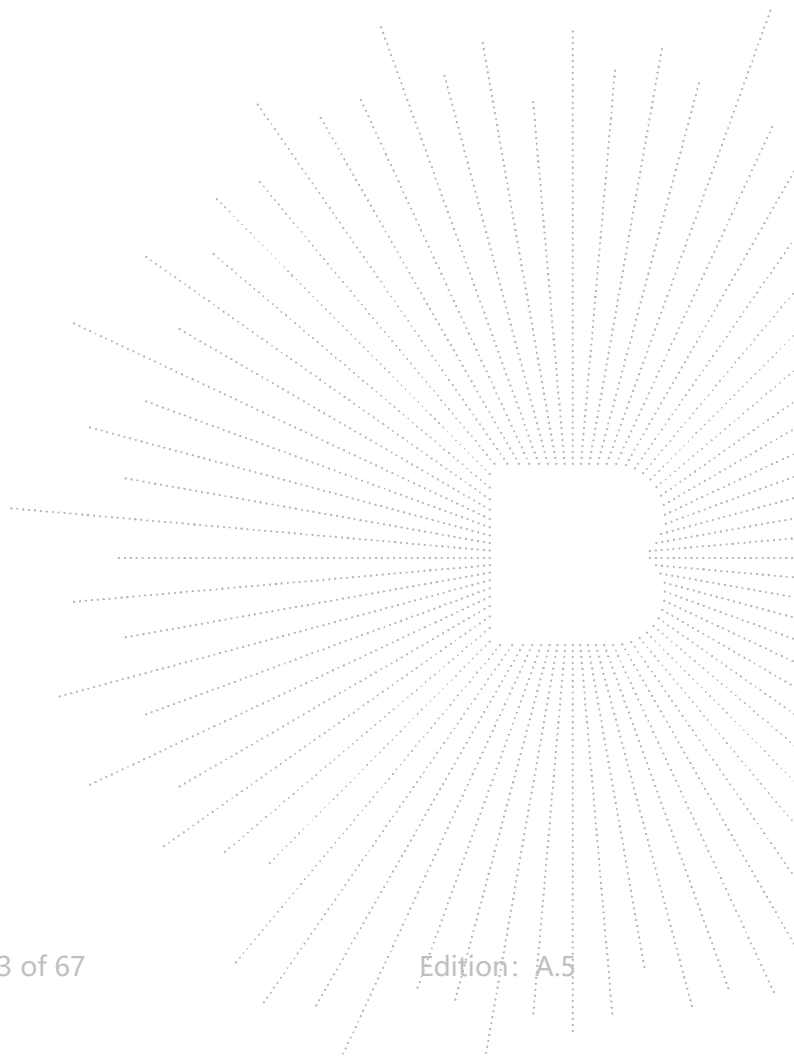
15. Antenna Requirement

15.1 Limit

15.203 requirement: For intentional device, according to 15.203: an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device.

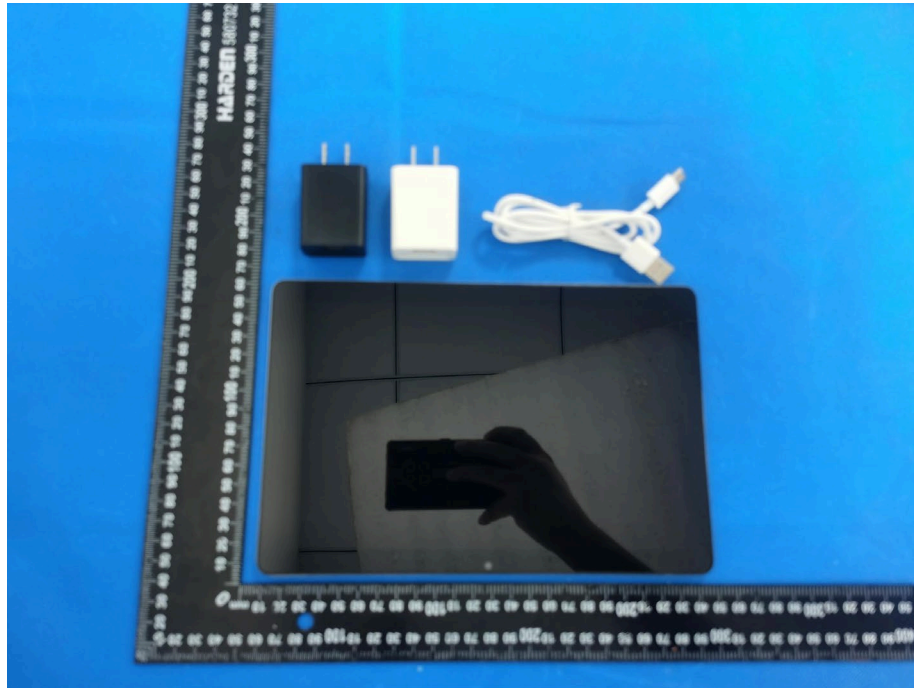
15.2 Test Result

The EUT antenna is FPCB antenna, The antenna gain is 2.41 dBi, fulfill the requirement of this section.



16. EUT Photographs

EUT Photo 1



EUT Photo 2

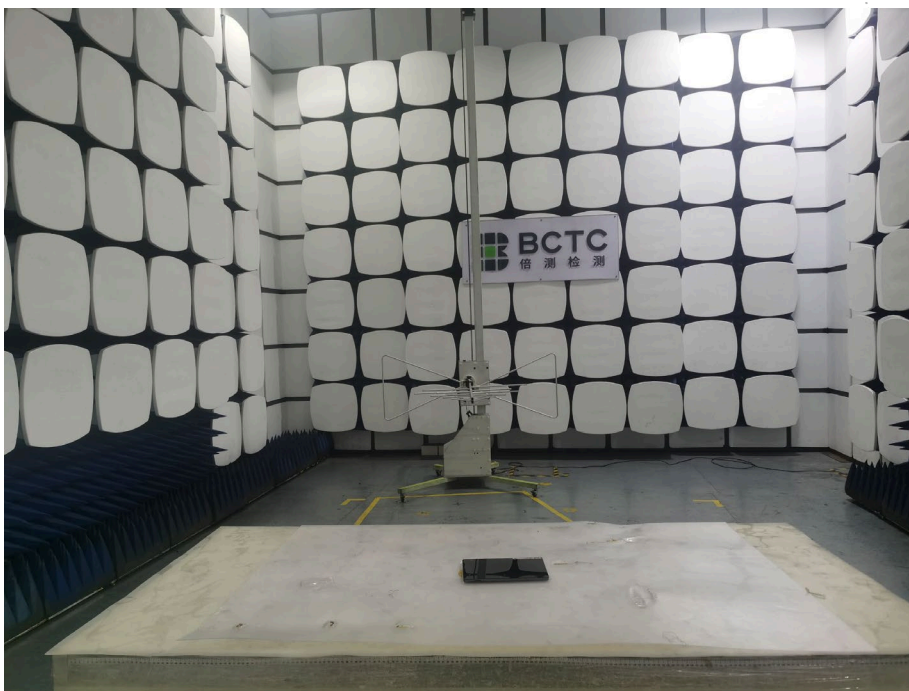


17. EUT Test Setup Photographs

Conducted emissions Photo



Radiated Measurement Photos





STATEMENT

1. The equipment lists are traceable to the national reference standards.
2. The test report can not be partially copied unless prior written approval is issued from our lab.
3. The test report is invalid without the "special seal for inspection and testing".
4. The test report is invalid without the signature of the approver.
5. The test process and test result is only related to the Unit Under Test.
6. Sample information is provided by the client and the laboratory is not responsible for its authenticity.
7. The test report without CMA mark is only used for scientific research, teaching, enterprise product development and internal quality control purposes.
8. The quality system of our laboratory is in accordance with ISO/IEC17025.
9. If there is any objection to this test report, the client should inform issuing laboratory within 15 days from the date of receiving test report.

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***** END *****