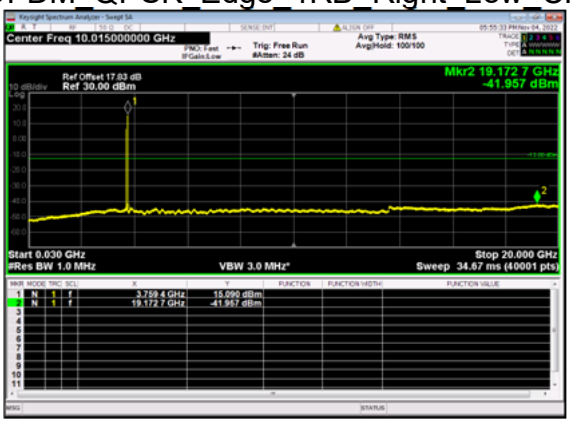
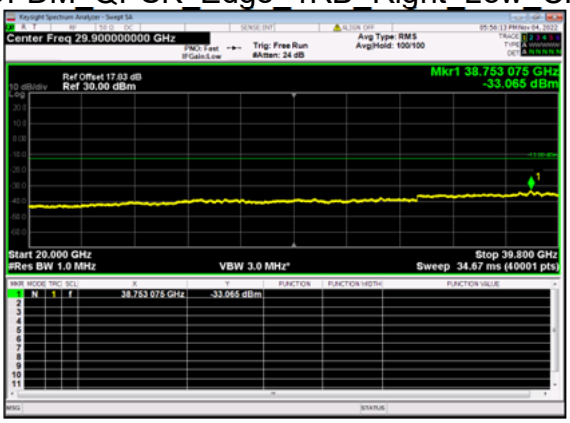
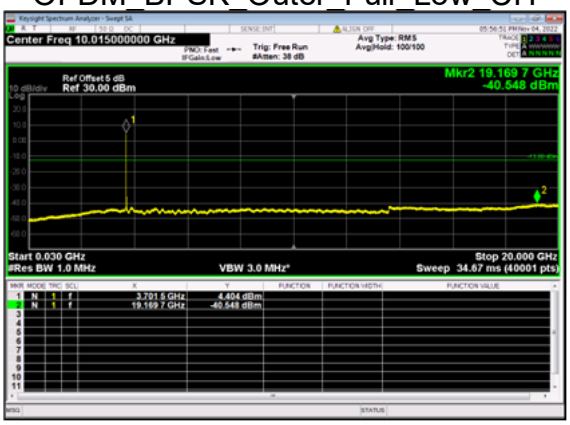
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



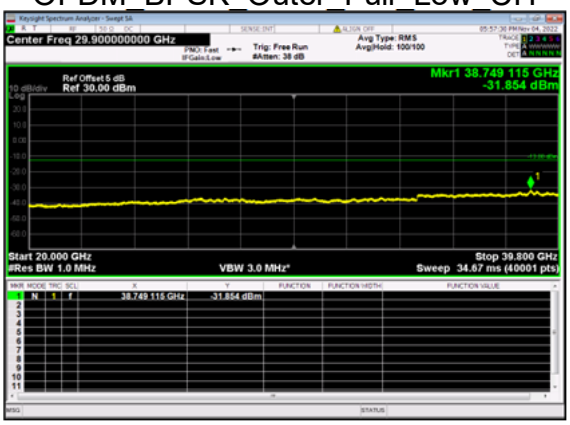
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



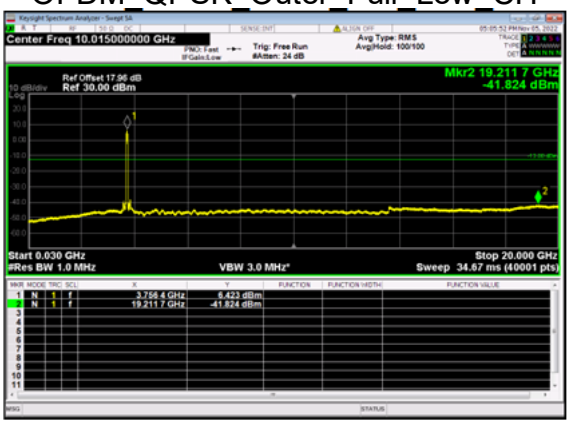
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



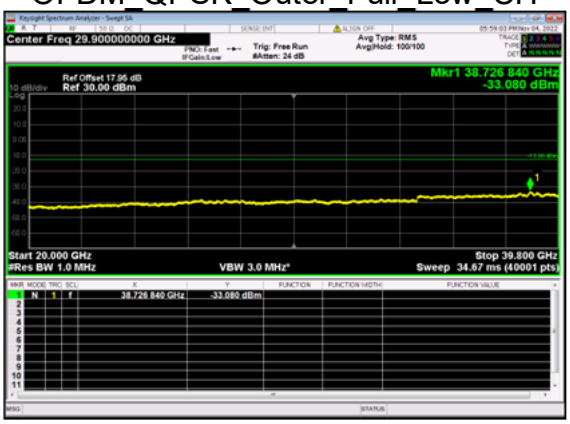
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



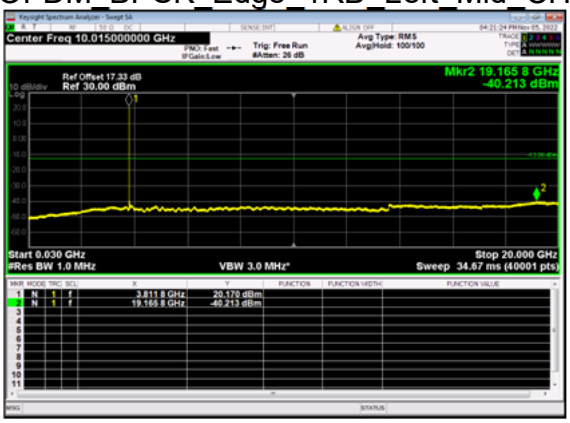
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



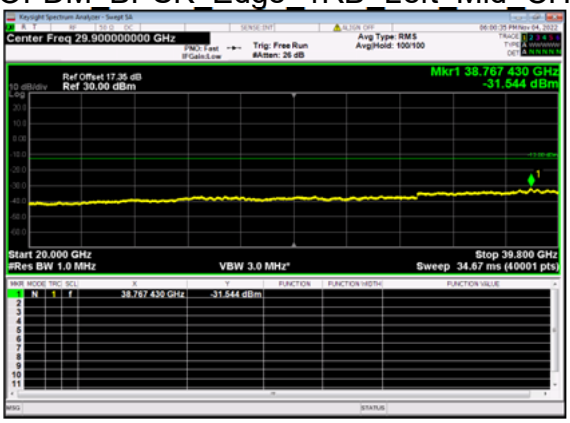
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



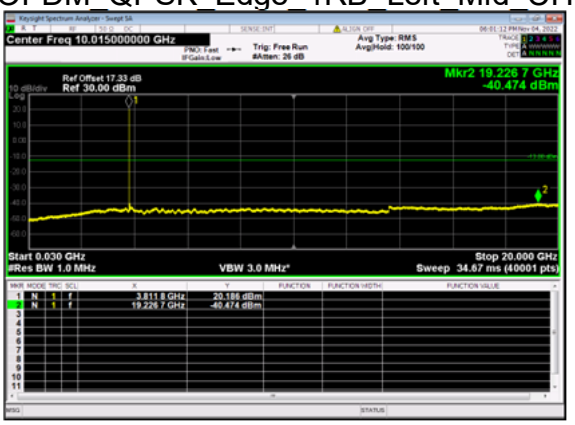
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



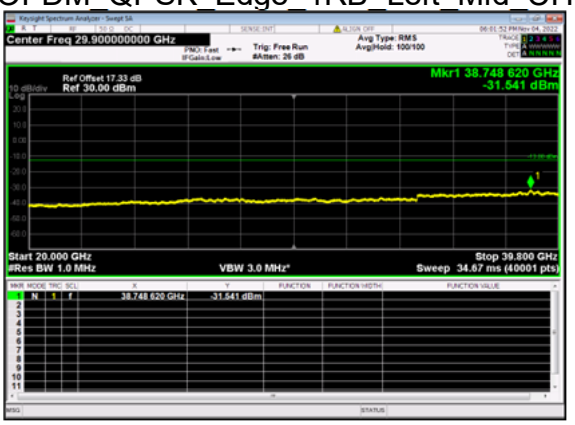
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



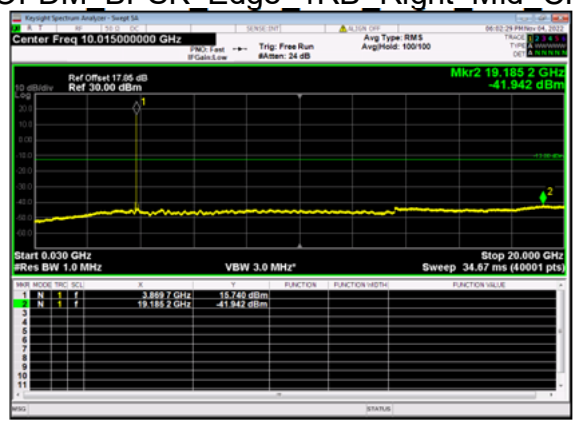
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



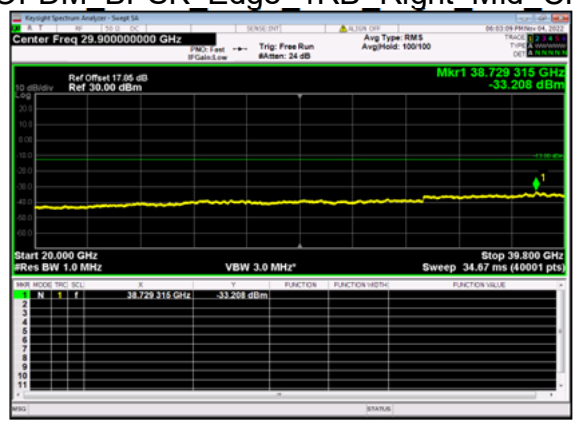
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



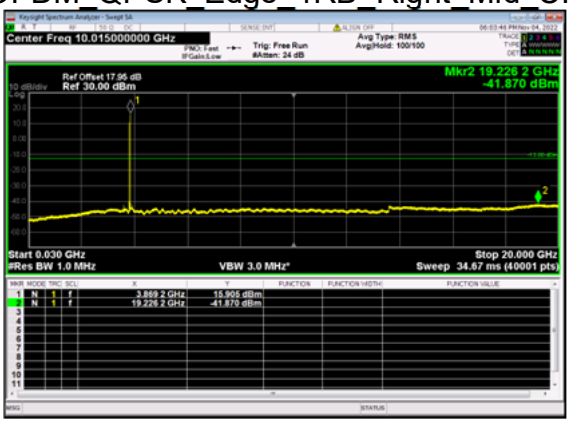
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



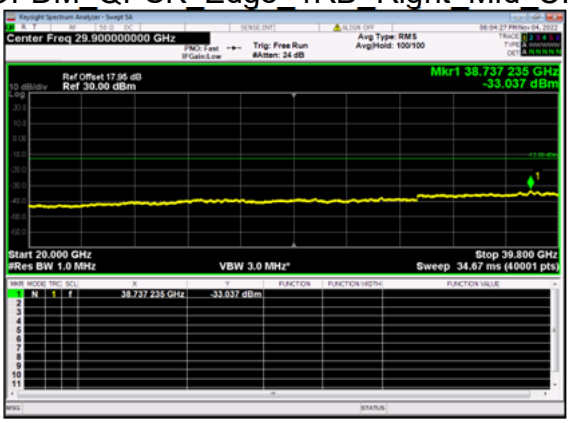
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



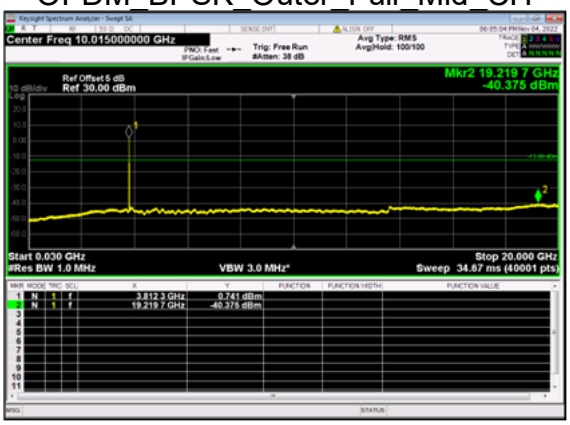
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



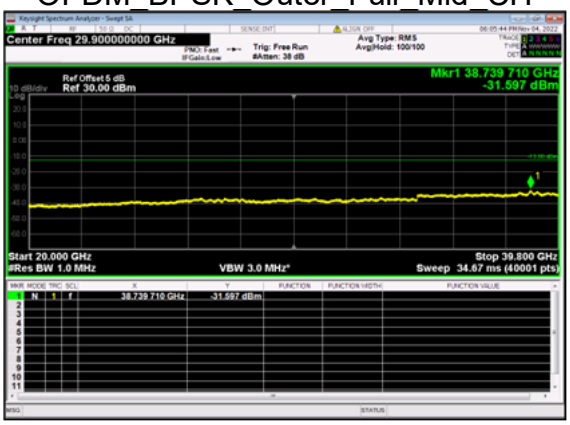
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



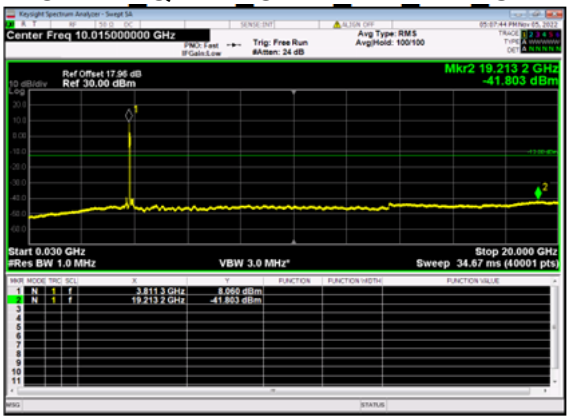
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



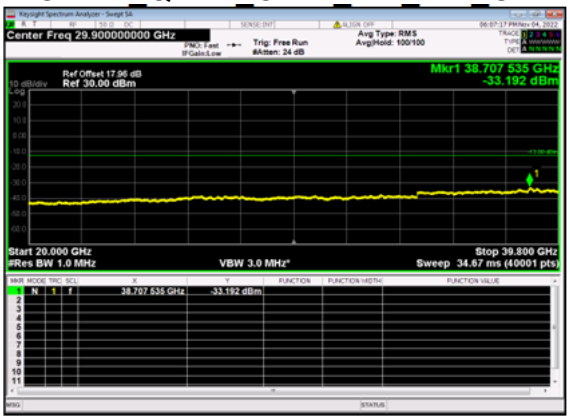
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



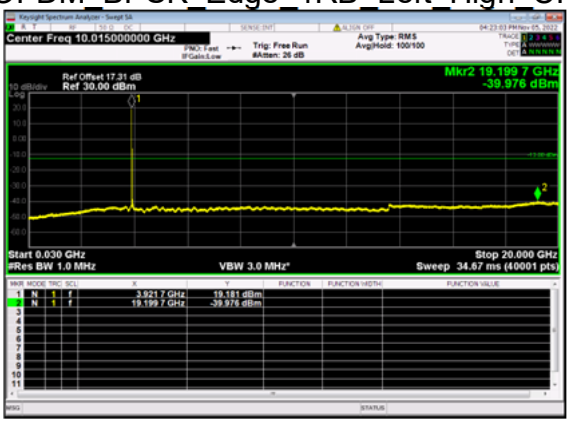
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



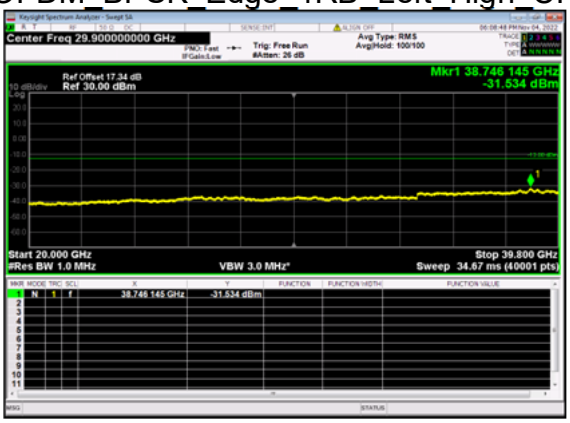
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



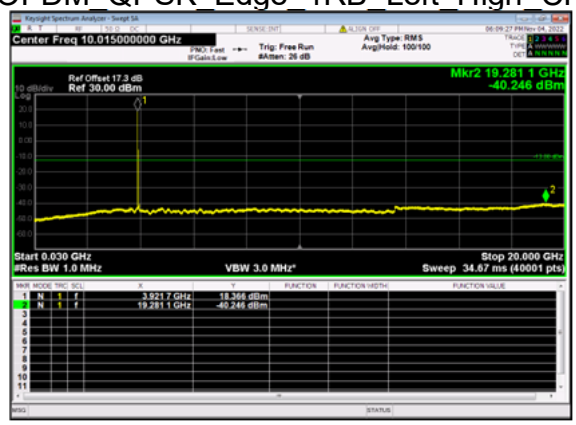
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



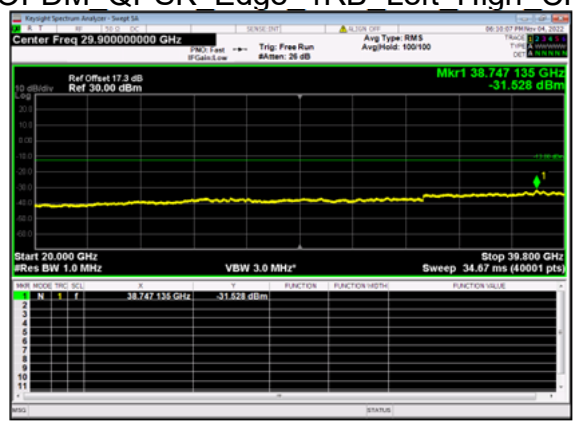
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



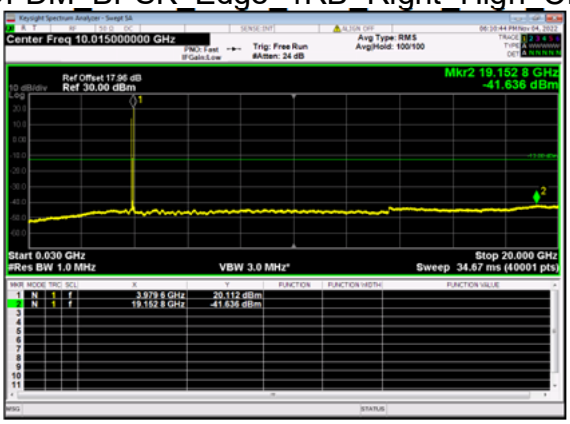
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



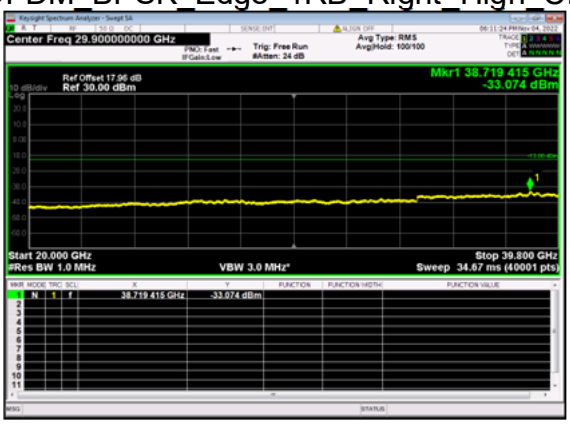
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



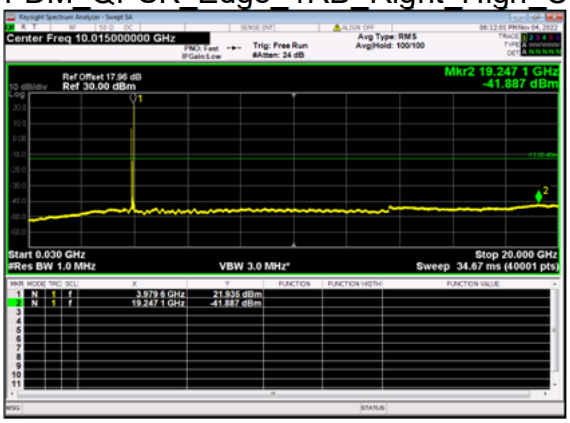
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



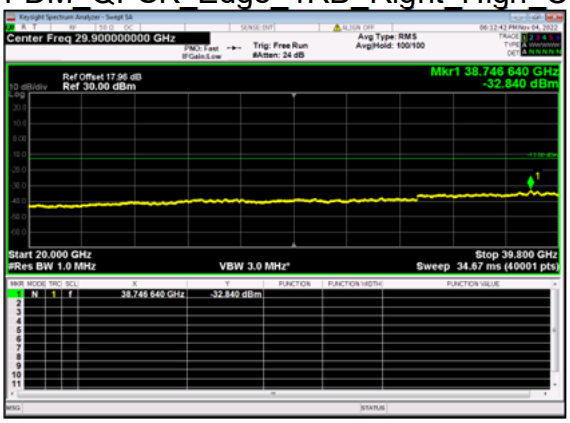
n77(60M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



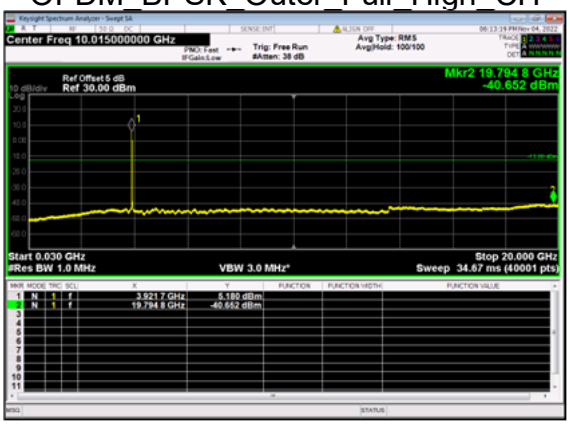
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



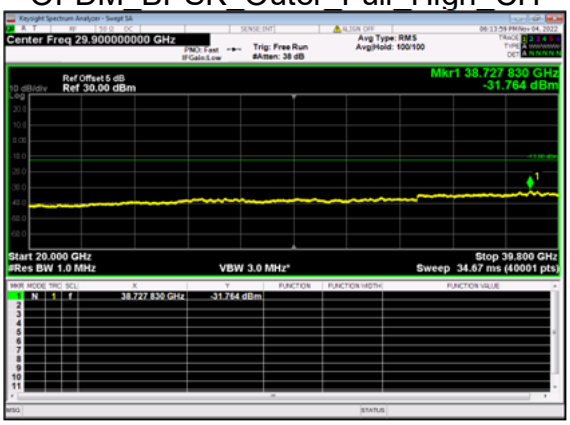
n77(60M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



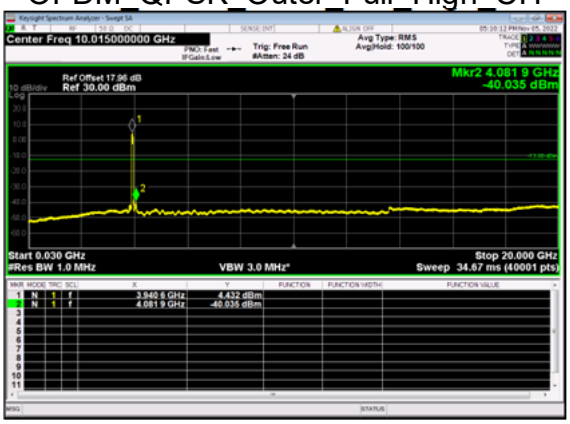
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



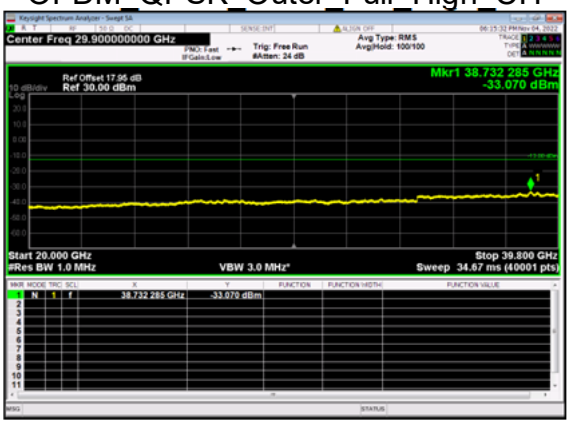
n77(60M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



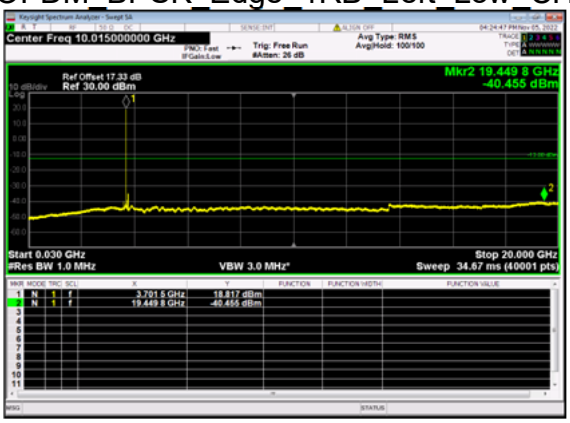
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH



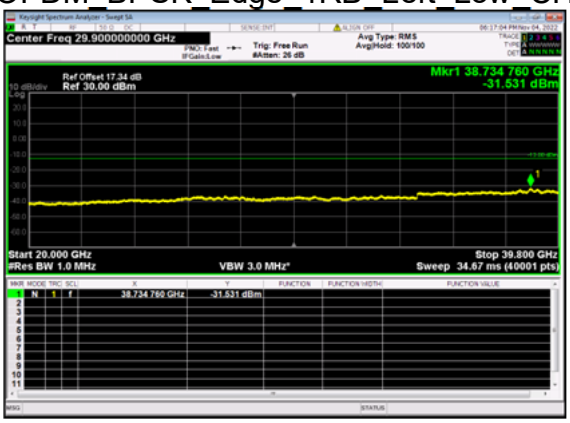
n77(60M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH



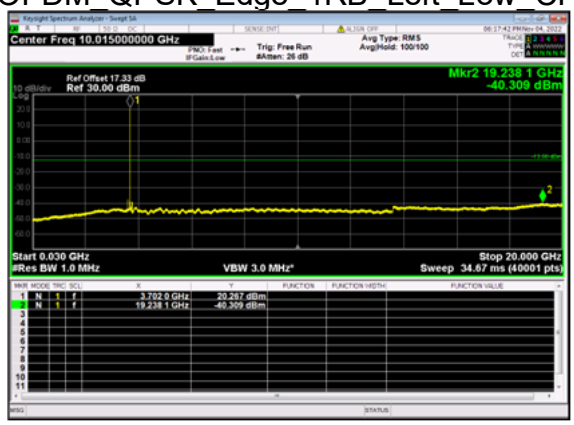
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



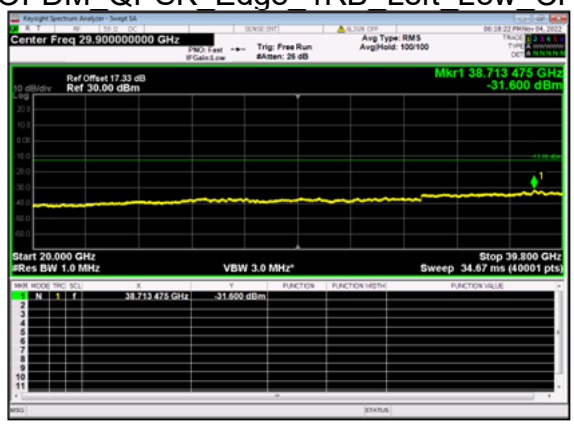
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



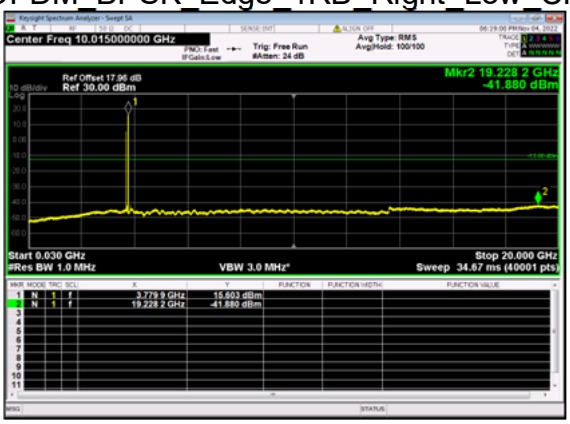
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



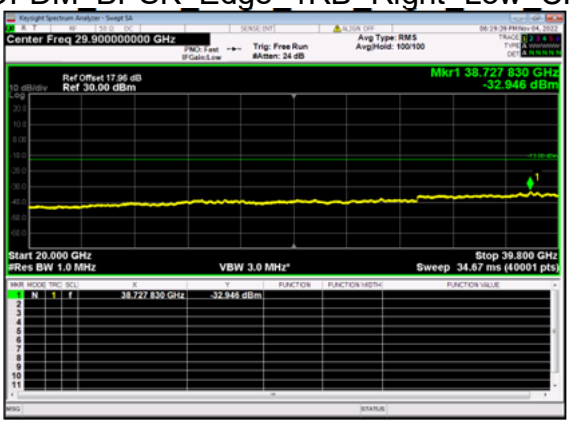
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



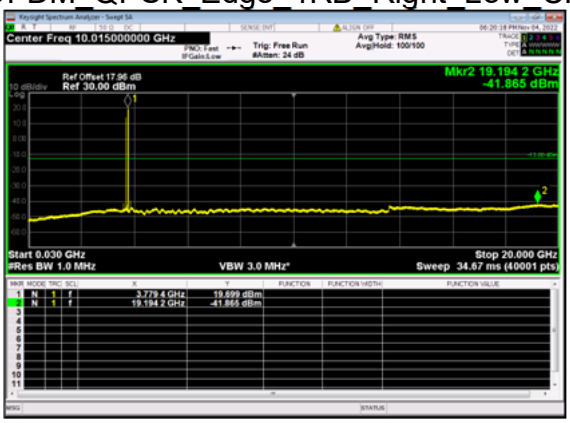
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Low_CH



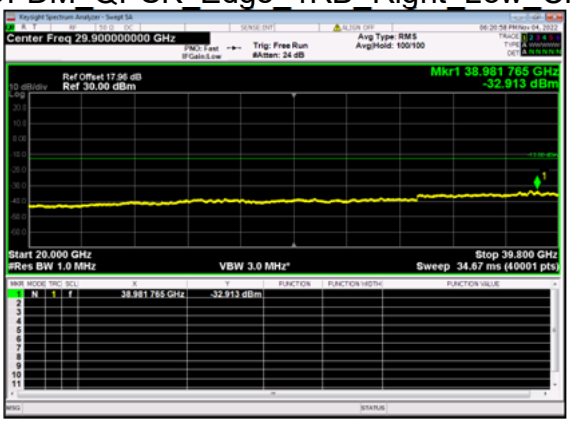
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Low_CH



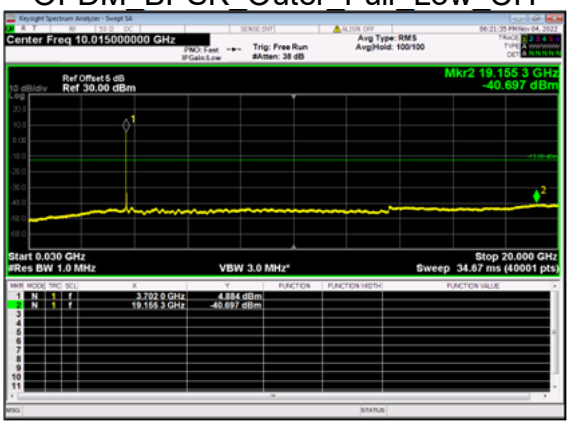
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



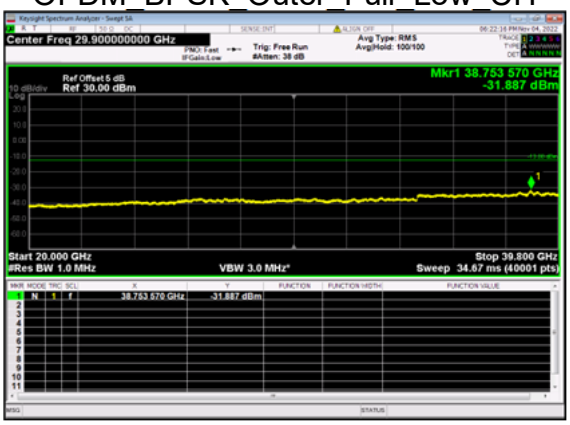
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



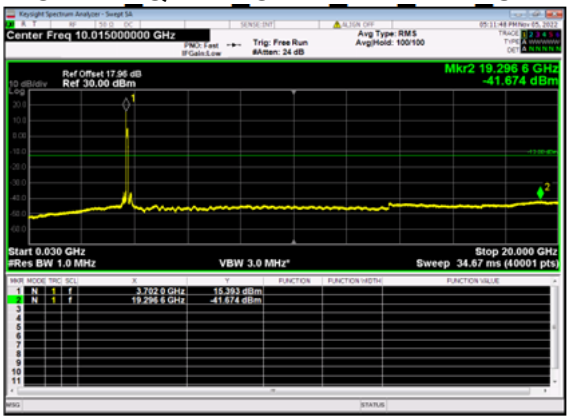
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



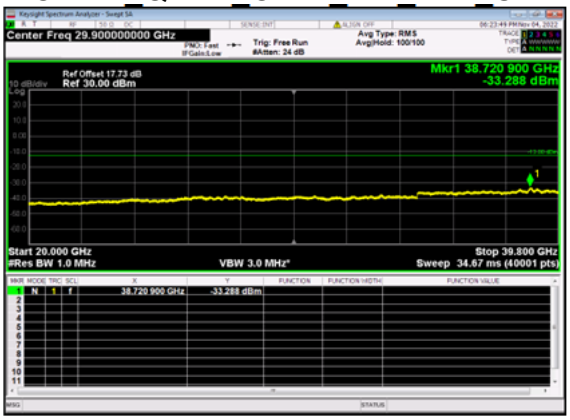
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



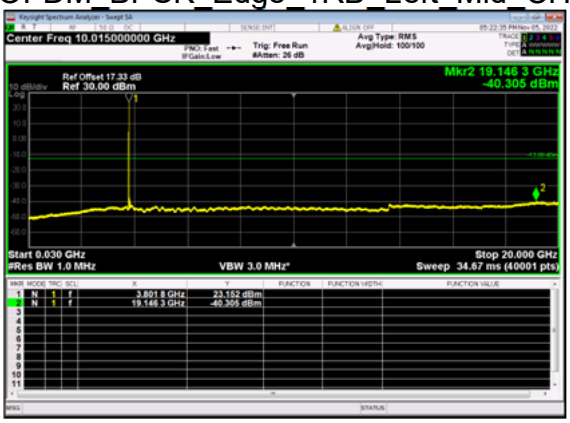
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



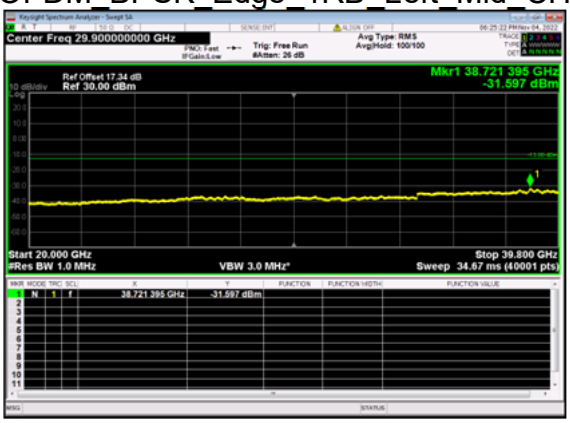
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



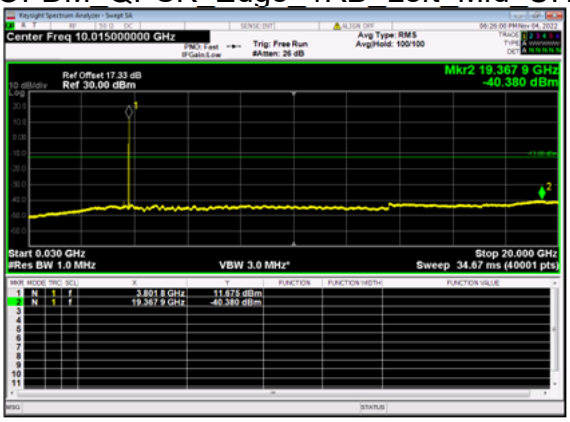
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



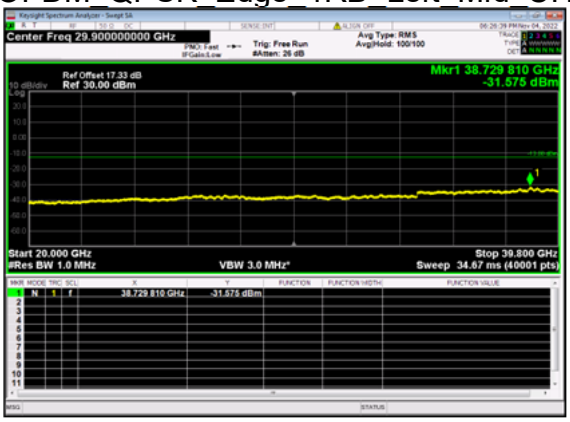
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



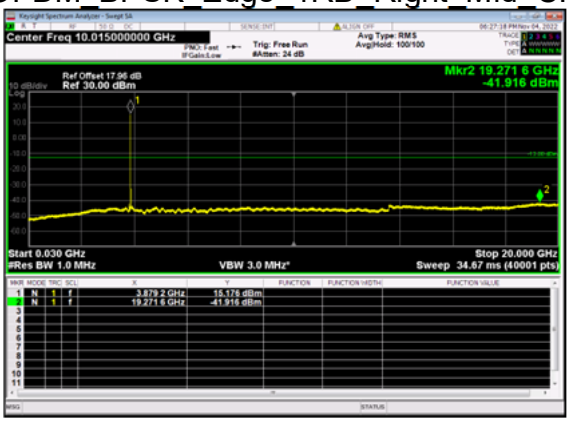
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



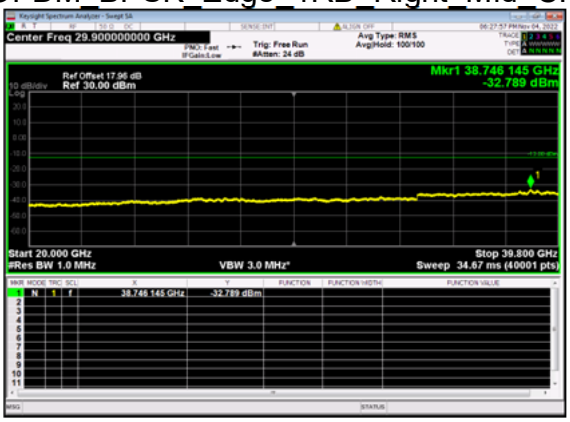
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



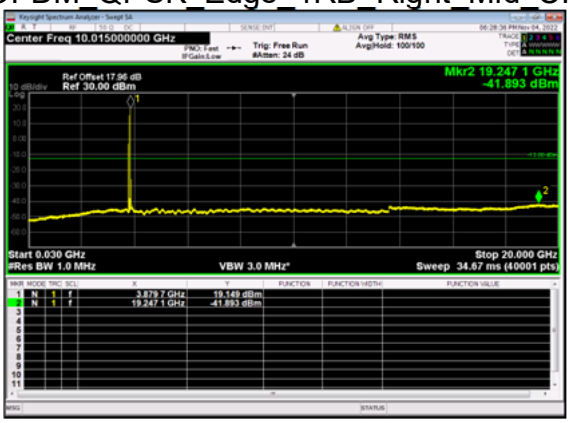
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



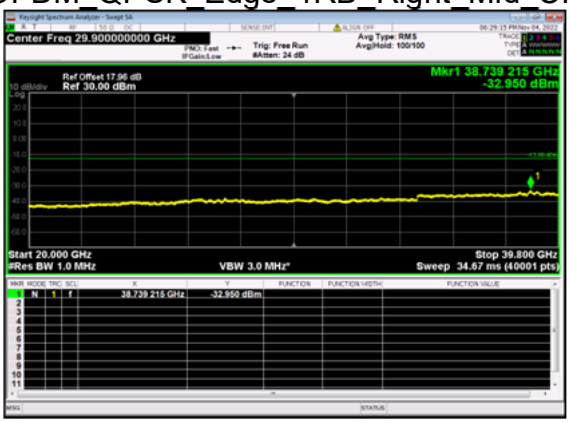
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



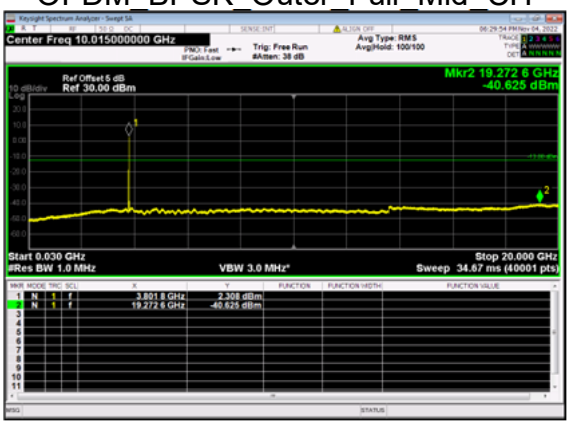
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



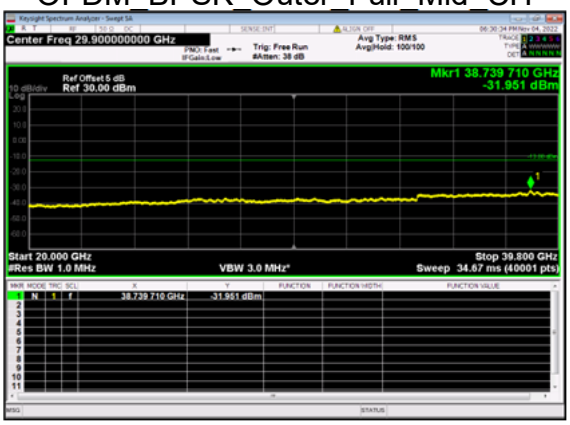
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



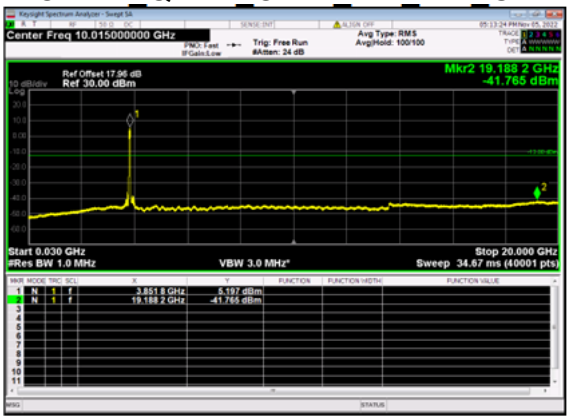
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



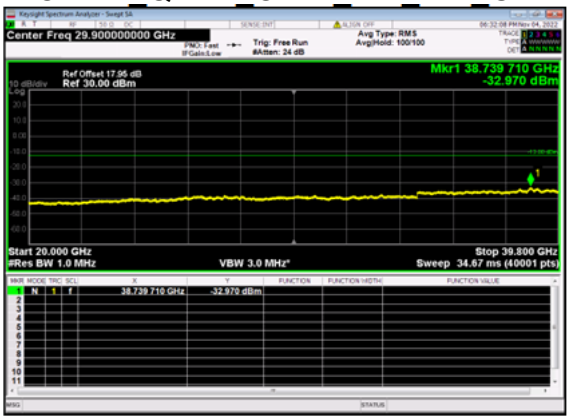
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



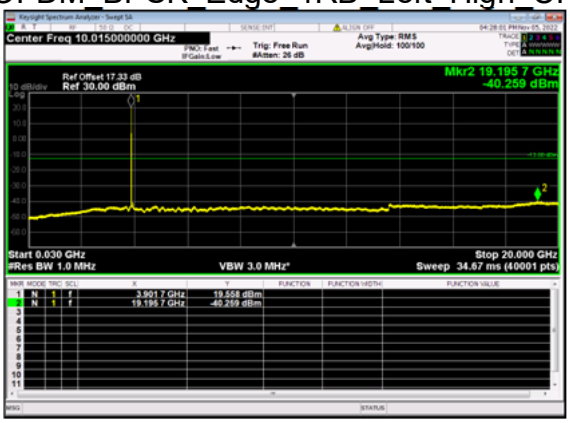
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



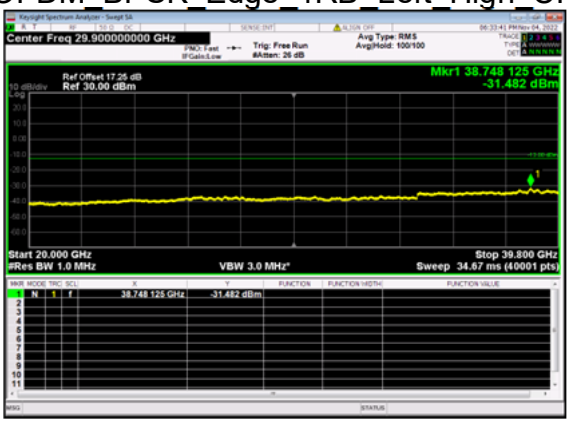
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



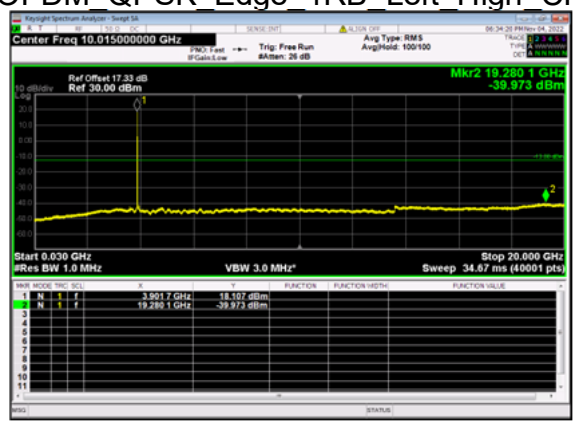
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



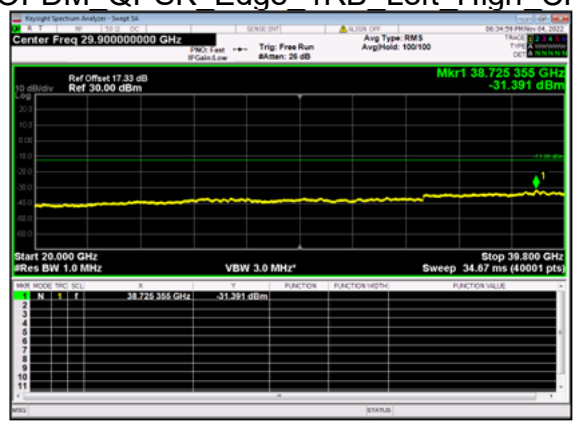
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



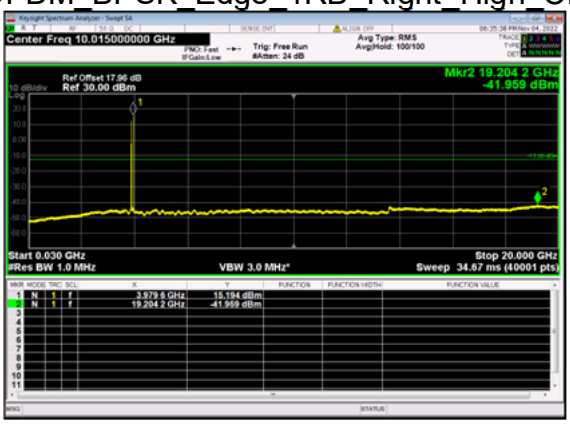
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



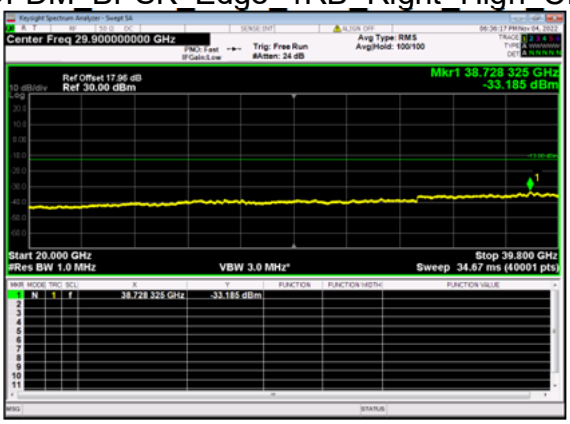
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



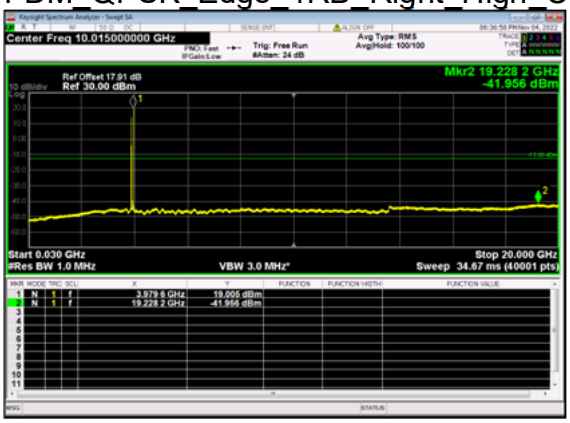
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



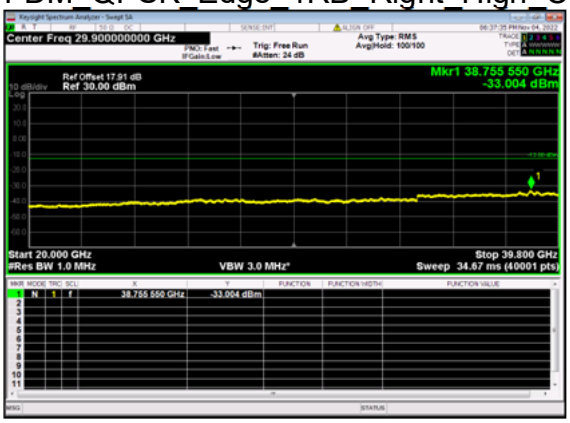
n77(80M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



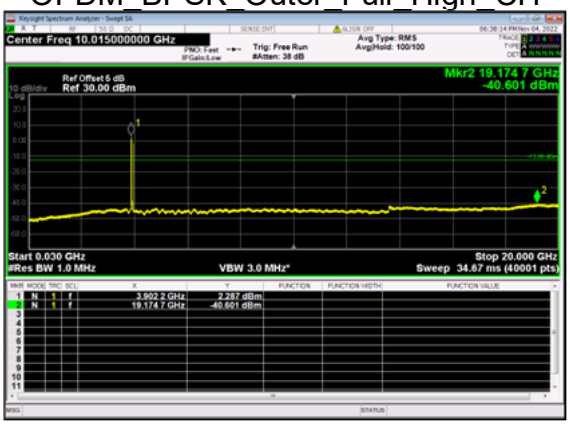
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



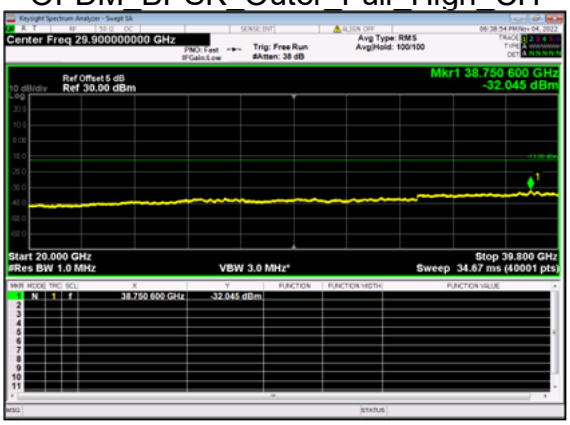
n77(80M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



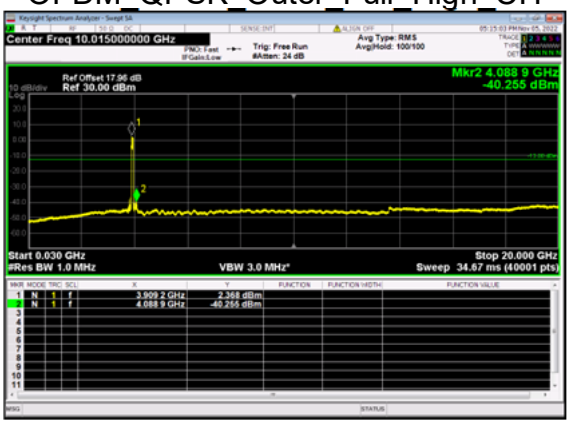
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



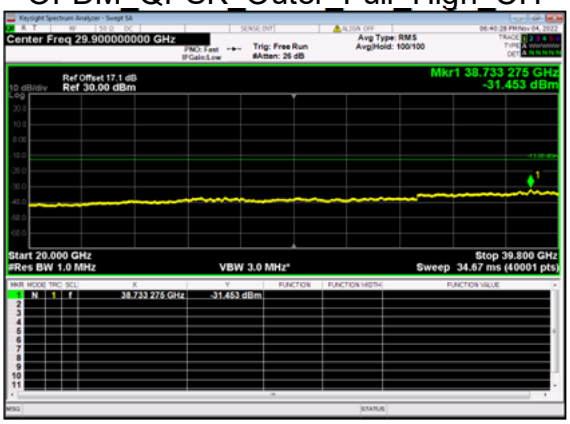
n77(80M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



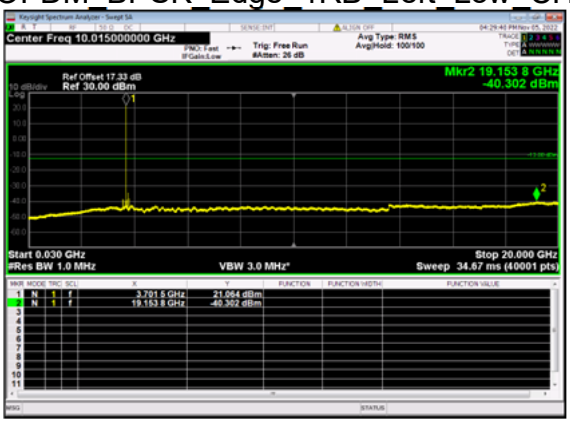
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH



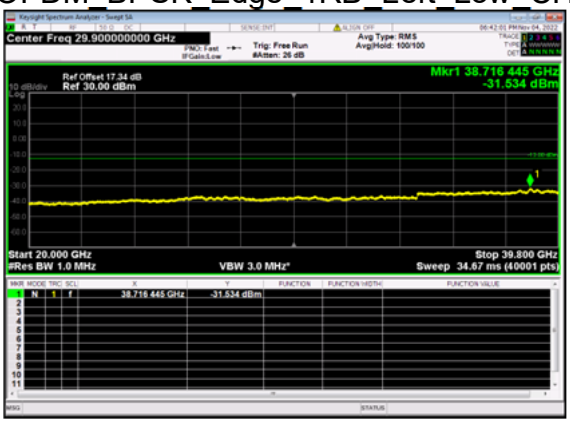
n77(80M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH



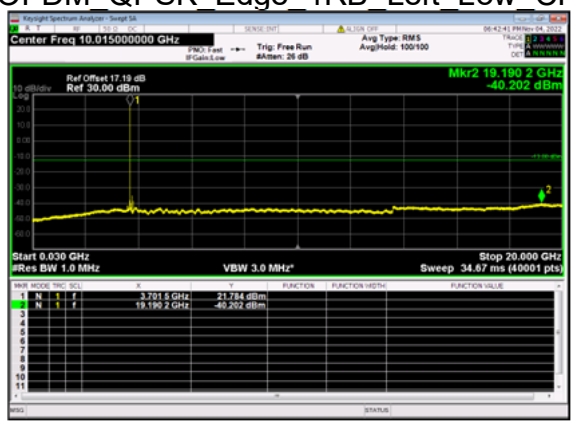
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



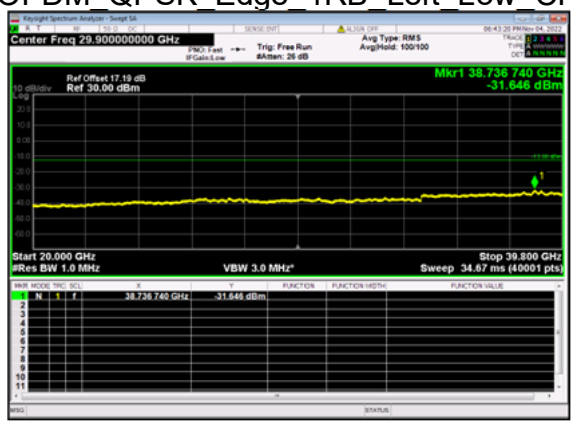
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



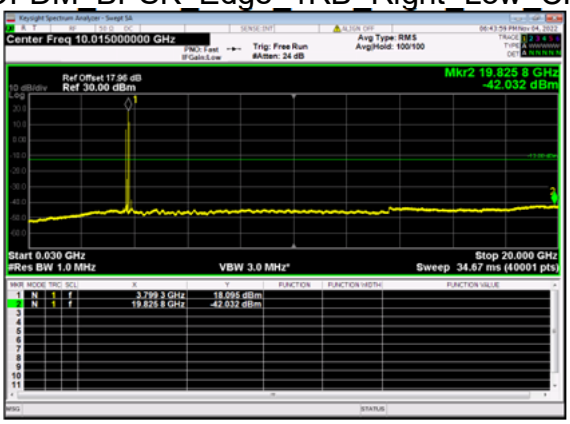
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



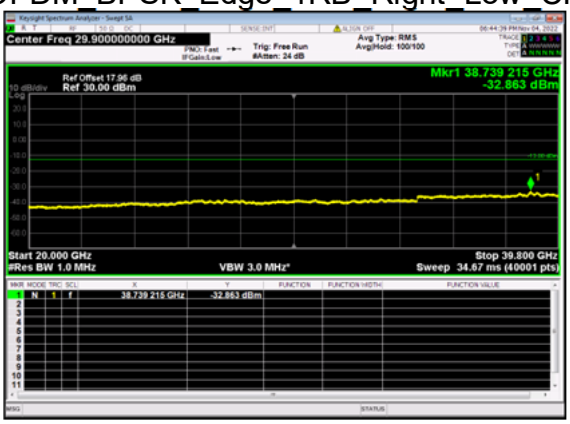
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



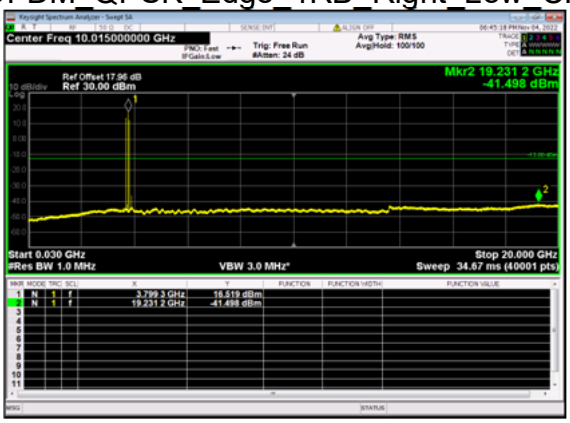
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Low_CH



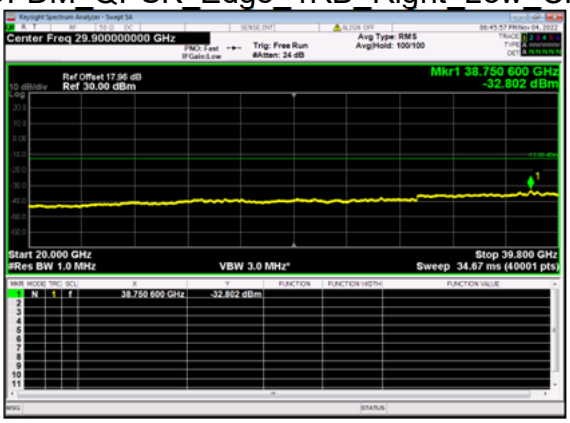
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Low_CH



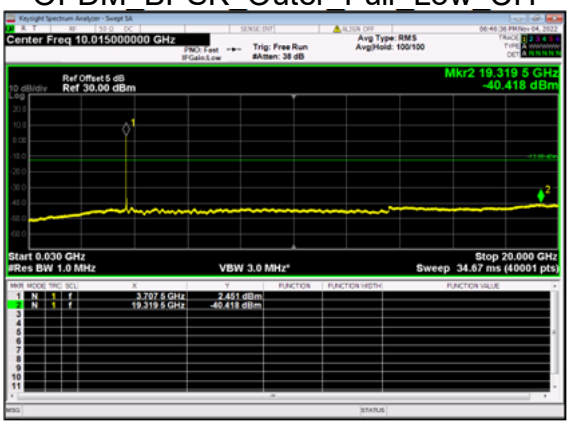
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



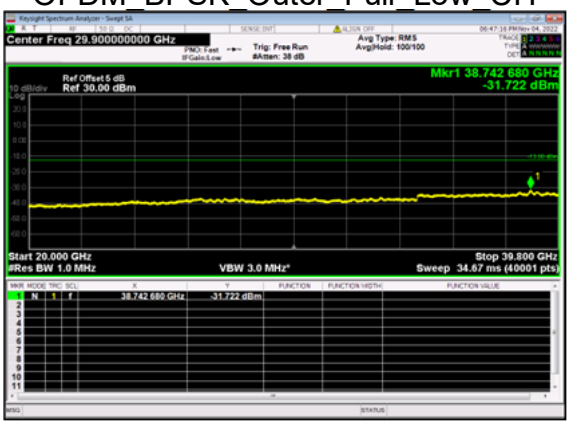
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Low_CH



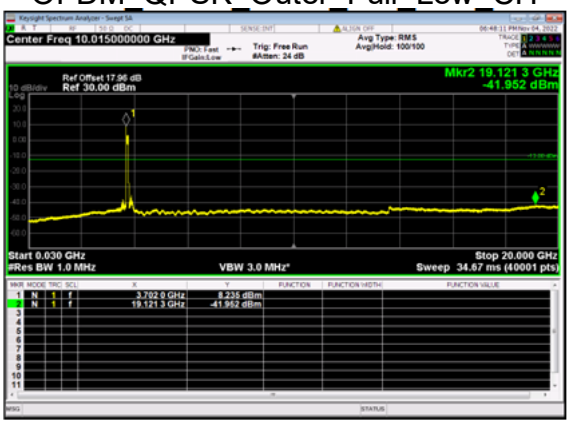
n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



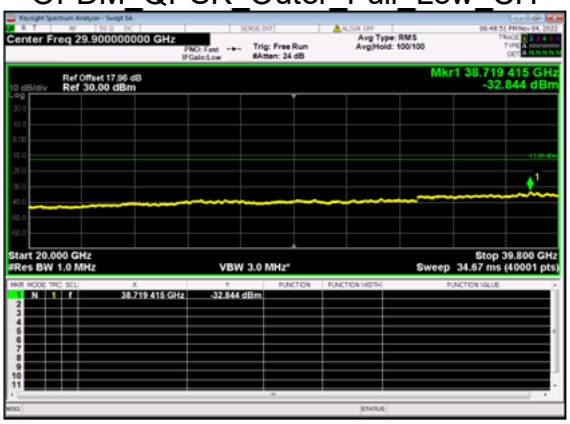
n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_Low_CH



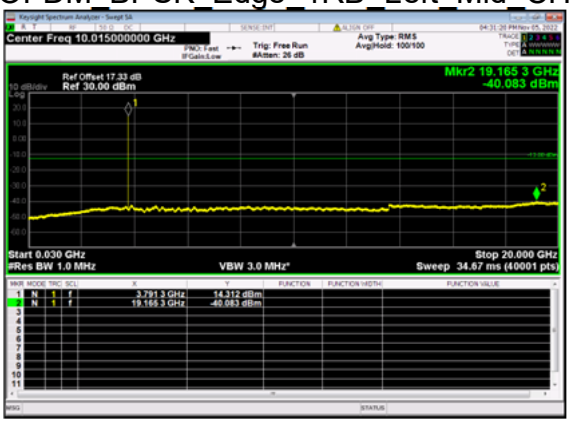
n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



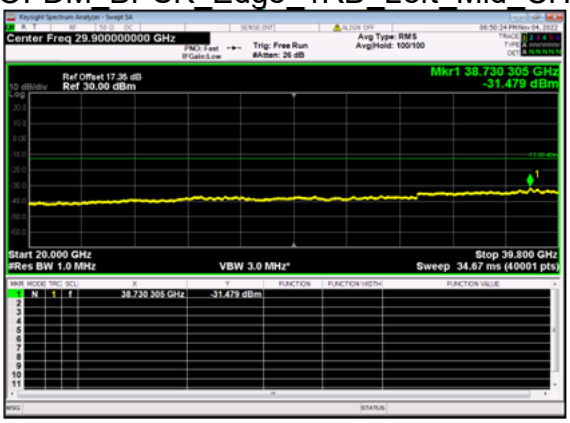
n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_Low_CH



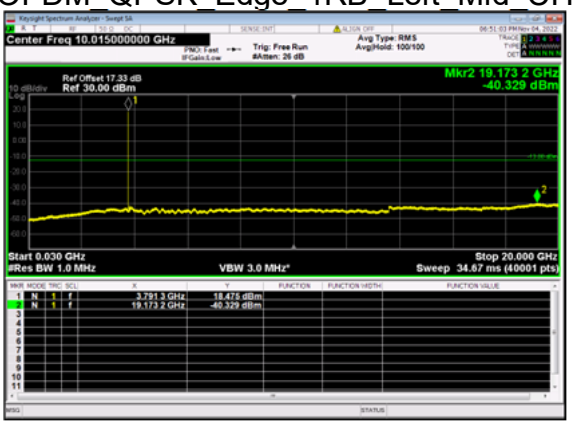
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



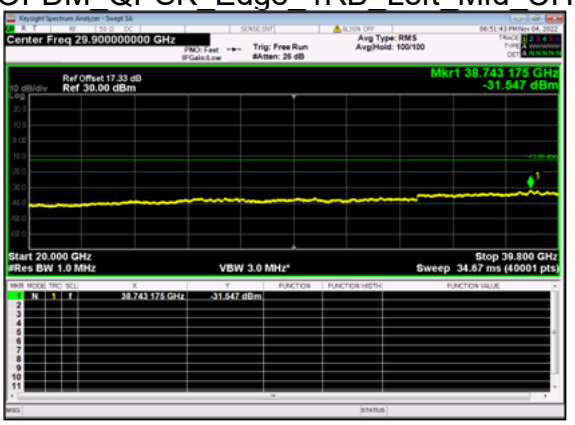
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



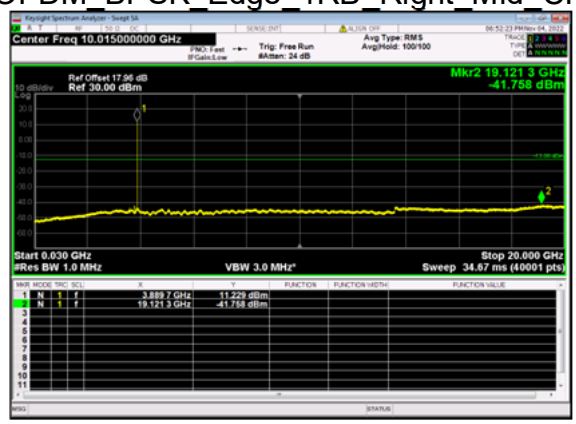
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



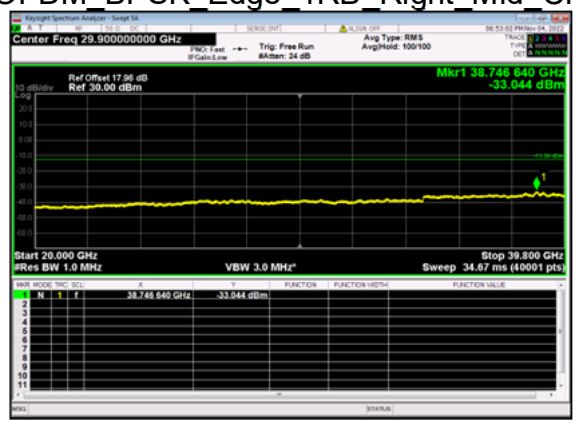
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



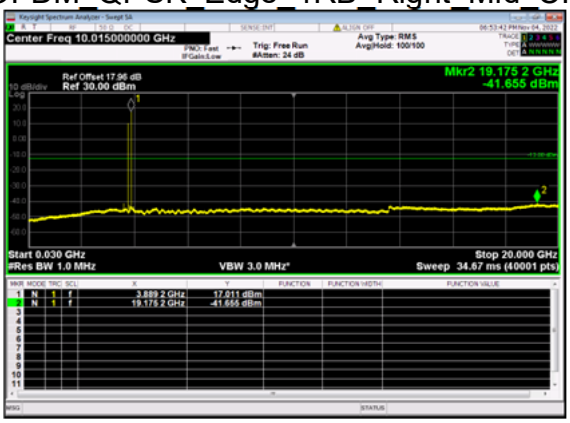
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



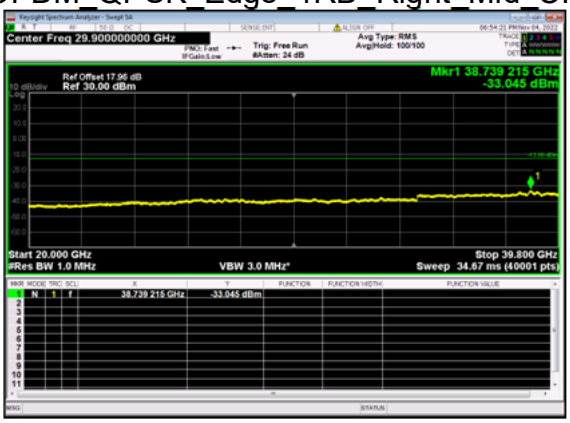
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_Mid_CH



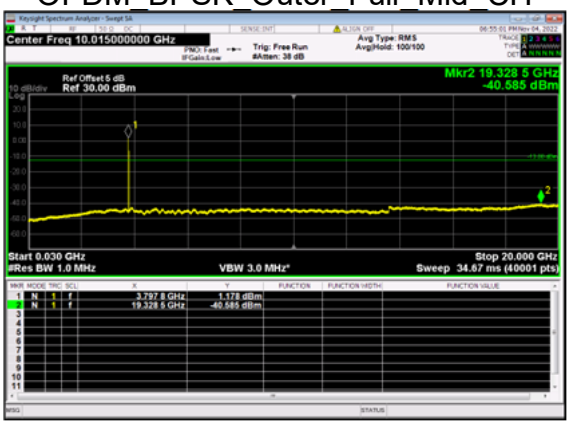
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



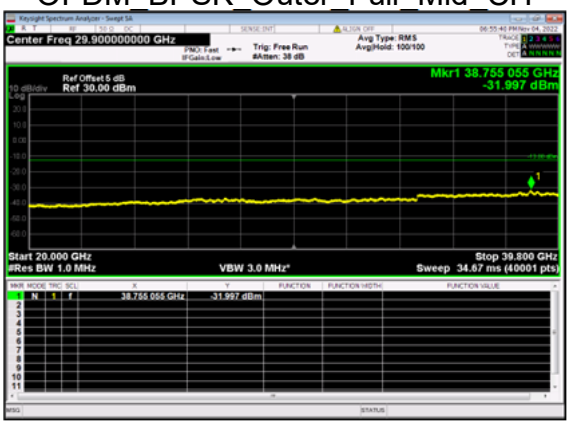
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Right_Mid_CH



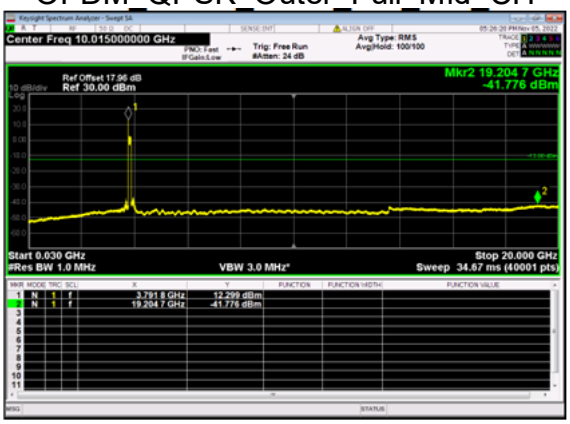
n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



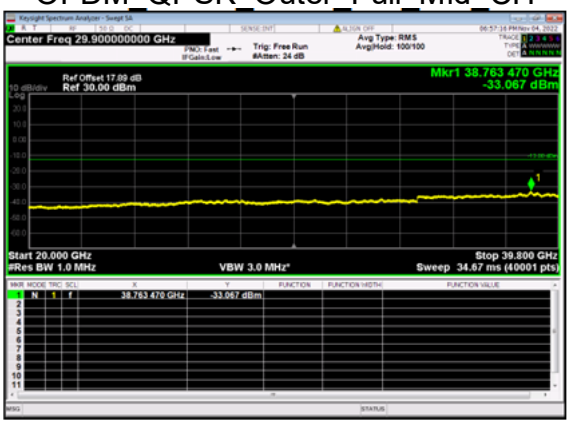
n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_Mid_CH



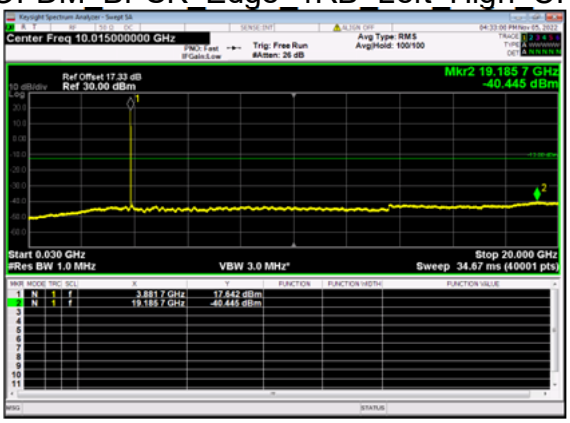
n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



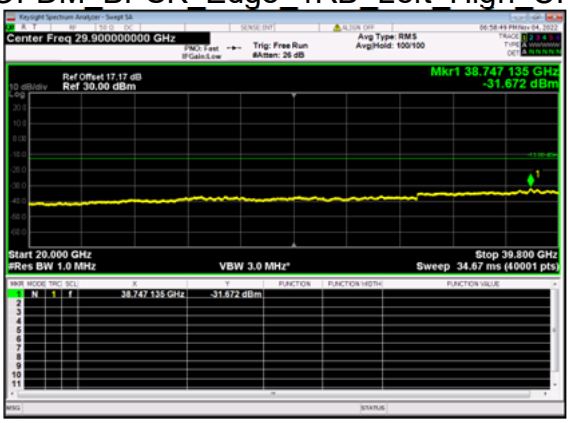
n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



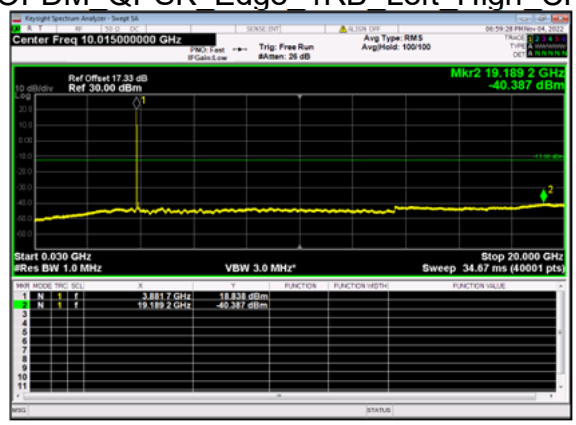
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



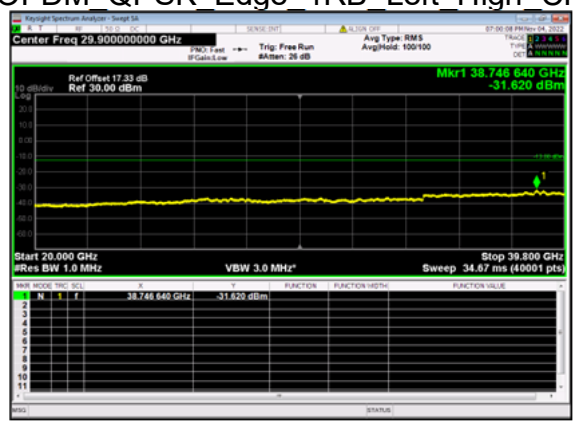
n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



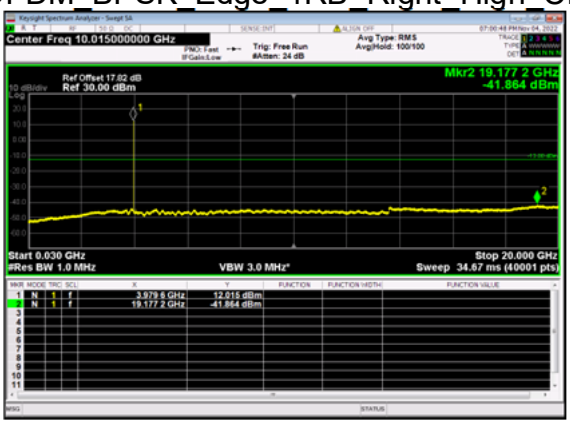
n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



n77(100M) _DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



n77(100M) _DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



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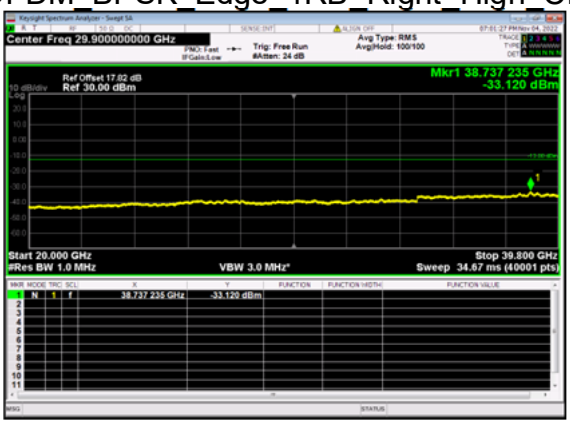

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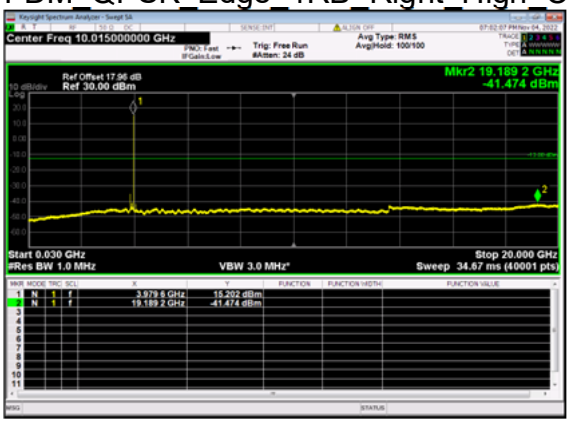

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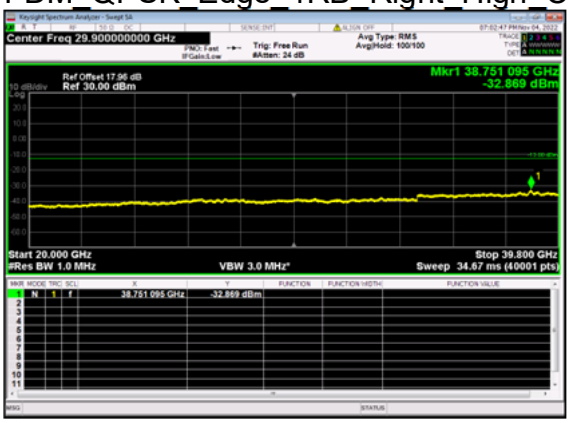
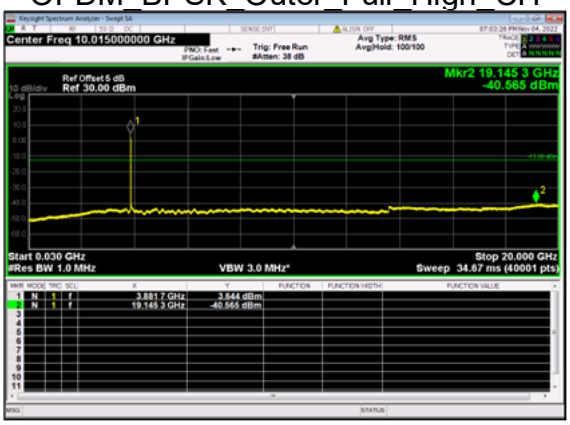
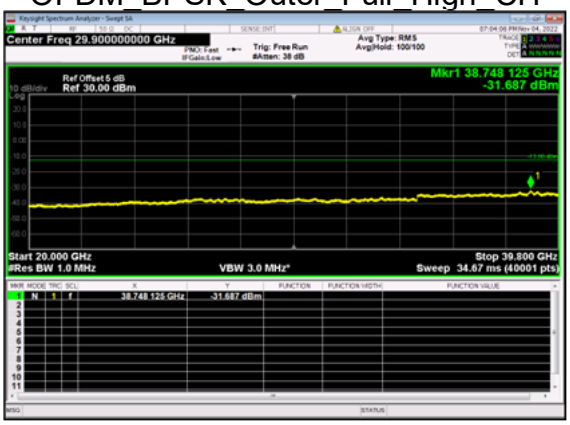


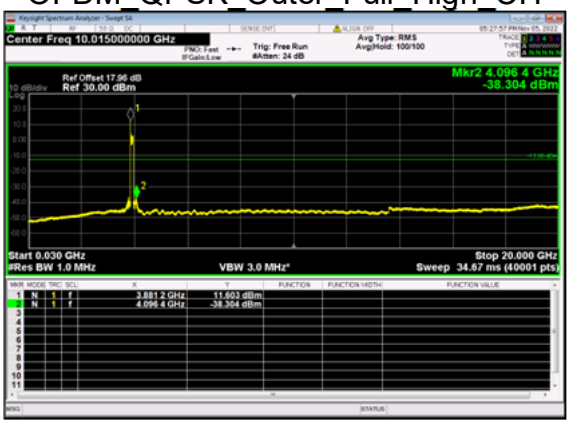
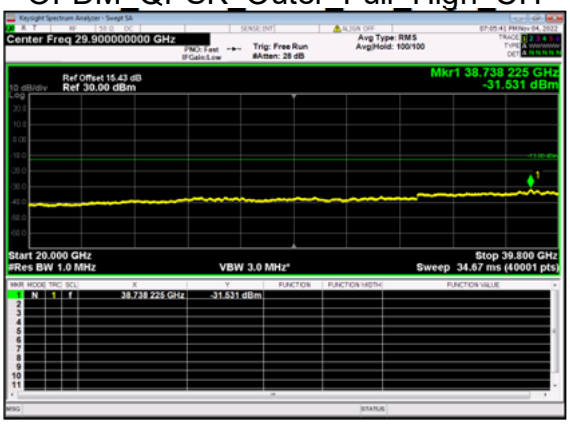
n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



n77(100M) _DFT-s-OFDM_BPSK_Outer_Full_High_CH



n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH



n77(100M) _DFT-s-OFDM_QPSK_Outer_Full_High_CH