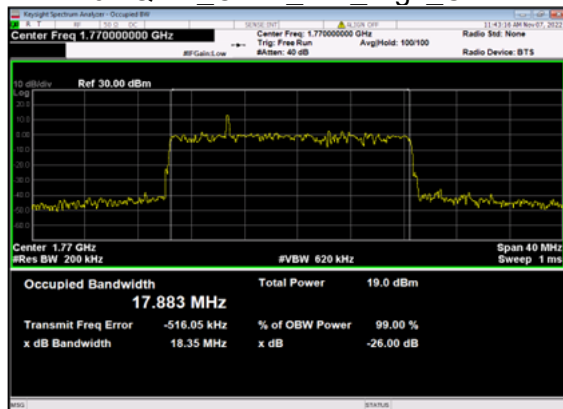
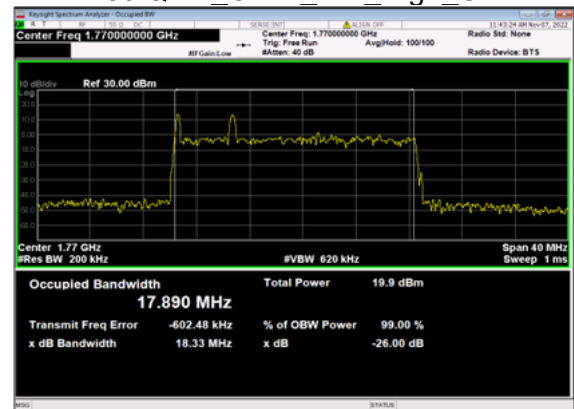


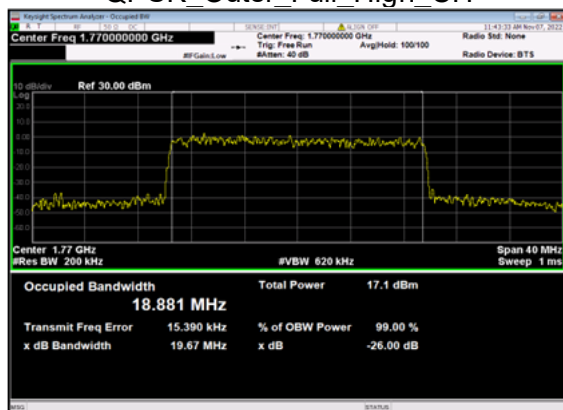
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64 QAM Outer Full High CH



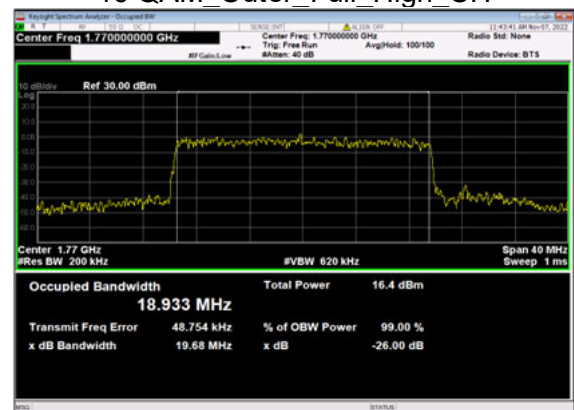
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256 QAM Outer Full High CH



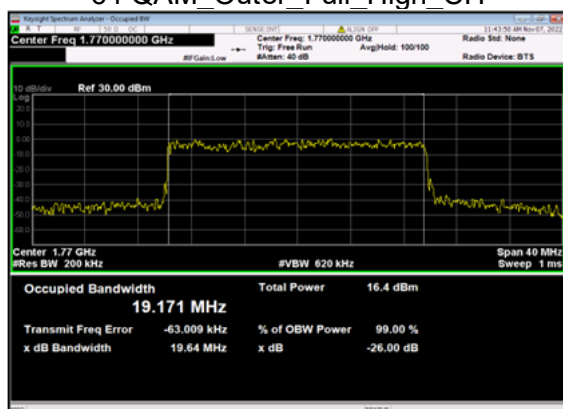
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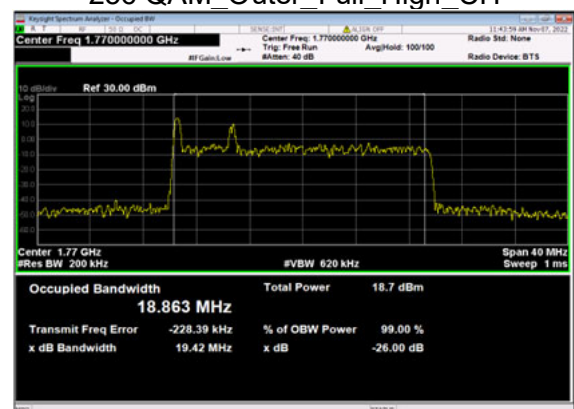
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16 QAM Outer Full High CH



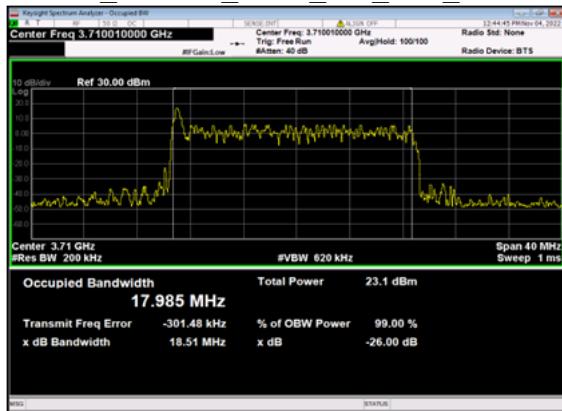
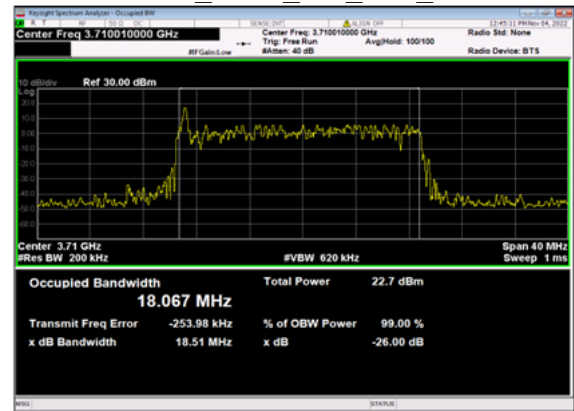
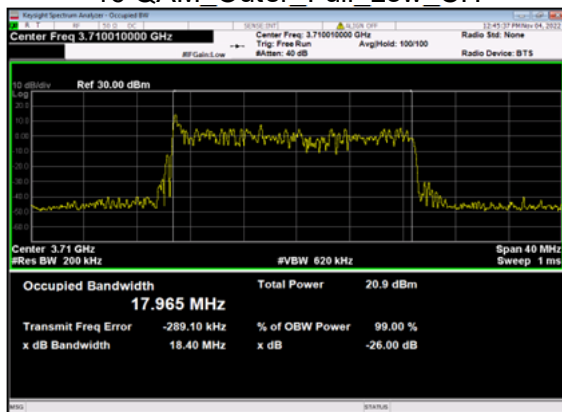
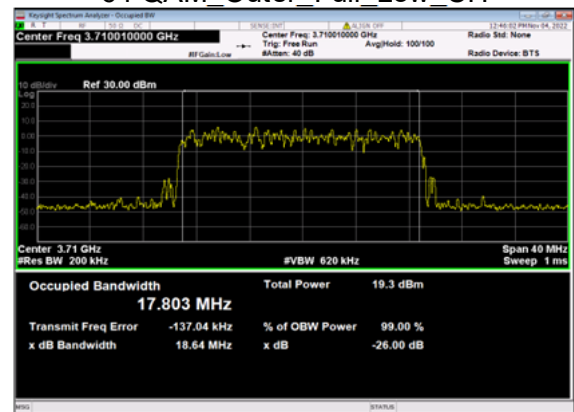
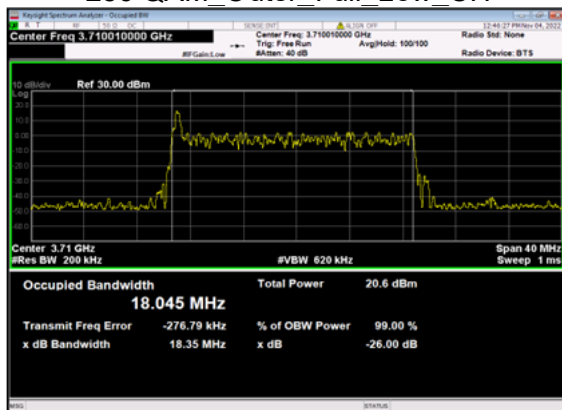
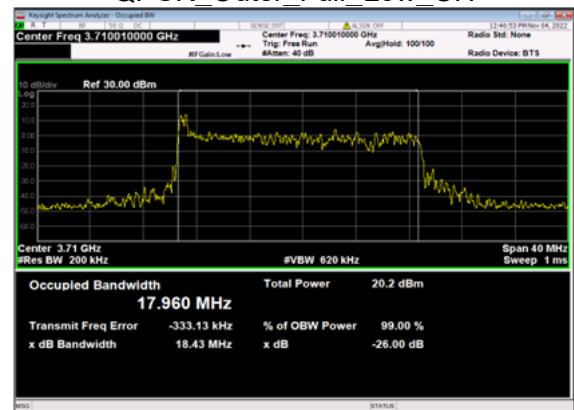
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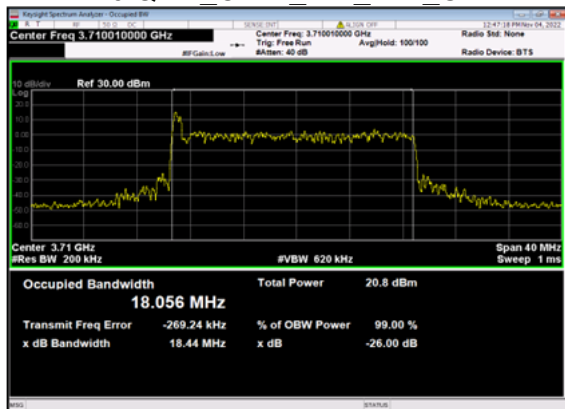
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256 QAM Outer Full High CH



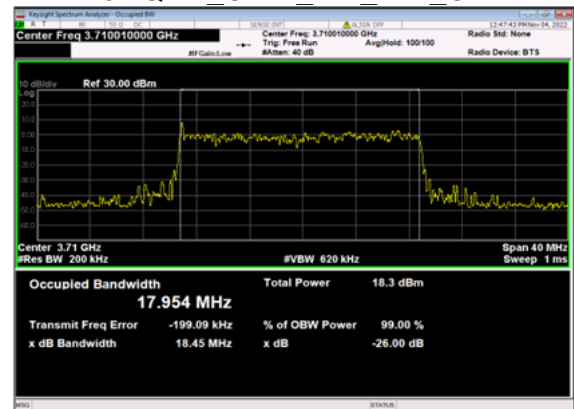
n77 (3700-3980MHz)

n77(20M)_DFT-s-OFDM_
PI 2-BPSK Outer Full Low CH

n77(20M)_DFT-s-OFDM_
QPSK Outer Full Low CH

n77(20M)_DFT-s-OFDM_
16 QAM Outer Full Low CH

n77(20M)_DFT-s-OFDM_
64 QAM Outer Full Low CH

n77(20M)_DFT-s-OFDM_
256 QAM Outer Full Low CH

n77(20M)_CP-OFDM_
QPSK Outer Full Low CH


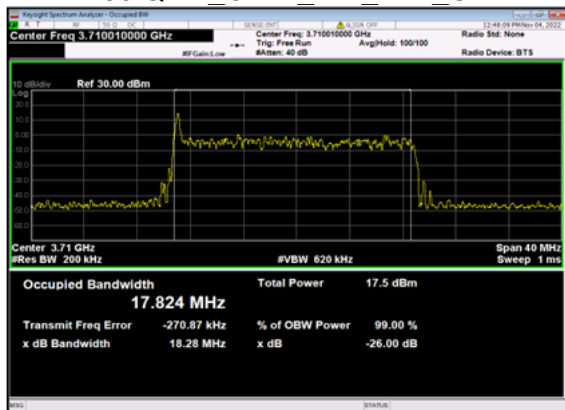
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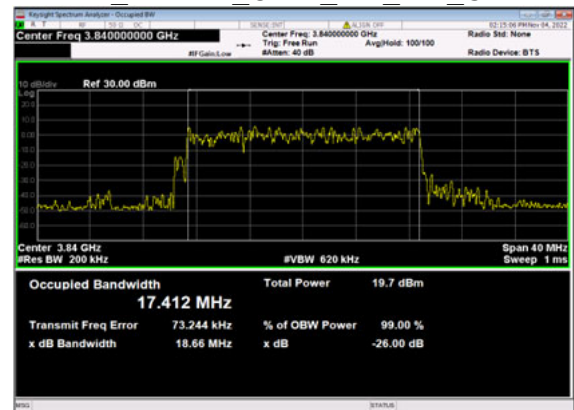
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64 QAM Outer Full Low CH



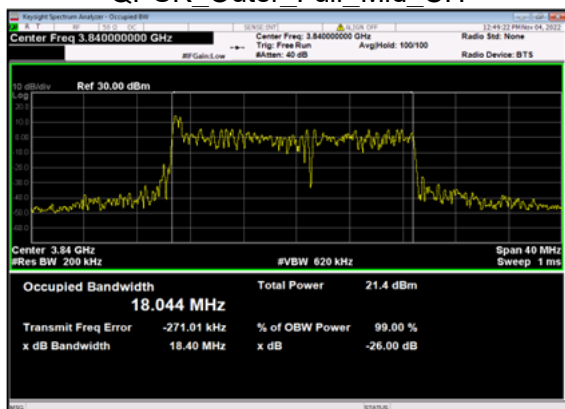
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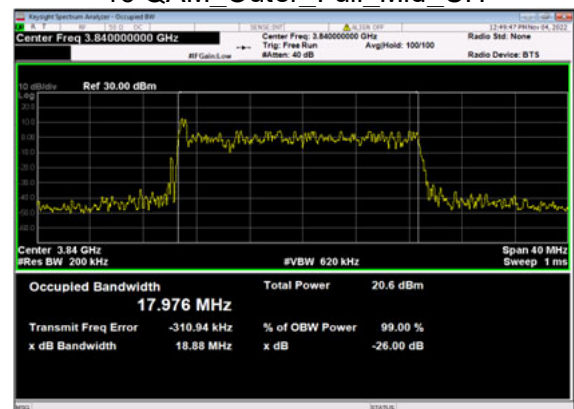
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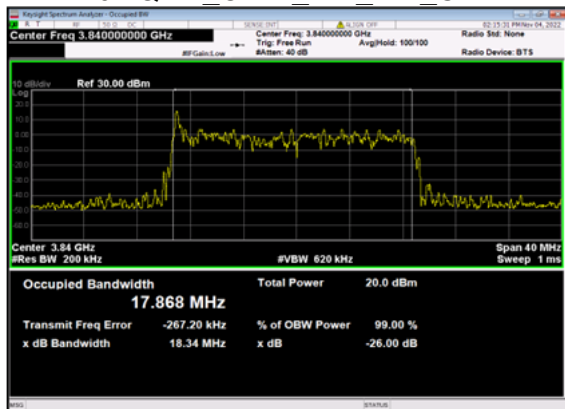
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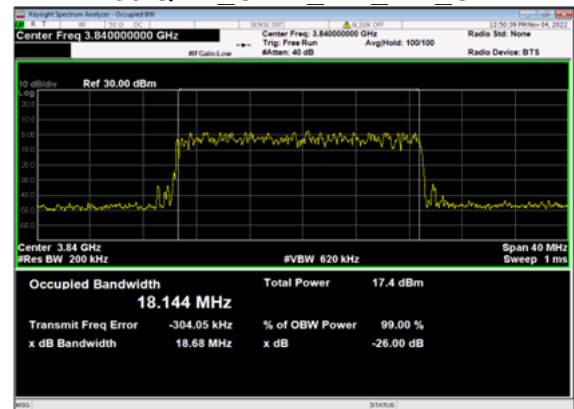
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16 QAM Outer Full Mid CH



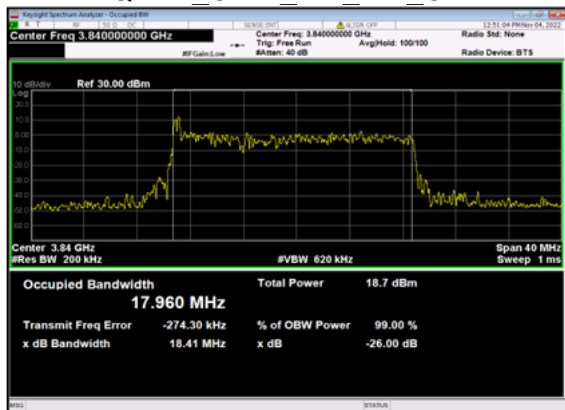
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64 QAM Outer Full Mid CH



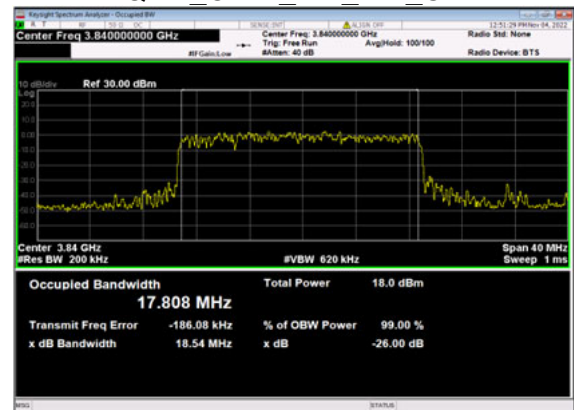
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256 QAM Outer Full Mid CH



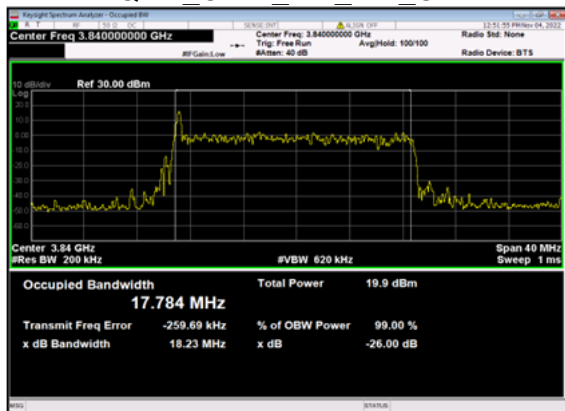
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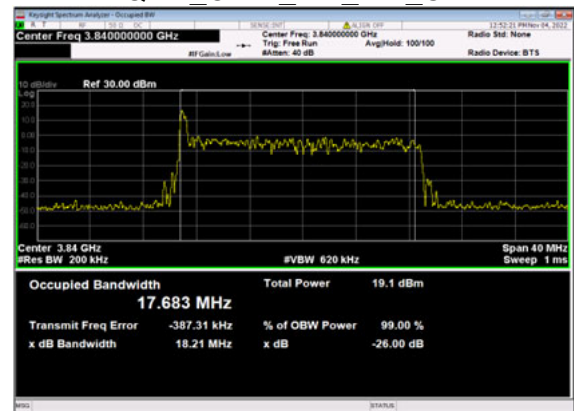
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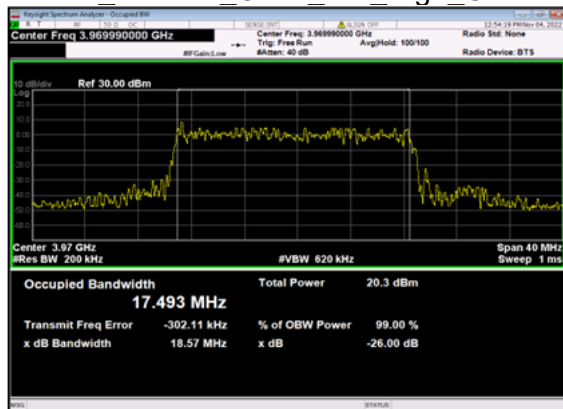
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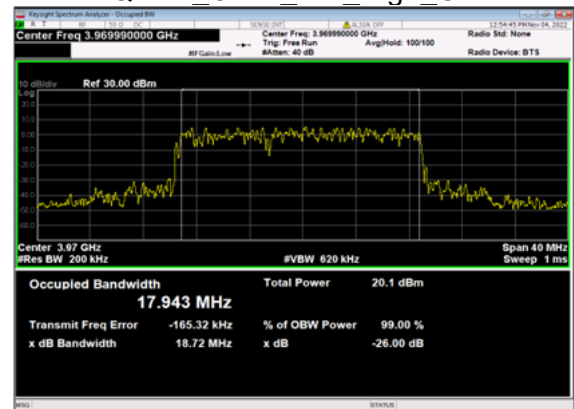
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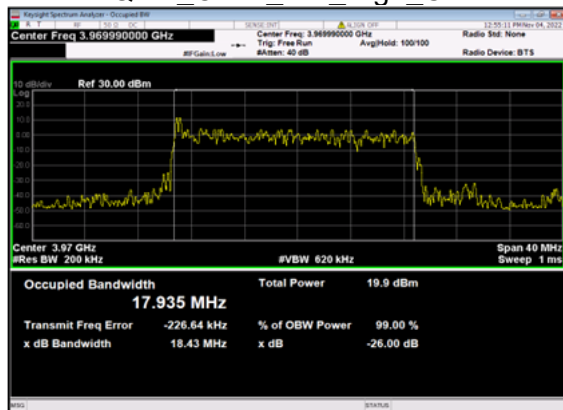
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PI 2-BPSK Outer Full High_CH



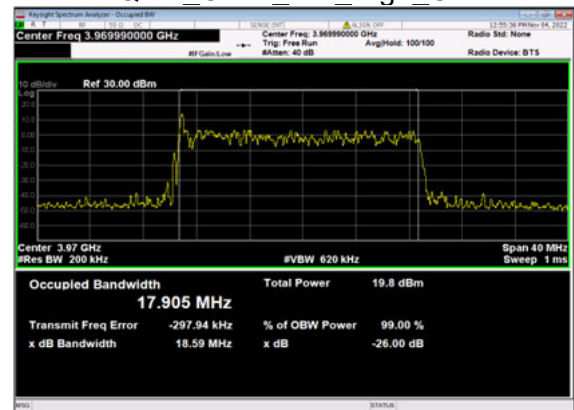
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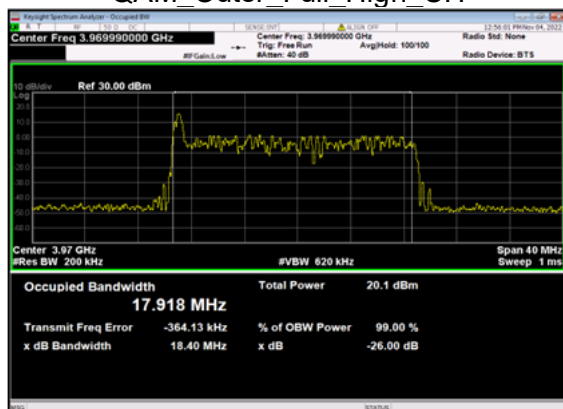
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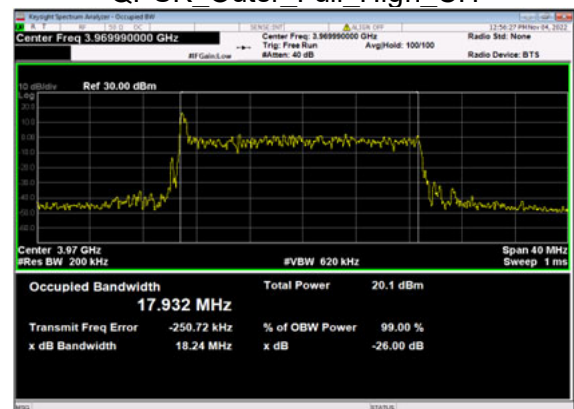
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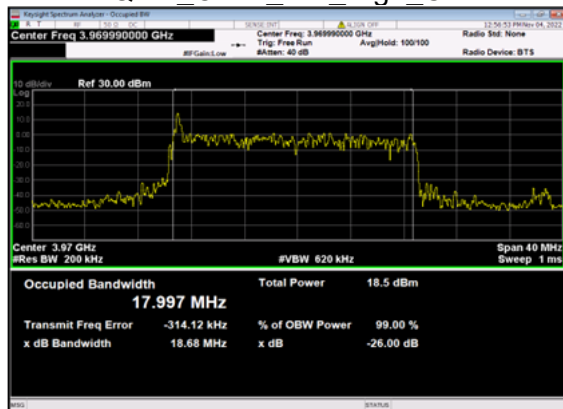
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QAM Outer Full High_CH



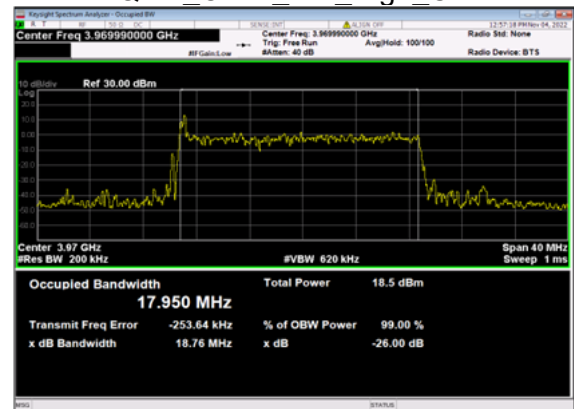
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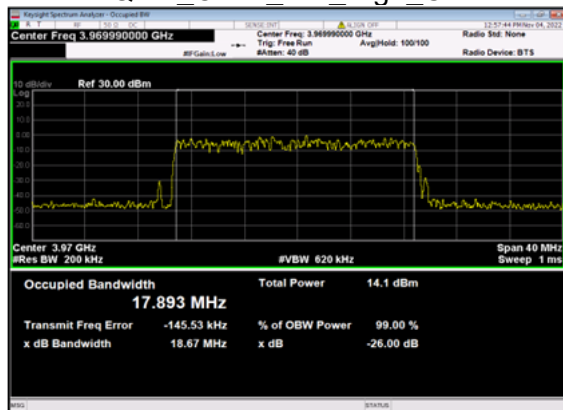
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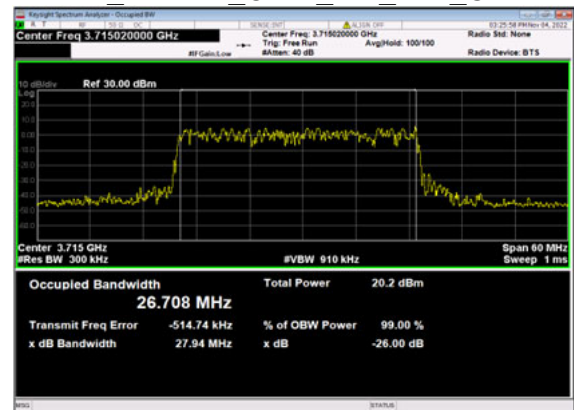
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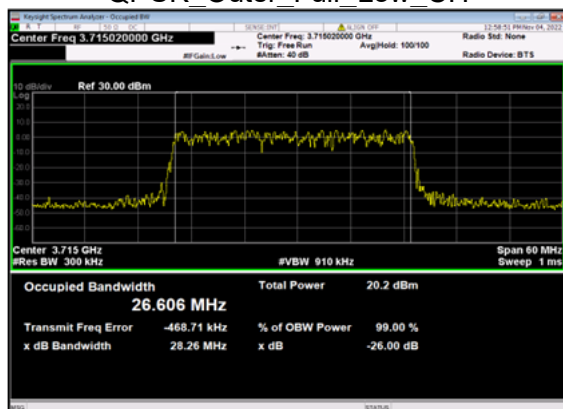
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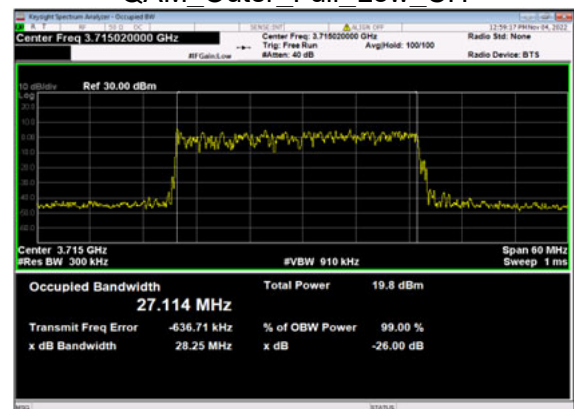
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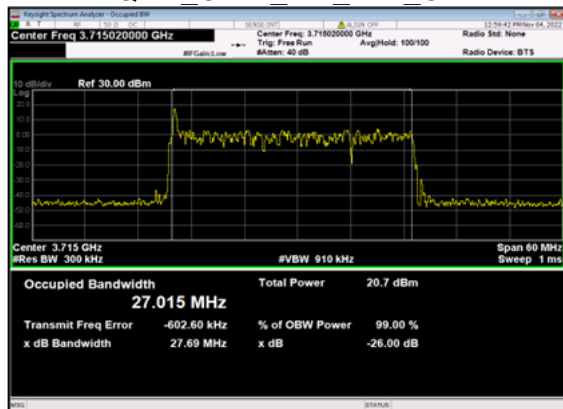
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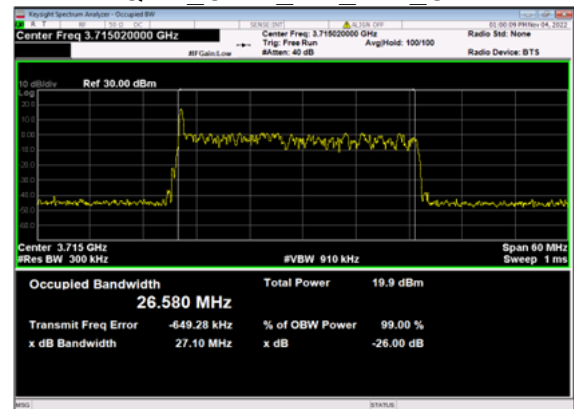
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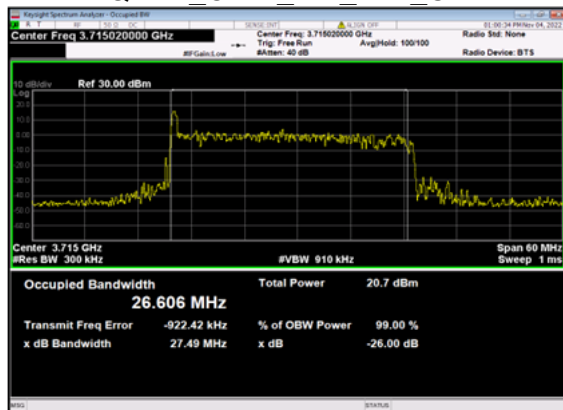
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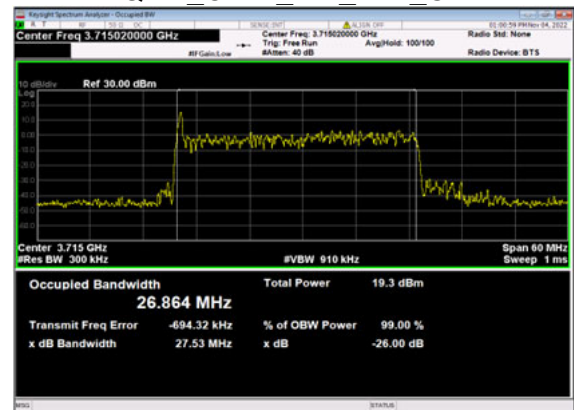
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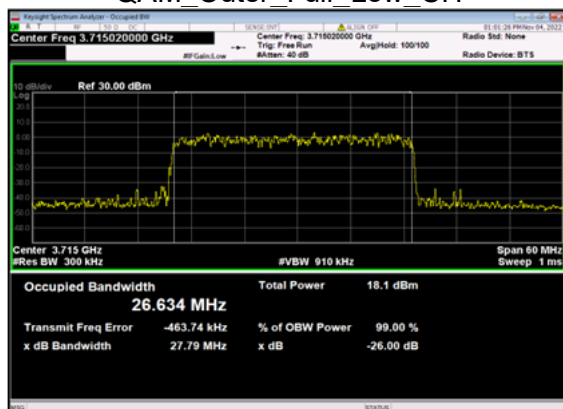
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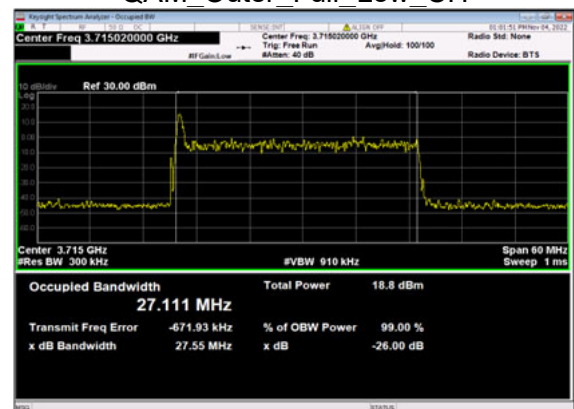
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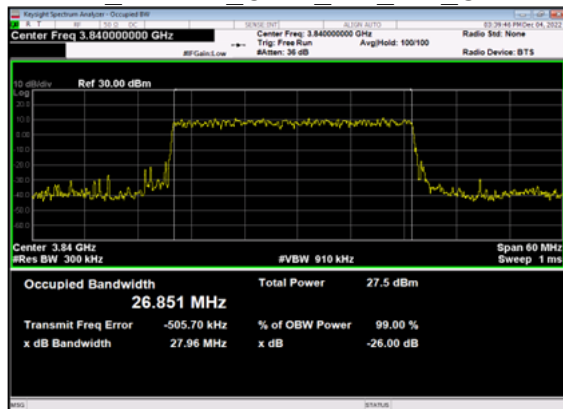
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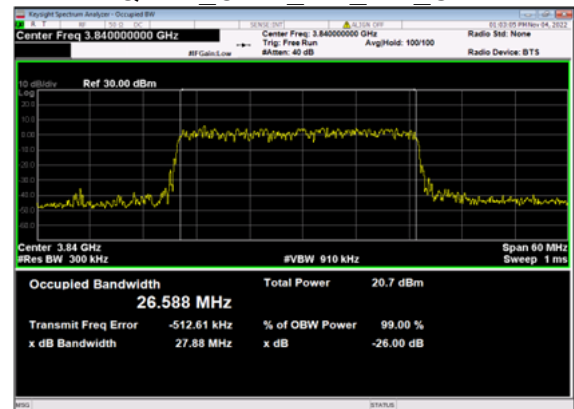
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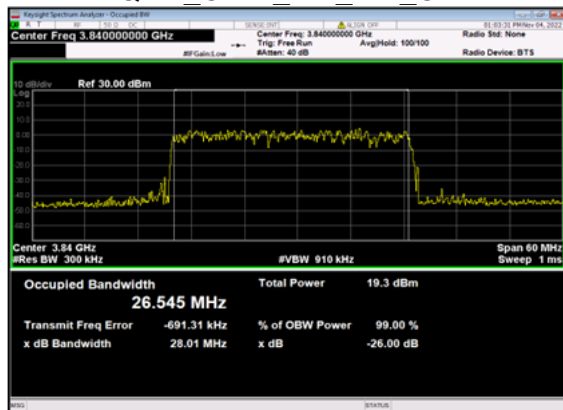
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PI 2-BPSK Outer Full Mid CH



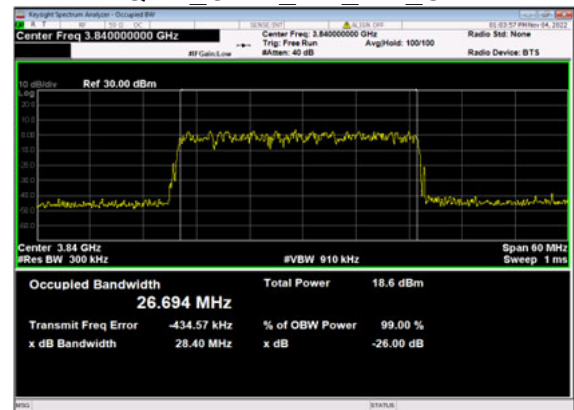
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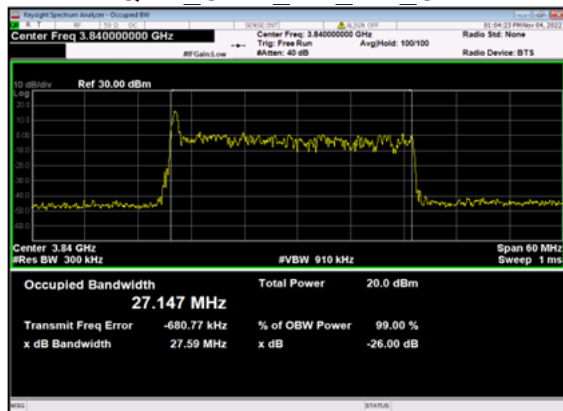
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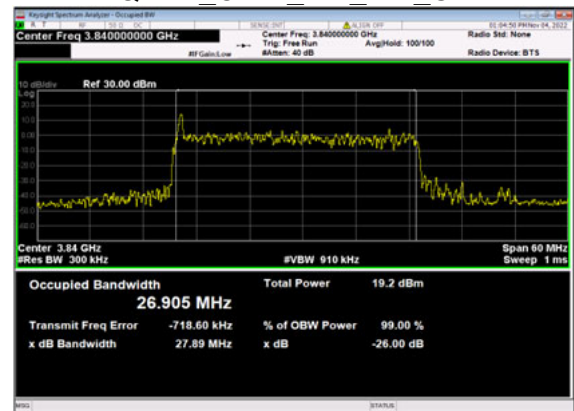
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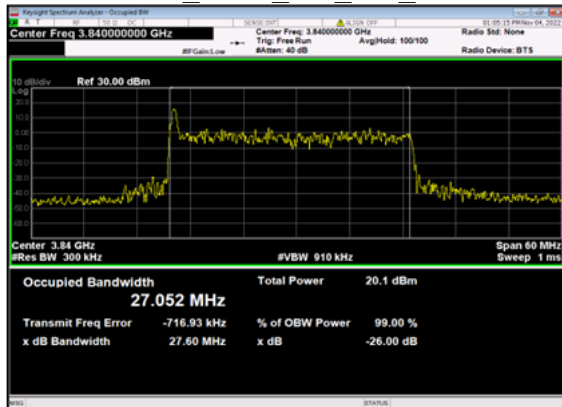
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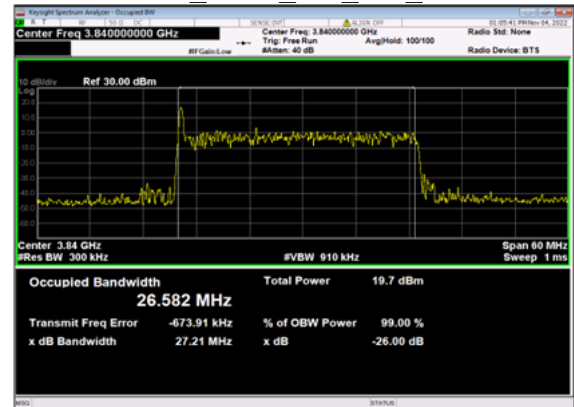
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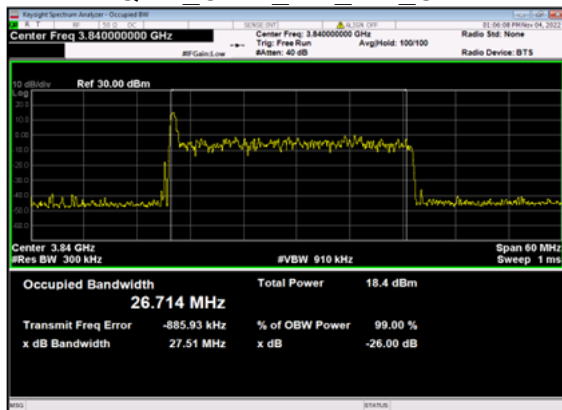
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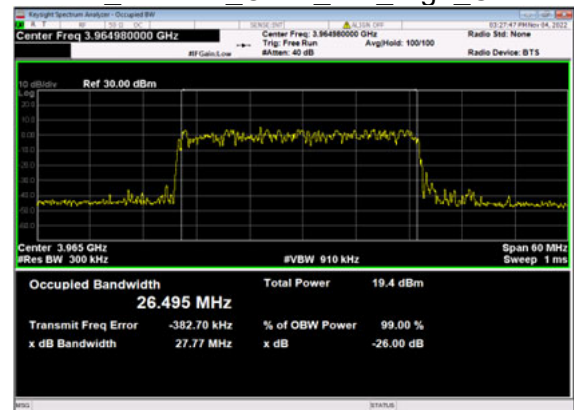
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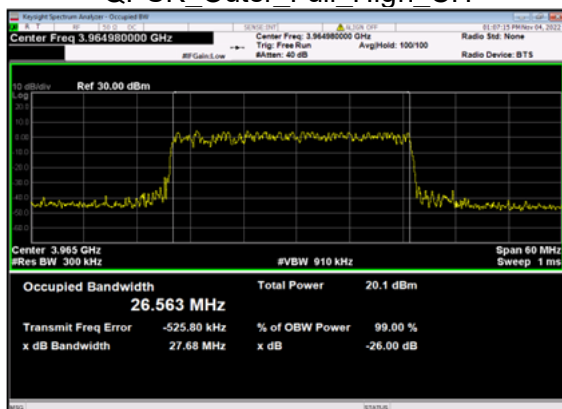
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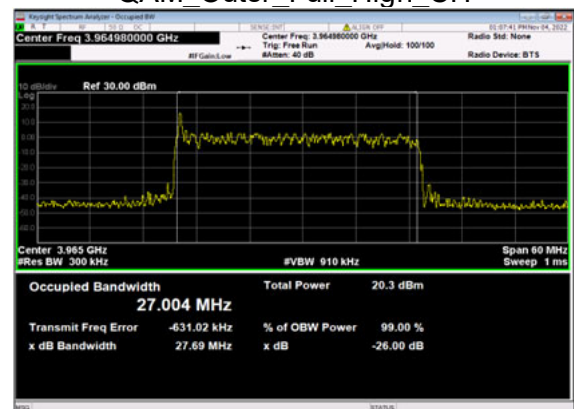
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PI 2-BPSK Outer Full High CH



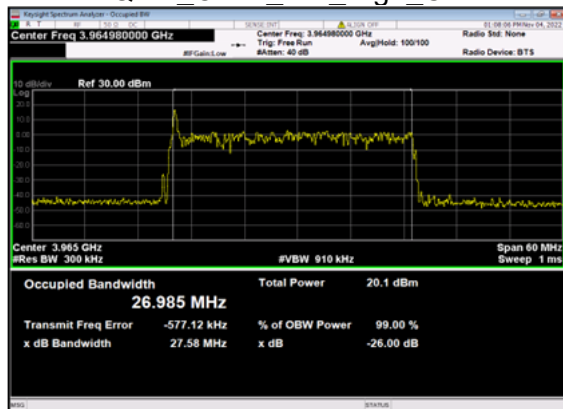
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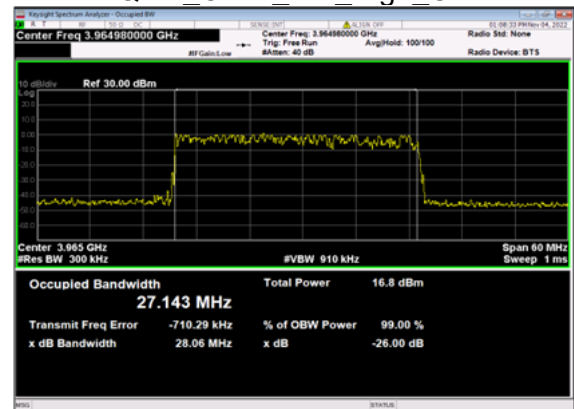
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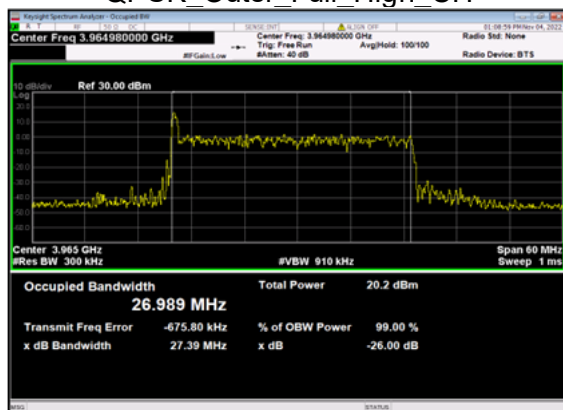
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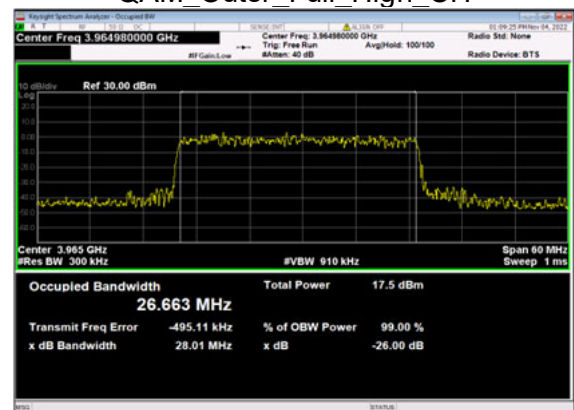
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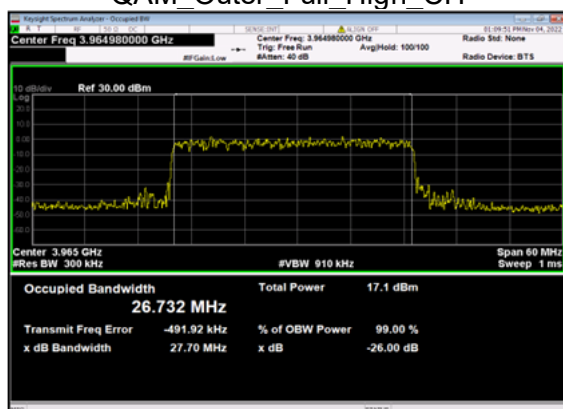
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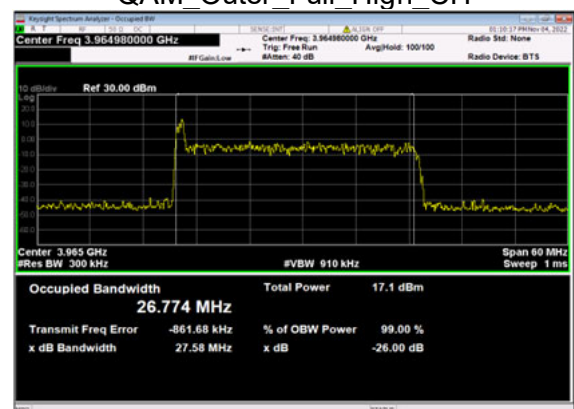
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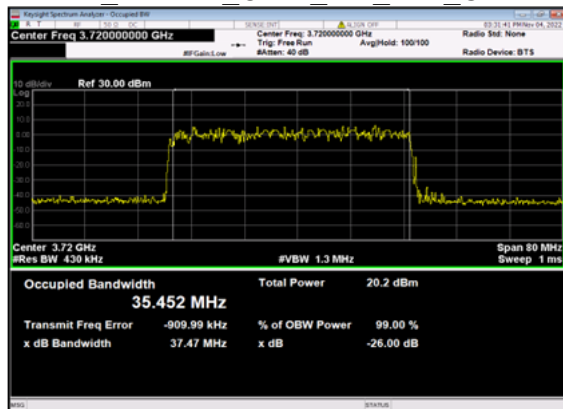
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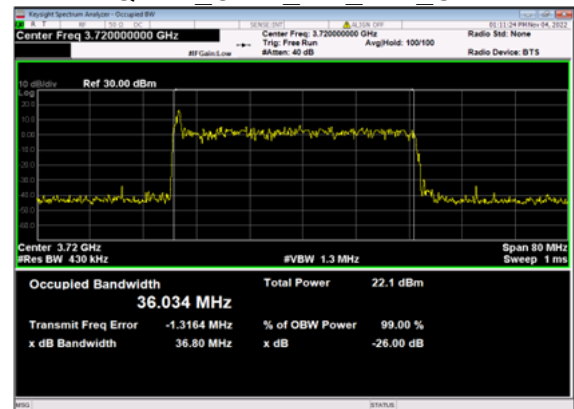
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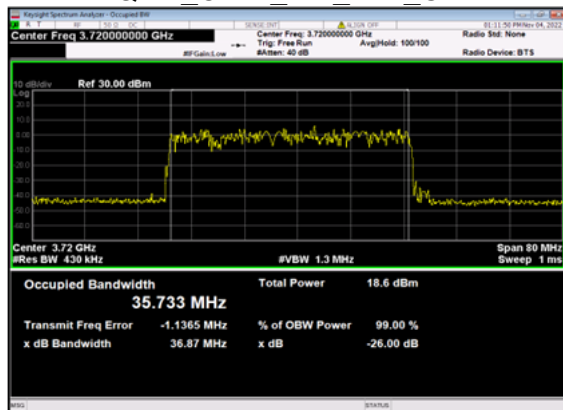
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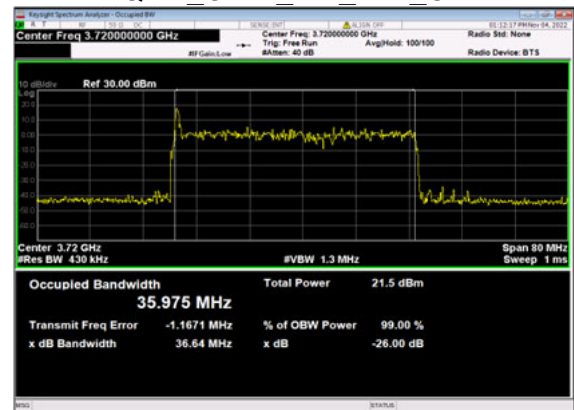
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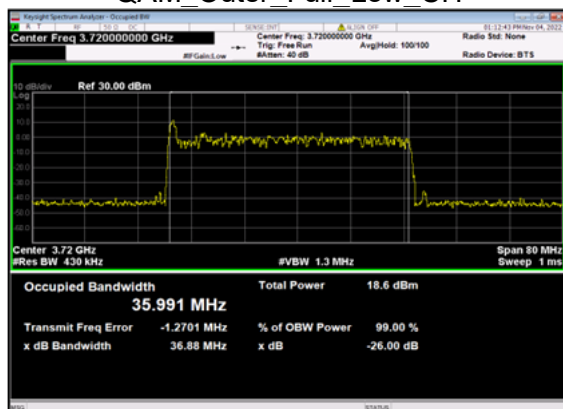
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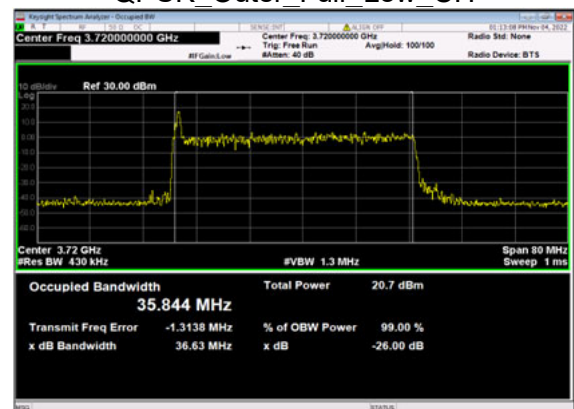
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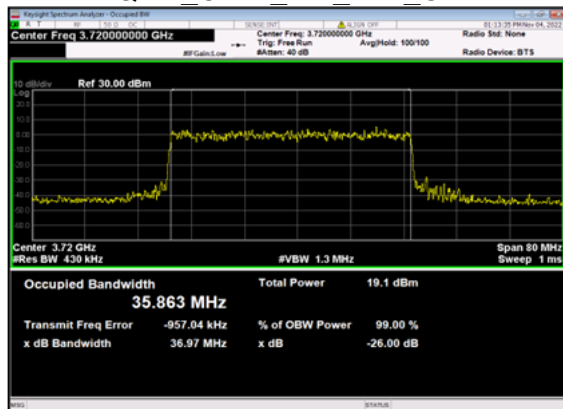
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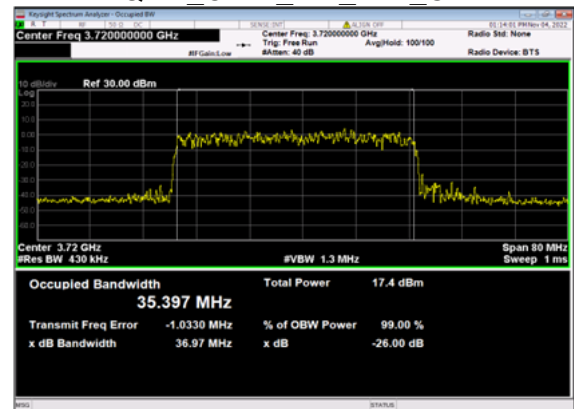
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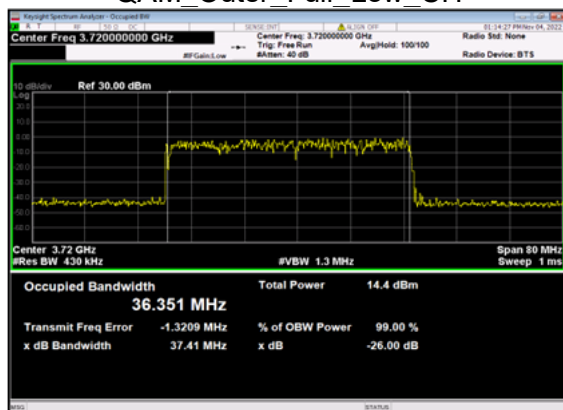
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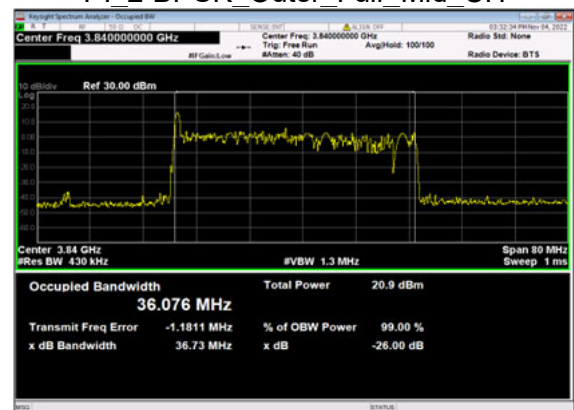
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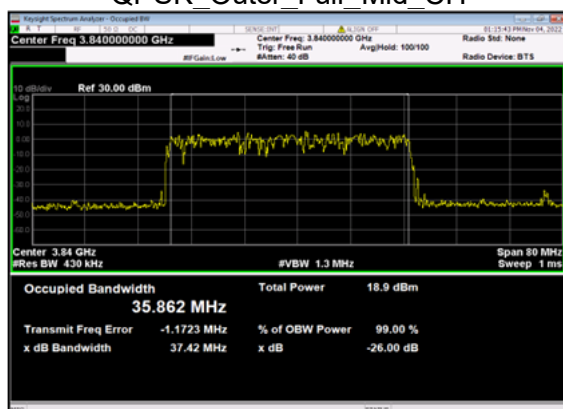
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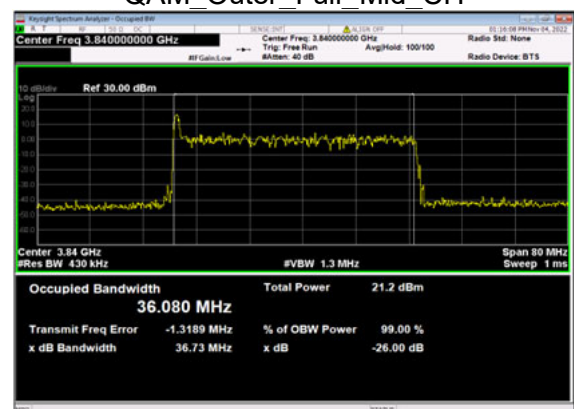
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PI 2-BPSK Outer Full Mid CH



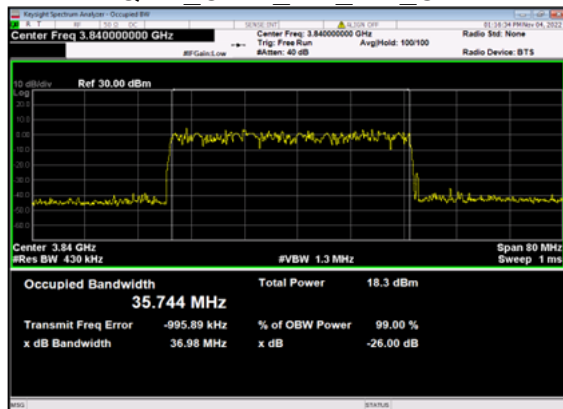
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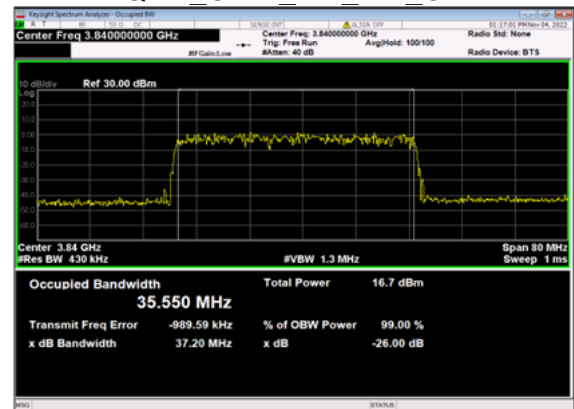
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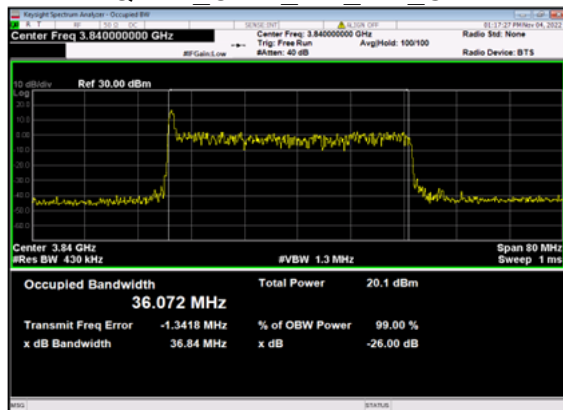
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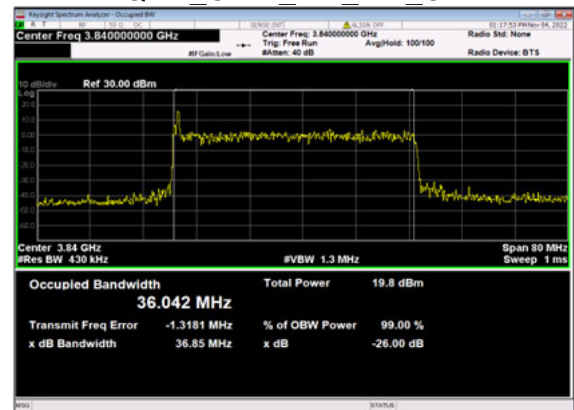
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QAM Outer Full Mid CH



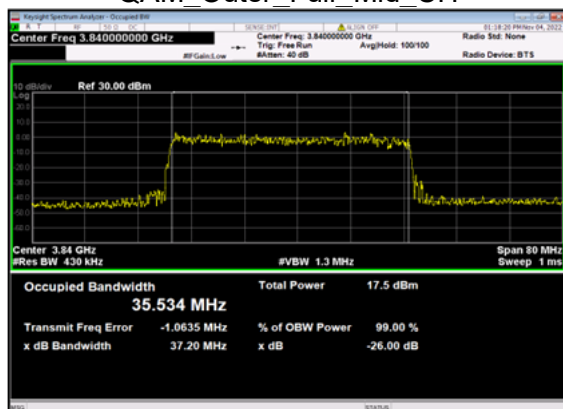
n77(40M)_CP-OFDM_
QPSK Outer Full Mid CH



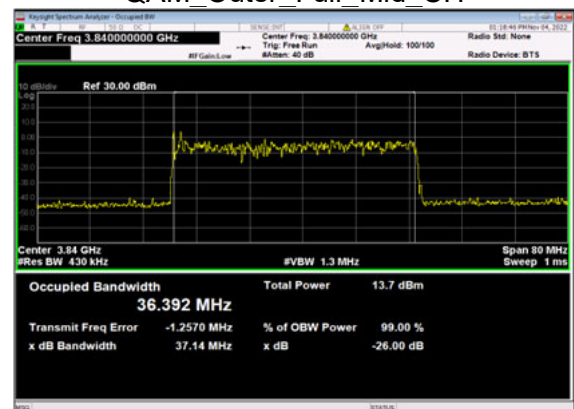
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QAM Outer Full Mid CH



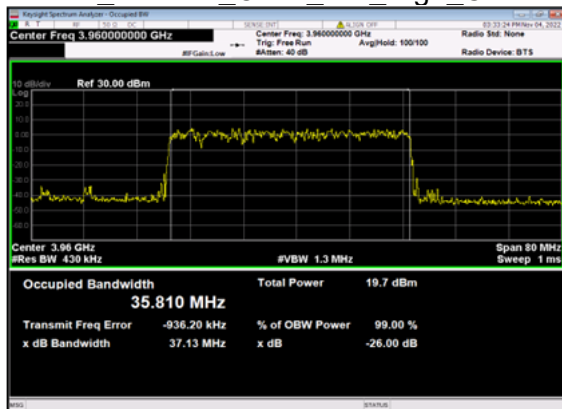
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QAM Outer Full Mid CH



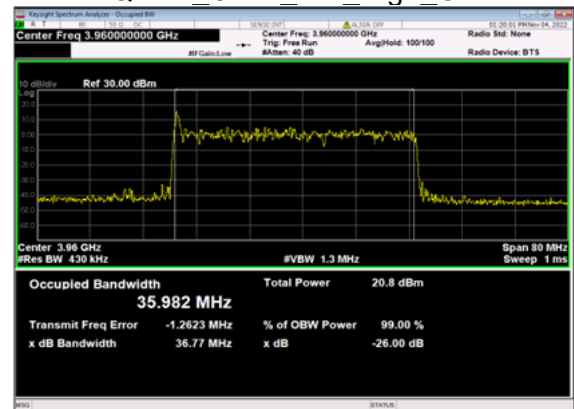
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QAM Outer Full Mid CH



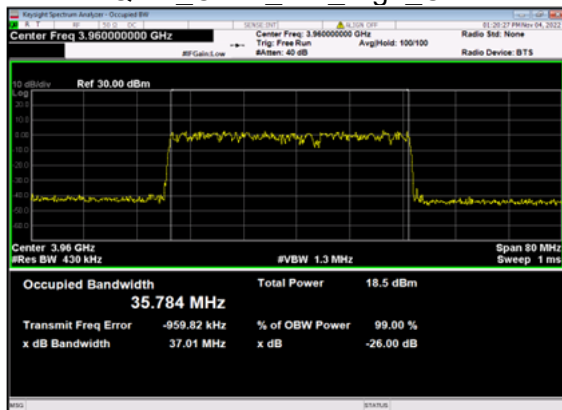
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PI 2-BPSK Outer Full High CH



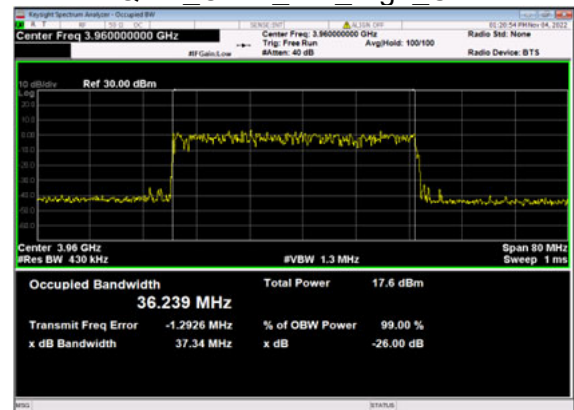
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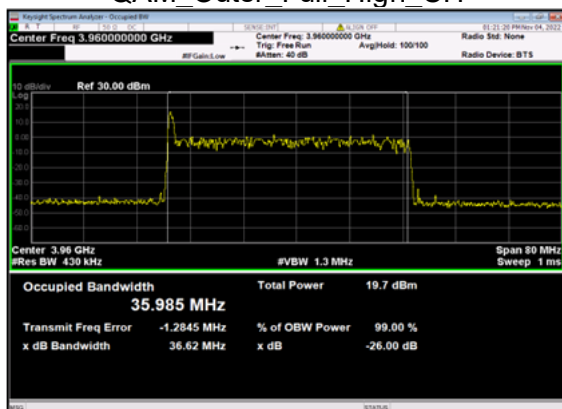
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QAM Outer Full High CH



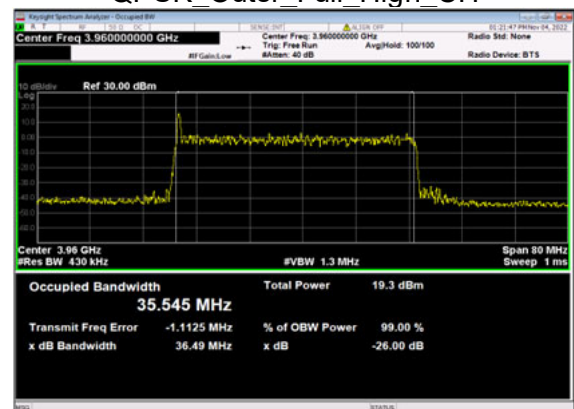
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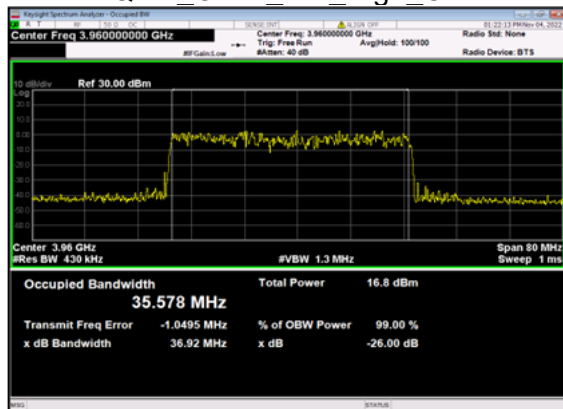
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QAM Outer Full High CH



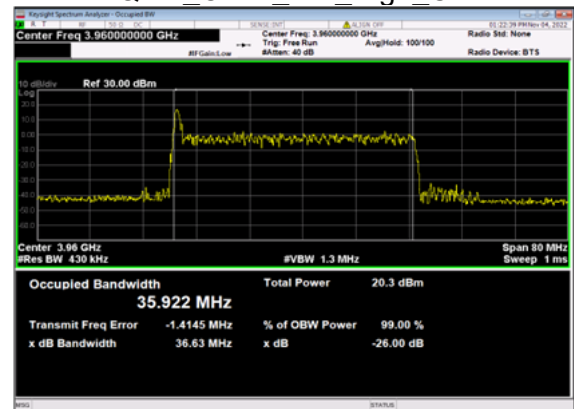
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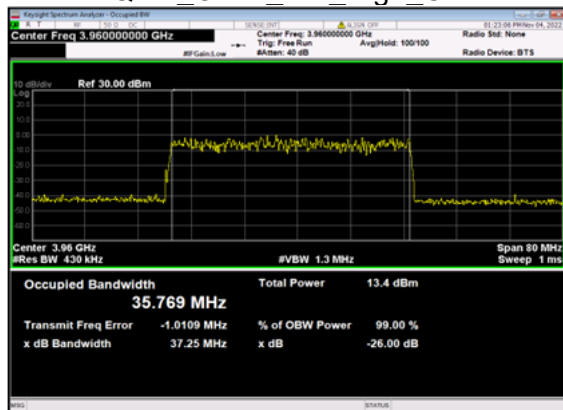
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QAM Outer Full High CH



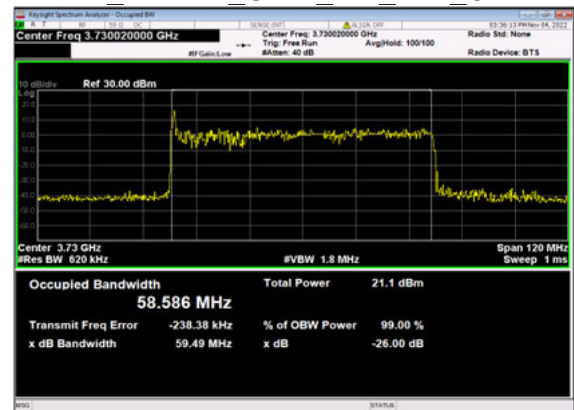
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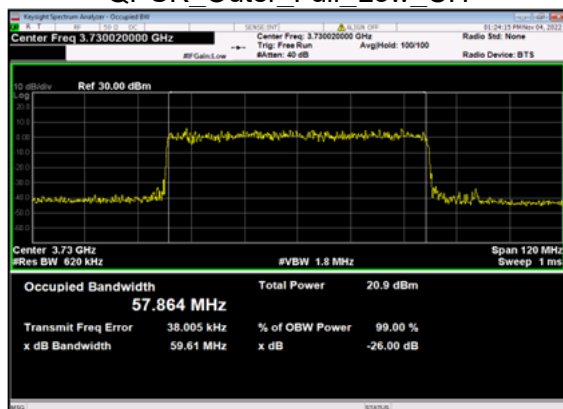
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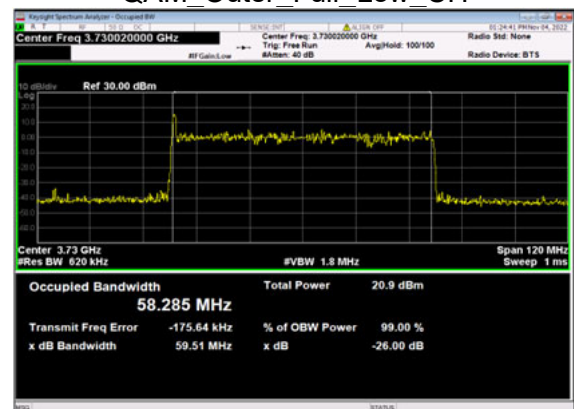
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PI 2-BPSK Outer Full Low CH

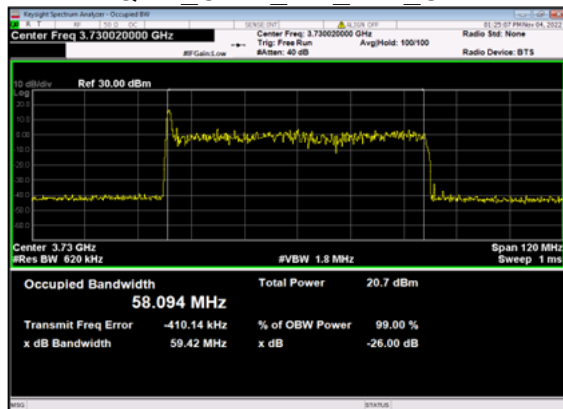
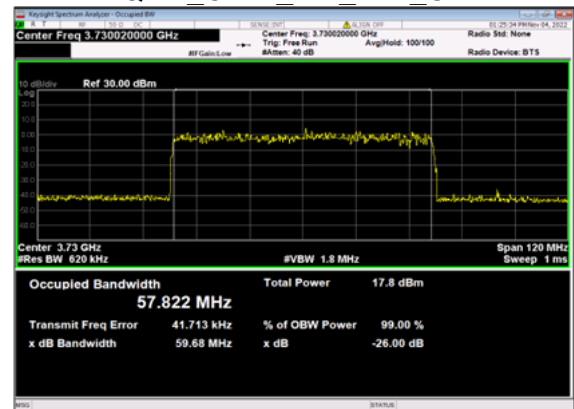
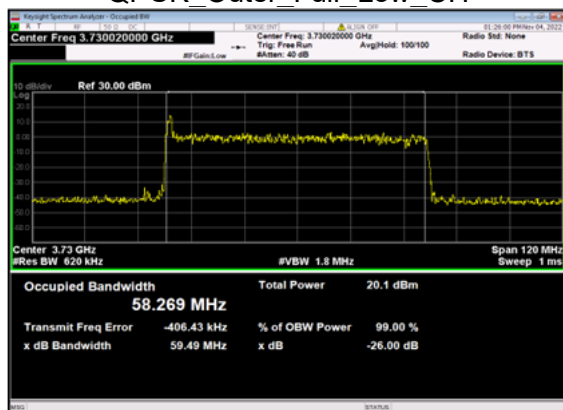
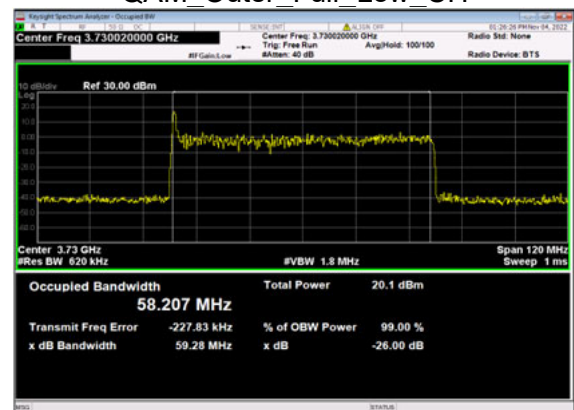
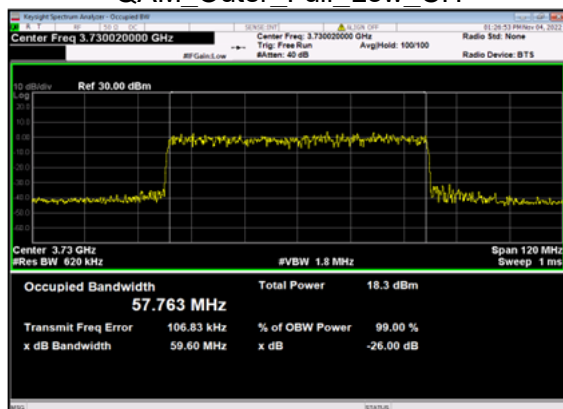
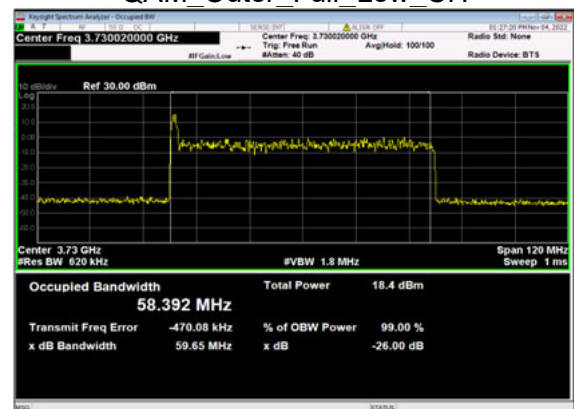


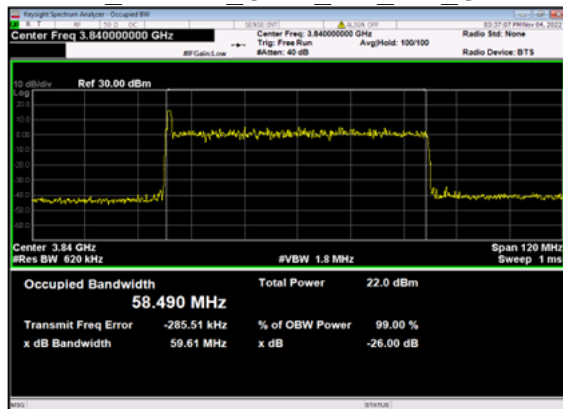
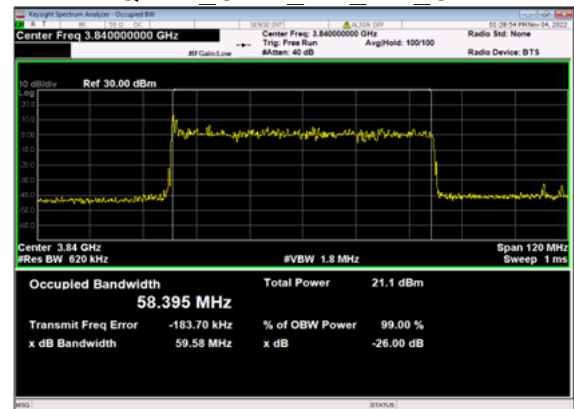
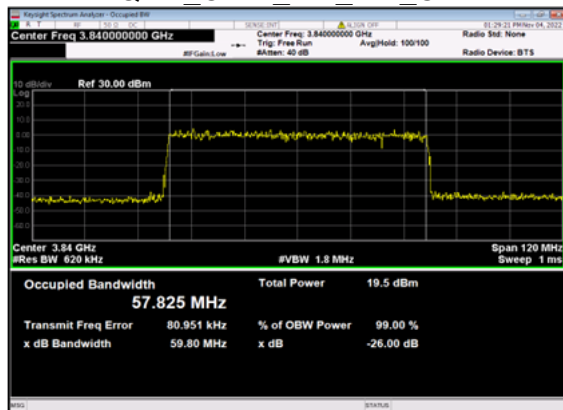
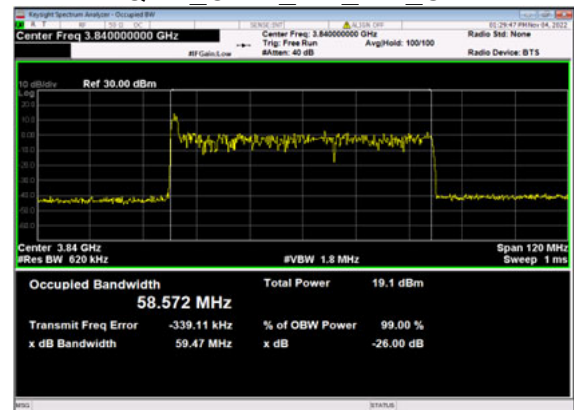
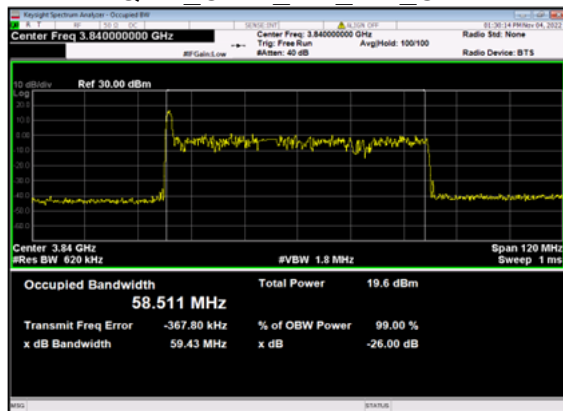
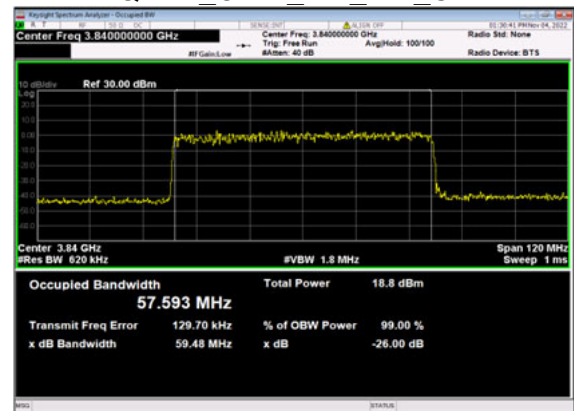
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QPSK Outer Full Low CH



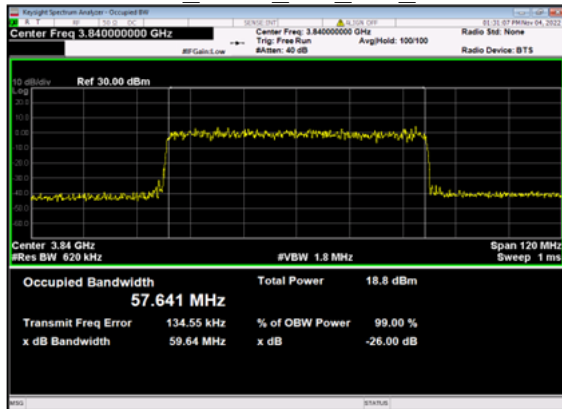
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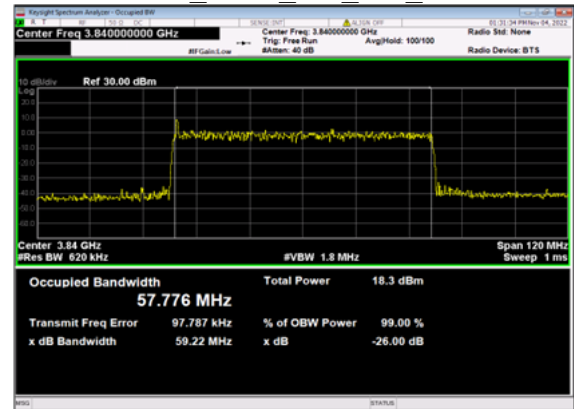
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QAM Outer Full Low CHn77(60M)_DFT-s-OFDM_256
QAM Outer Full Low CHn77(60M)_CP-OFDM_
QPSK Outer Full Low CHn77(60M)_CP-OFDM_16
QAM Outer Full Low CHn77(60M)_CP-OFDM_64
QAM Outer Full Low CHn77(60M)_CP-OFDM_256
QAM Outer Full Low CH

n77(60M)_DFT-s-OFDM_
PI 2-BPSK Outer Full Mid CH

n77(60M)_DFT-s-OFDM_
QPSK Outer Full Mid CH

n77(60M)_DFT-s-OFDM_16
QAM Outer Full Mid CH

n77(60M)_DFT-s-OFDM_64
QAM Outer Full Mid CH

n77(60M)_DFT-s-OFDM_256
QAM Outer Full Mid CH

n77(60M)_CP-OFDM_
QPSK Outer Full Mid CH


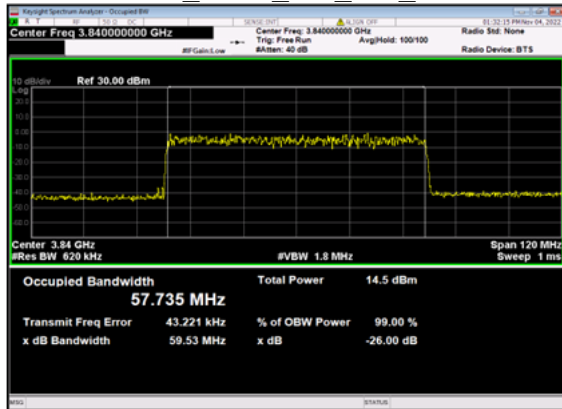
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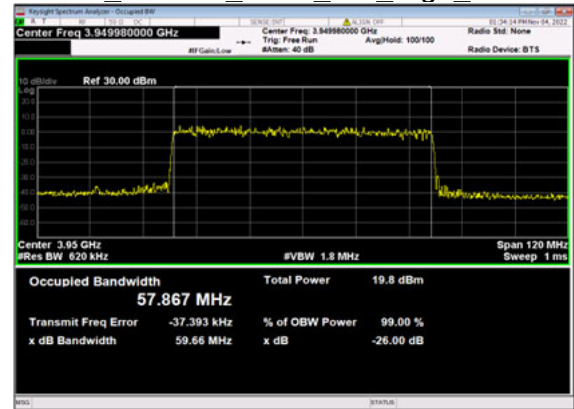
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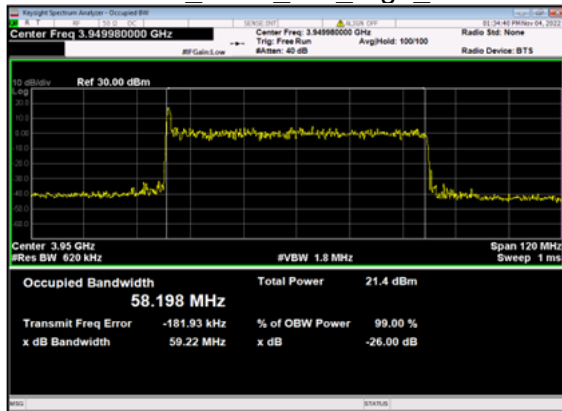
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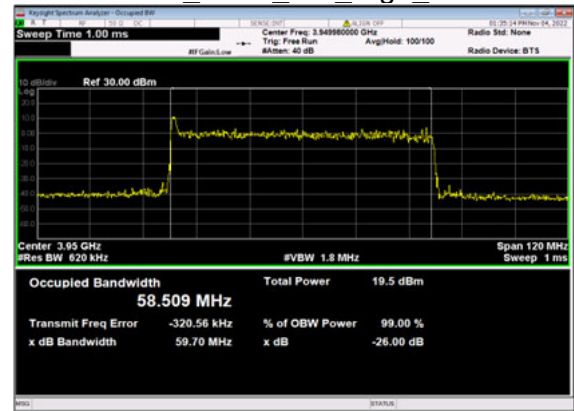
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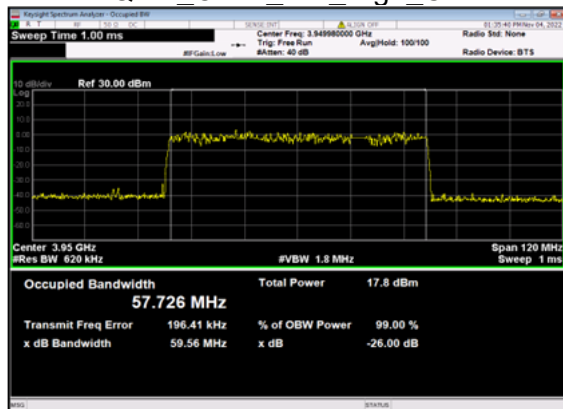
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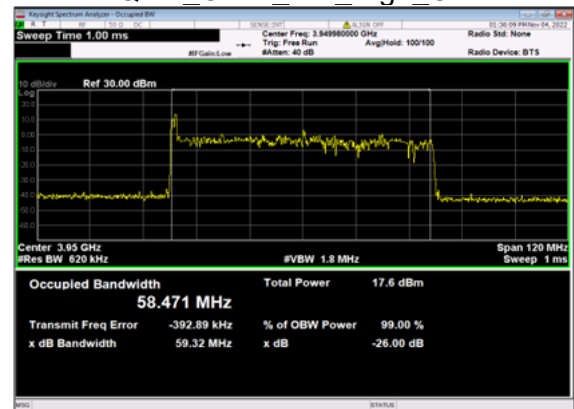
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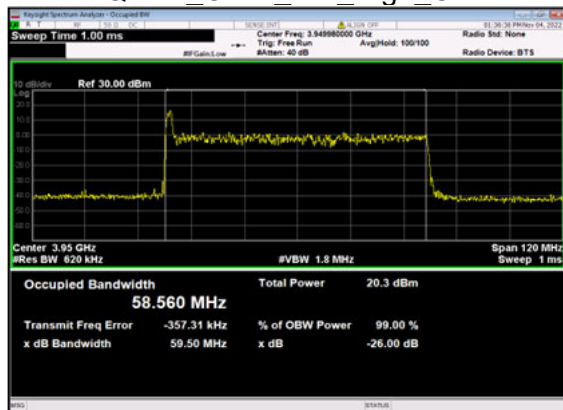
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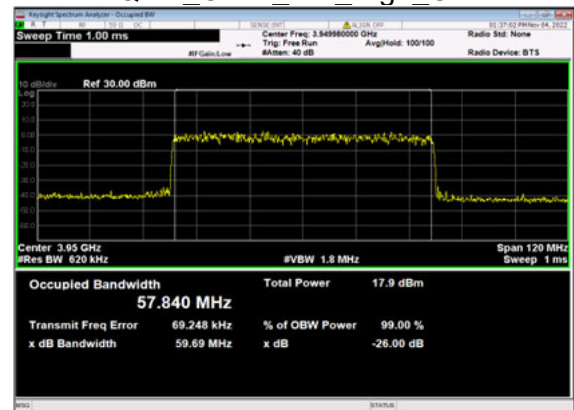
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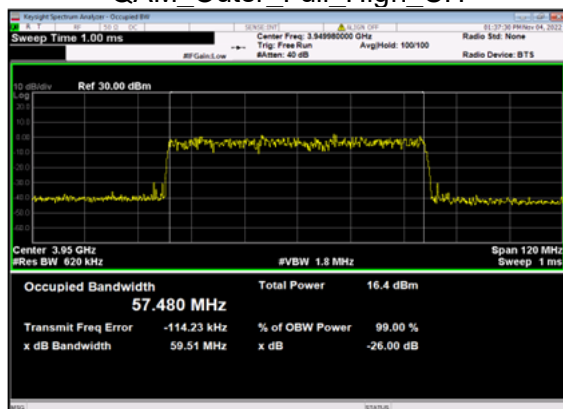
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QPSK_Outer_Full_High_CH



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QAM_Outer_Full_High_CH



n77(60M)_CP-OFDM_64
QAM_Outer_Full_High_CH



n77(60M)_CP-OFDM_256
QAM_Outer_Full_High_CH

