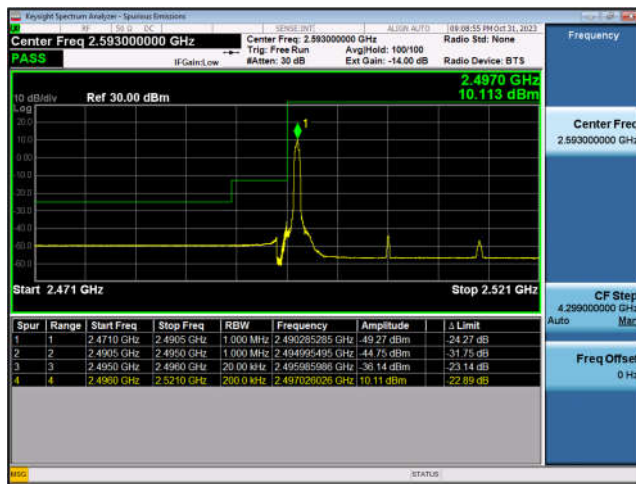
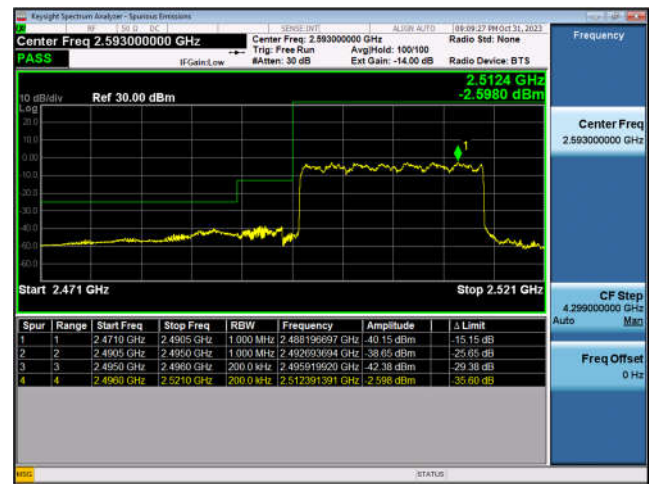


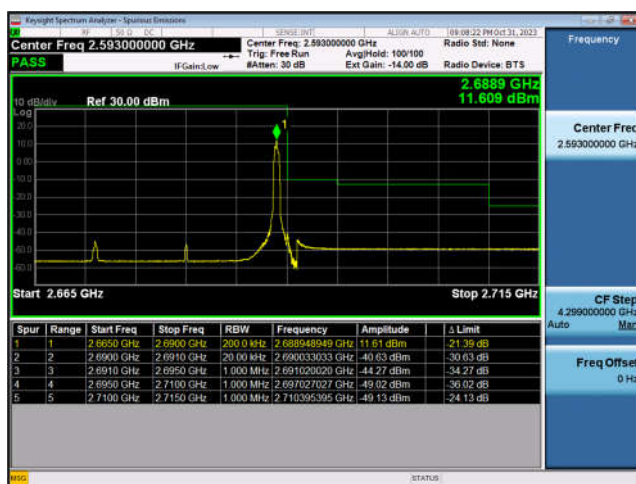
N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



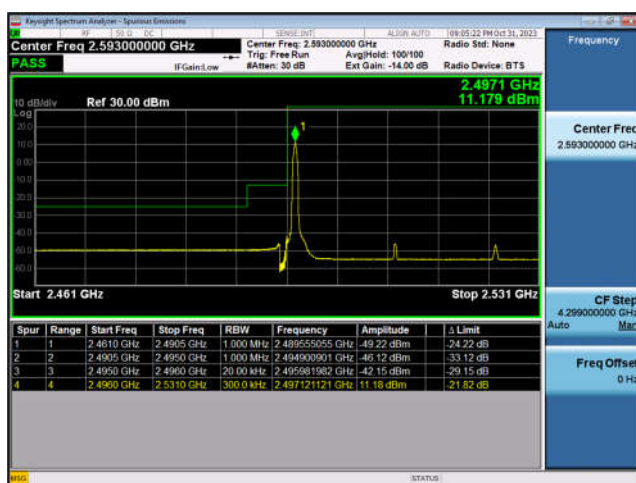
N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



N41_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

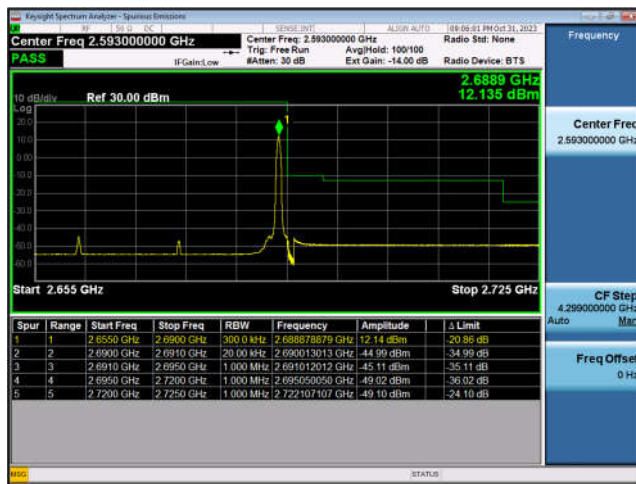


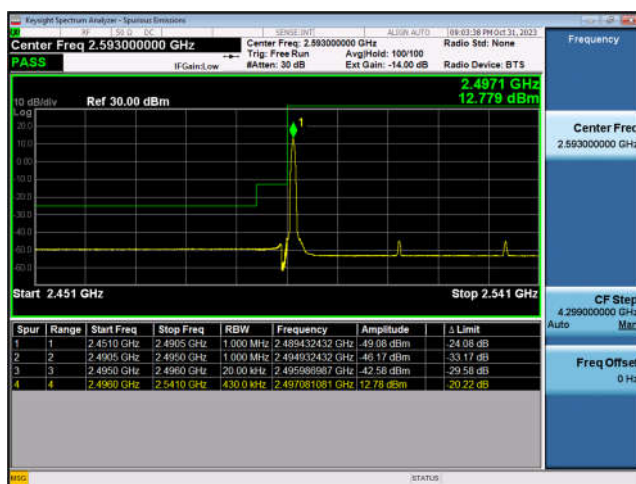
N41_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

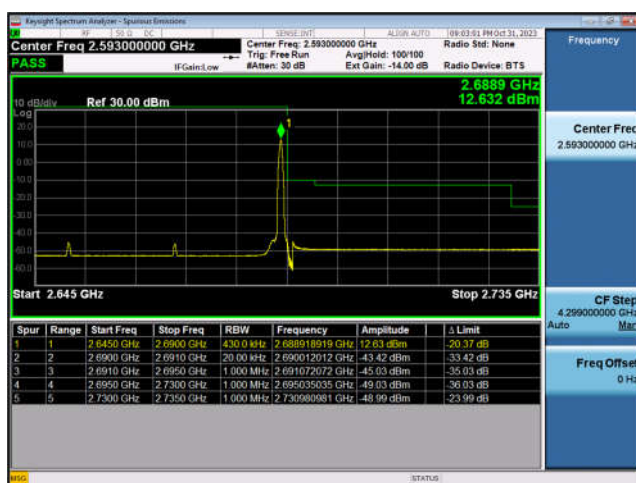


N41_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

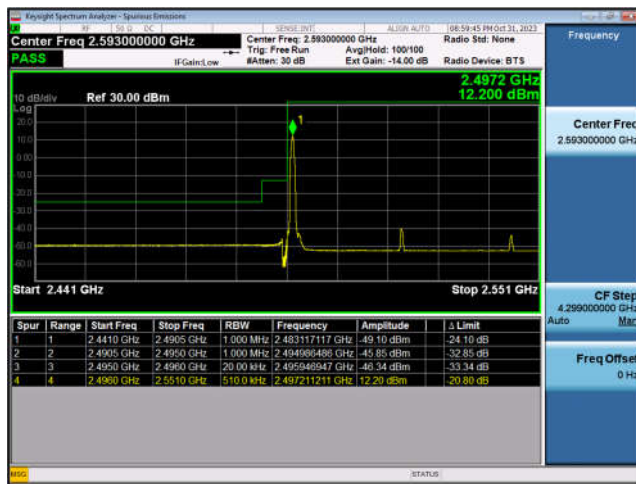


N41_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

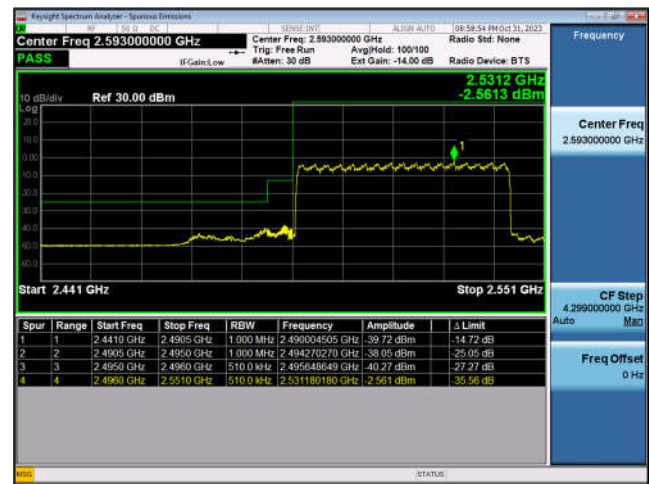
N41_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N41_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N41_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

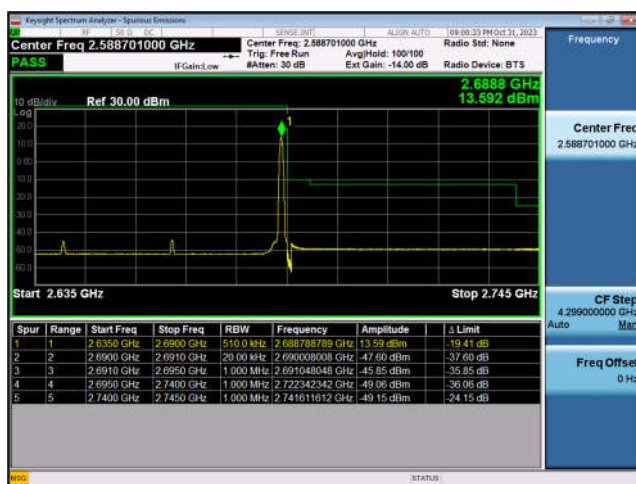

N41_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N41_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



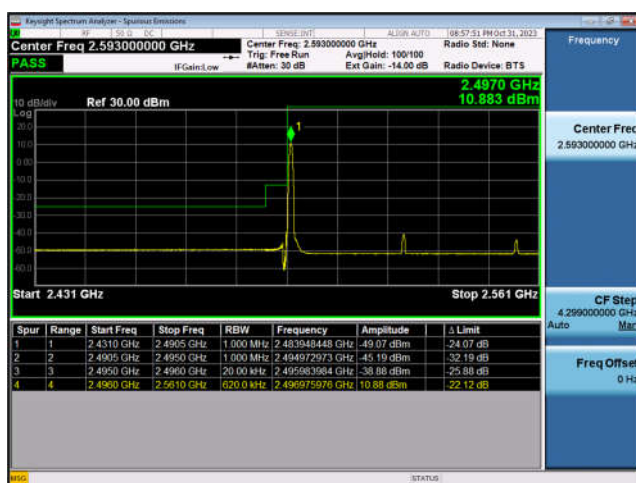
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1RB_Right_High_CH



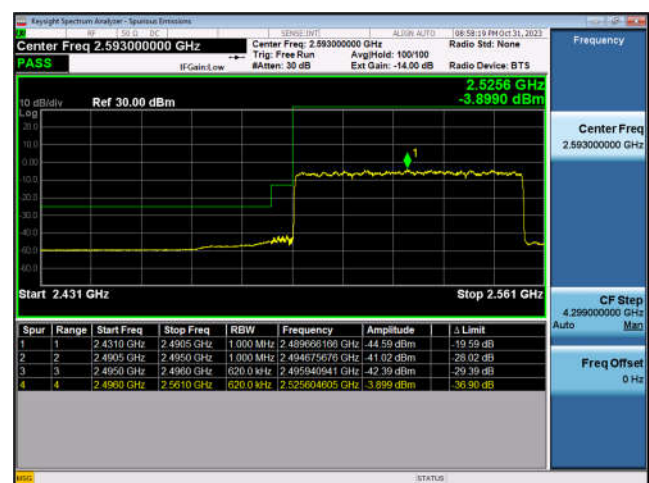
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Outer_Full_High_CH

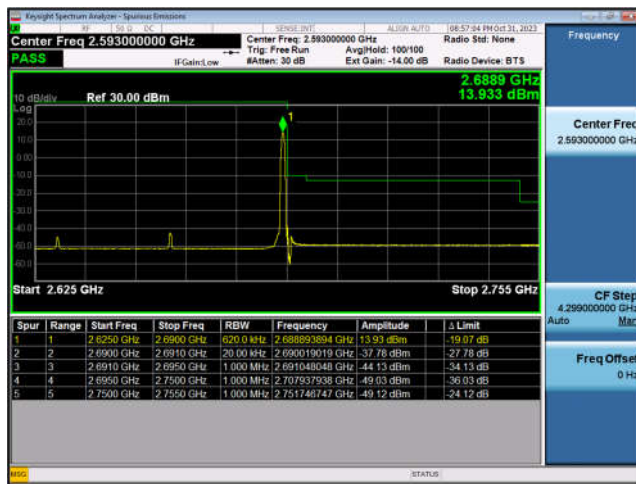


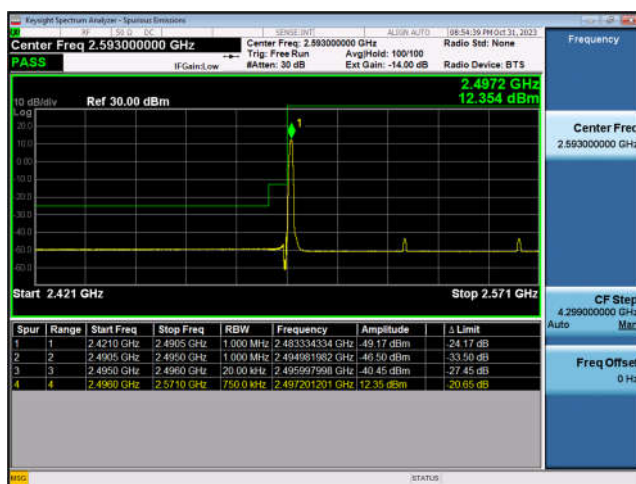
N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

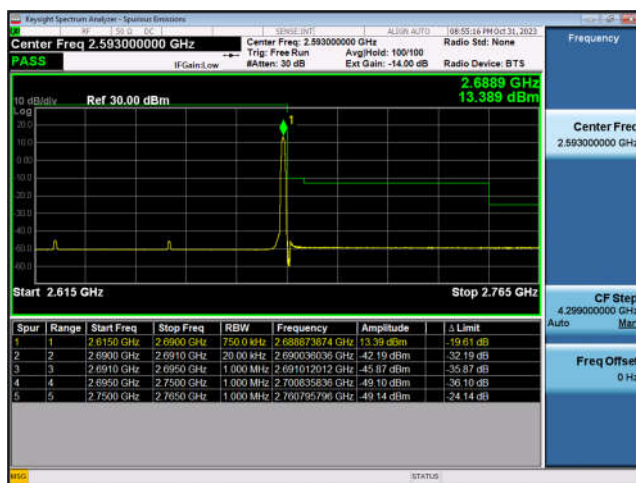


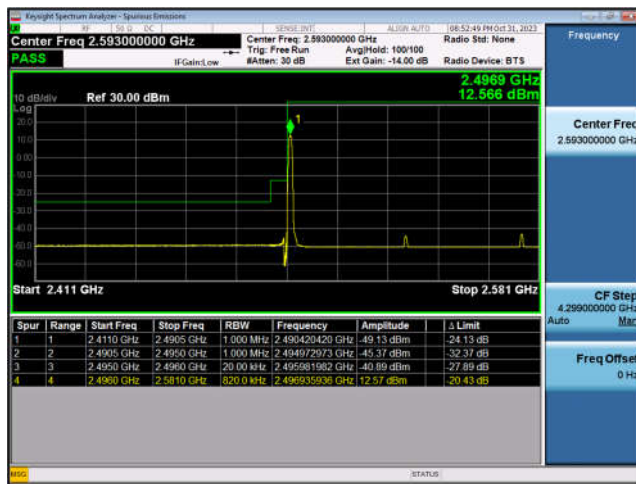
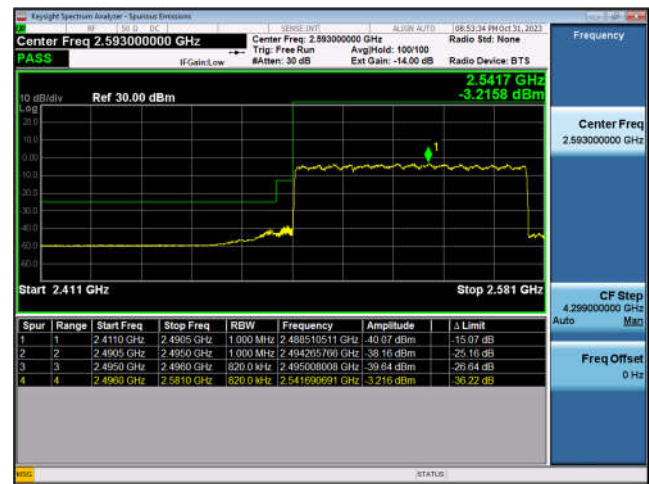
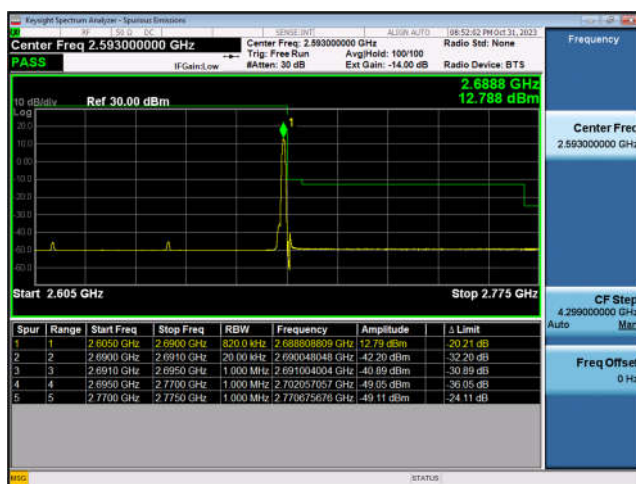
N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

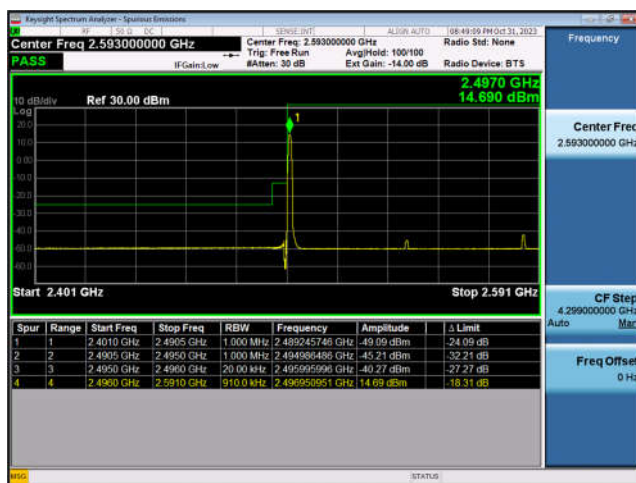


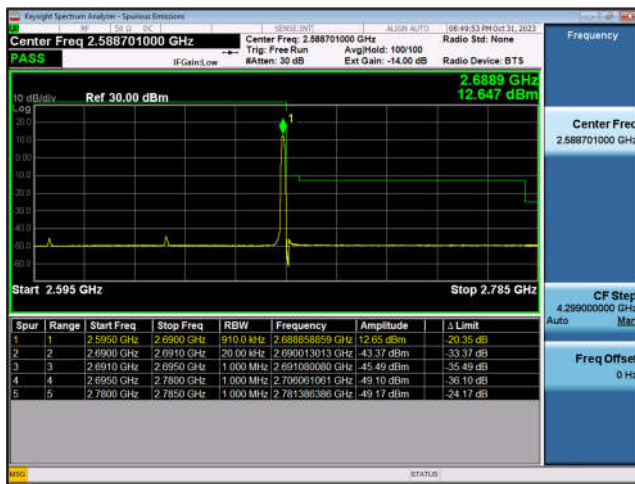
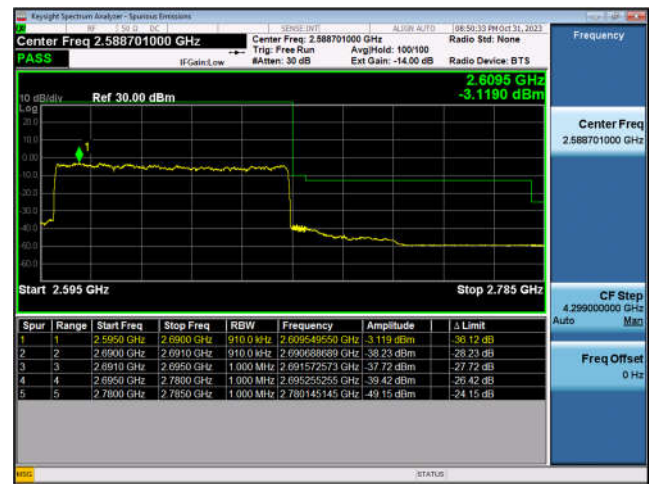
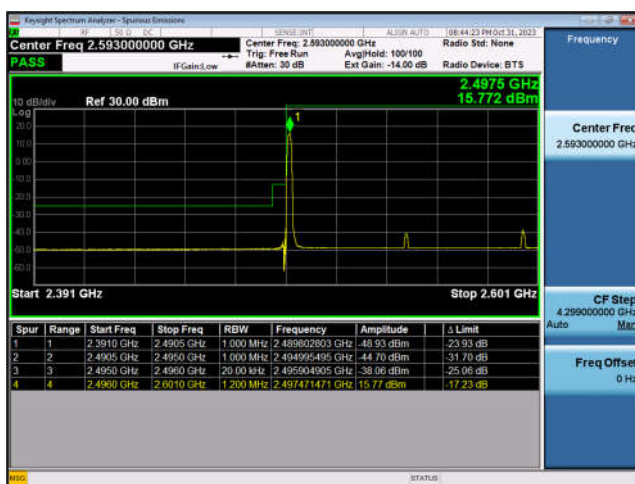
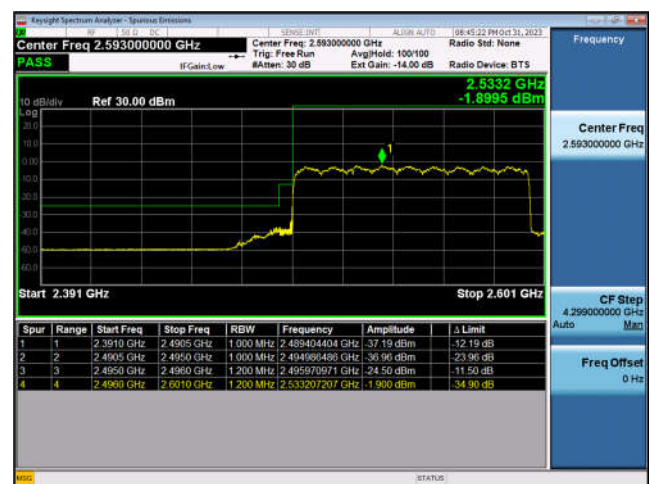
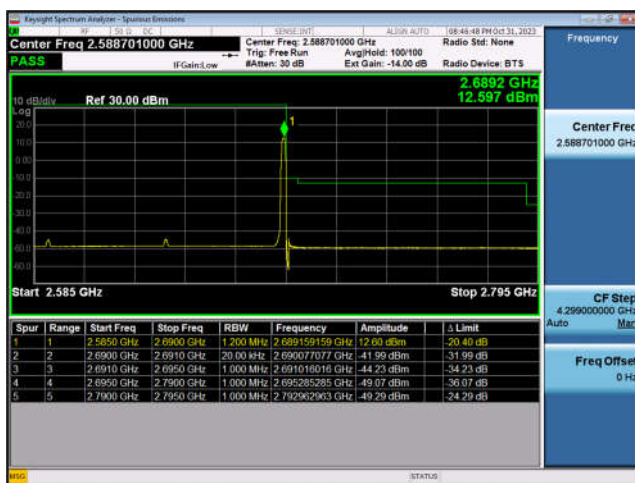
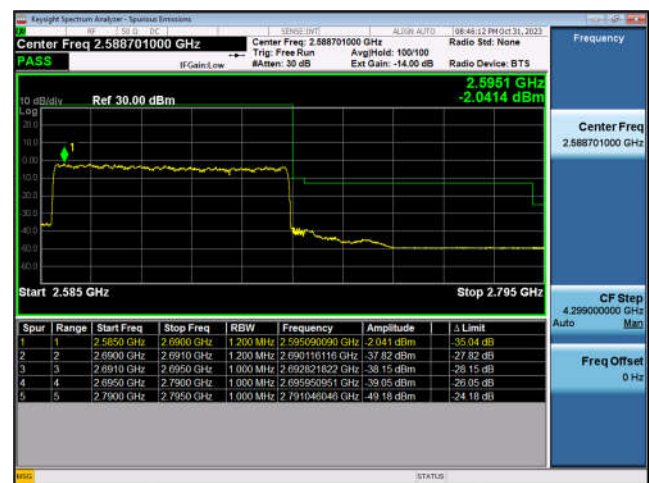
N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

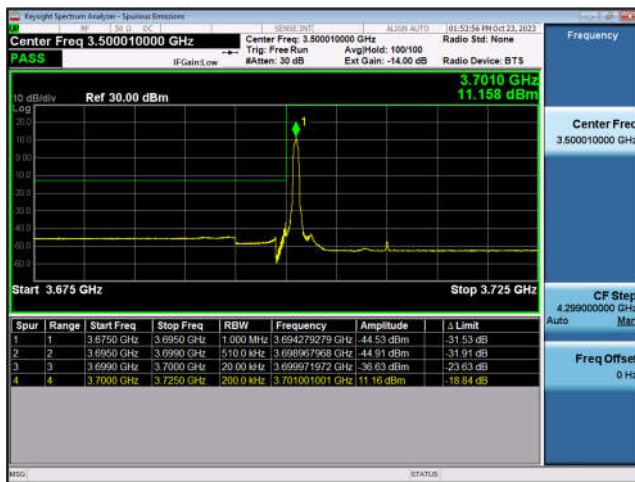
N41_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N41_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

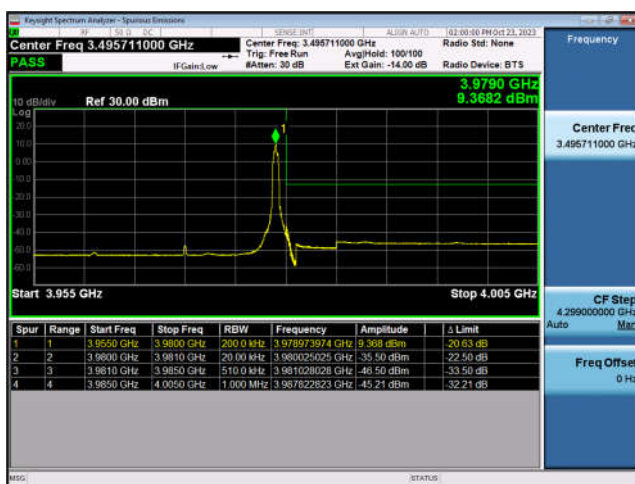
N41_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

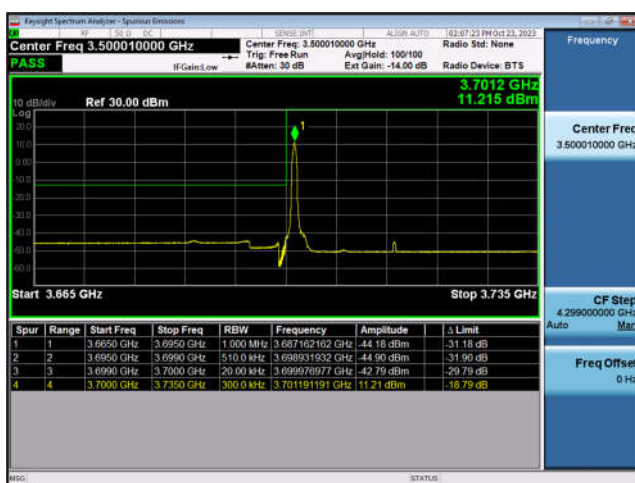

N41_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N41_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N41_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

N41_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N41_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

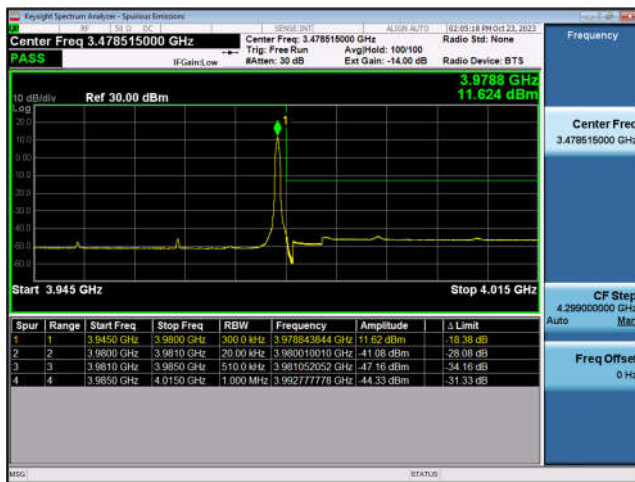

N41_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N41_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH


N77_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N77_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N77_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N77_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

N77_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N77_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

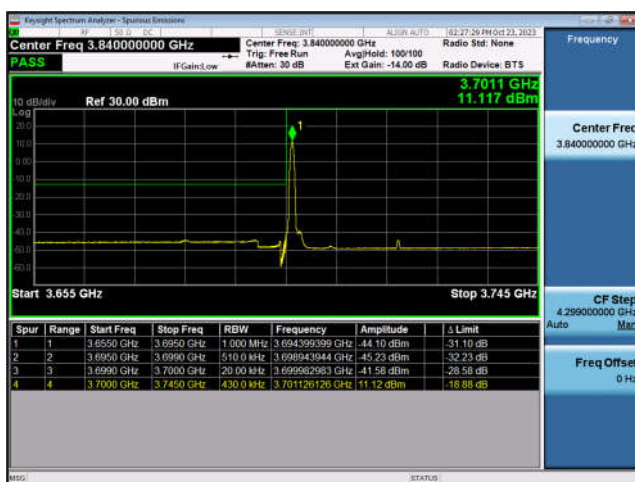

N77_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



N77_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



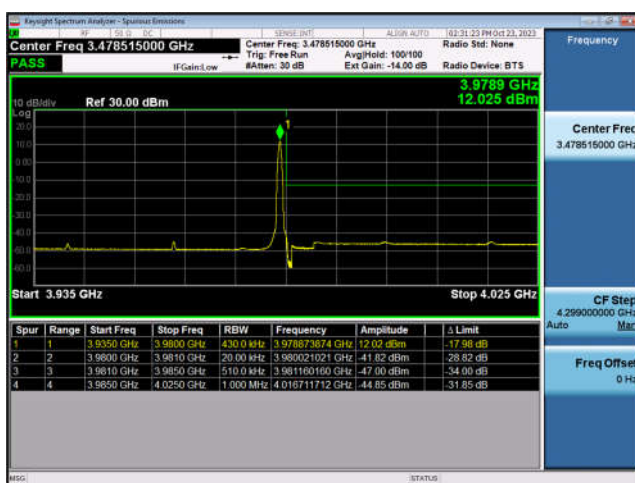
N77_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N77_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



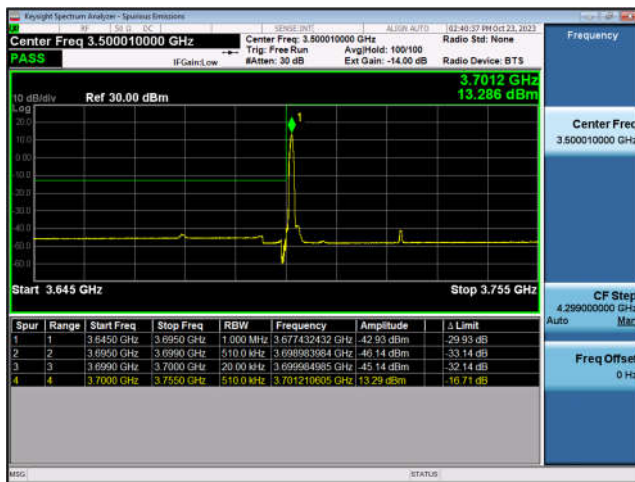
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1RB_Right_High_CH



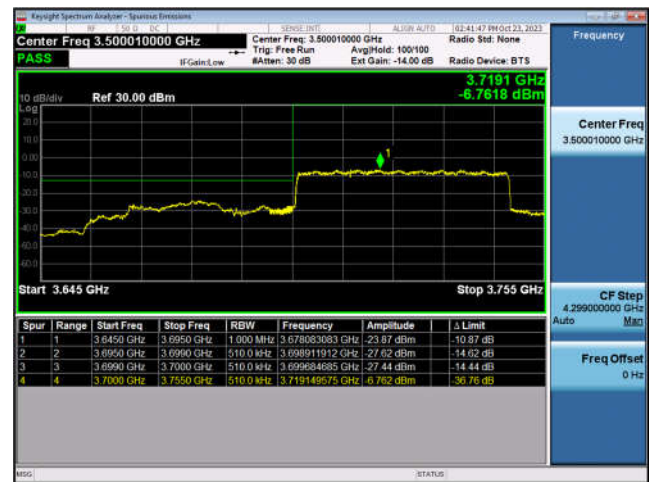
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Outer_Full_High_CH



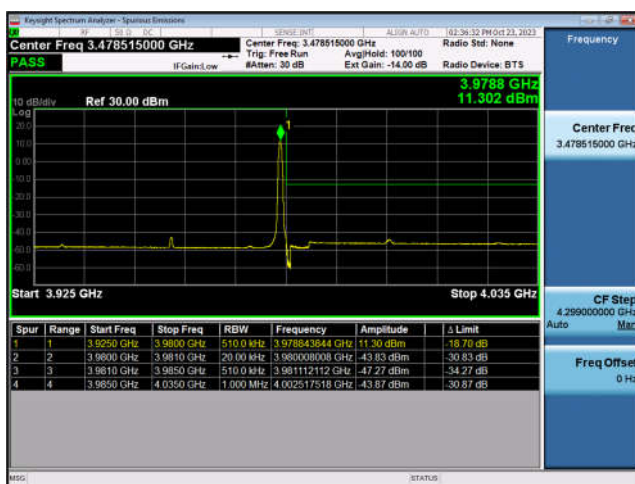
N77_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N77_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



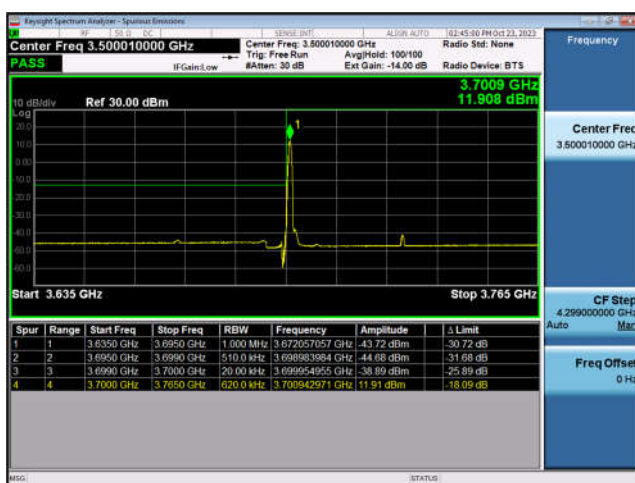
N77_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



N77_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

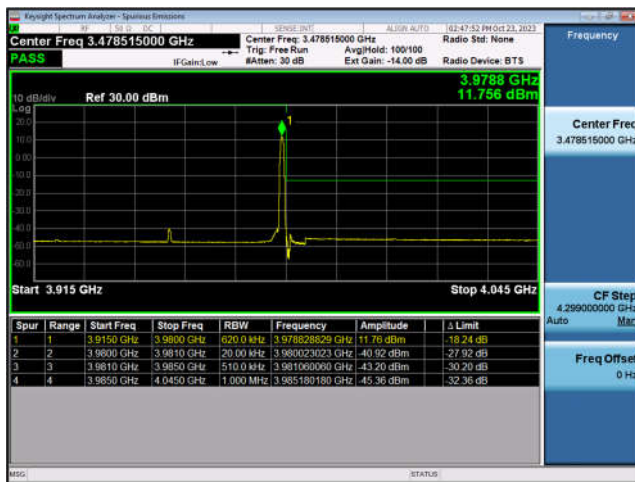


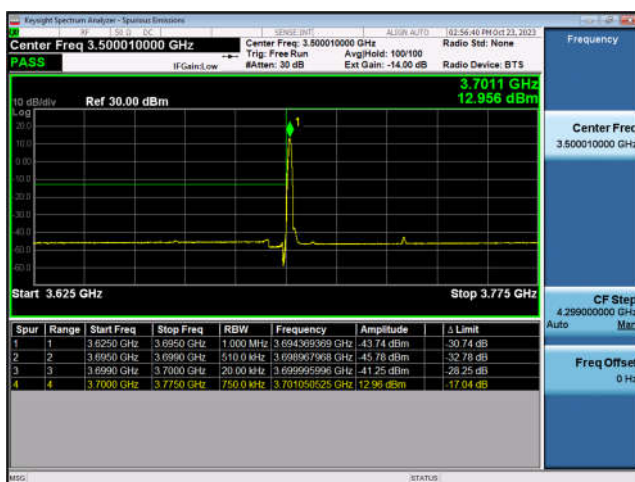
N77_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

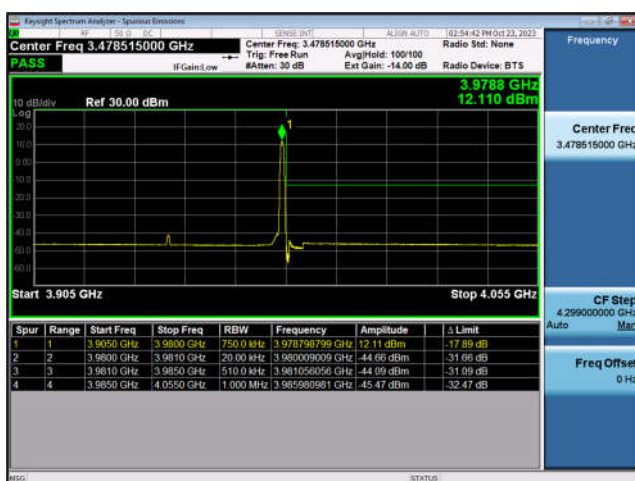


N77_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

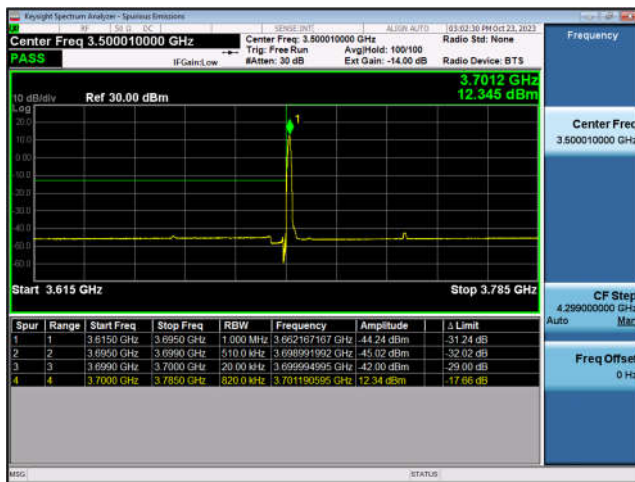


N77_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N77_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

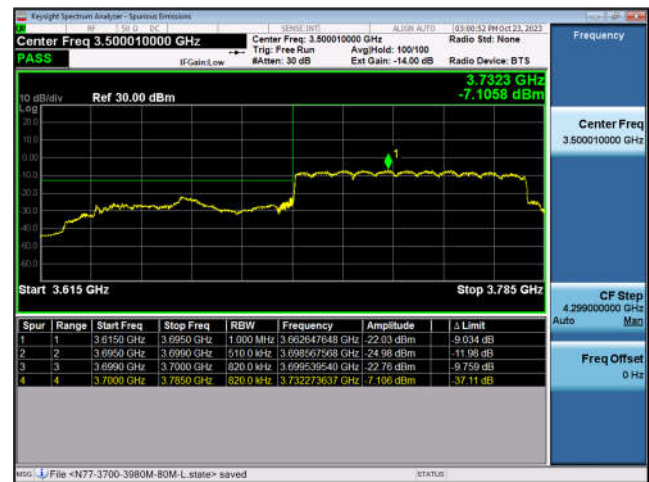
N77_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N77_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N77_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N77_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

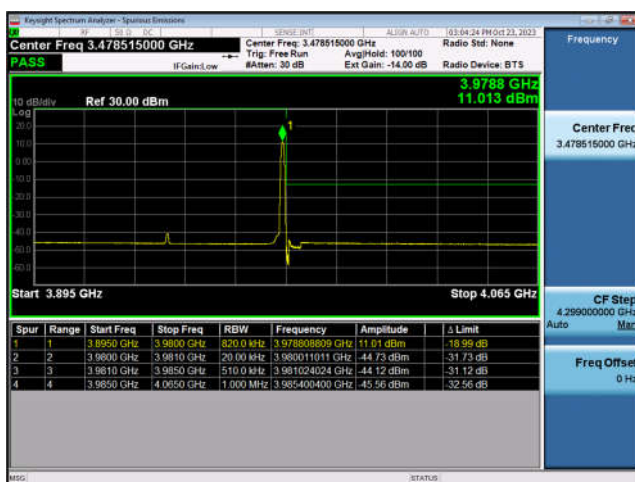

N77_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N77_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



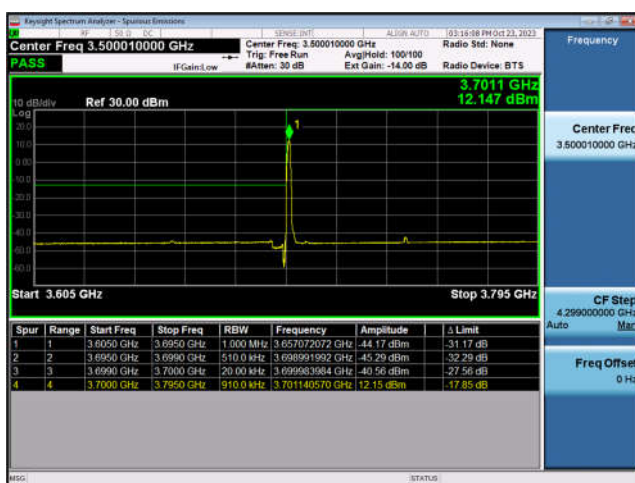
N77_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



N77_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



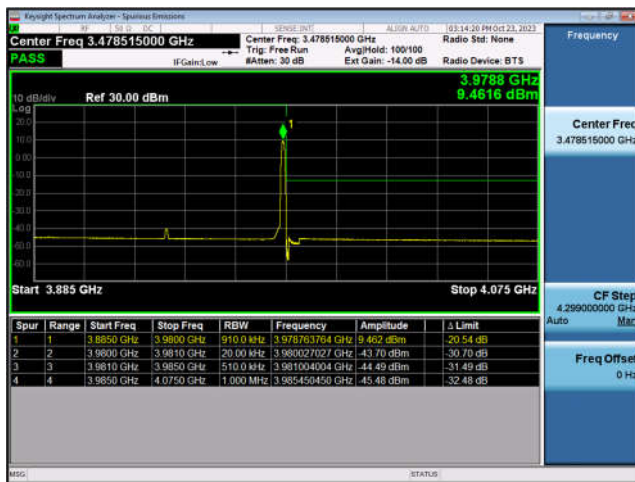
N77_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N77_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



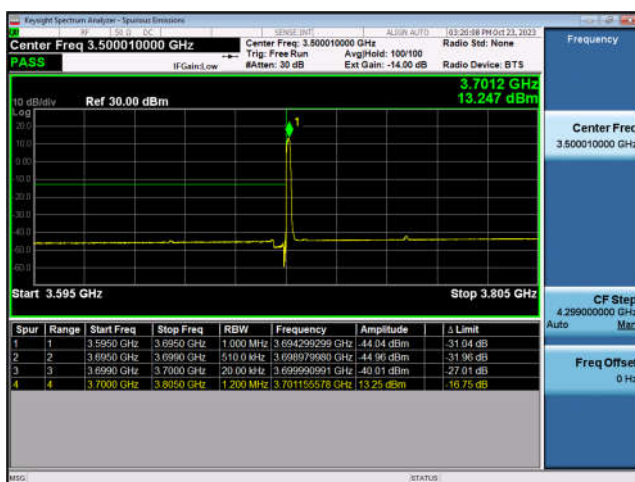
N77_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



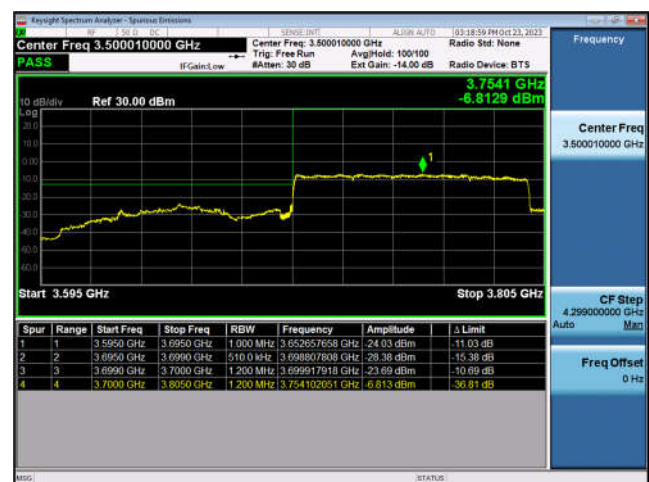
N77_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



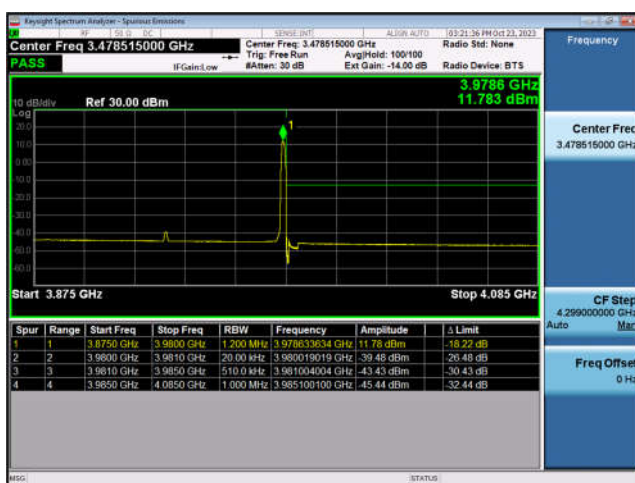
N77_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



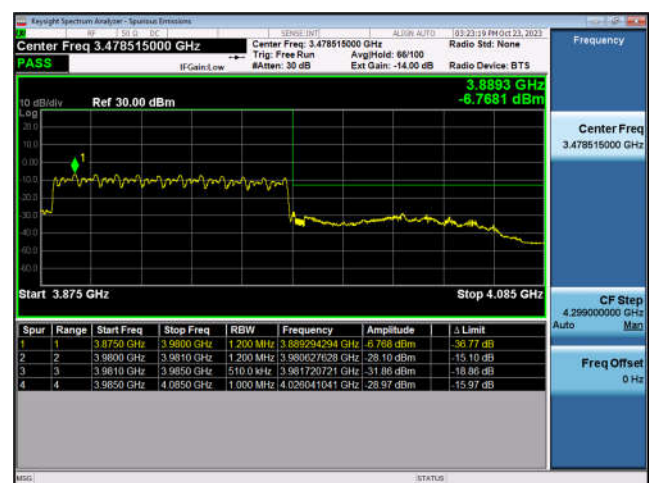
N77_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



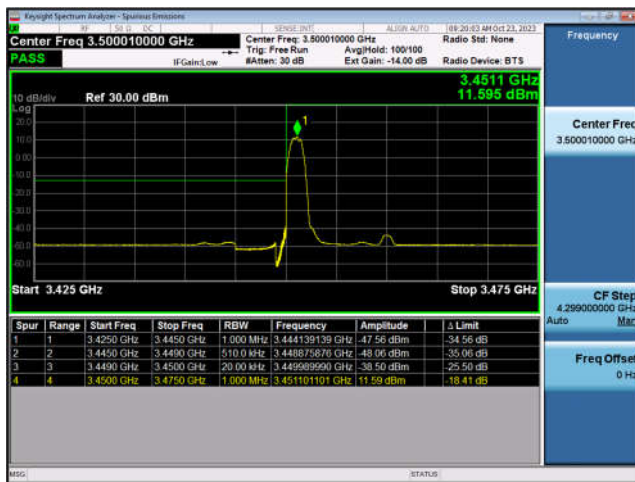
N77_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



N77_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



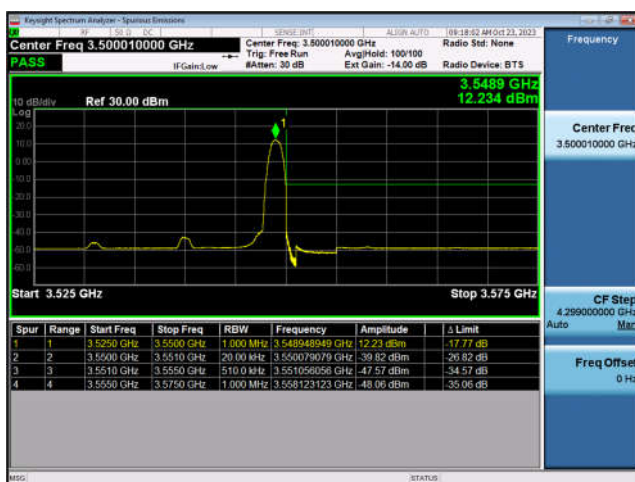
N78_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



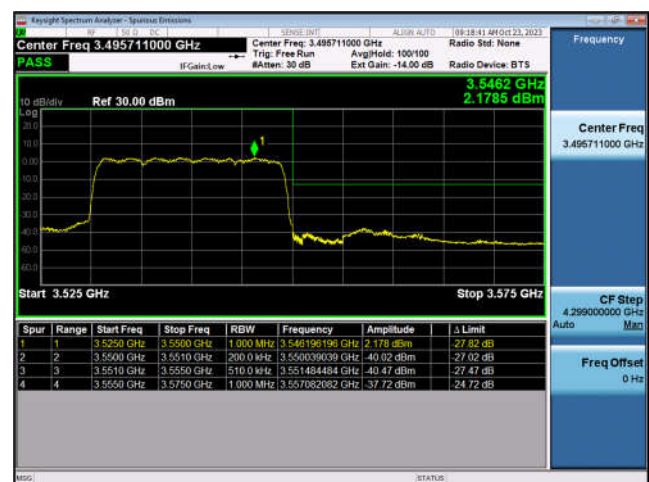
N78_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



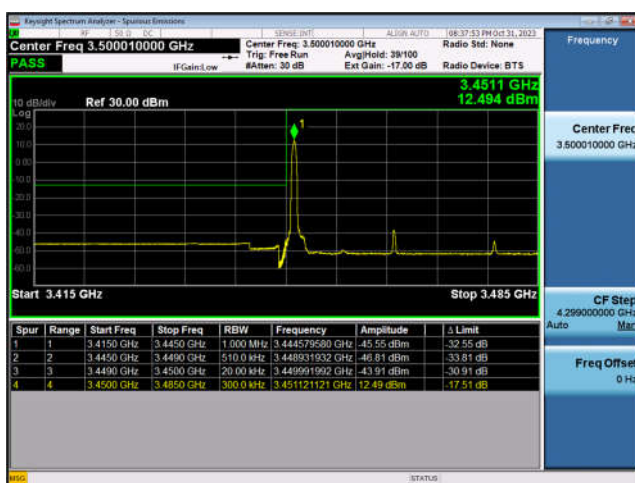
N78_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



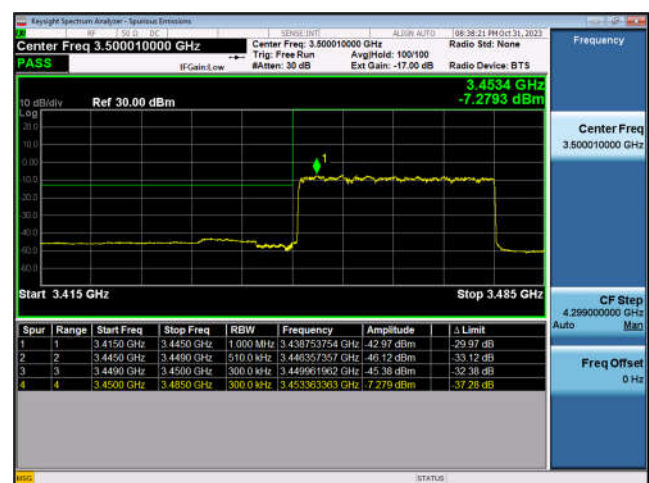
N78_20MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



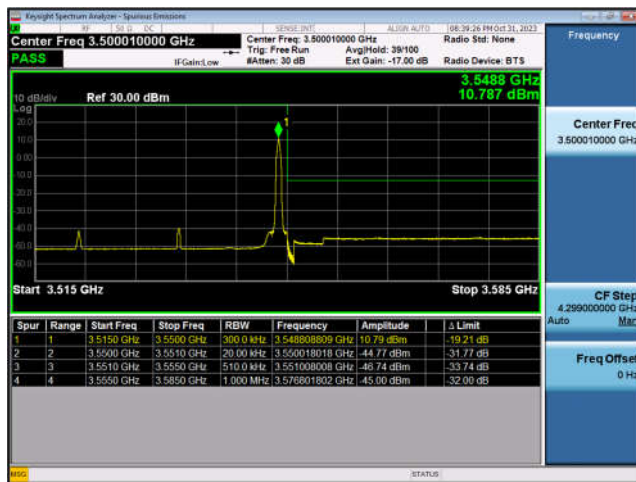
N78_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



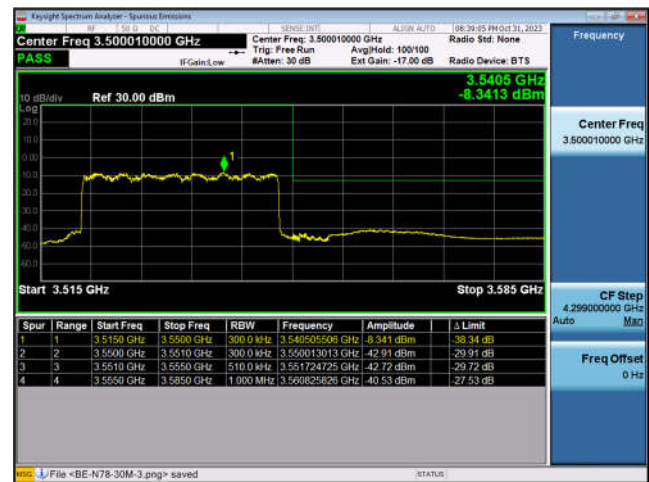
N78_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



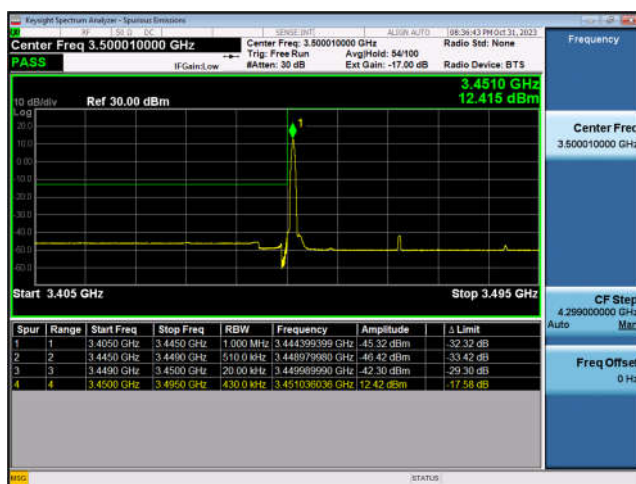
N78_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



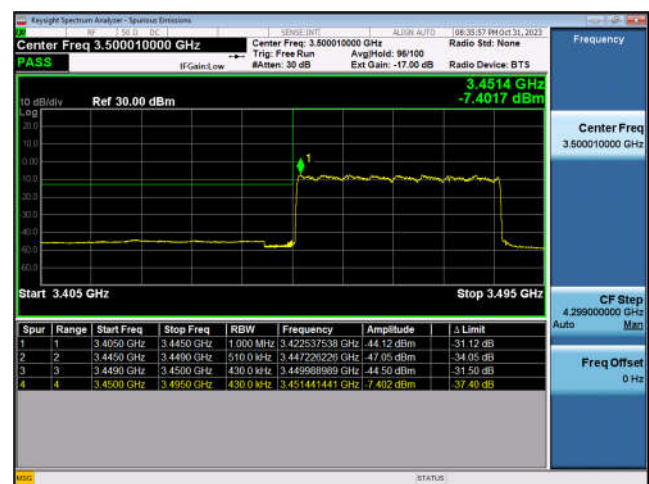
N78_30MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



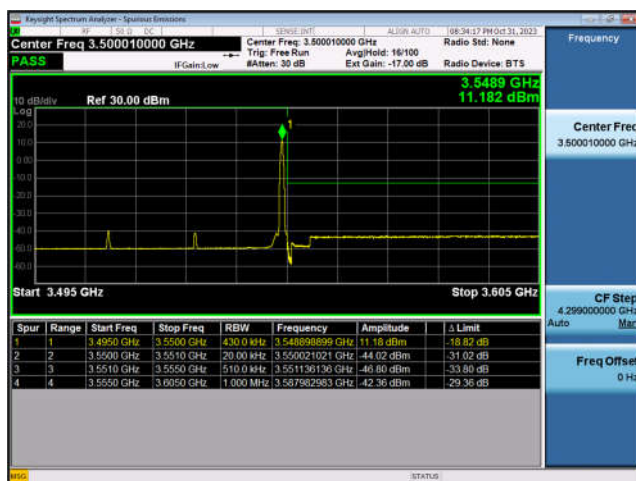
N78_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



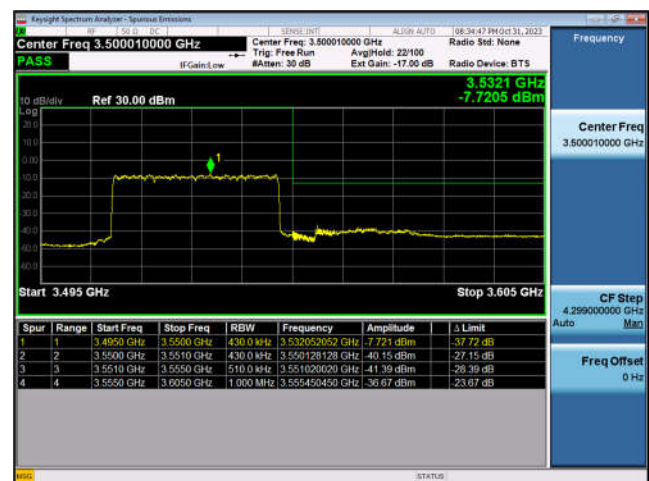
N78_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



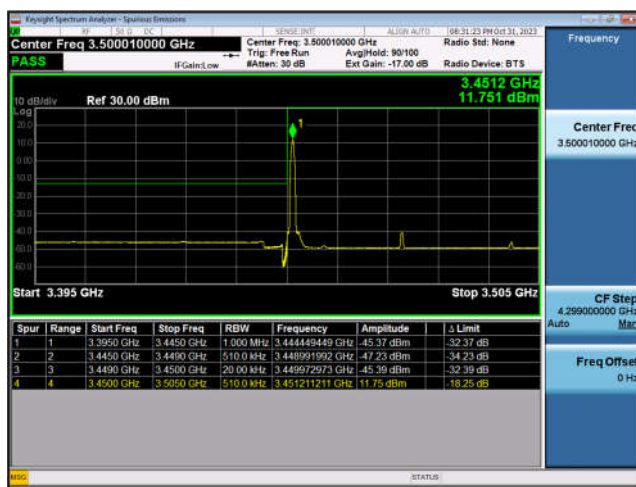
N78_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



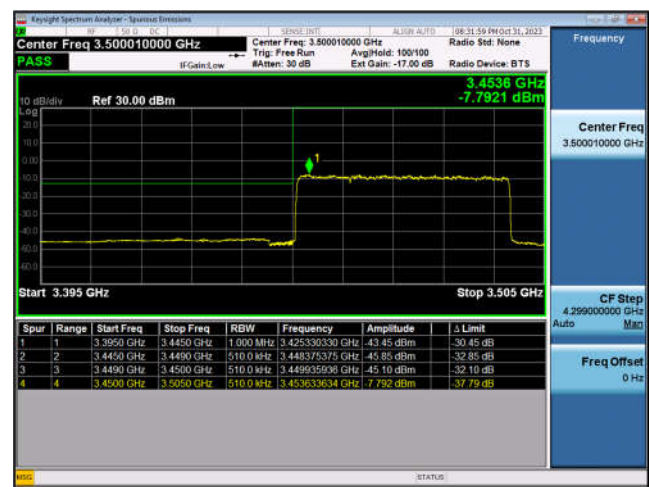
N78_40MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH



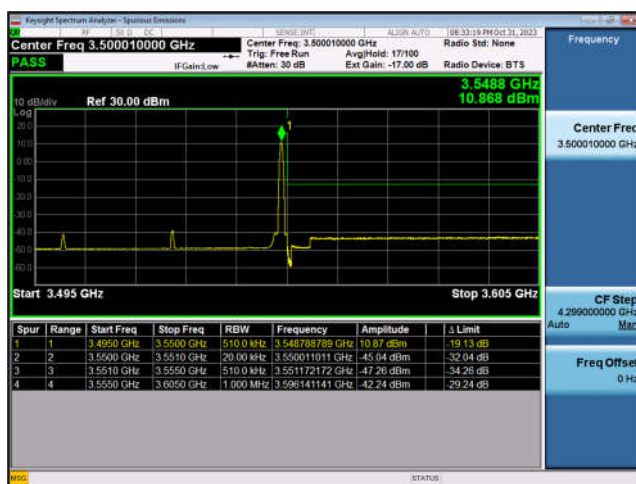
N78_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



N78_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH



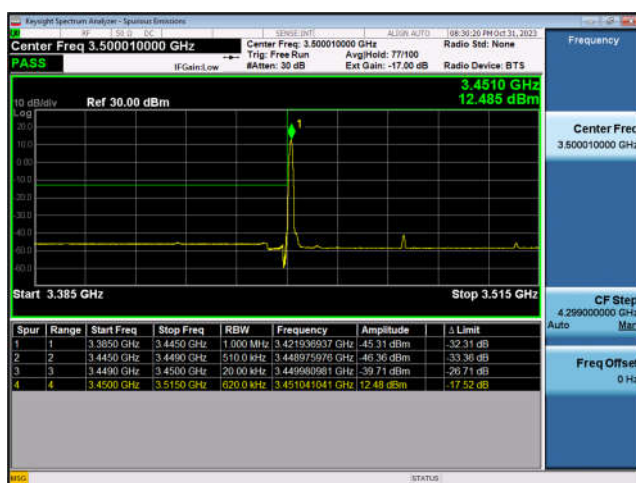
N78_50MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH



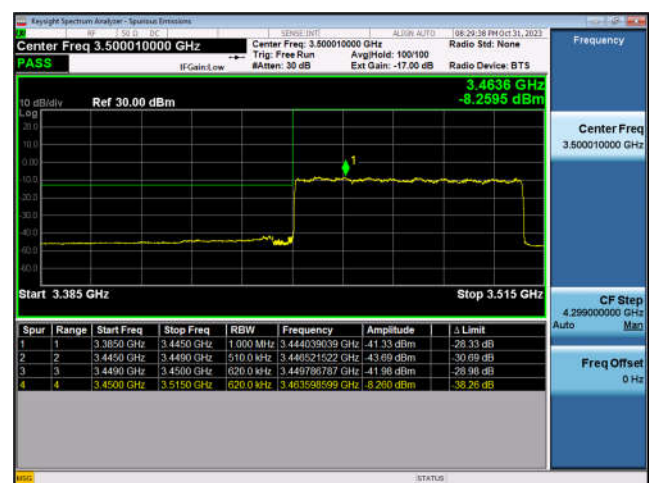
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Outer_Full_High_CH

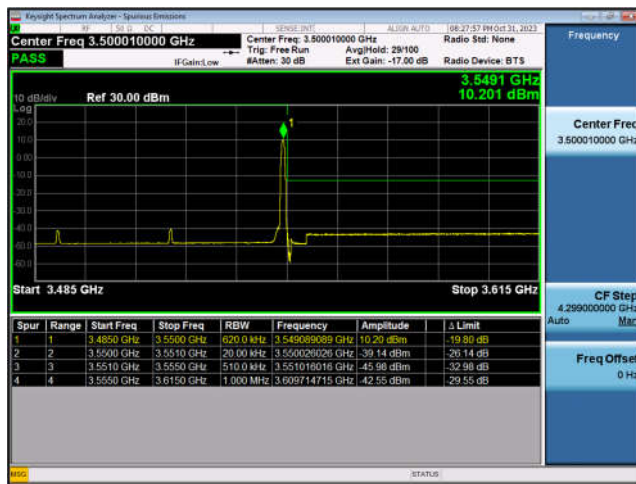
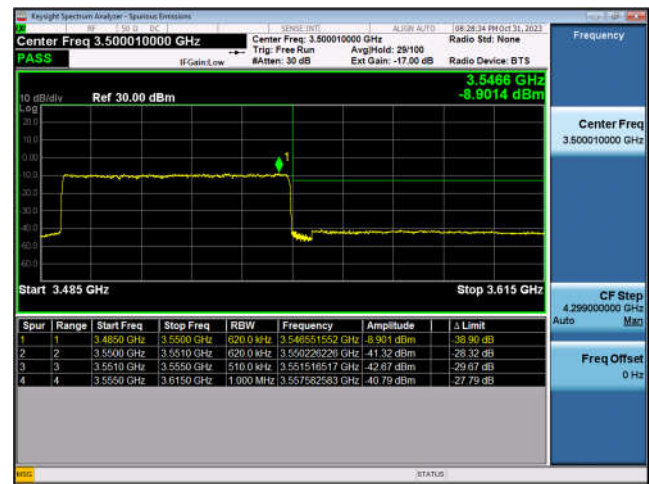
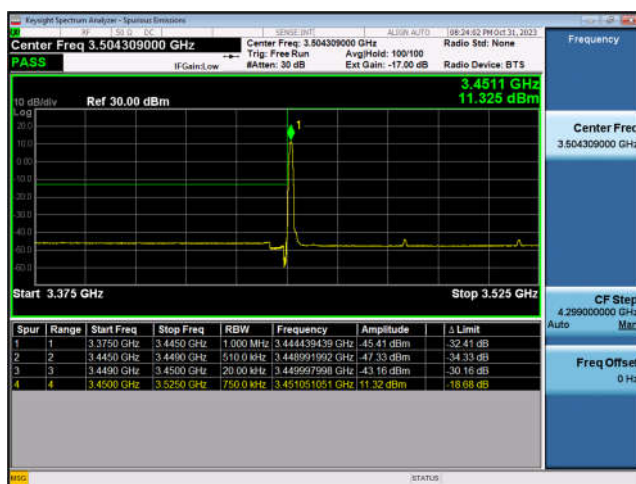
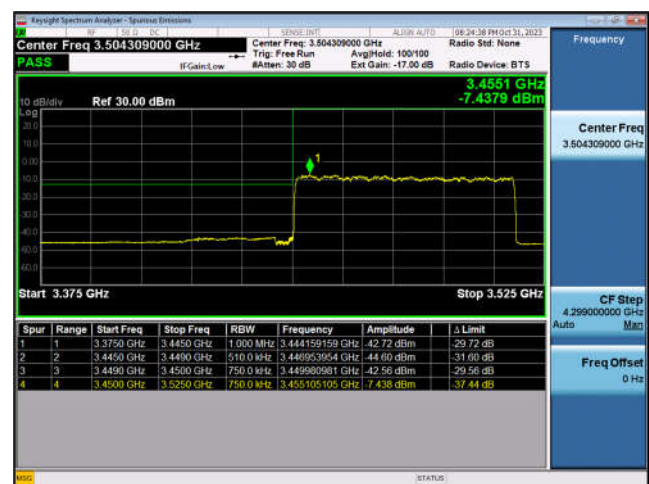
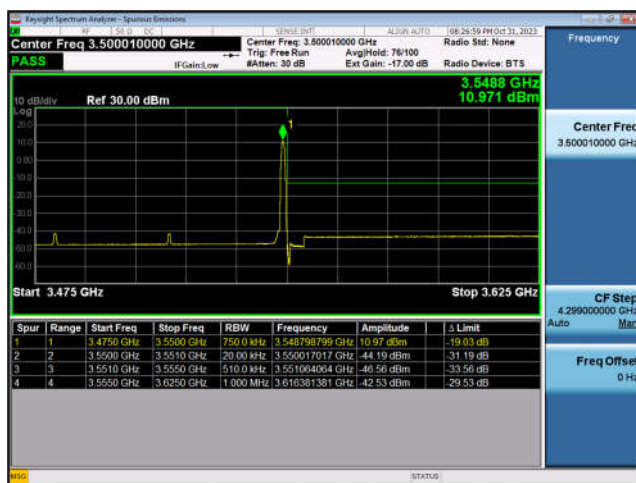
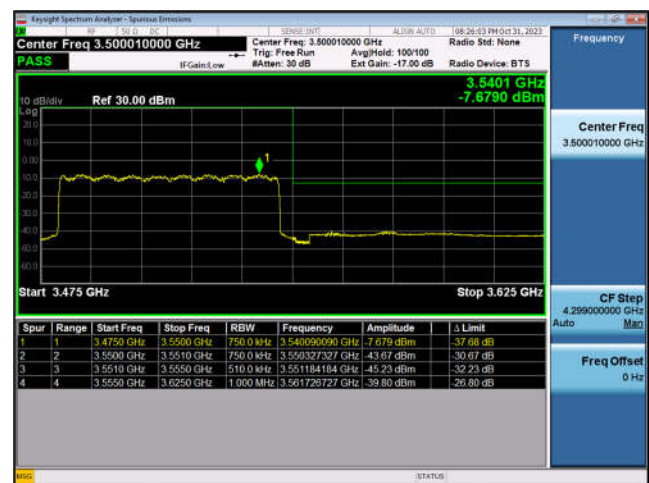


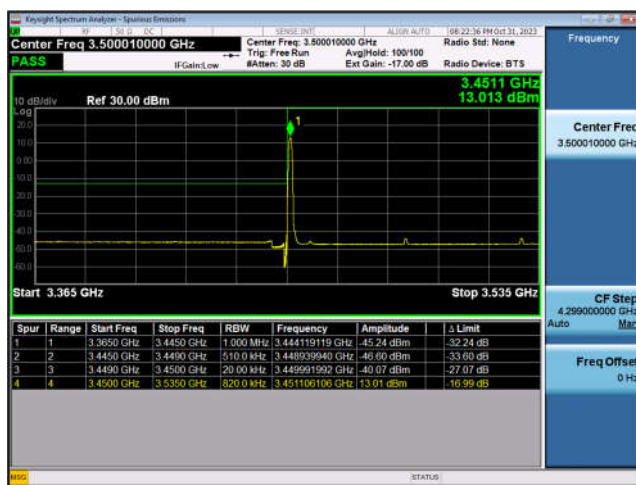
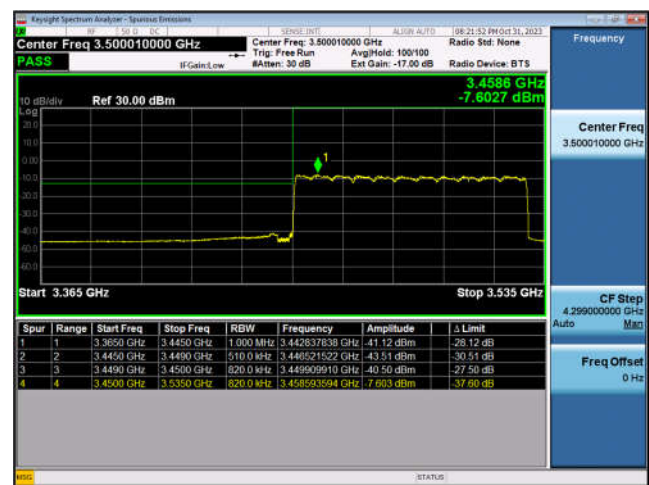
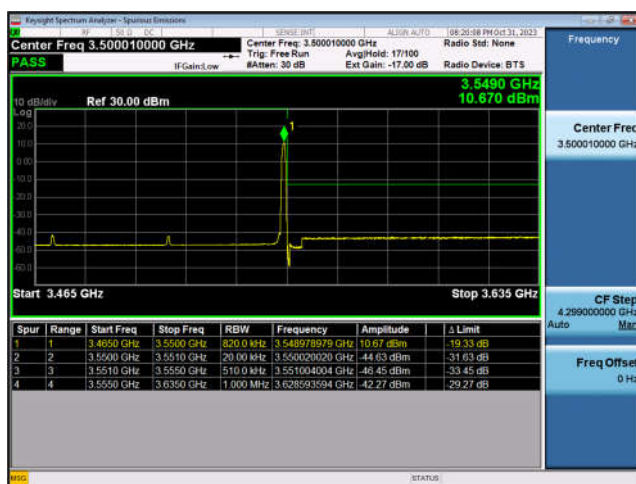
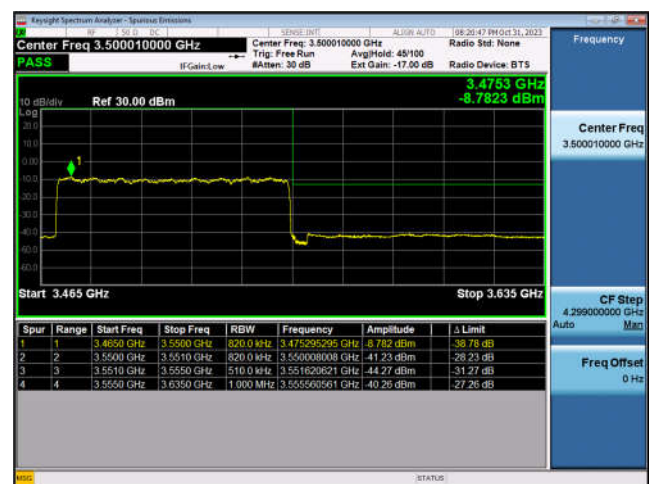
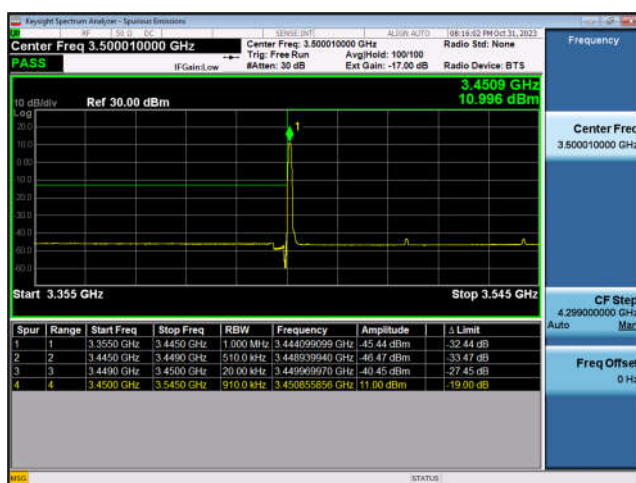
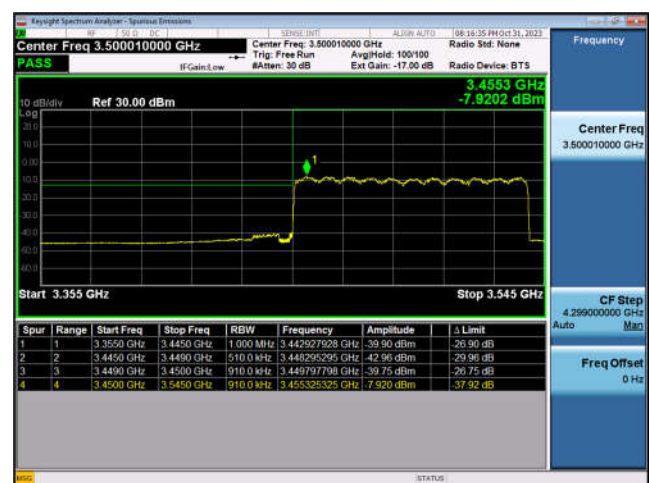
N78_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH



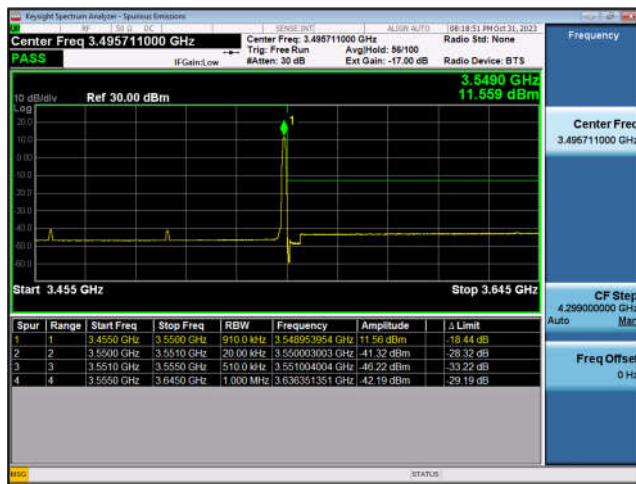
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Outer_Full_Low_CH



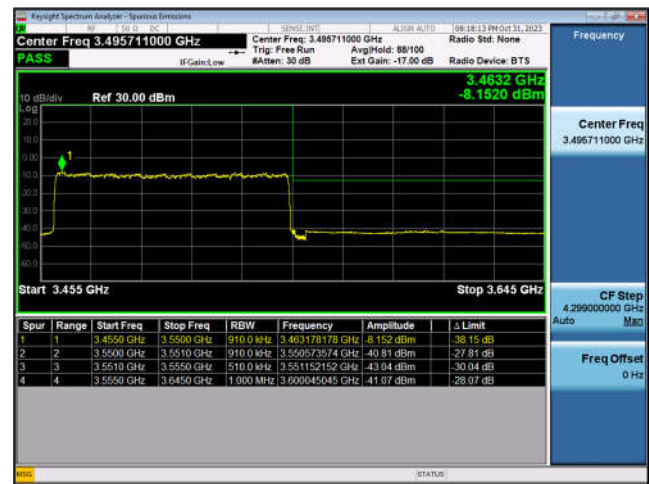
N78_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N78_60MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

N78_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N78_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N78_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N78_70MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH


N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH

N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_High_CH

N78_80MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_High_CH

N78_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Low_CH

N78_90MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Low_CH


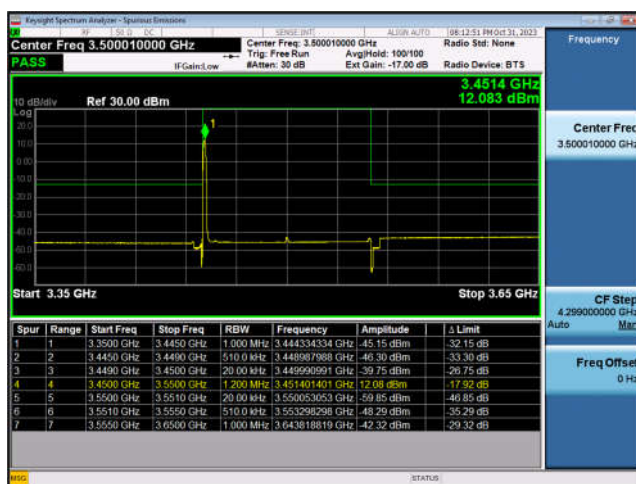
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1RB_Right_High_CH



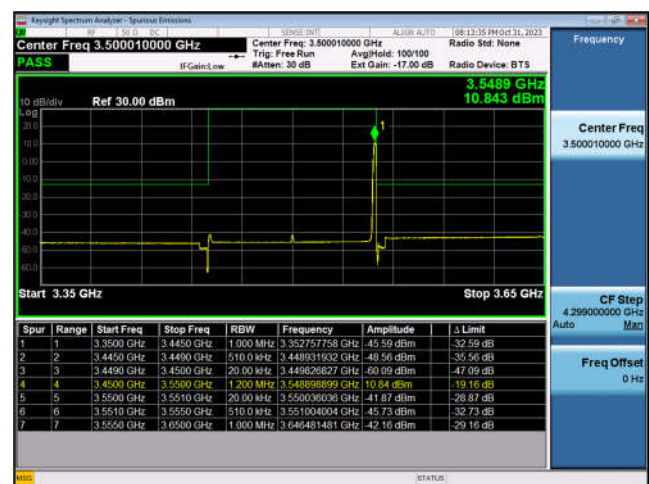
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Outer_Full_High_CH



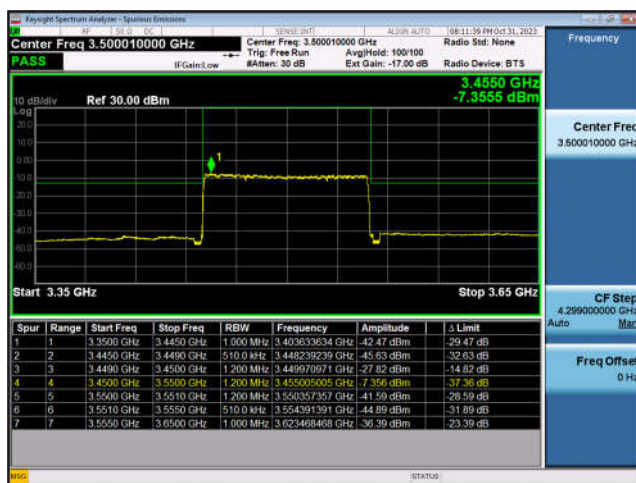
N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Left_Mid_CH



N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
1RB_Right_Mid_CH



N78_100MHz_DFT-s-OFDM_PI/2-BPSK_Edge_
Outer_Full_Mid_CH_CH



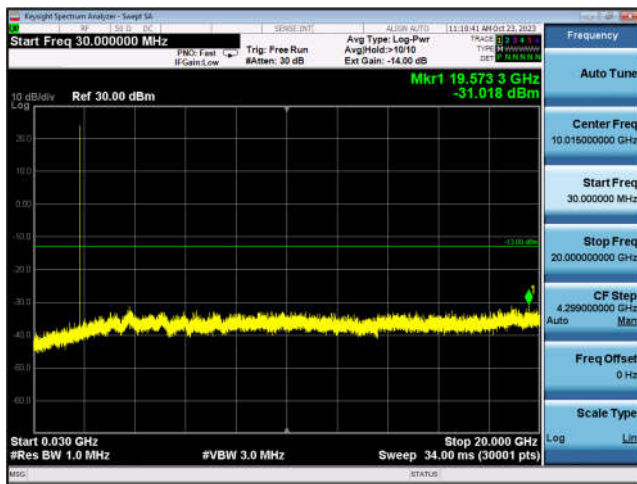
Conducted Spurious Emissions

NR Band	SCS (kHz)	Bandwidth (MHz)	Freq (MHz)	Modulation	RB Configuration	Result	Verdict
n2	15	20	1860.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n2	15	20	1880.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n2	15	20	1900.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n5	15	20	834.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n5	15	20	836.5	DFT-s-OFDM BPSK	1@0	see graph	PASS
n5	15	20	839.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n7	15	20	2510.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n7	15	20	2535.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n7	15	20	2560.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n26_part 22	15	20	834.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n26_part 22	15	20	836.5	DFT-s-OFDM BPSK	1@0	see graph	PASS
n26_part 22	15	20	839.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n26_part 90	15	10	819.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n66	15	40	1730.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n66	15	40	1745.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n66	15	40	1760.0	DFT-s-OFDM BPSK	1@0	see graph	PASS
n41	30	100	2546.01	DFT-s-OFDM BPSK	1@0	see graph	PASS
n41	30	100	2592.99	DFT-s-OFDM BPSK	1@0	see graph	PASS
n41	30	100	2640.00	DFT-s-OFDM BPSK	1@0	see graph	PASS

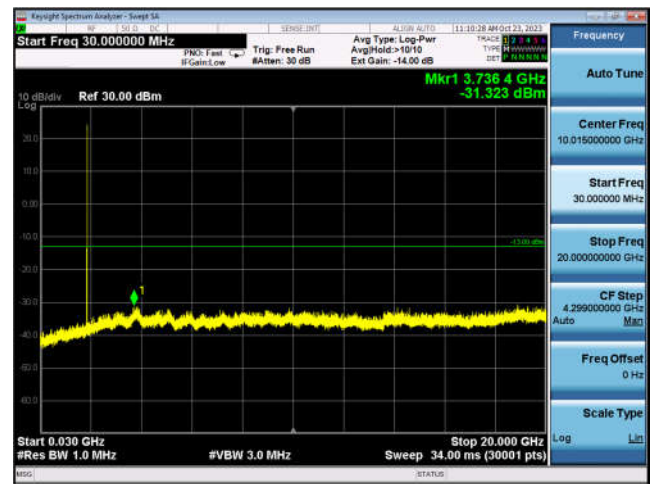


n77	30	100	3750.00	DFT-s-OFDM BPSK	1@0	see graph	PASS
n77	30	100	3840.00	DFT-s-OFDM BPSK	1@0	see graph	PASS
n77	30	100	3930.00	DFT-s-OFDM BPSK	1@0	see graph	PASS
n78	30	100	3500.01	DFT-s-OFDM BPSK	1@0	see graph	PASS

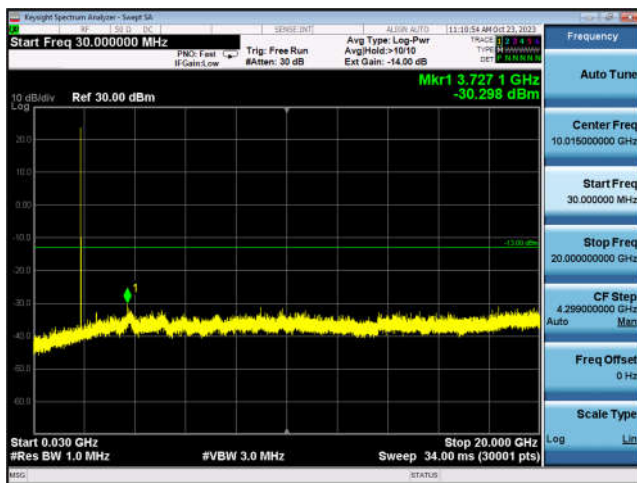
N2_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



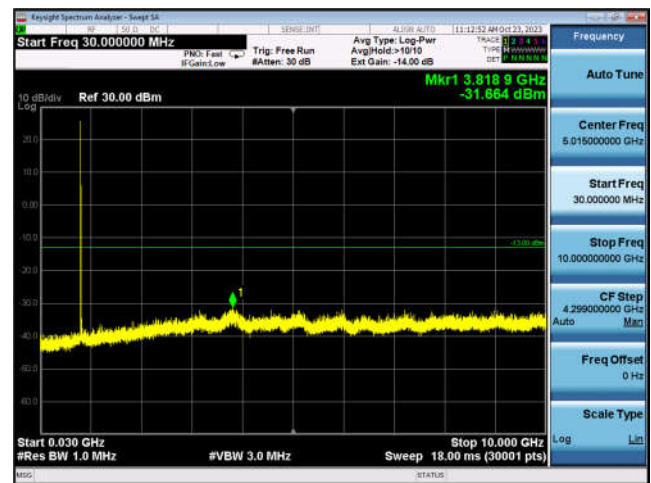
N2_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



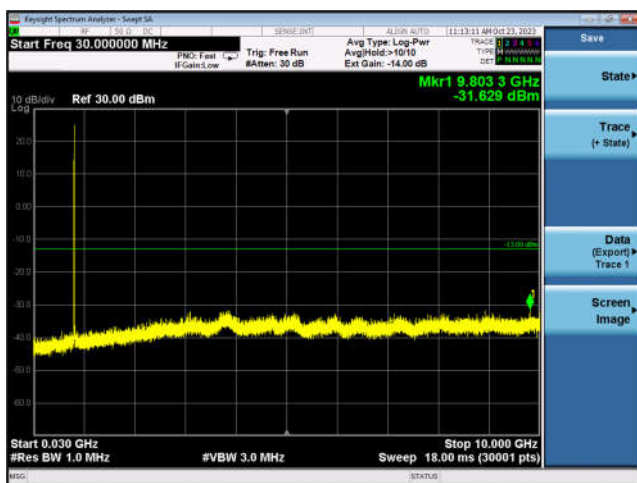
N2_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



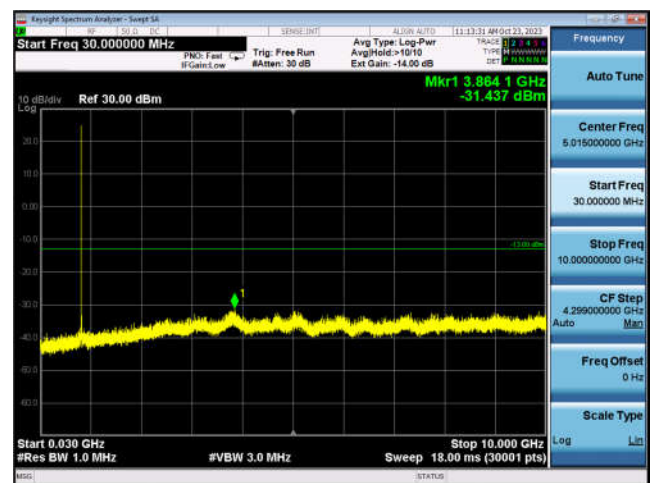
N5_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



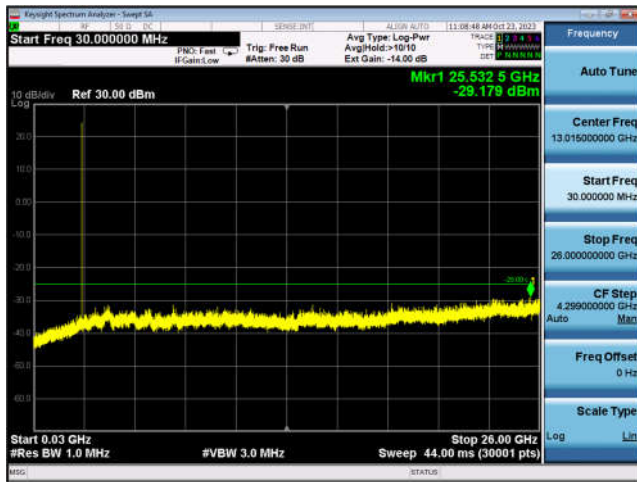
N5_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



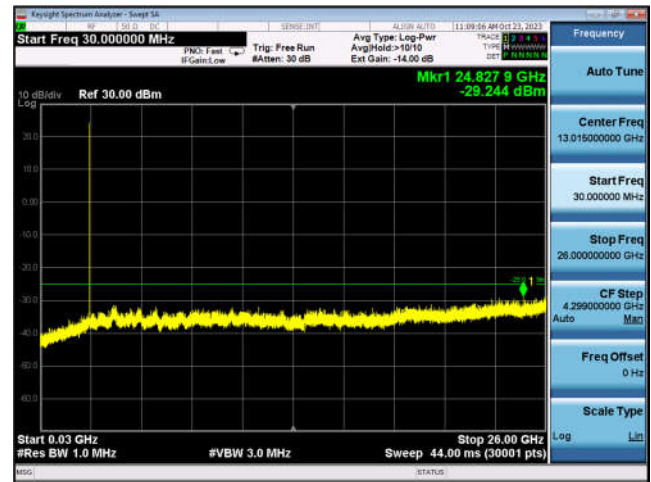
N5_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



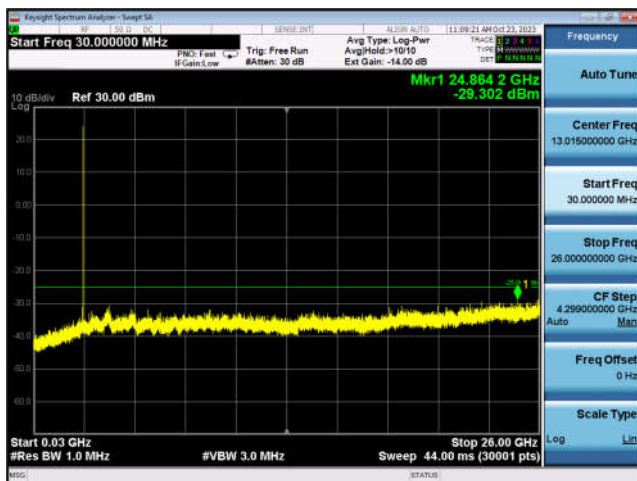
N7_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



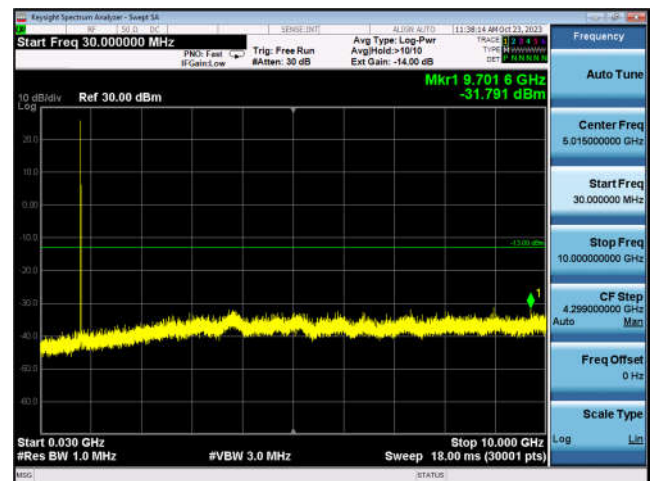
N7_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



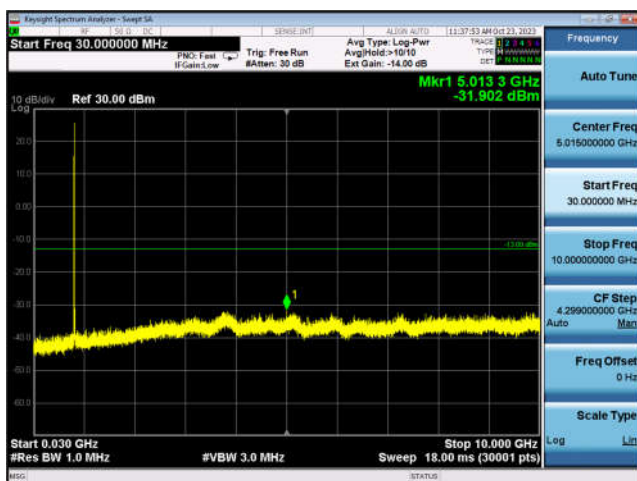
N7_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



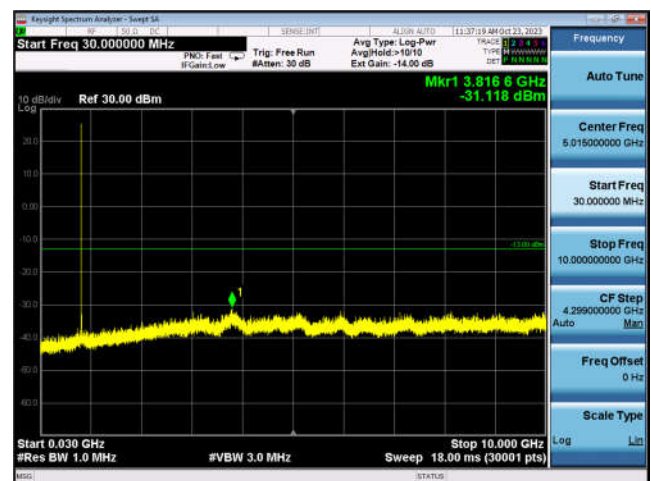
N26_Part 22_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



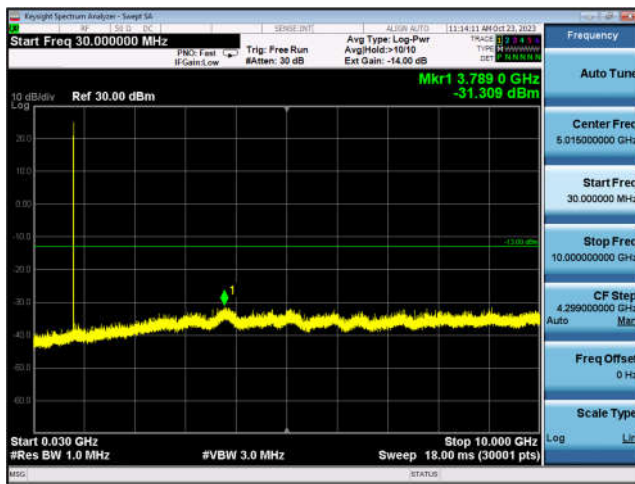
N26_Part 22_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



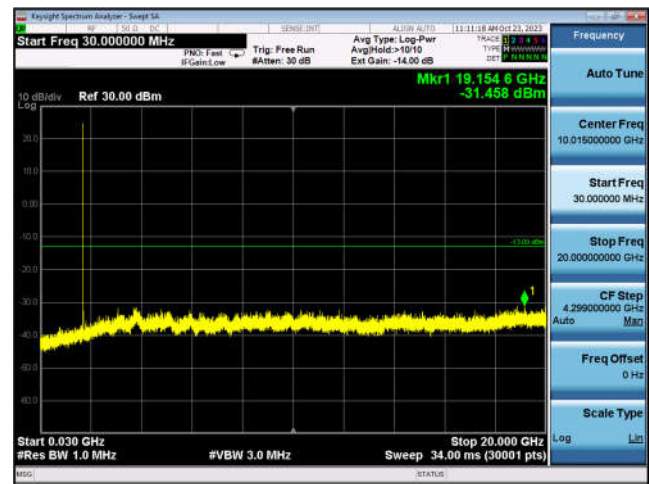
N26_Part 22_20MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



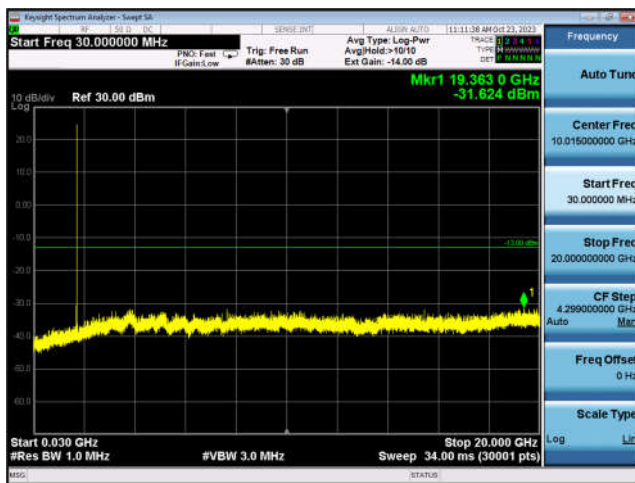
N26_Part 90_10MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



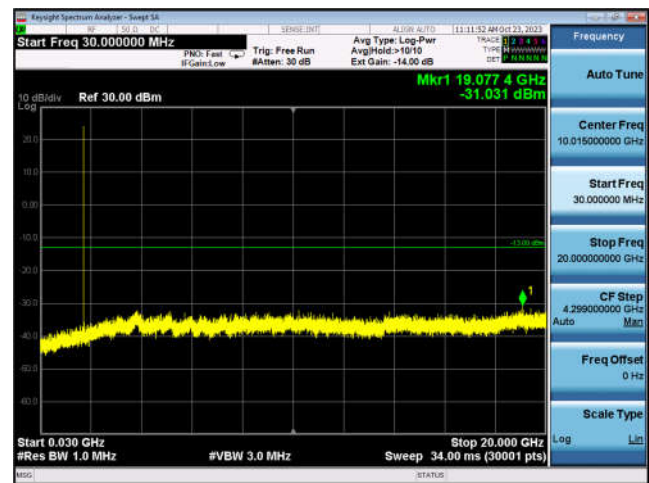
N66_40MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



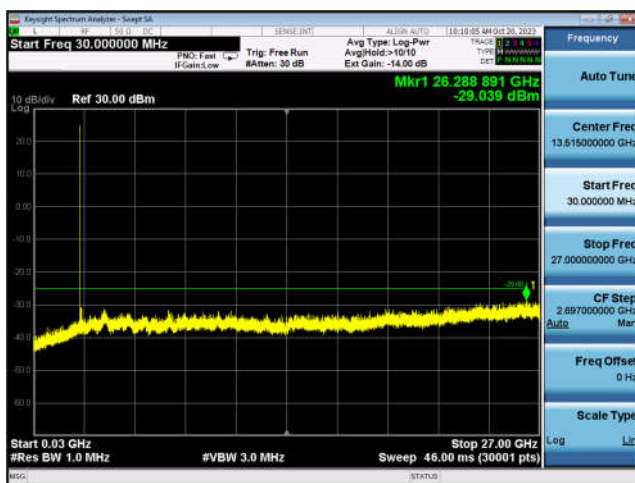
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Edge_1RB_Left_High_CH



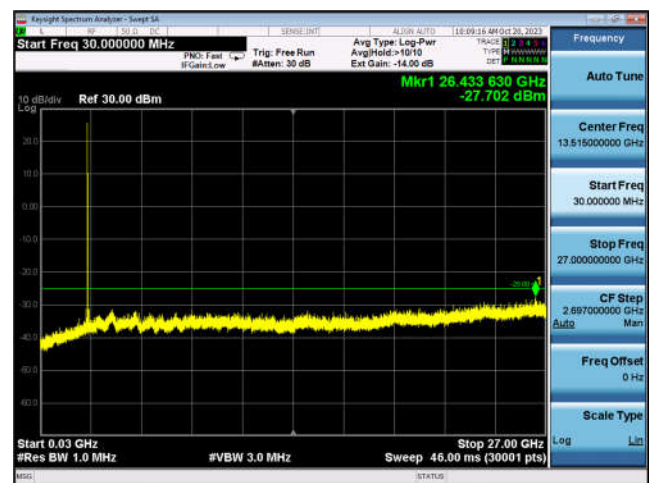
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Edge_1RB_Left_Low_CH



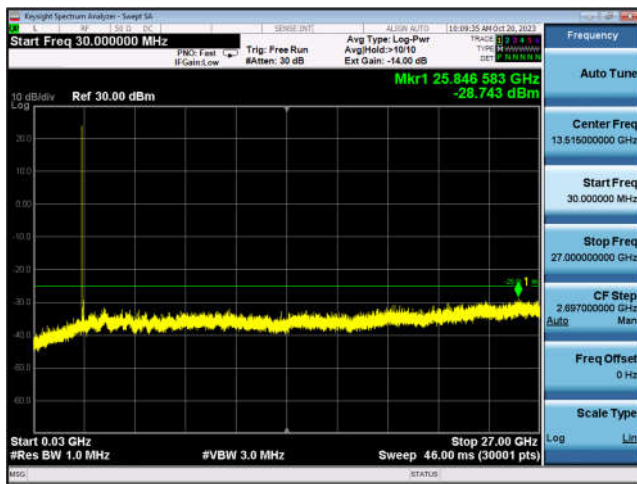
N41_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



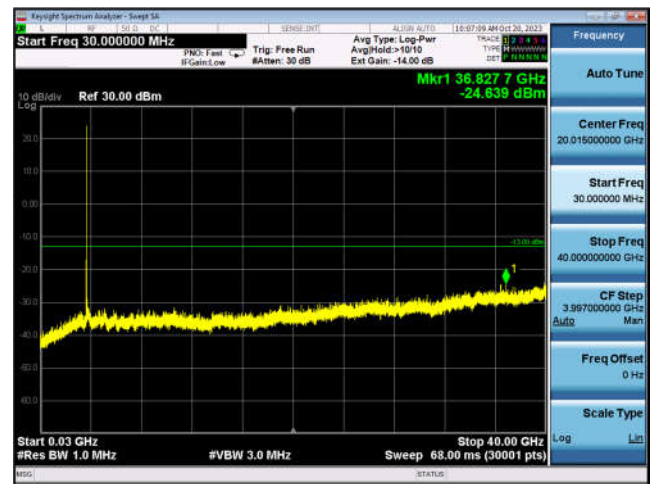
N41_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



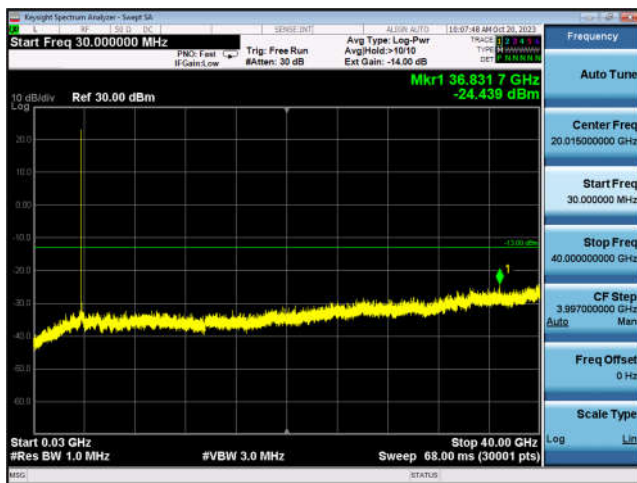
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Edge_1RB_Left_High_CH



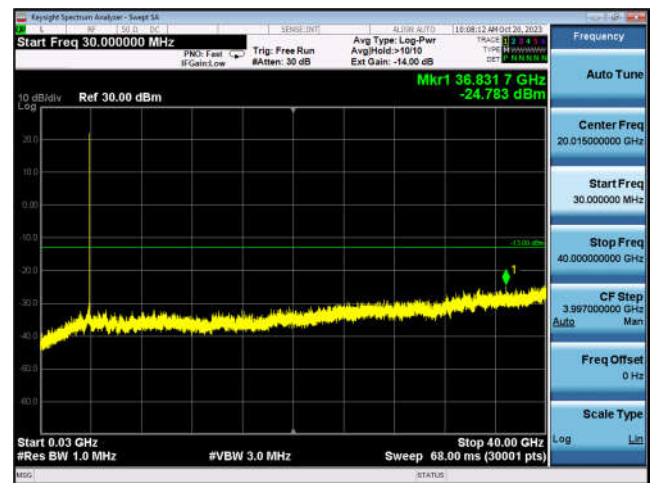
N77_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



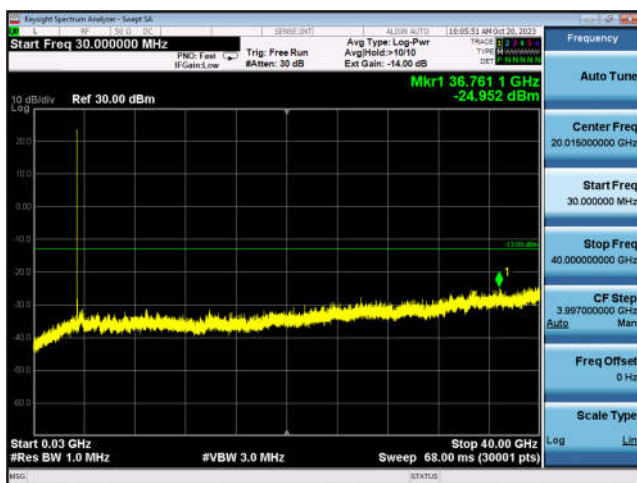
N77_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



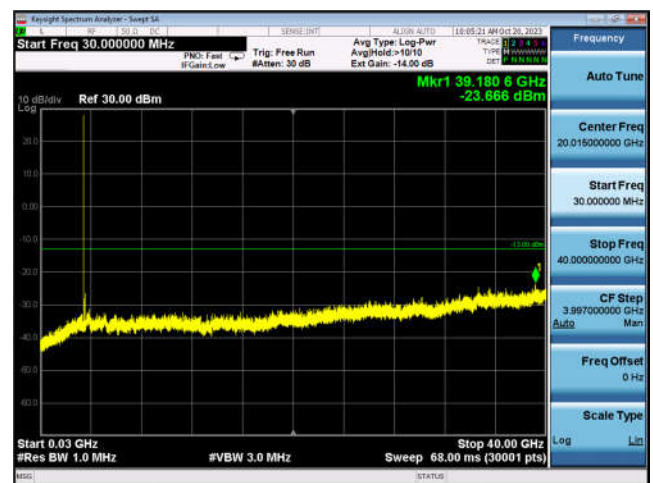
N77_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



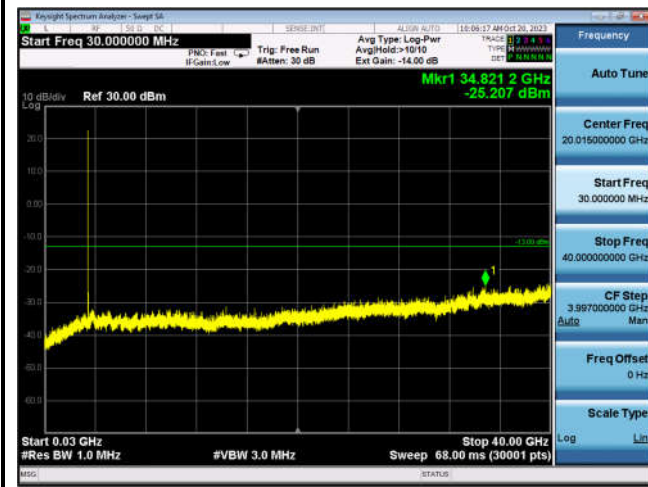
N78_90MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Low_CH



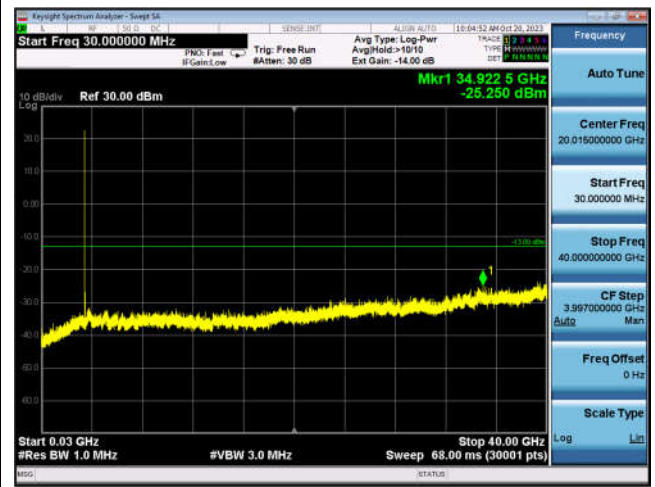
N78_90MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



N78_90MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_High_CH



N78_100MHz_DFT-s-OFDM_BPSK_
Edge_1RB_Left_Mid_CH



** END OF REPORT **