

Corsair Memory, Inc.

Declaration of Product Modifications

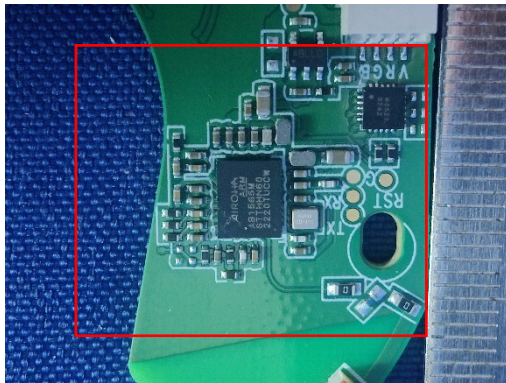
Date: Oct. 14, 2024

This is to request a Class II permissive change for FCC ID: 2AAFMRDA0047 originally granted on 03/17/2023.

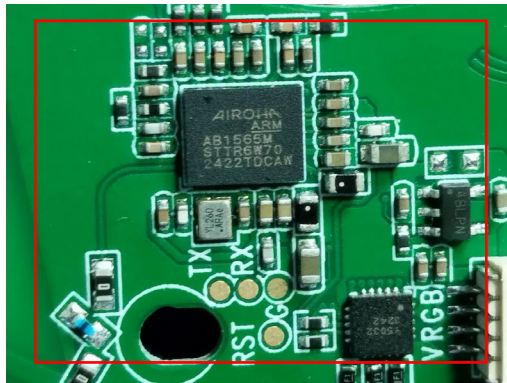
The major change filed under this application is:

MAIN_L PCB hole position design adjustment. It can't see the change from the outside:

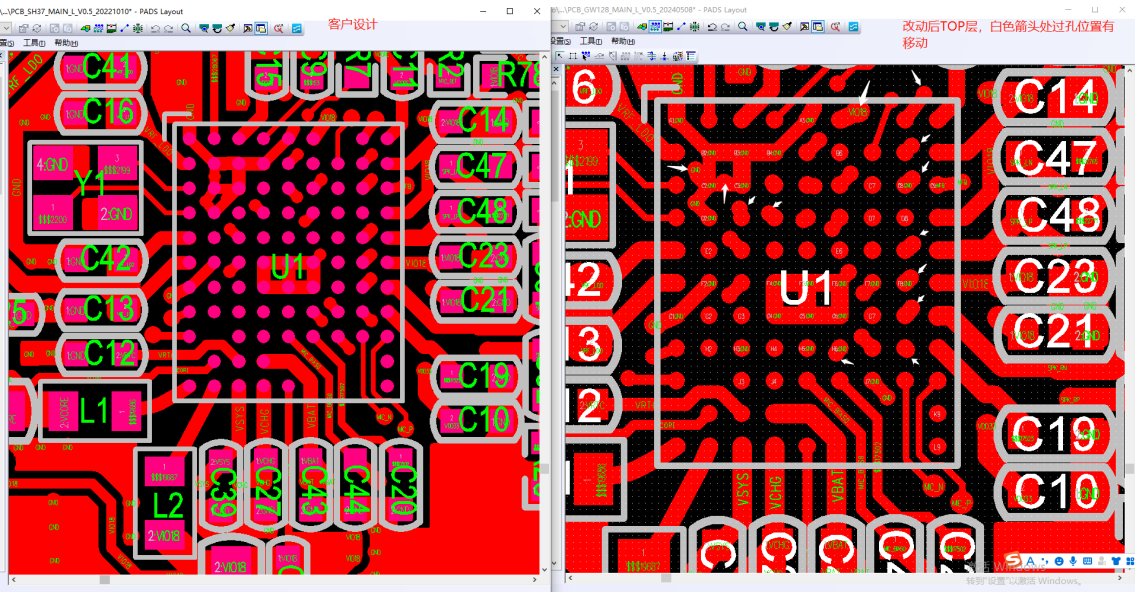
Before change:



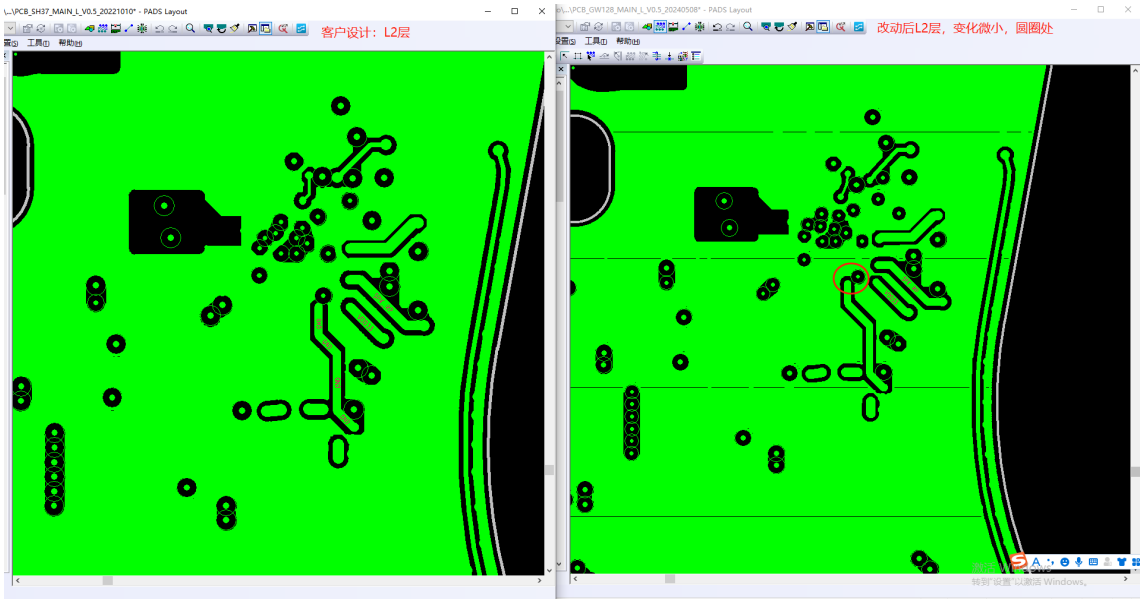
After the change:



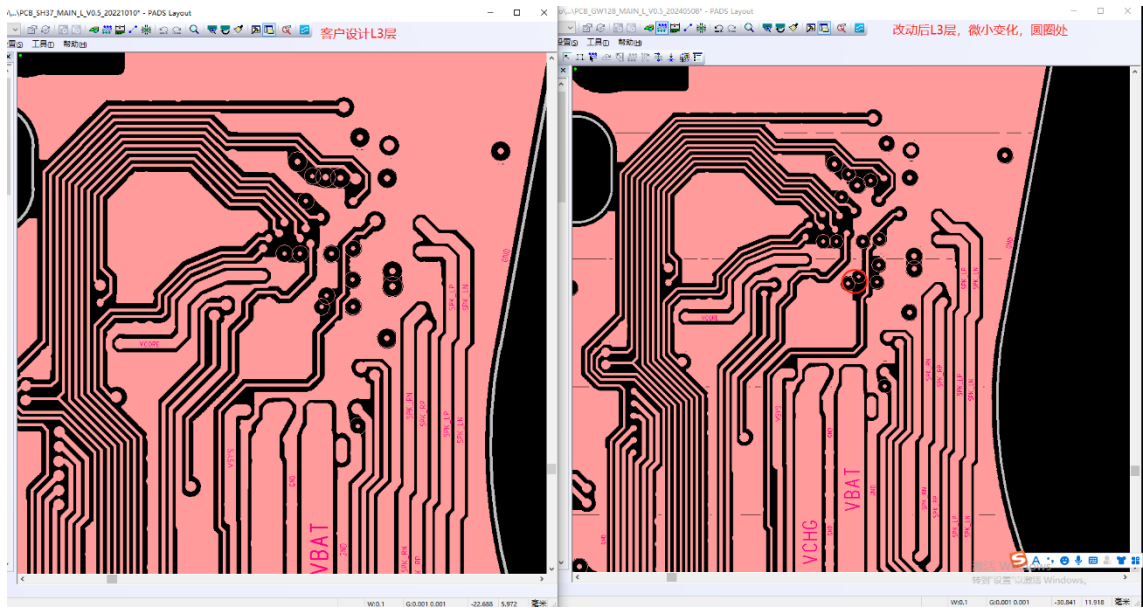
Change #1: After the change, the position of the hole at the white arrow has moved to the direction of the pad at most 0.1mm Minor pore changes on the TOP Layer



Change #2: Minor pore changes on the Layer 2

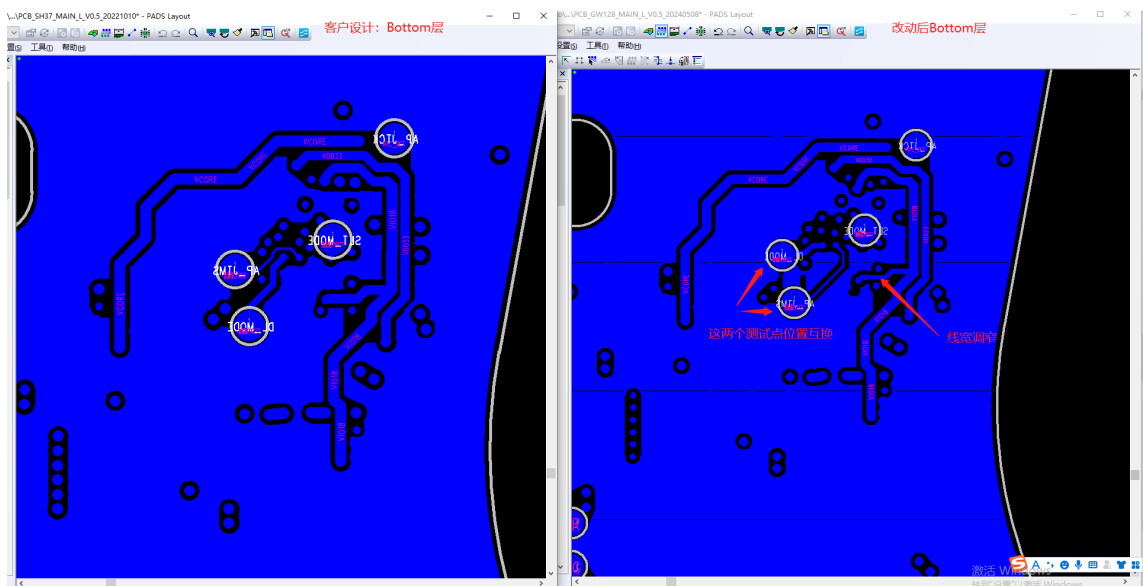


Change #3: Minor pore changes on the Layer 3



Change #4: Minor pore changes on the Layer 4

The positions of the two test points are exchanged. After the change, the narrowest line of VIO18 in the middle of the two holes is 0.15mm



Sincerely,

(Signature)

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