

Date: 2026-09-11

Declaration for KDB 996369 Module Q&A

FCC ID: 2AATHAV62MB-X

To whom it may concern,

Question 1. Information that includes permitted variances (e.g., trace boundary limits, thickness, length, width, shape(s), dielectric constant, and impedance as applicable for each type of antenna);

Description:

This information is included

- a) in the provided Gerber Data and**
- b) in the description of the PCB layer stackup used**

Question 2. Each design shall be considered a different type (e.g., antenna length in multiple(s) of frequency, the wavelength, and antenna shape (traces in phase) can affect antenna gain and must be considered);

Description:

Customer documentation, e.g. “HID” (Hardware Interface Description), contains the required information.

Especially chapter “RF Antenna Interface” contains requirements for building proper antenna connectivity.

Question 3. The parameters shall be provided in a manner permitting host manufacturers to design the printed circuit (PC) board layout;

Description:

Customer documentation, e.g. “HID” (Hardware Interface Description), contains requirements for proper designing connectivity in the host application.

Question 4. Appropriate parts by manufacturer and specifications.

Description:

The integrator chooses components according to his own preferences.

Usually e.g. individually designed antennas are used.

Question 5. Test procedures for design verification.

Description:

During development phase the design has been verified according to test specifications.

Question 6. Production test procedures for ensuring compliance.

Description:

There is a calibration procedure ("Tune-up procedure") as part of the production process. This procedure takes care that all given restrictions are fulfilled

Thank you for your attention.

Sincerely yours,

Dirk
Goesche

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