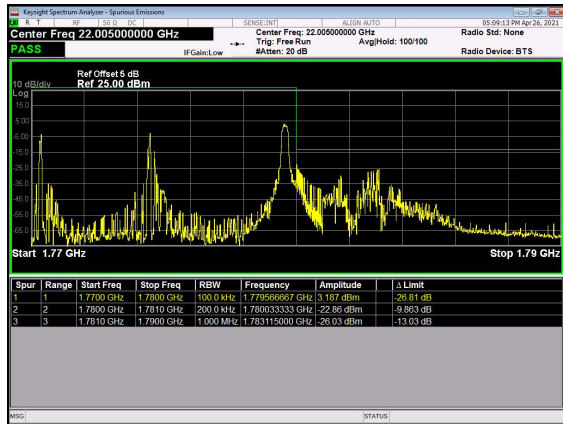
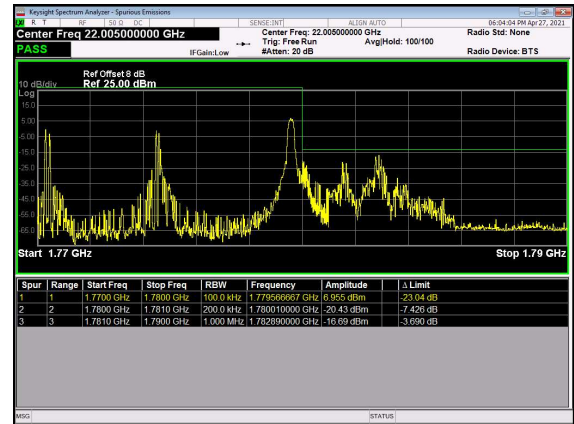
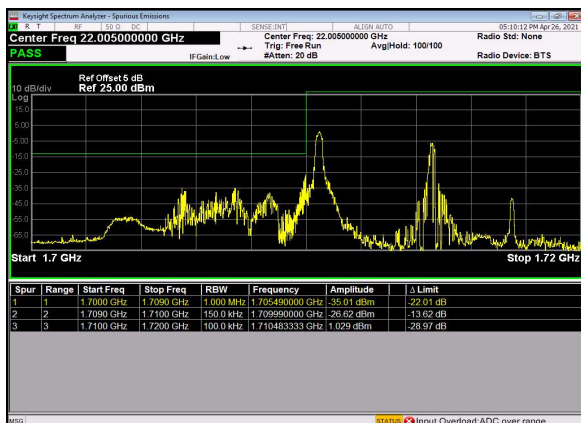
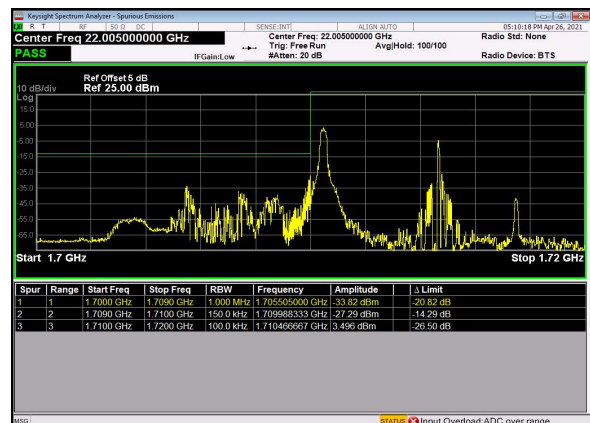
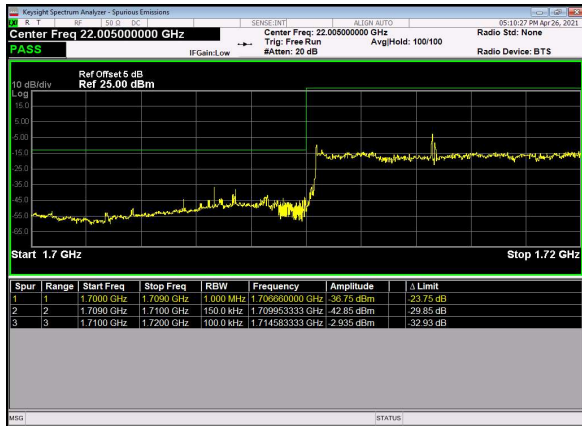
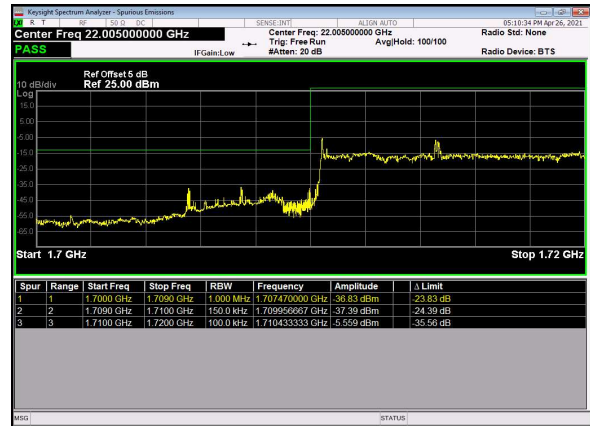
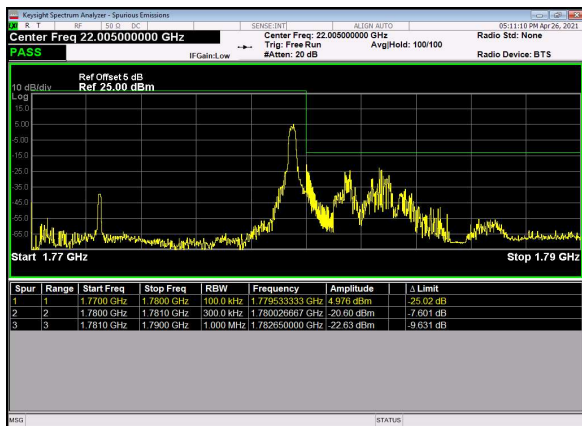
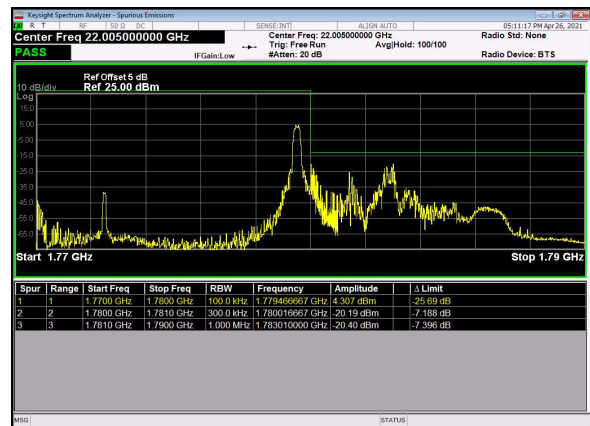
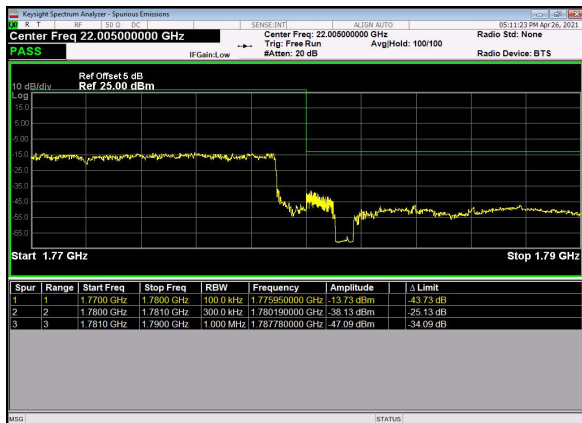
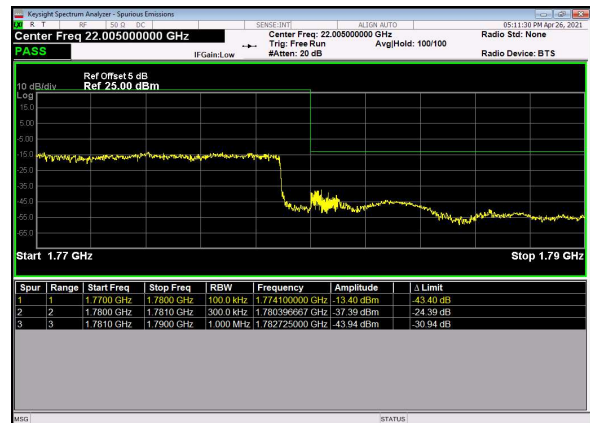
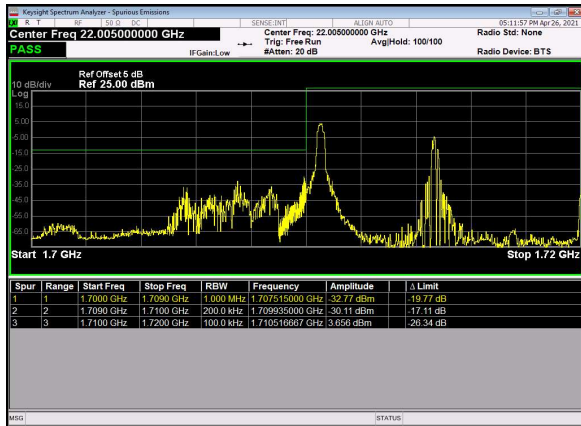
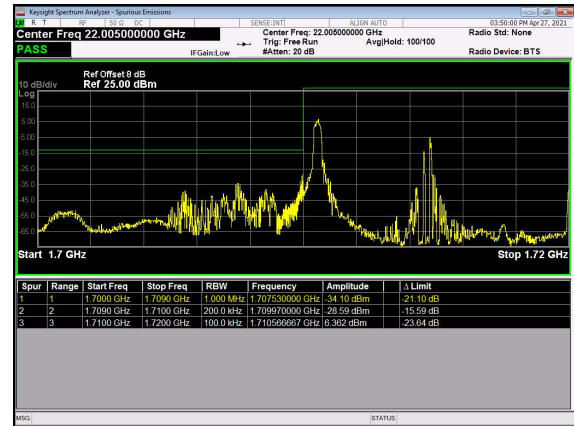
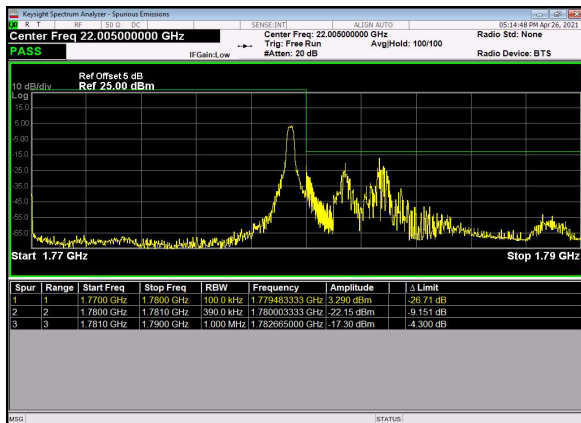
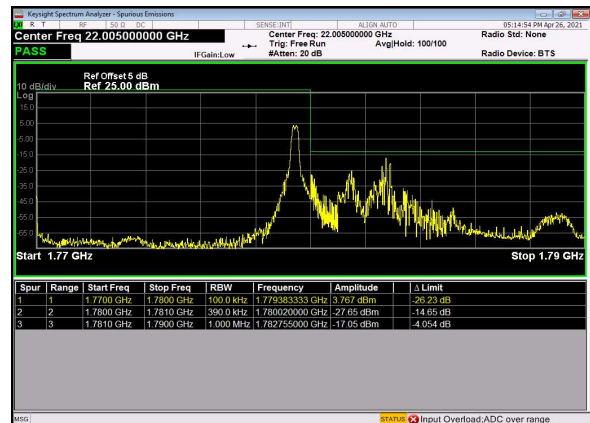


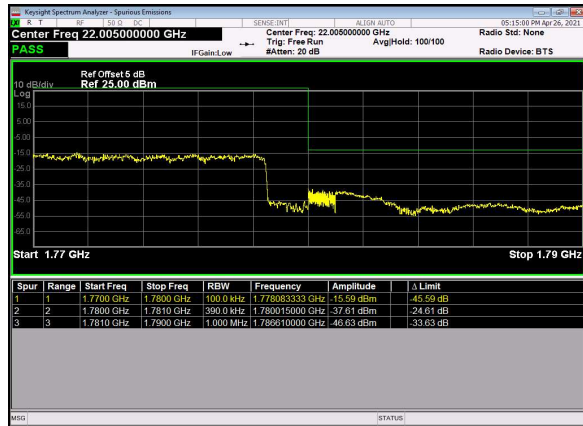
N66(10M)_DFT-s-OFDM_BPSK_Edge_1RB_R
ght_High_CHN66(10M)_DFT-s-OFDM_QPSK_Edge_1RB_R
ght_High_CHN66(10M)_DFT-s-OFDM_BPSK_Outer_Full_Hi
gh_CHN66(10M)_DFT-s-OFDM_QPSK_Outer_Full_Hi
gh_CHN66(15M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Low_CHN66(15M)_DFT-s-OFDM_QPSK_Edge_1RB_L
eft_Low_CH

N66(15M)_DFT-s-OFDM_BPSK_Outer_Full_Lo
w_CHN66(15M)_DFT-s-OFDM_QPSK_Outer_Full_L
ow_CHN66(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Ri
ght_High_CHN66(15M)_DFT-s-OFDM_QPSK_Edge_1RB_R
ight_High_CHN66(15M)_DFT-s-OFDM_BPSK_Outer_Full_Hi
gh_CHN66(15M)_DFT-s-OFDM_QPSK_Outer_Full_Hi
gh_CH

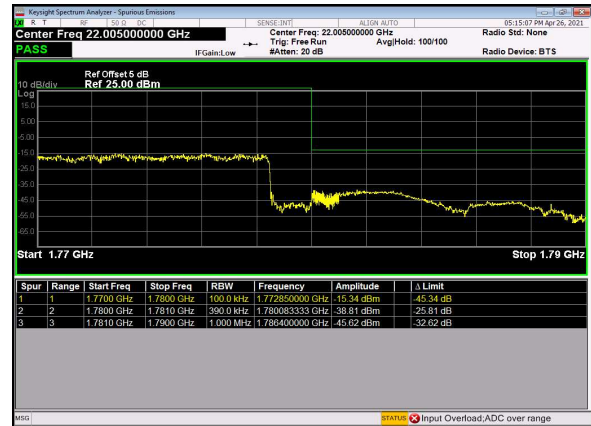
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eft_Low_CHN66(20M)_DFT-s-OFDM_QPSK_Edge_1RB_L
eft_Low_CHN66(20M)_DFT-s-OFDM_BPSK_Outer_Full_L
w_CHN66(20M)_DFT-s-OFDM_QPSK_Outer_Full_L
ow_CHN66(20M)_DFT-s-OFDM_BPSK_Edge_1RB_R
ght_High_CHN66(20M)_DFT-s-OFDM_QPSK_Edge_1RB_R
ght_High_CH



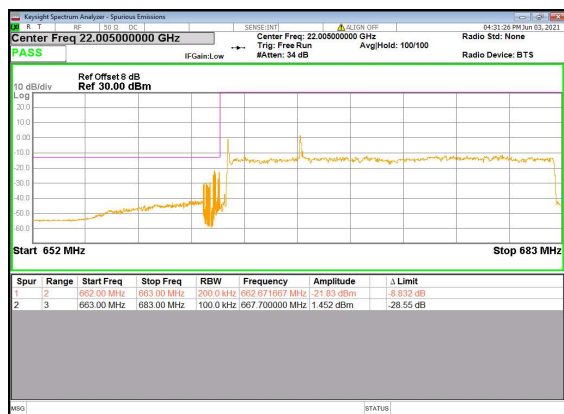
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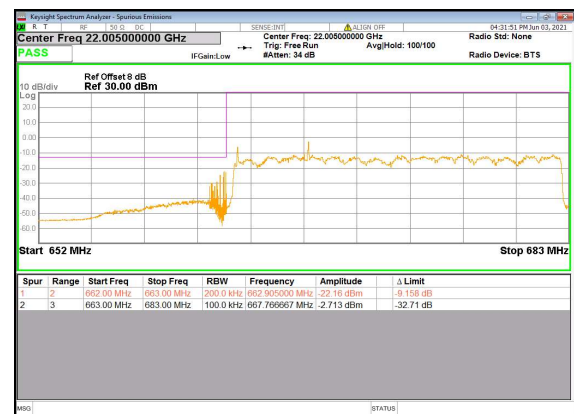
N66(20M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



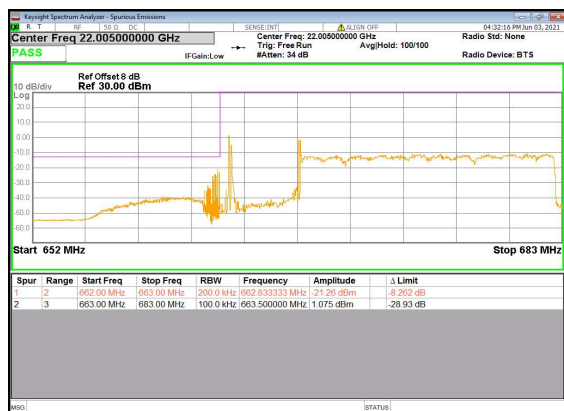
N71(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



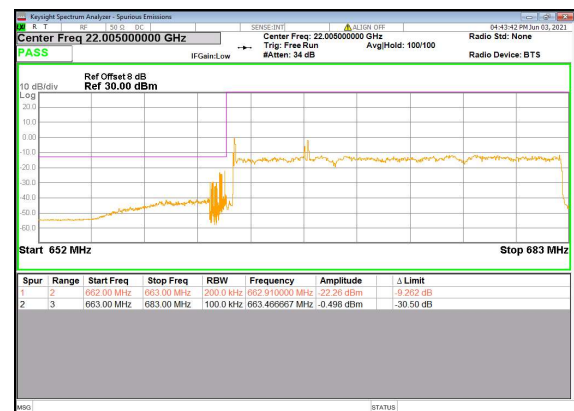
N71(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N71(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_Low_CH

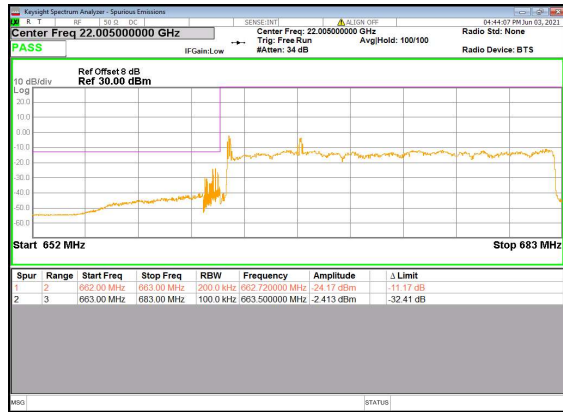


N71(5M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH





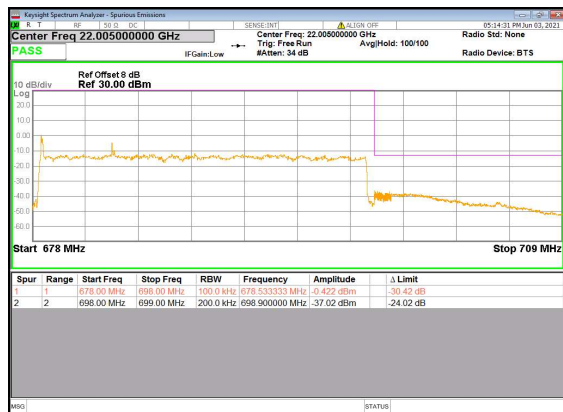
N71(5M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



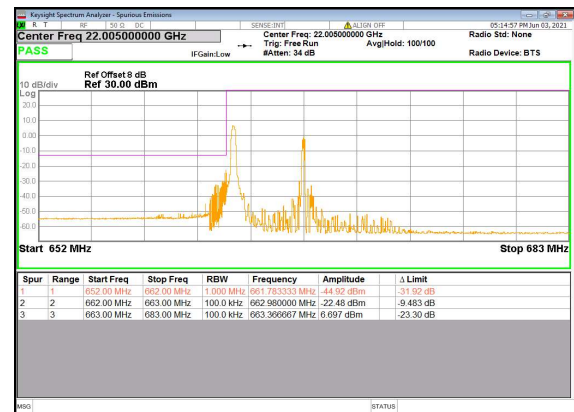
N71(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



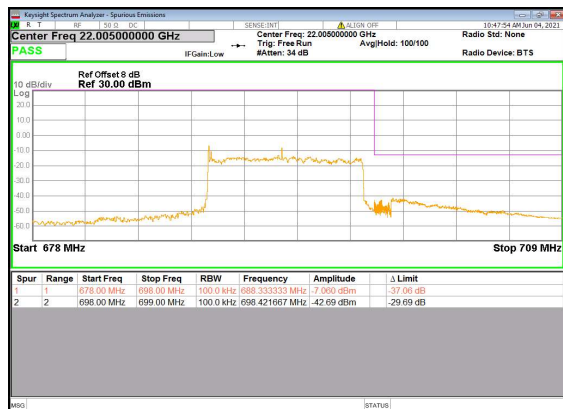
N71(5M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



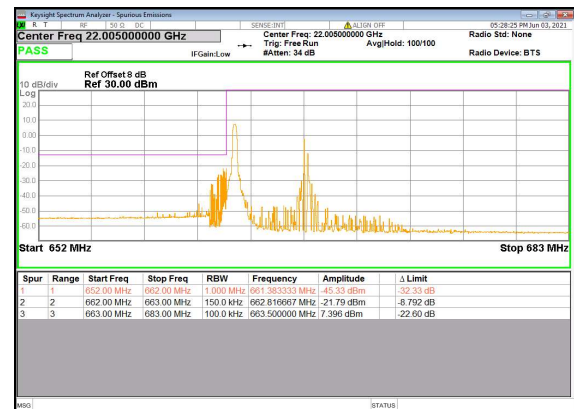
N71(10M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH

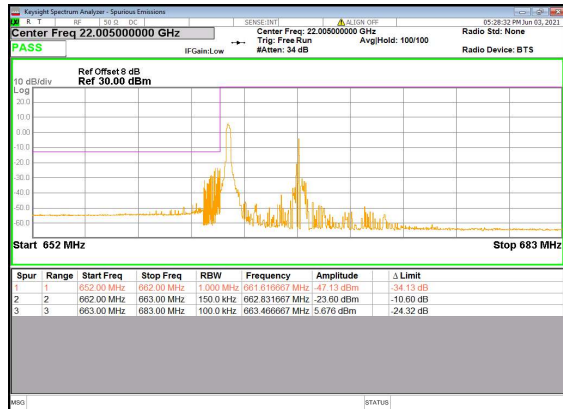
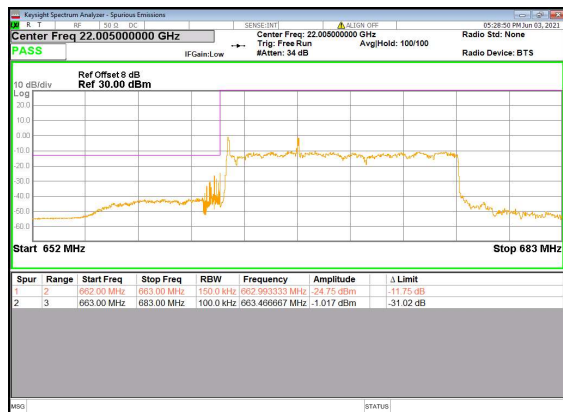
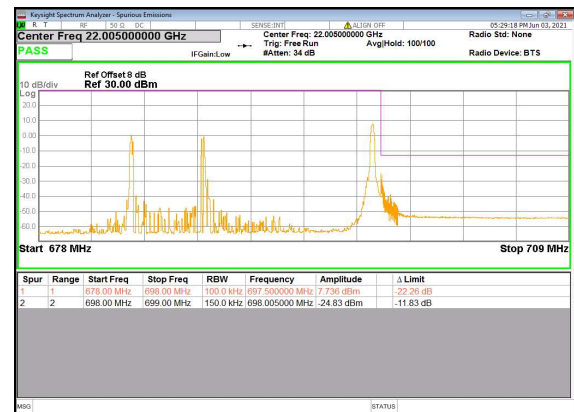
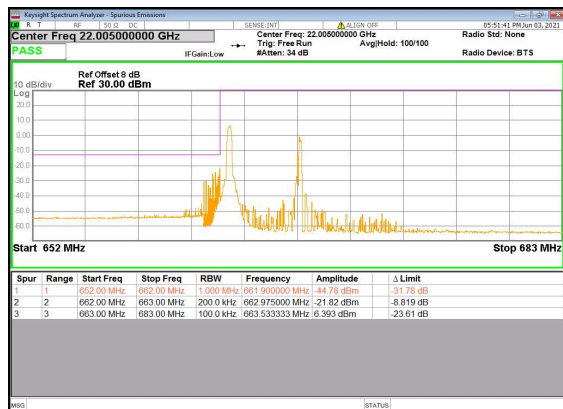


N71(10M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



N71(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



N71(15M)_DFT-s-OFDM_QPSK_Edge_1RB_L
eft_Low_CHN71(15M)_DFT-s-OFDM_BPSK_Outer_Full_Lo
w_CHN71(15M)_DFT-s-OFDM_QPSK_Outer_Full_L
ow_CHN71(15M)_DFT-s-OFDM_BPSK_Edge_1RB_Ri
ght_High_CHN71(20M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Low_CHN71(20M)_DFT-s-OFDM_QPSK_Edge_1RB_L
eft_Low_CH