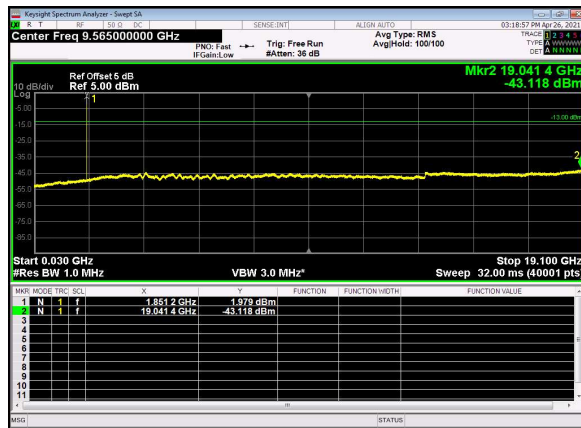
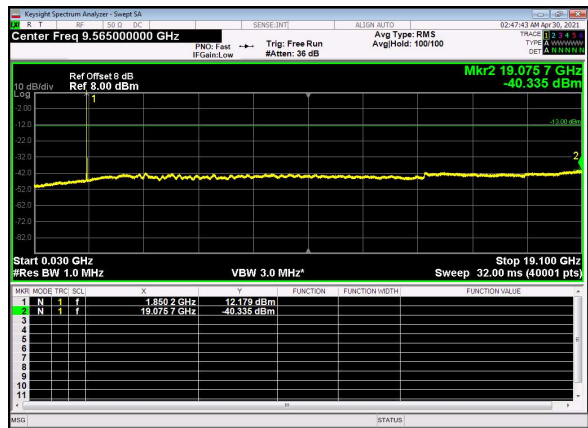
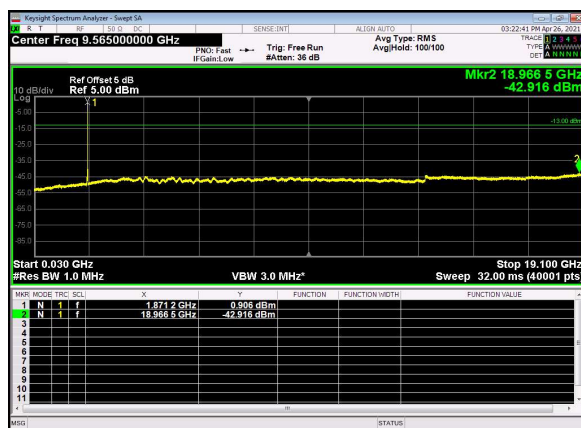
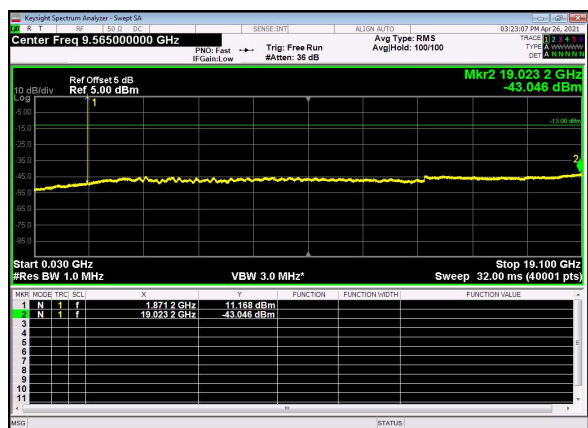
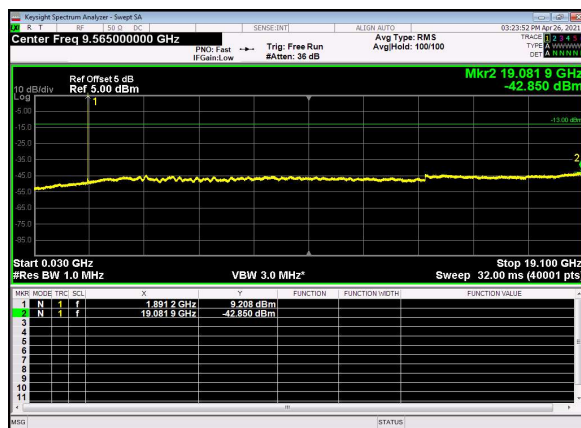
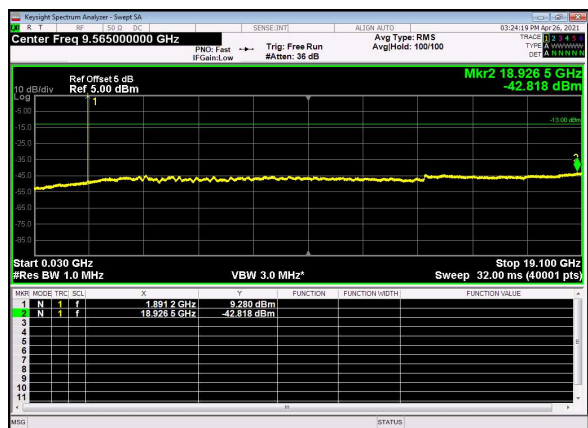
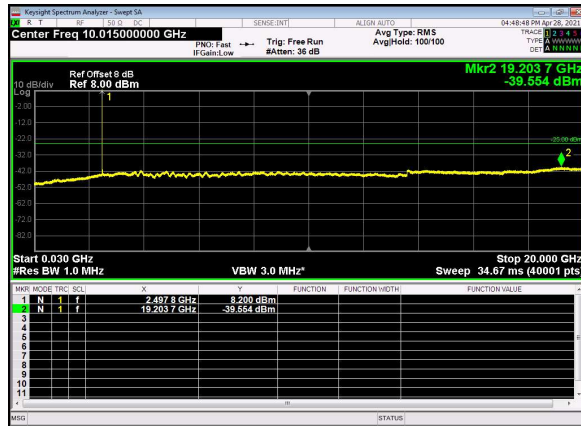
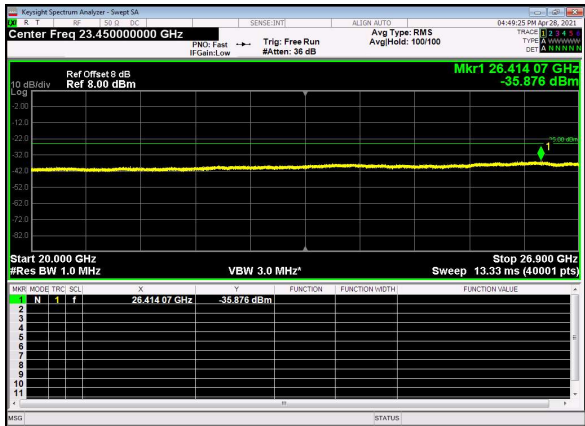
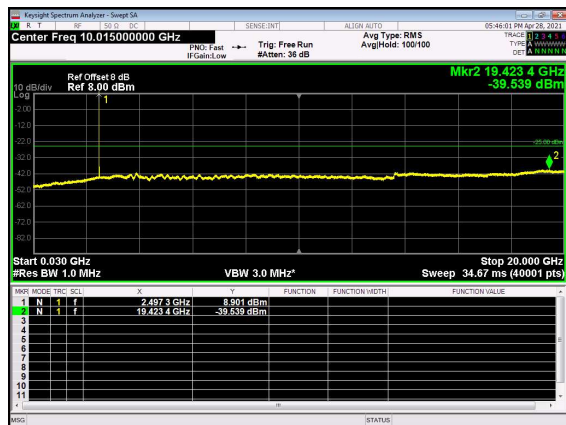
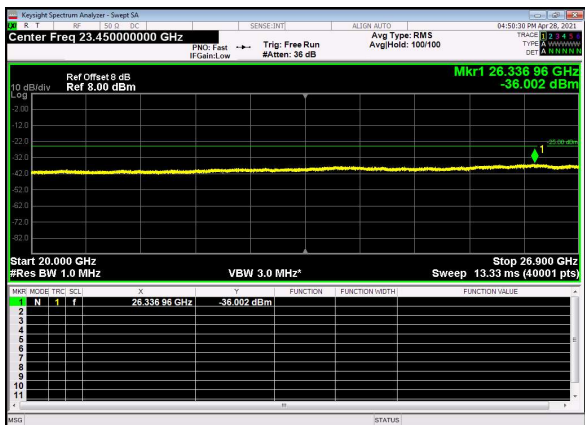
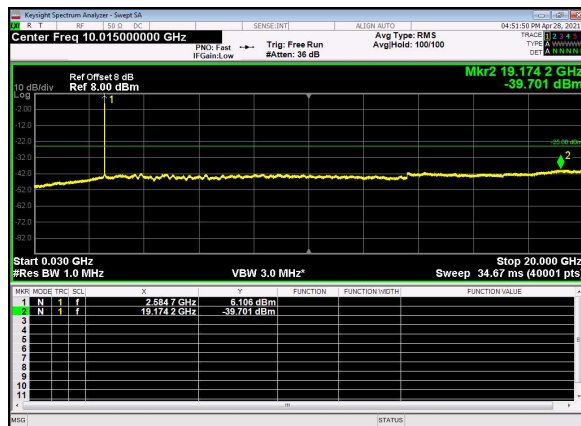
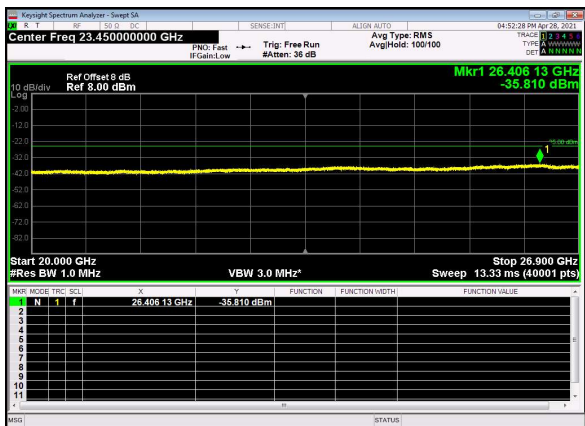
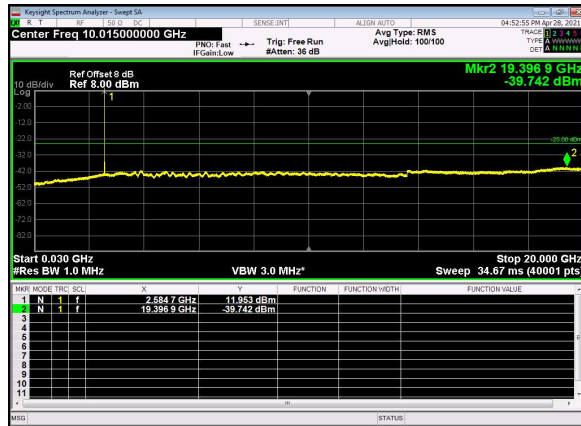
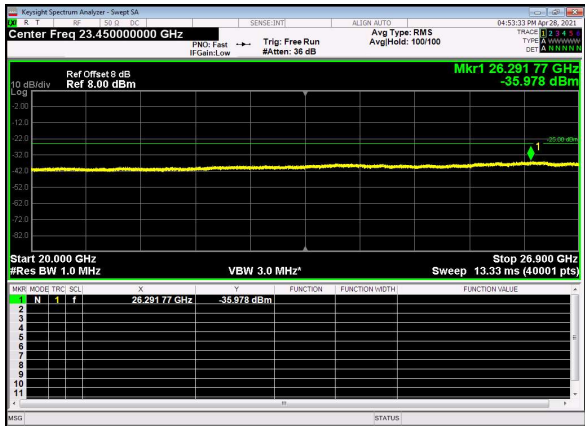
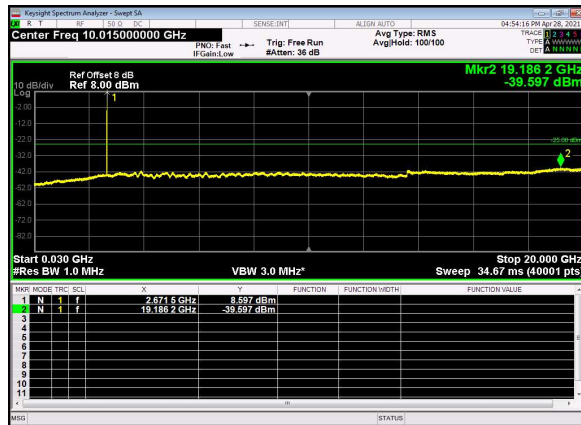
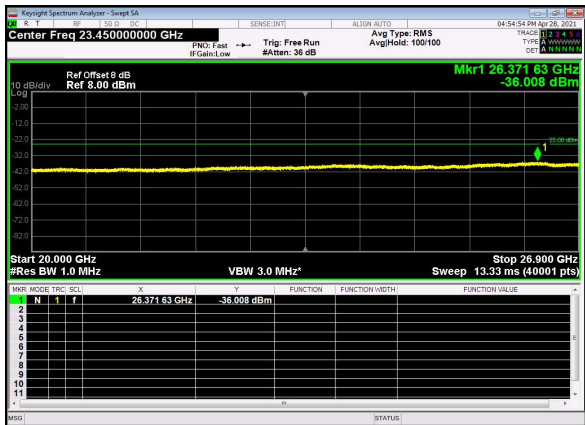
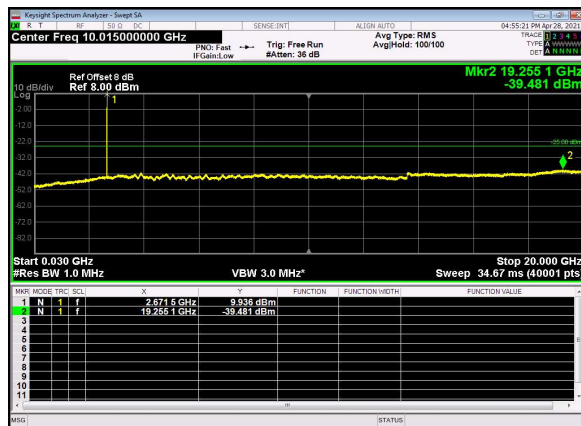
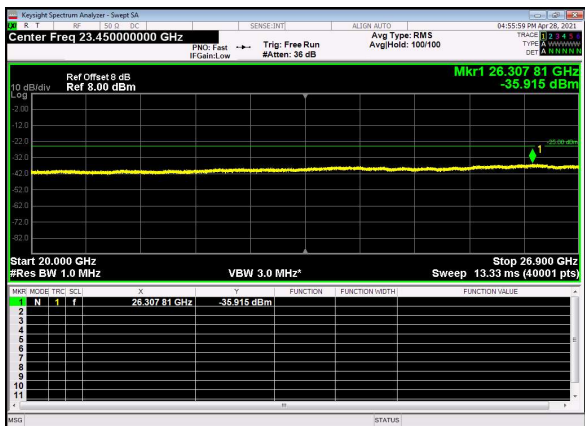
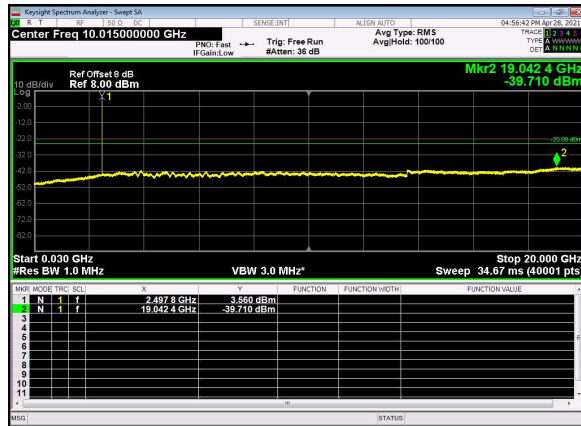
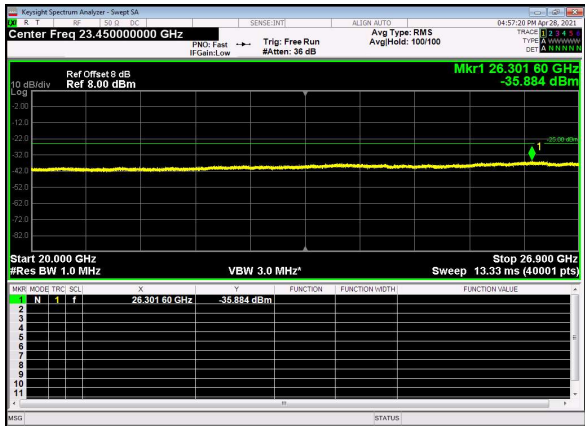
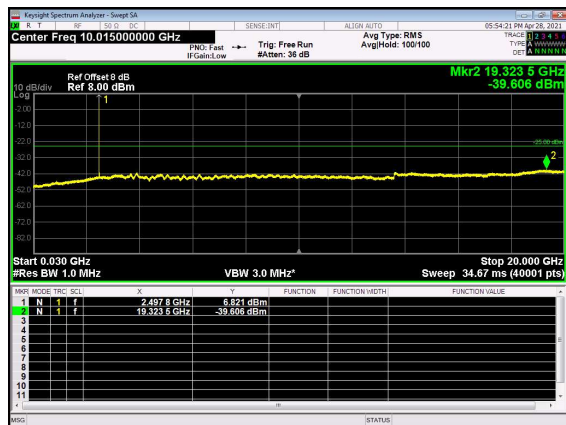
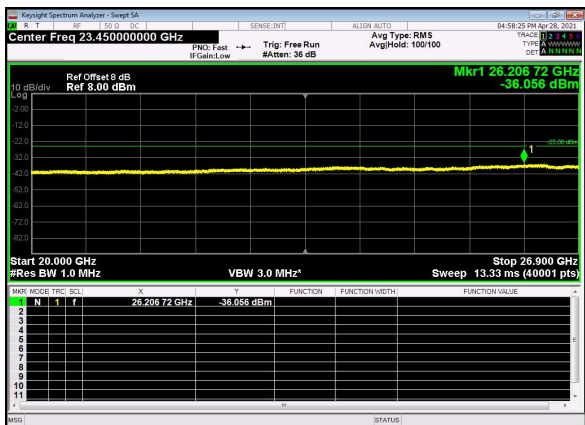
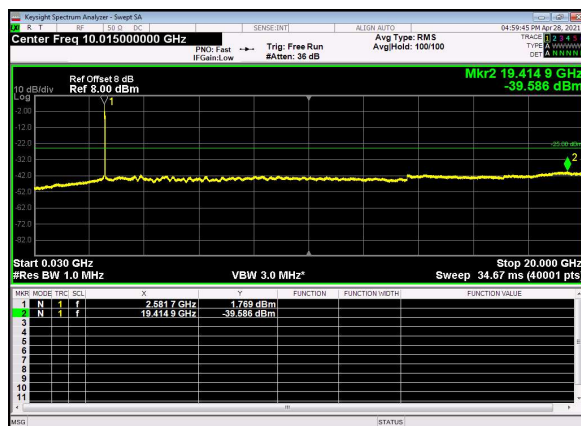
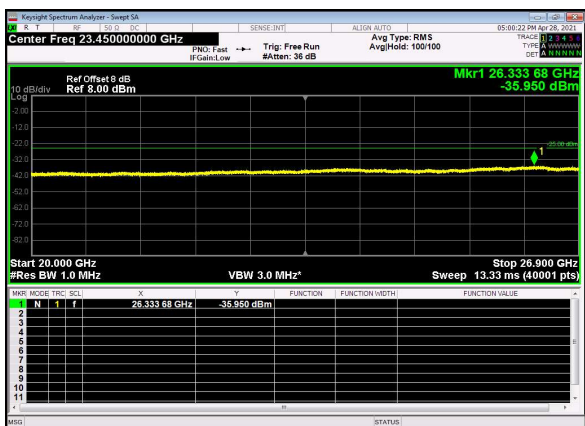
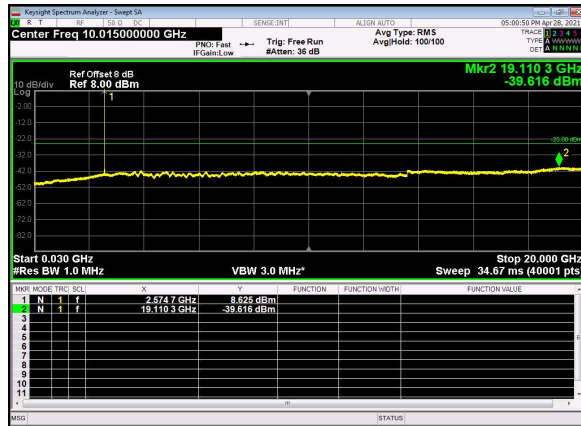
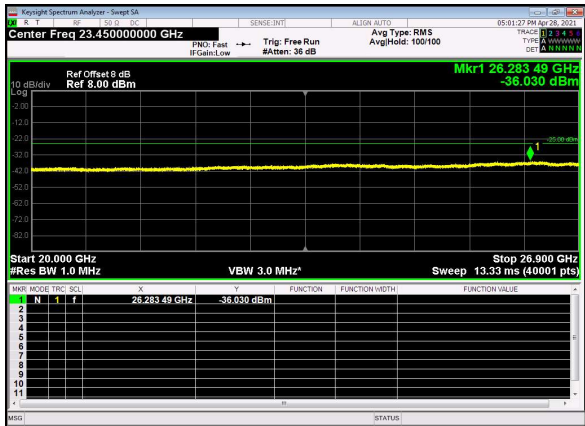
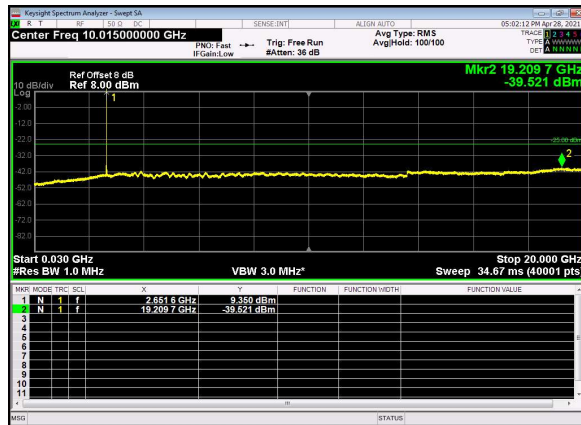
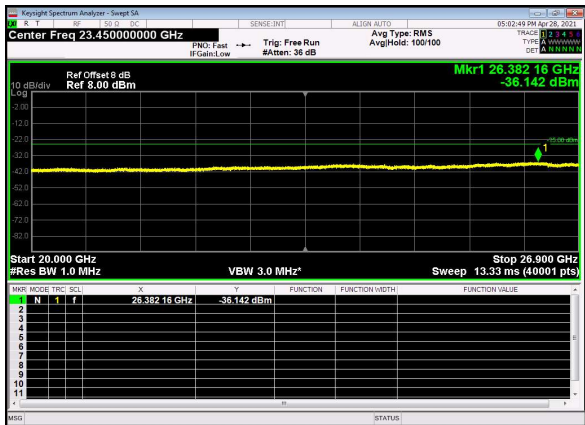
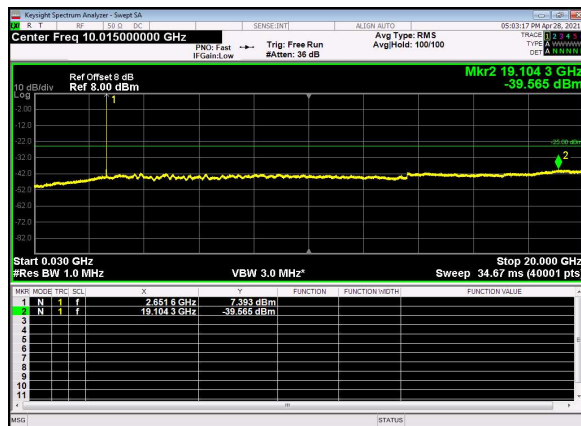
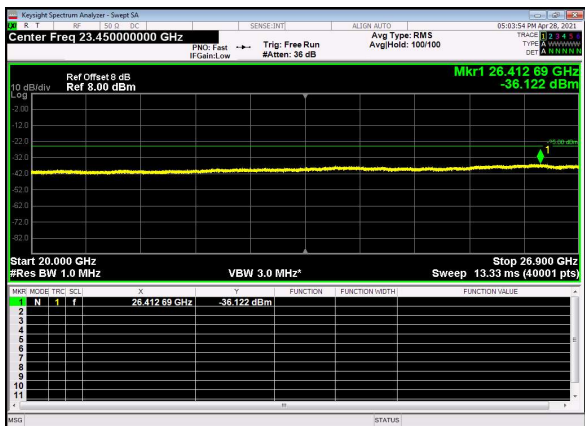


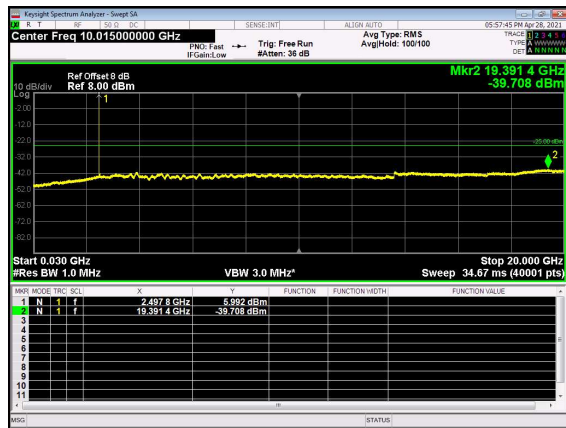
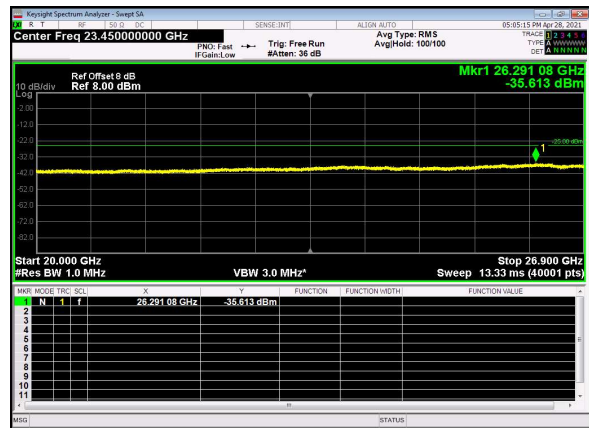
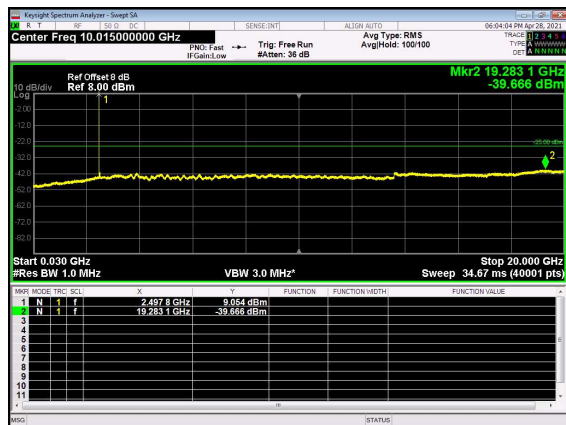
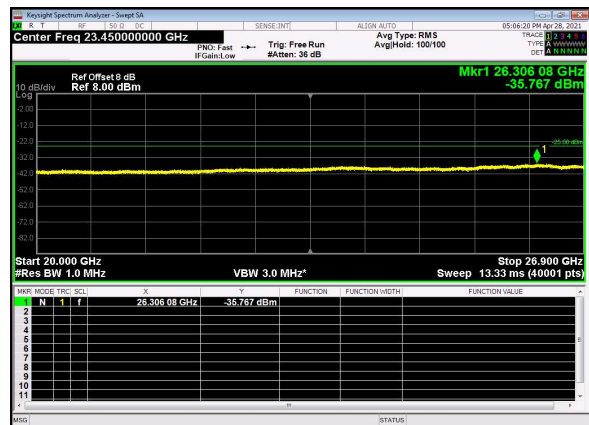
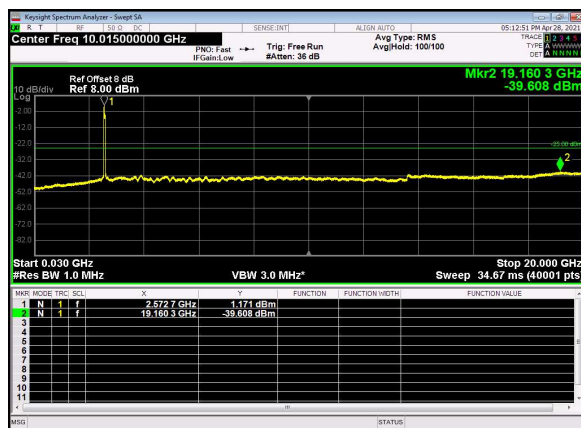
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\_Left\_Mid\_CHN2(20M)\_DFT-s-OFDM\_QPSK\_Edge\_1RB  
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\_Left\_High\_CHN2(20M)\_DFT-s-OFDM\_QPSK\_Edge\_1RB  
\_Left\_High\_CH

N41(20M)\_DFT-s-OFDM\_BPSK\_Edge\_1R  
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B\_Left\_Low\_CHN41(20M)\_DFT-s-OFDM\_QPSK\_Edge\_1R  
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