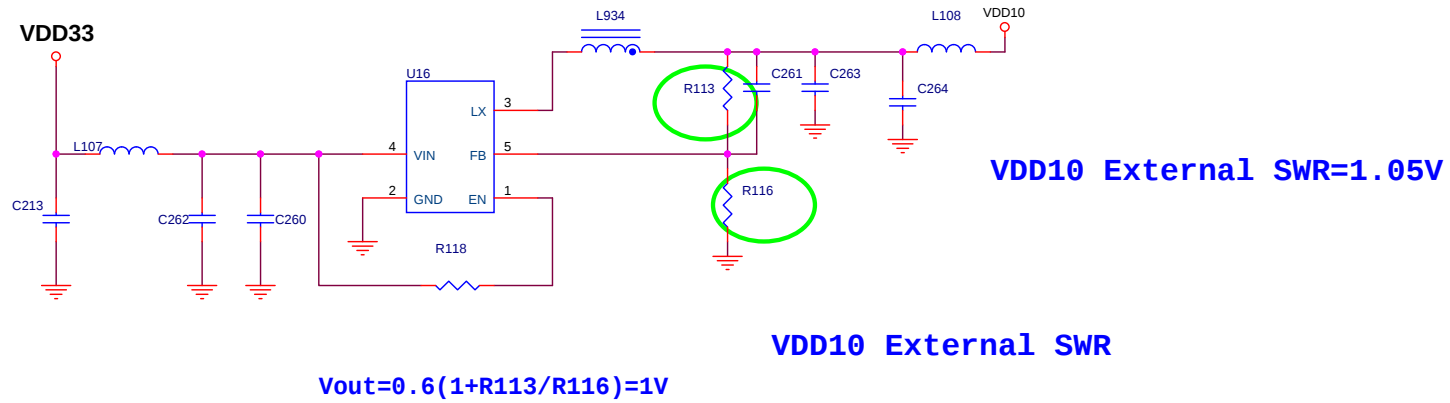
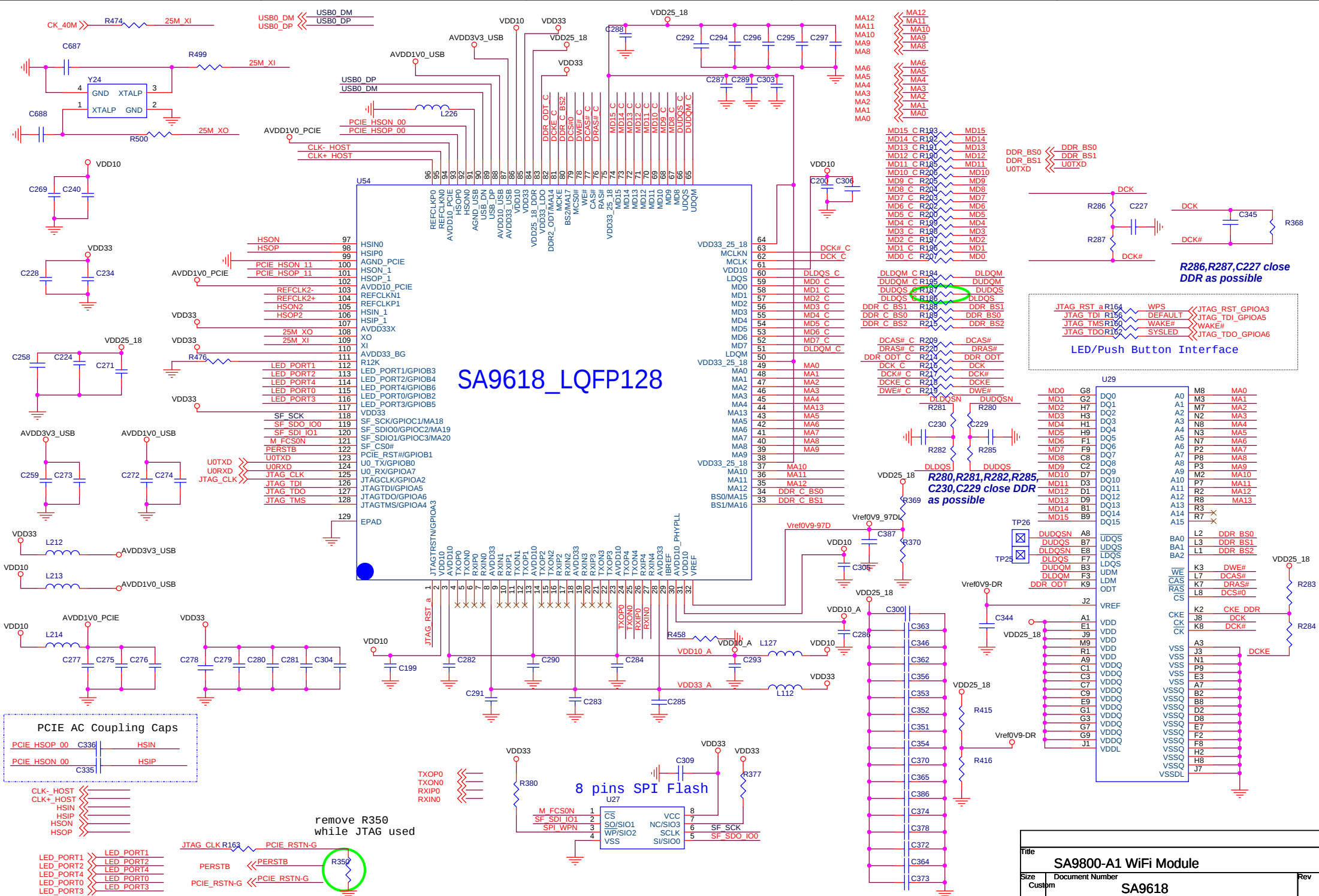
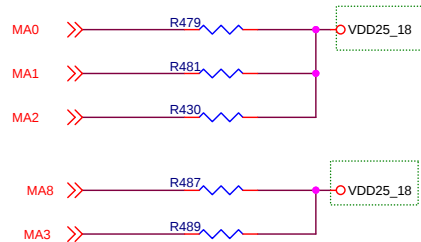
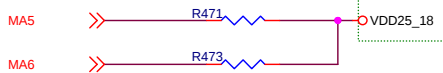
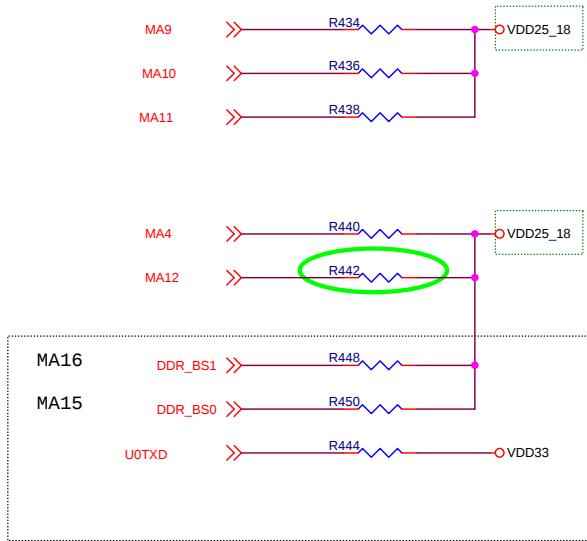




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		ck_mx_freq_sel[2:0] (MHZ)	
PIN_M_A9	ck_mx_freq_sel[0]	000: 156.25MHz	001: 193.75MHz
PIN_M_A10	ck_mx_freq_sel[1]	010: 181.25MHz	011: Reserved
PIN_M_A11	ck_mx_freq_sel[2]	100: Reserved	101: 125MHz
		110: Reserved	111: 168.75MHz

PIN_M_A12	sel_40m	sel_40m
		1=40MHZ
		0=25MHZ(default)

PIN_M_A4	ENABLE_EXT_RSTN	ENABLE_EXT_RSTN
		1=enable
		0=disable

		ck_cpu_freq_sel[3:1] (MHZ)	
PIN_M_A16	ck_cpu_freq_sel[3]	000: 660	100: 620
PIN_M_A15	ck_cpu_freq_sel[2]	001: Reserved	101: 580
		010: Reserved	110: 540
		011: Reserved	111: 500
PIN_U0_TX	ck_cpu_freq_sel[1]		

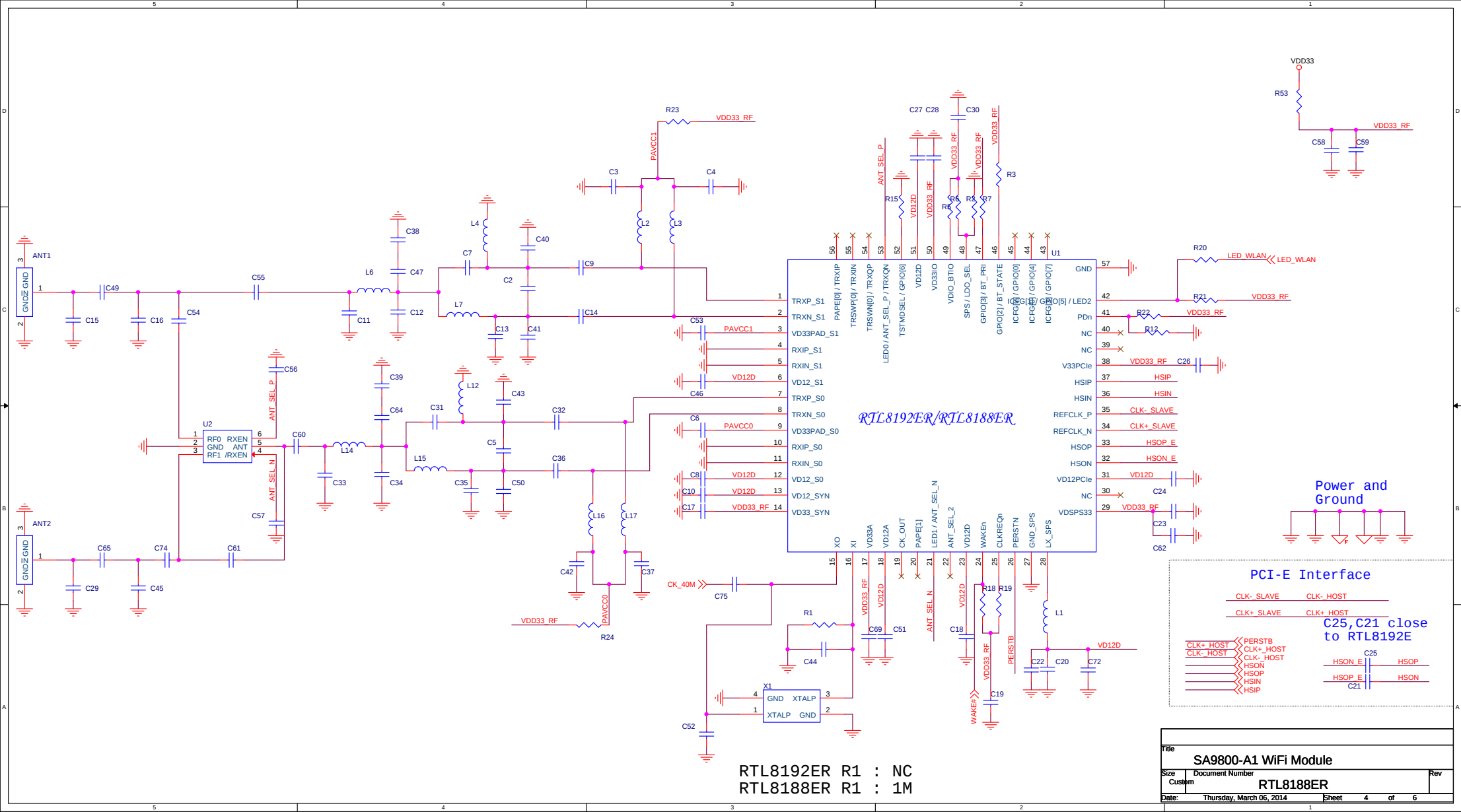
		dbg_olt_mode[1:0]	
PIN_M_A5	dbg_olt_mode[1]	00: EXT PHYmode	
PIN_M_A6	dbg_olt_mode[0]	01: Analog PHYmode	
		10: en_olt_auto_test	
		11: strap_ctl_sys_dbg_sel(default)	

pin name	function name	function detail
		boot_select[2:0]
PIN_M_A0	boot_sel[0]	000: SPI
PIN_M_A1	boot_sel[1]	001: NOR
PIN_M_A2	boot_sel[2]	010: NAND
		011: NAND
		100: ROM1
		101: ROM2
		110: ROM3
		111: ROM4

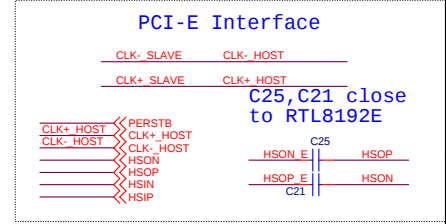
PIN_M_A8	DRAM_TYPE[1]	DRAM_TYPE[1]
		1=DDR
		0=SDR
		(Default)

PIN_M_A3	DRAM_TYPE[0]_INV	DRAM_TYPE[0]_INV
		1=DDR1
		0=DDR2 (Default)

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RTL8188ER R1 : 1M



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