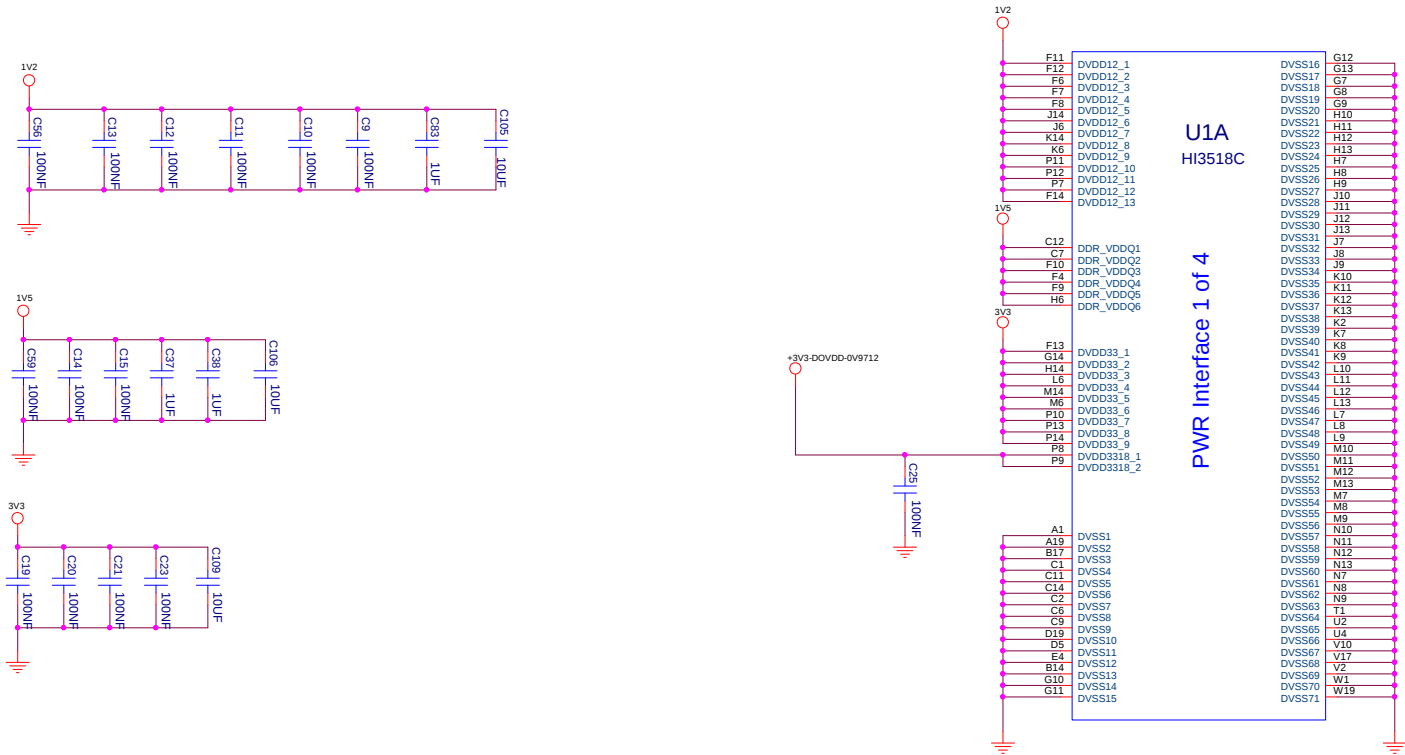
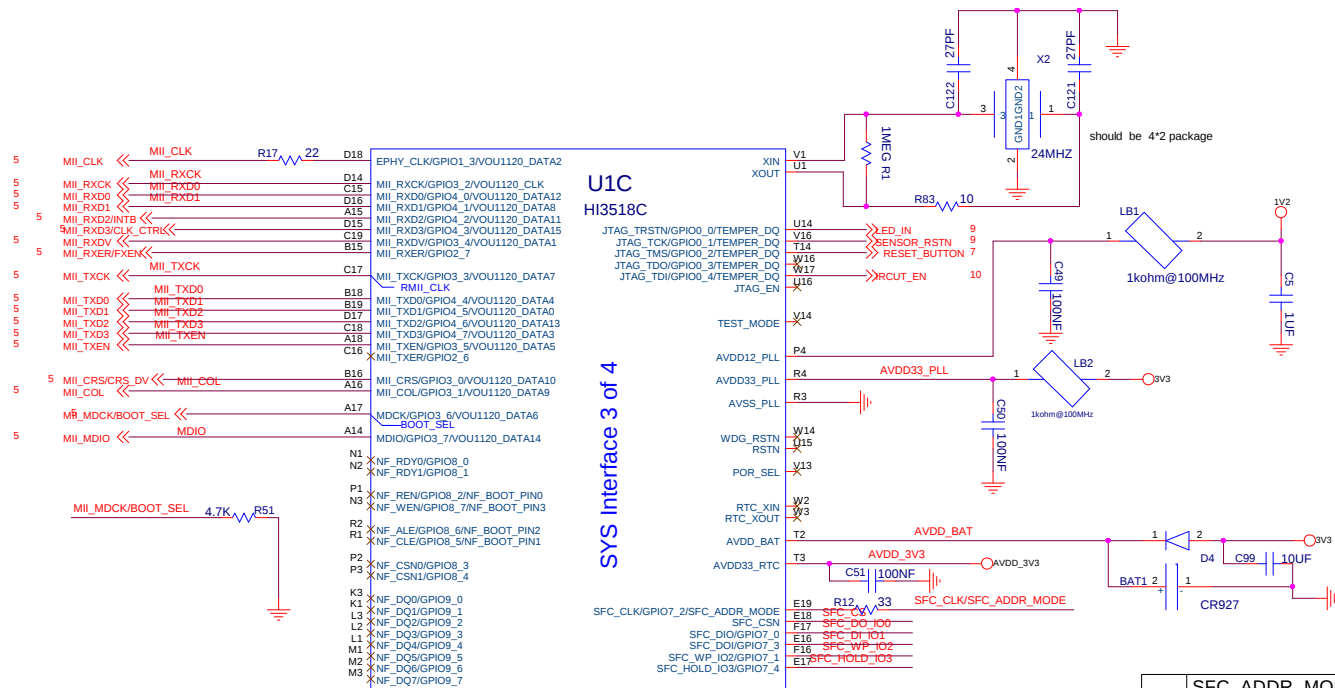






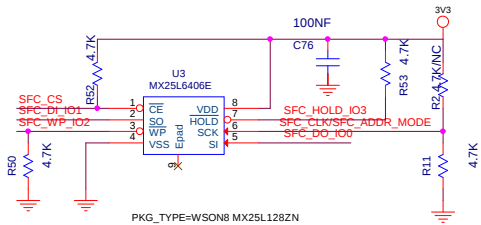
	JTAG_EN
0	Disable JTAG
1	Enable JTAG

	POR_SEL
0	Enable POR
1	Disable POR

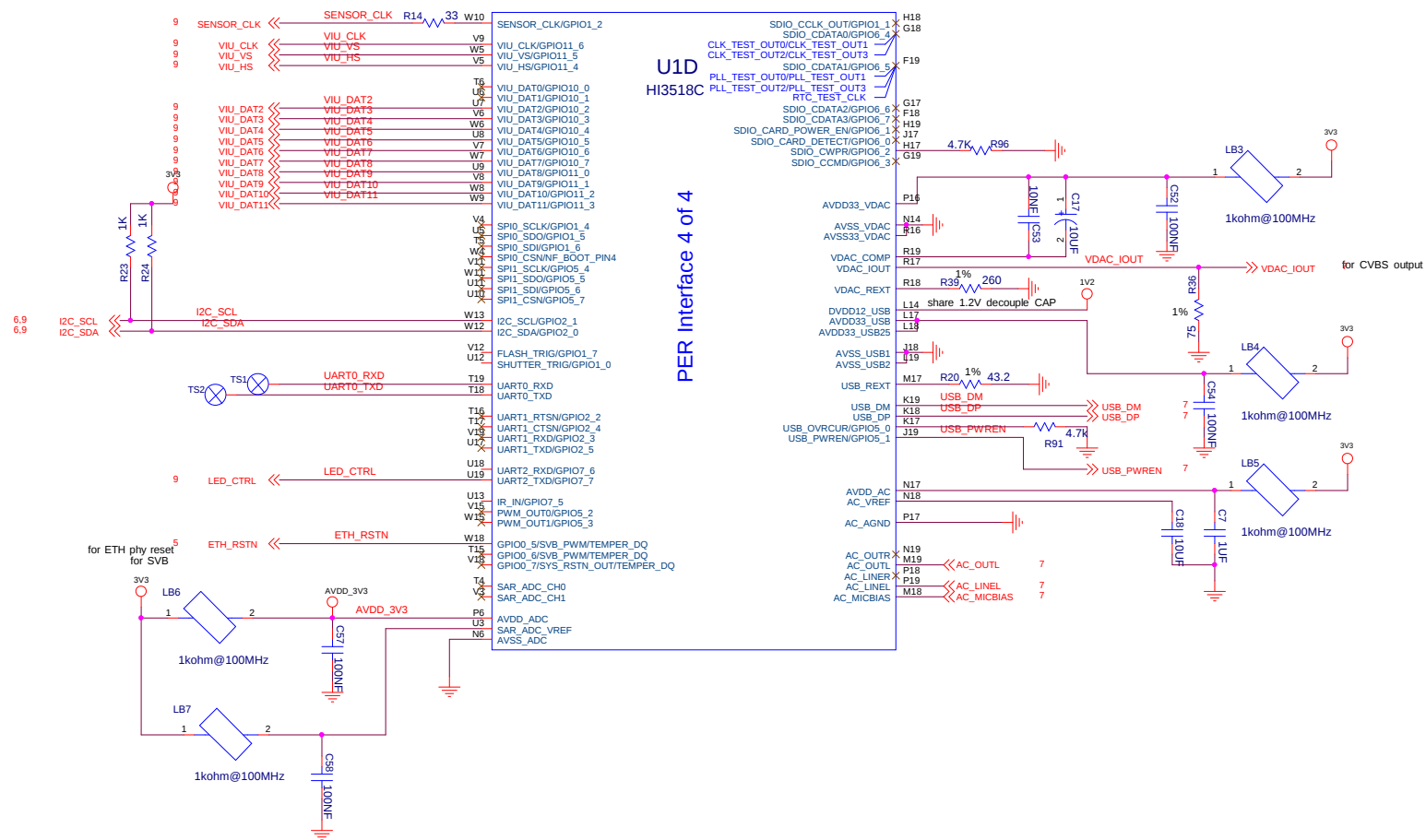
	BOOT_SEL
0	SPI FLASH
1	NAND FLASH

	SFC_ADDR_MODE
0	3 Byte mode
1	4 Byte mode

The configuration of SFC_ADDR_MODE is related to the type of the SPI FLASH.



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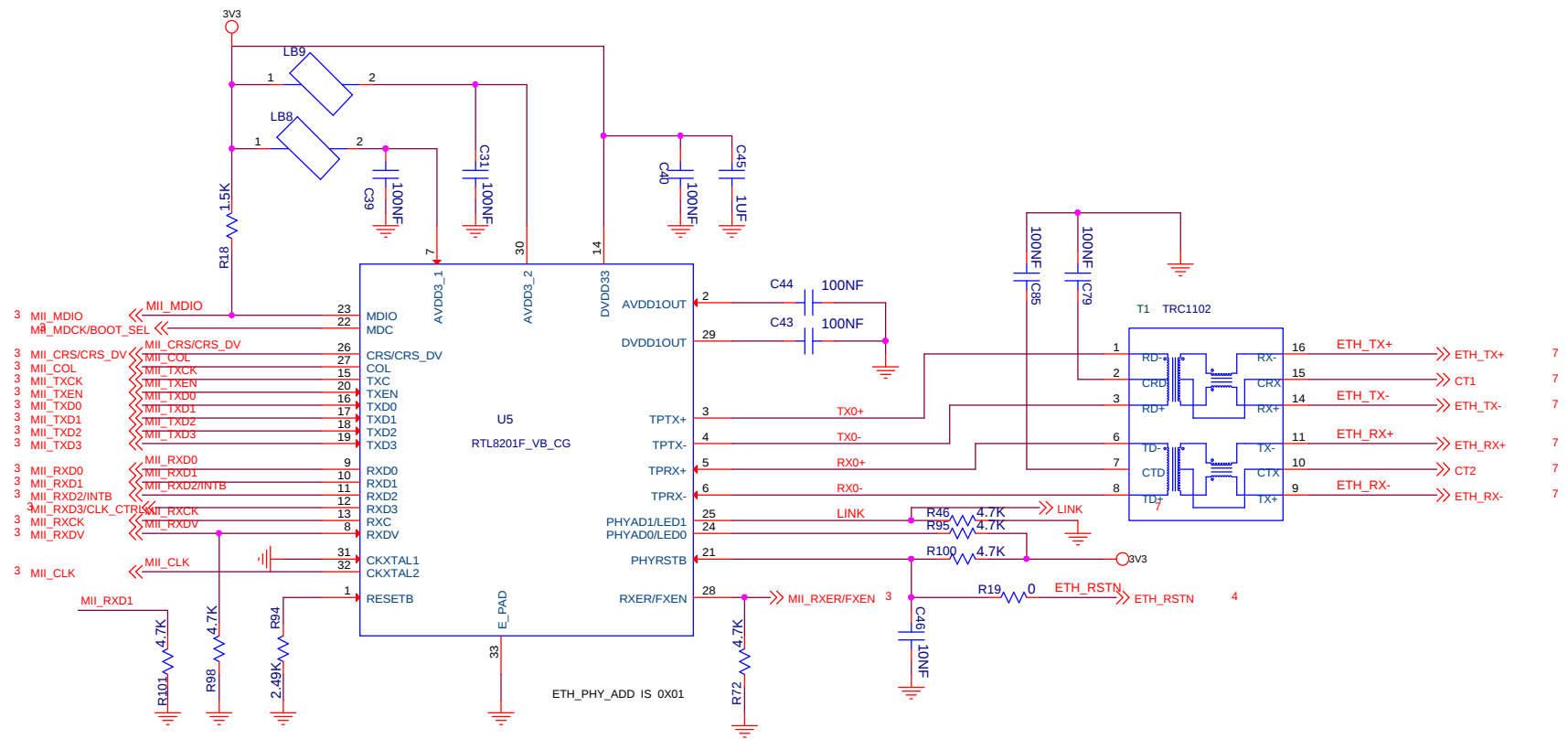
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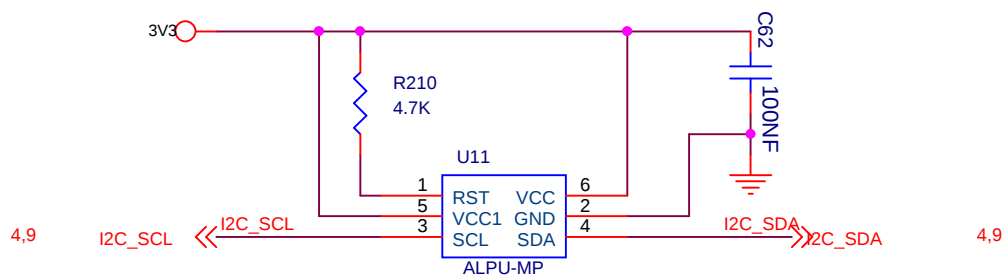
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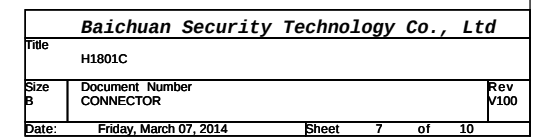
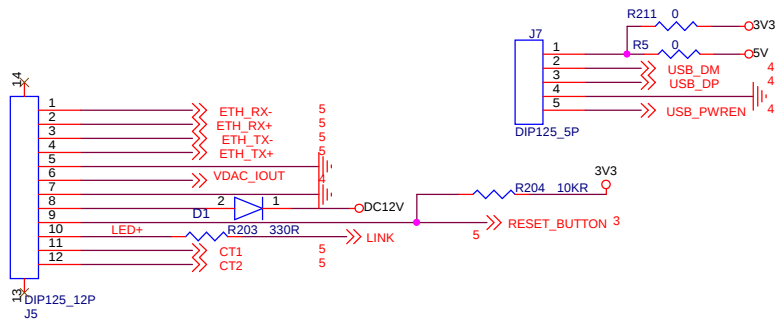


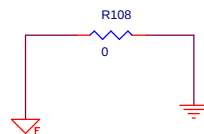
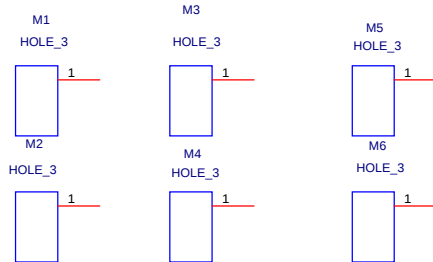
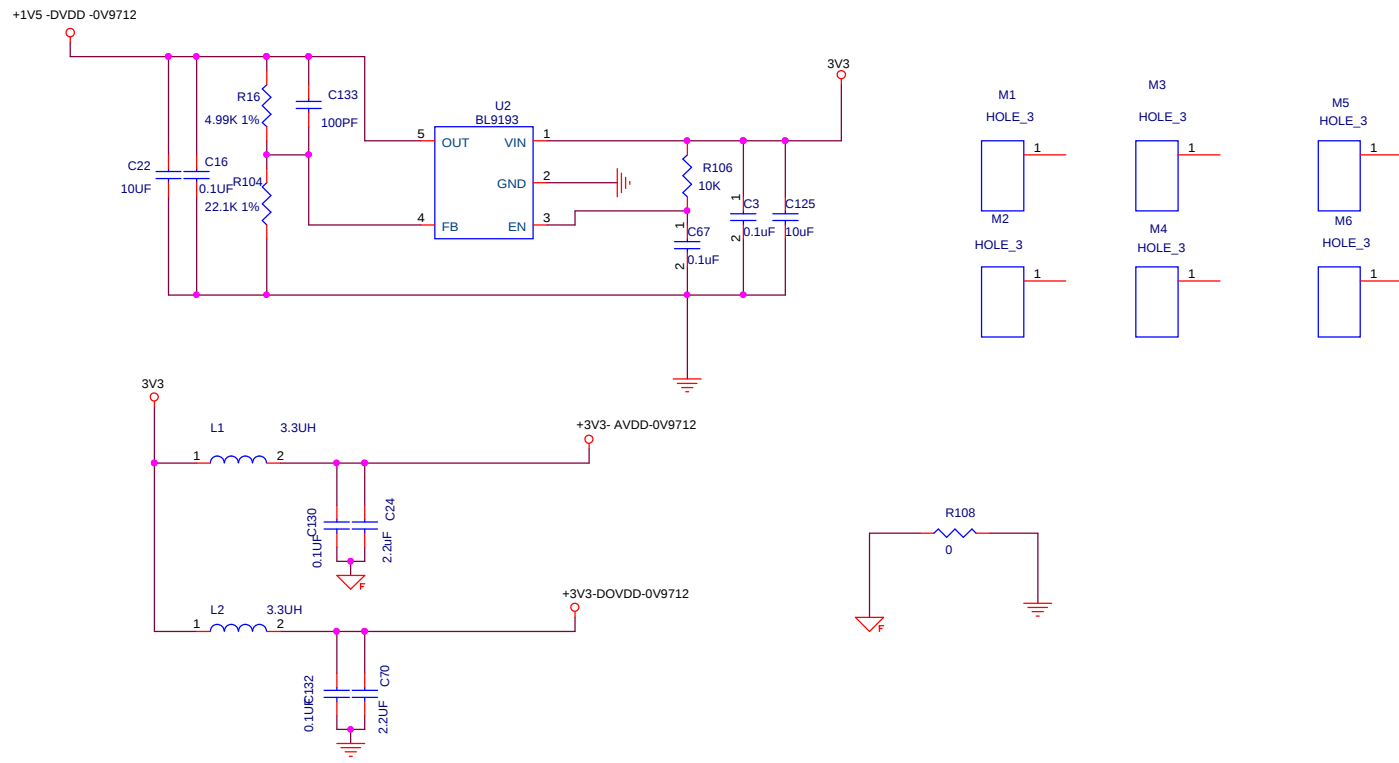
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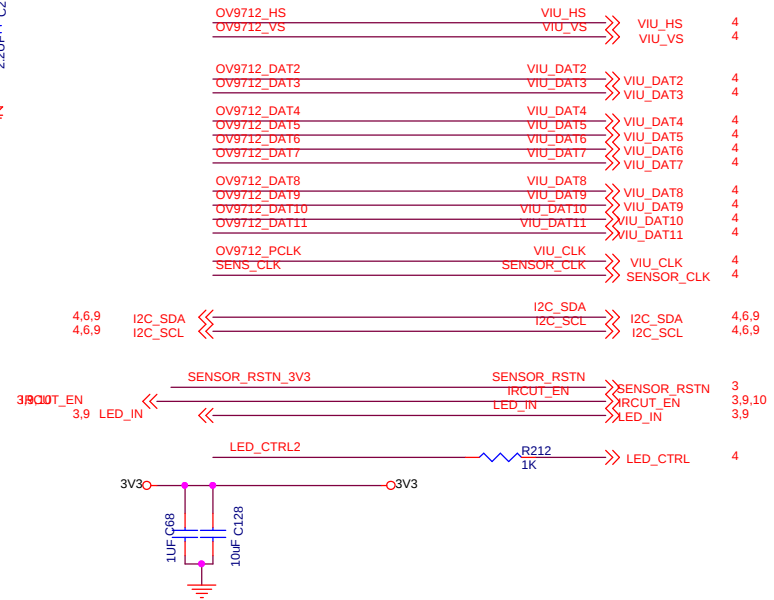
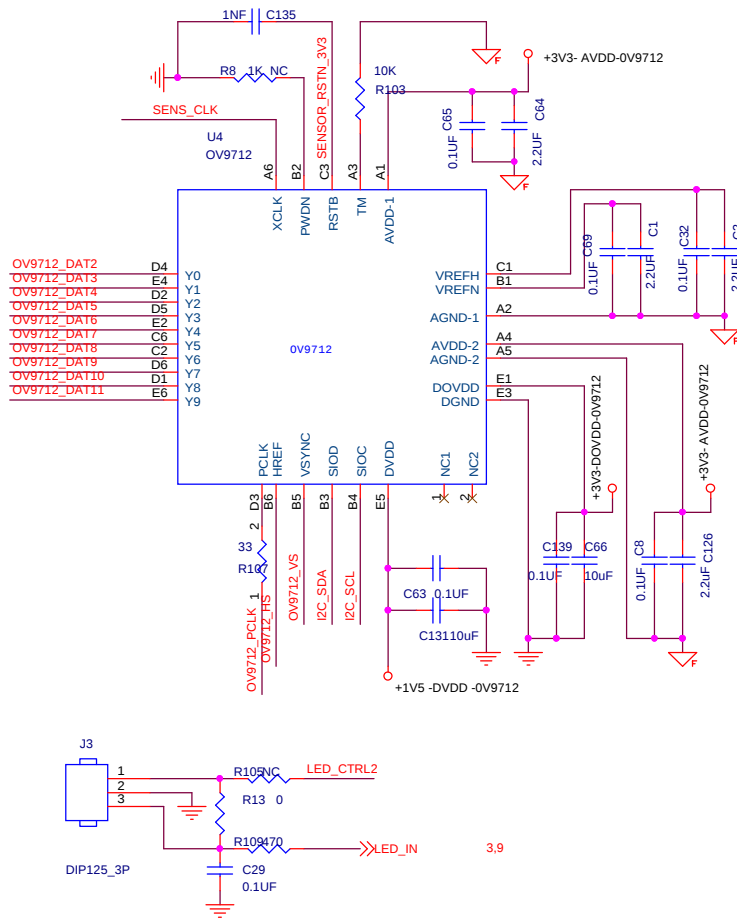
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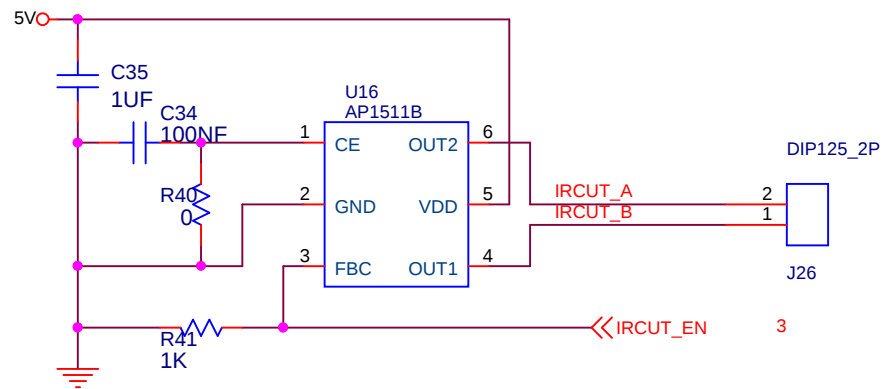




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