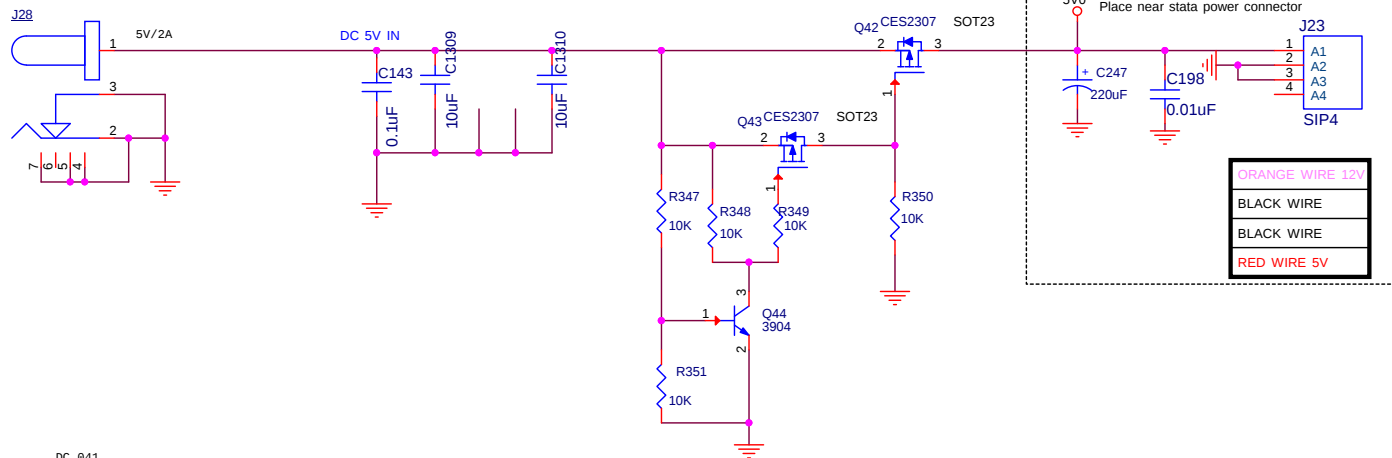
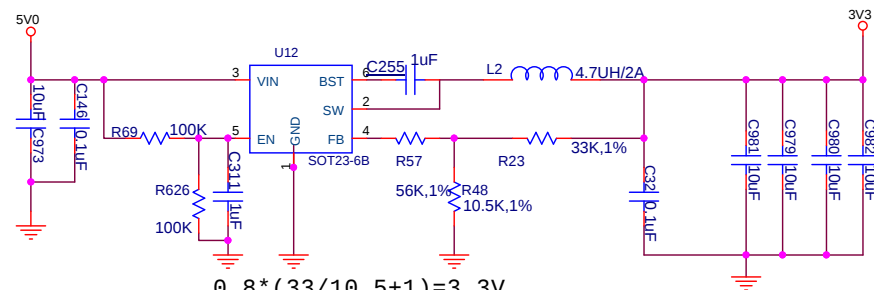
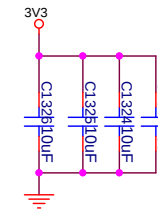
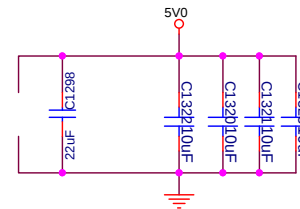
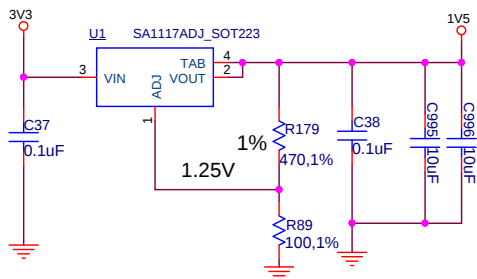
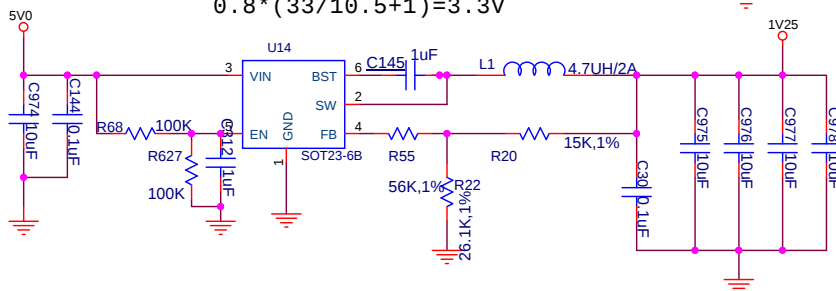


DC_041



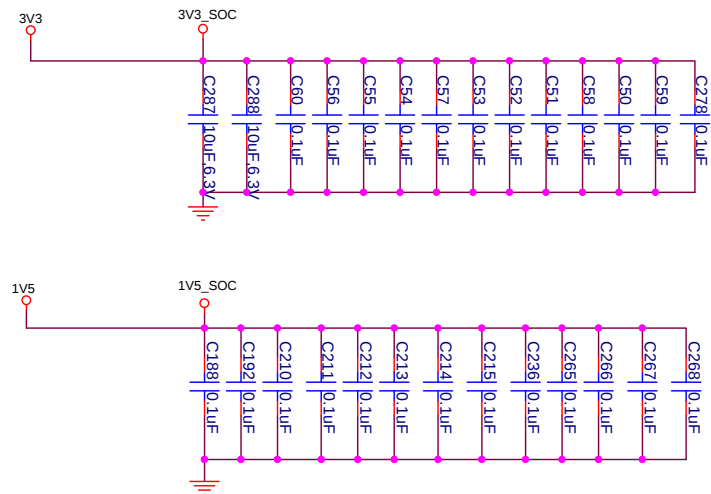
$$0.8 * (33/10.5 + 1) = 3.3V$$



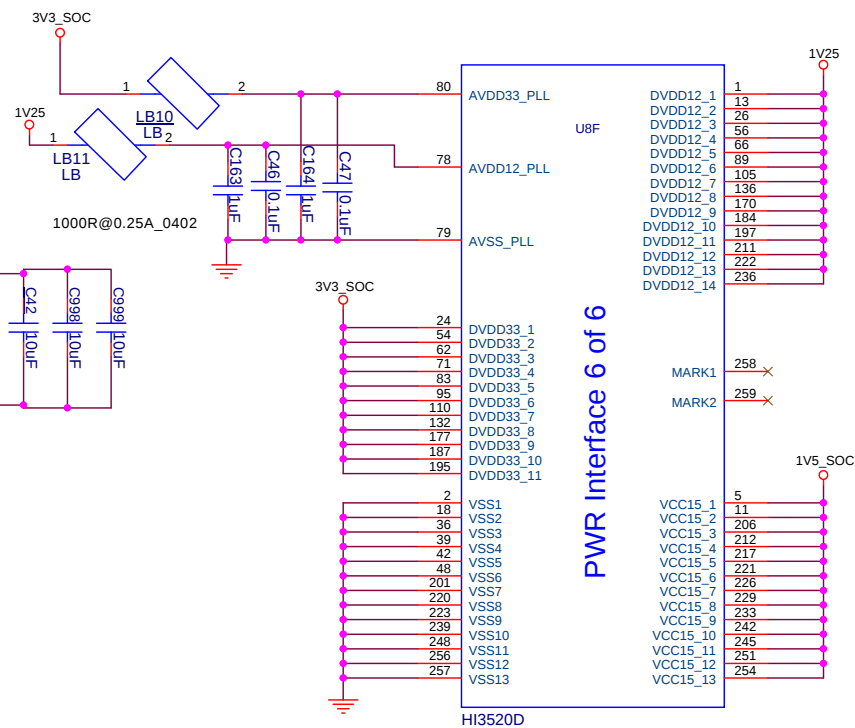
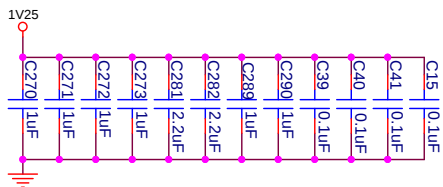
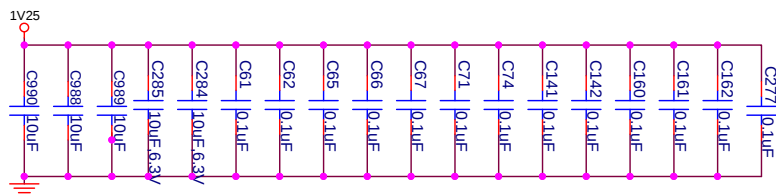
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File			
H3520D			
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Hi3520D POWER_GND



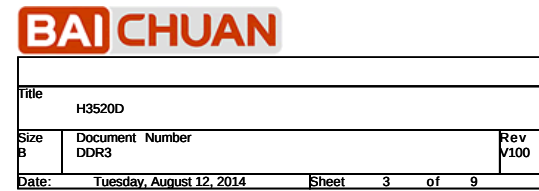
The kind and quantity of the caps must be the same as that for the Hi3520D demo board.



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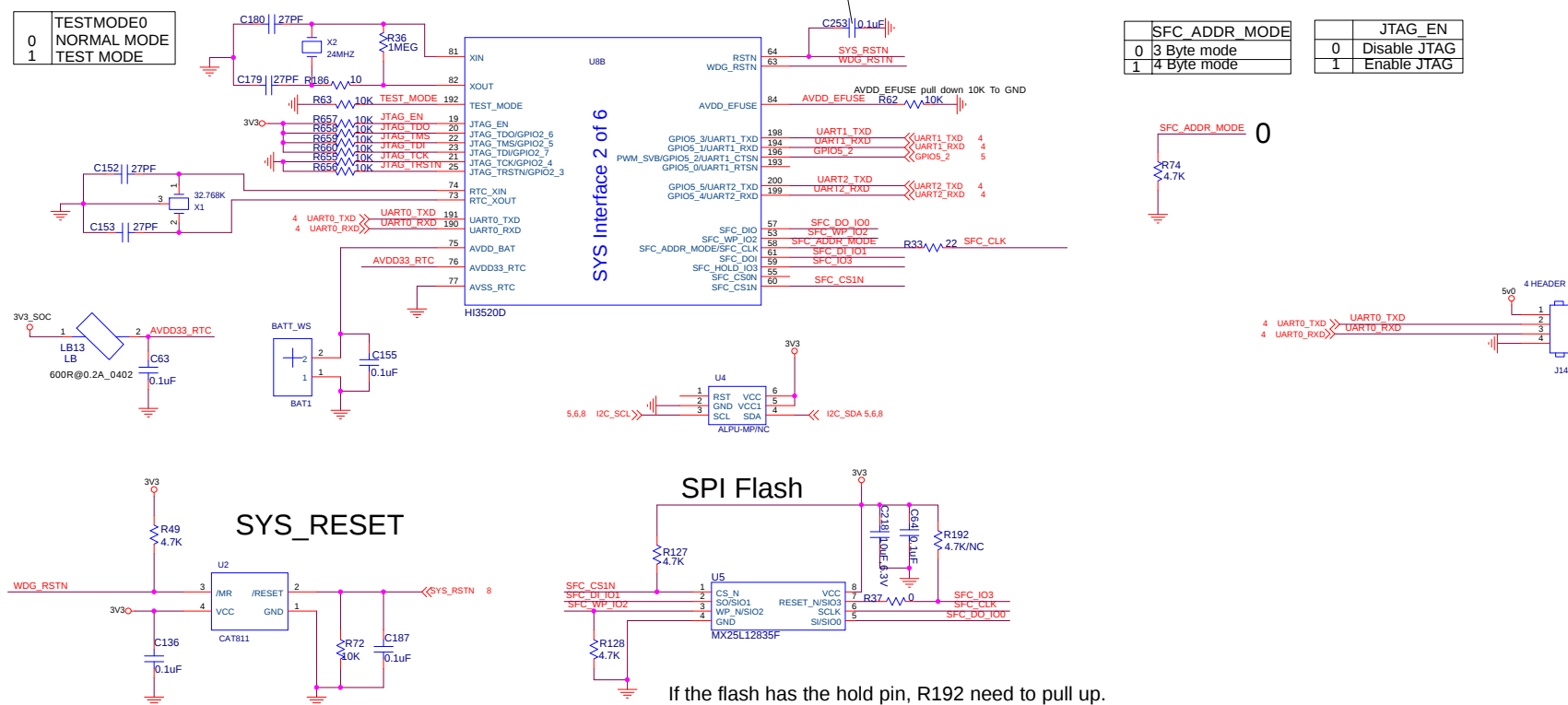
Title			
H3520D			
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The routing design for the DDR must be the same as that for the Hi3520D demo board.



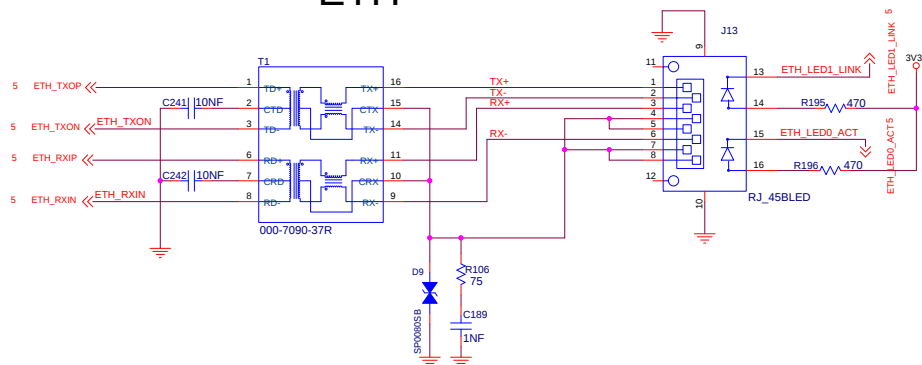
Hi3520D SYS_RST_JTAG

put this Cap close to Hi3520D

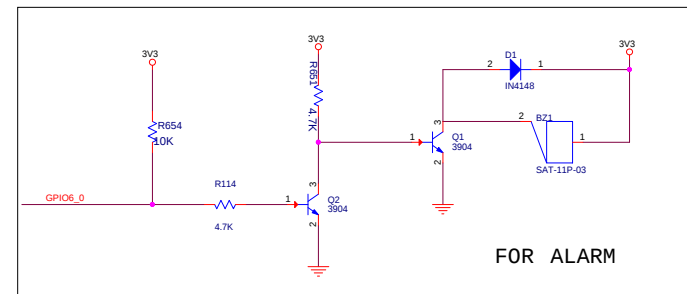
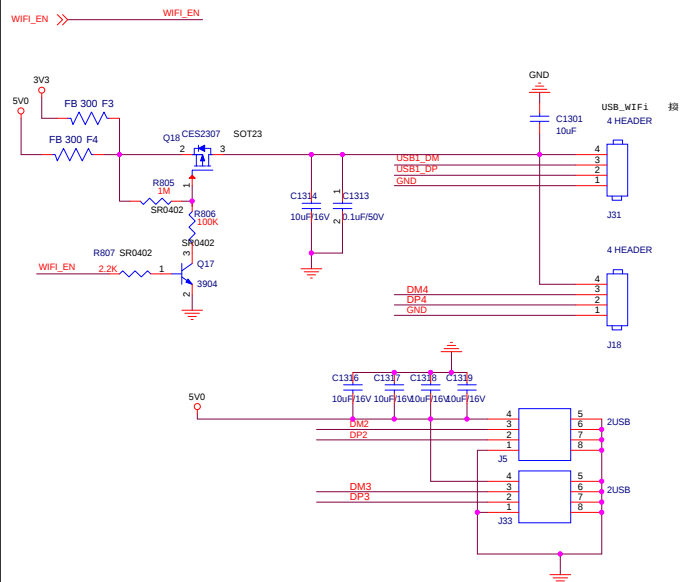
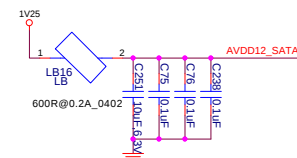
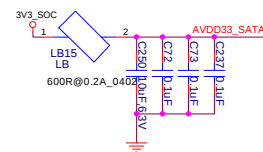
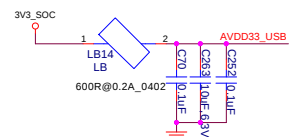
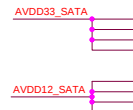
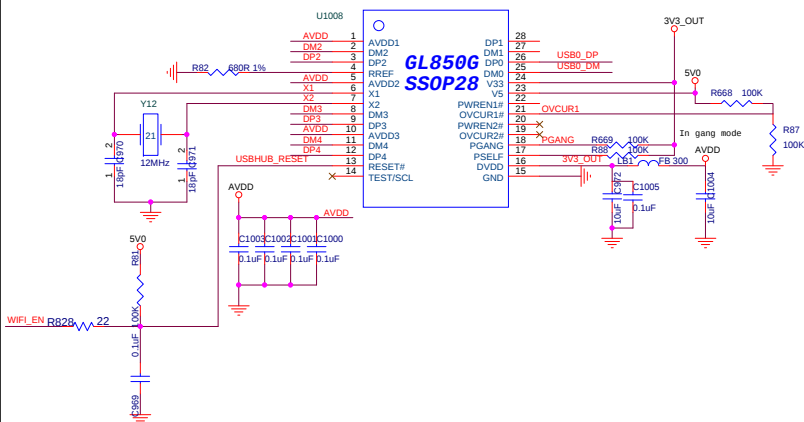
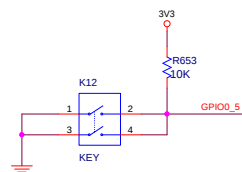
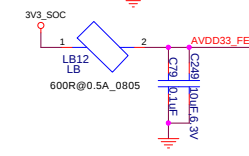
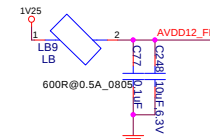
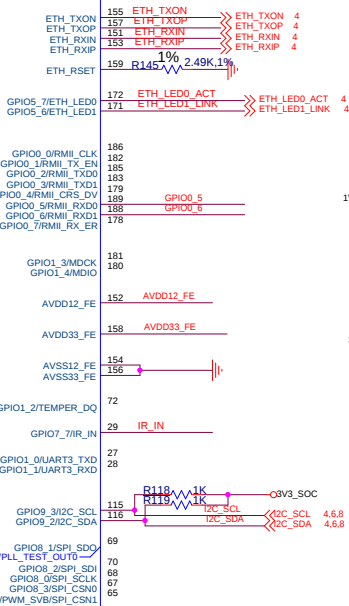
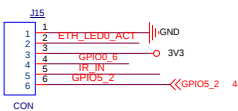
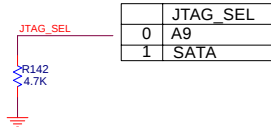
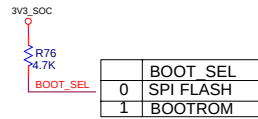
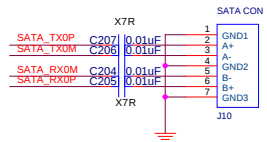


The USB differential trace impedance is 90 OHM
9/5/9 trace width is OK for two-layer board

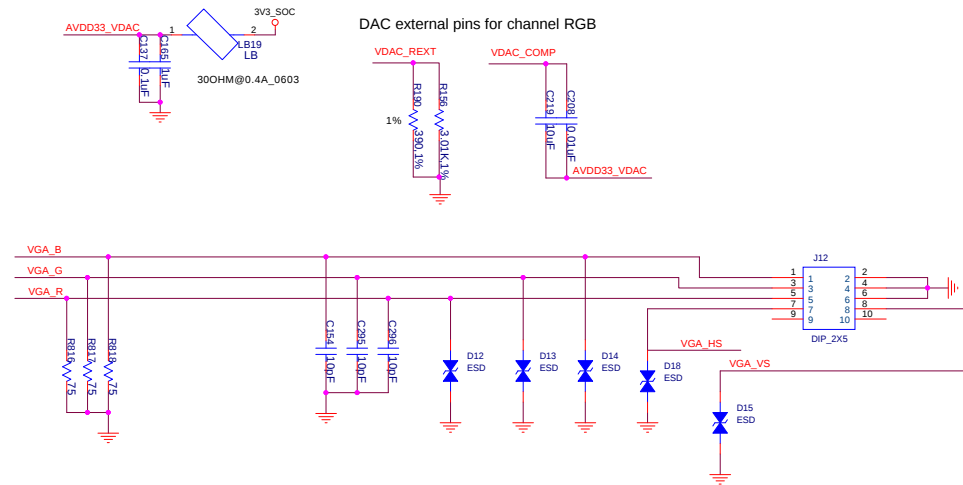
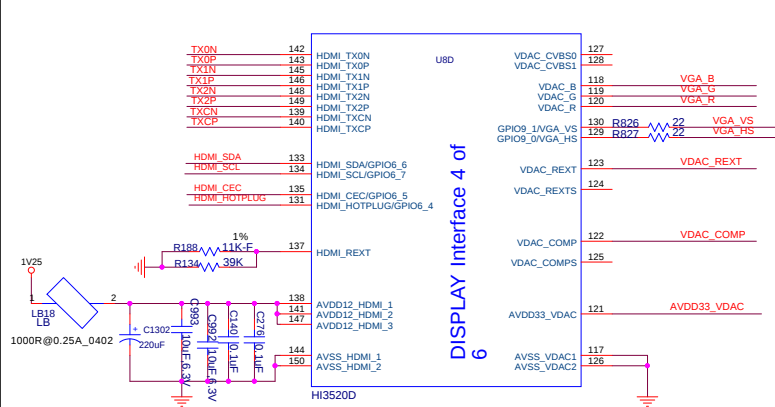
ETH



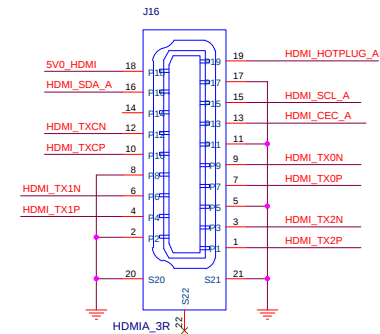
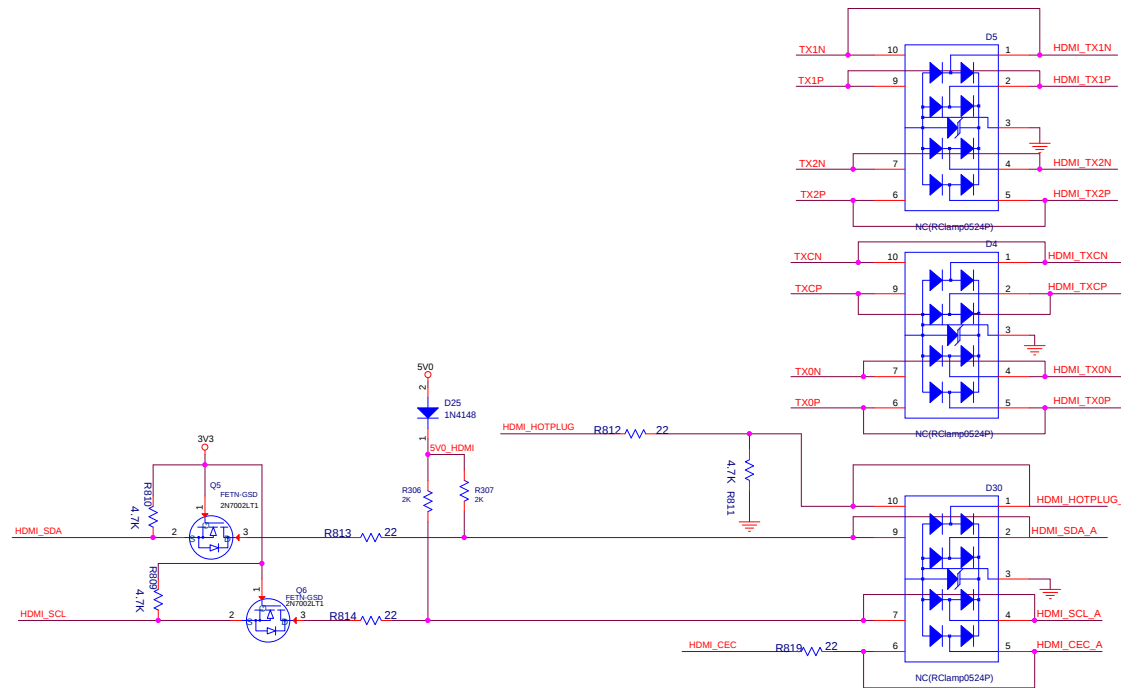
SATA0



DISPLAY



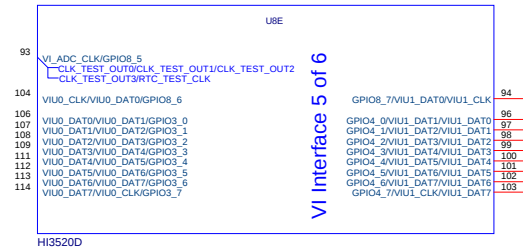
HDMI



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Baichuan Security Technology Co., Ltd

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- 1) 2xBT.656@108MHz for 8D1/CIF real-time inputs.
- 2) 1xBT.656@144MHz for 4x960H real-time inputs.
But only 4x960H(not 8x960H) real-time inputs can be Supported.You can choose VIU0 or VIU1.
- 3) 2xBT.656@148MHz for 2x720P real-time inputs.The Demo board does not have this funtion.
- 4) 2xBT.656 can combin into 1xBT.1120@74/148MHz for 1x720P/1080P real-time input.
The Demo board does not have this funtion.
- 5) VI_ADC_CLK can output 24MHz 27MHz and 54MHz.
- 6) You can change the VI_DATA order form DAT0 to DAT7,or from DAT7 to DAT0.

