

Appendix for NSA N77

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Product Name: 5G Wireless Data Terminal

Model: GLMU21A01

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Appendix A: Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Power Class	Verdict
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	24.43	26.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	24.41	26.29	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	24.07	25.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.43	26.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.52	26.4	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	24.03	25.91	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	24.05	25.93	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	24.61	26.49	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	24.65	26.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	24.07	25.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	24.00	25.88	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	24.68	26.56	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	24.39	26.27	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Outer_Full	24.00	25.88	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Inner_Full	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	24.22	26.1	PC3	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	24.36	26.24	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Outer_Full	24.53	26.41	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Inner_Full	24.47	26.35	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	24.42	26.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	24.38	26.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Inner_Full	24.98	26.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	24.58	26.46	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	24.43	26.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	24.94	26.82	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Inner_Full	24.05	25.93	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	24.49	26.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	24.48	26.36	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Outer_Full	24.49	26.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Inner_Full	24.57	26.45	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	24.54	26.42	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	24.65	26.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Outer_Full	24.57	26.45	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Inner_Full	24.46	26.34	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Left	24.96	26.84	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Edge_1RB_Right	24.58	26.46	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	24.22	26.1	PC3	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Inner_Full	24.23	26.11	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.92	26.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.55	26.43	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	24.18	26.06	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	24.22	26.1	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	24.09	25.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	24.87	26.75	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	24.09	25.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	24.13	26.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	24.53	26.41	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	24.02	25.9	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Outer_Full	24.24	26.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Inner_Full	24.34	26.22	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	24.69	26.57	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	24.71	26.59	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Outer_Full	24.78	26.66	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Inner_Full	24.58	26.46	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	24.93	26.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	24.45	26.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	24.29	26.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Inner_Full	24.16	26.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	24.90	26.78	PC3	PASS

DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	24.65	26.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	24.31	26.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Inner_Full	24.24	26.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	24.88	26.76	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	24.79	26.67	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Outer_Full	24.87	26.75	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Inner_Full	24.78	26.66	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	24.85	26.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	24.76	26.64	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Outer_Full	24.78	26.66	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Inner_Full	24.79	26.67	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Left	24.51	26.39	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Edge_1RB_Right	24.84	26.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	24.09	25.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Inner_Full	24.00	25.88	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.42	26.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	24.85	26.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	24.14	26.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Inner_Full	24.99	26.87	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	24.66	26.54	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	24.92	26.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	24.14	26.02	PC3	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	24.04	25.92	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	24.06	25.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	24.40	26.28	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Outer_Full	24.12	26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Inner_Full	24.11	25.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	24.31	26.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Outer_Full	24.66	26.54	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Inner_Full	24.50	26.38	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	24.48	26.36	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	24.55	26.43	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	24.13	26.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Inner_Full	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	24.64	26.52	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	24.84	26.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	24.06	25.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Inner_Full	24.20	26.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	24.70	26.58	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	24.86	26.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Outer_Full	24.65	26.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Inner_Full	24.64	26.52	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	24.58	26.46	PC3	PASS

DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	24.85	26.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Outer_Full	24.67	26.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Inner_Full	24.63	26.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Left	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Edge_1RB_Right	23.64	25.52	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Inner_Full	23.84	25.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.85	25.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	23.84	25.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	23.83	25.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	24.13	26.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	23.89	25.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Inner_Full	23.92	25.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Outer_Full	23.93	25.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Inner_Full	23.89	25.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	24.23	26.11	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	23.75	25.63	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Outer_Full	23.92	25.8	PC3	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Inner_Full	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	23.97	25.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	23.74	25.62	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	23.83	25.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Inner_Full	23.93	25.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	23.95	25.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	23.75	25.63	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	23.90	25.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Inner_Full	23.91	25.79	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	24.15	26.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	23.98	25.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Outer_Full	23.92	25.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Inner_Full	23.98	25.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	23.85	25.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	23.98	25.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Outer_Full	23.91	25.79	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Inner_Full	23.98	25.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Edge_1RB_Right	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	23.44	25.32	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Inner_Full	23.45	25.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.84	25.72	PC3	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.30	25.18	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	23.41	25.29	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	23.39	25.27	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.74	25.62	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.49	25.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	23.56	25.44	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	23.38	25.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	23.47	25.35	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Outer_Full	23.44	25.32	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Inner_Full	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	23.83	25.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	23.63	25.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Outer_Full	23.46	25.34	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Inner_Full	23.40	25.28	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	23.90	25.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	23.38	25.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Inner_Full	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	23.83	25.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	23.55	25.43	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	23.35	25.23	PC3	PASS

DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Inner_Full	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Edge_1RB_Left	24.05	25.93	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Edge_1RB_Right	23.43	25.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Outer_Full	23.44	25.32	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Inner_Full	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Edge_1RB_Left	23.97	25.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Edge_1RB_Right	23.54	25.42	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Outer_Full	23.45	25.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Inner_Full	23.37	25.25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Left	23.57	25.45	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Edge_1RB_Right	23.34	25.22	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Inner_Full	23.08	24.96	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.50	25.38	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.61	25.49	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	23.22	25.1	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	23.68	25.56	PC3	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	23.44	25.32	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Outer_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Inner_Full	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	23.69	25.57	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	23.52	25.4	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Outer_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Inner_Full	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	23.74	25.62	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	23.38	25.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Inner_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	23.40	25.28	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	22.98	24.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	23.17	25.05	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Inner_Full	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	23.59	25.47	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	23.37	25.25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Outer_Full	23.23	25.11	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Inner_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	23.76	25.64	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	23.38	25.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Outer_Full	23.22	25.1	PC3	PASS

DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Inner_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Left	23.85	25.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Edge_1RB_Right	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	23.74	25.62	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Inner_Full	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.88	25.76	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	23.75	25.63	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	23.87	25.75	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.81	25.69	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	23.77	25.65	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Edge_1RB_Right	23.45	25.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Outer_Full	23.79	25.67	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Inner_Full	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	24.36	26.24	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Edge_1RB_Right	23.51	25.39	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Outer_Full	23.43	25.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Inner_Full	23.52	25.4	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	24.00	25.88	PC3	PASS

DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	23.79	25.67	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Inner_Full	23.84	25.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	23.99	25.87	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	23.77	25.65	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Inner_Full	23.80	25.68	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Edge_1RB_Left	24.14	26.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Edge_1RB_Right	23.49	25.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Outer_Full	23.76	25.64	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Inner_Full	23.86	25.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Edge_1RB_Left	24.06	25.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Edge_1RB_Right	23.50	25.38	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Outer_Full	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Inner_Full	23.87	25.75	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.40	24.28	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Inner_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.51	24.39	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	23.09	24.97	PC3	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.45	24.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	23.14	25.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	22.42	24.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Outer_Full	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Inner_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	23.45	25.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Edge_1RB_Right	22.63	24.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Outer_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Inner_Full	23.17	25.05	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	22.42	24.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	23.06	24.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Inner_Full	23.17	25.05	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	23.31	25.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	22.43	24.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Inner_Full	23.18	25.06	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Edge_1RB_Left	23.57	25.45	PC3	PASS

DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Edge_1RB_Right	22.64	24.52	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Outer_Full	23.08	24.96	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Inner_Full	23.16	25.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Edge_1RB_Left	23.38	25.26	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Edge_1RB_Right	22.63	24.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Outer_Full	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Inner_Full	23.16	25.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Left	23.34	25.22	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Edge_1RB_Right	23.05	24.93	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Inner_Full	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	23.34	25.22	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.03	24.91	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	23.51	25.39	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Edge_1RB_Right	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Outer_Full	23.10	24.98	PC3	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Inner_Full	23.16	25.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	23.44	25.32	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	23.35	25.23	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Outer_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Inner_Full	23.28	25.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	23.49	25.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Inner_Full	23.31	25.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	23.53	25.41	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	23.18	25.06	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Inner_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Edge_1RB_Left	23.31	25.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Edge_1RB_Right	23.58	25.46	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Outer_Full	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Inner_Full	23.30	25.18	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Edge_1RB_Left	23.50	25.38	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Edge_1RB_Right	23.28	25.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Outer_Full	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Inner_Full	23.31	25.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Left	23.59	25.47	PC3	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.85	24.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	23.63	25.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Inner_Full	23.73	25.61	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.63	25.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.92	24.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	23.61	25.49	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	23.65	25.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	23.73	25.61	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.84	24.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	23.61	25.49	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	23.82	25.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Outer_Full	23.69	25.57	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Inner_Full	23.68	25.56	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	23.72	25.6	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	23.06	24.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Outer_Full	23.53	25.41	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Inner_Full	23.59	25.47	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	23.66	25.54	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	22.90	24.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	23.56	25.44	PC3	PASS

DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Inner_Full	23.63	25.51	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Edge_1RB_Left	23.65	25.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Edge_1RB_Right	23.02	24.9	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Outer_Full	23.72	25.6	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Inner_Full	23.72	25.6	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Edge_1RB_Left	23.91	25.79	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Edge_1RB_Right	22.89	24.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Outer_Full	23.60	25.48	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Inner_Full	23.66	25.54	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Edge_1RB_Left	23.95	25.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Edge_1RB_Right	23.33	25.21	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Outer_Full	23.68	25.56	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Inner_Full	23.69	25.57	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.32	24.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Inner_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.31	24.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.90	24.78	PC3	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.28	24.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	23.36	25.24	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	22.42	24.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Outer_Full	23.01	24.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Inner_Full	23.13	25.01	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	23.33	25.21	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	22.49	24.37	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Outer_Full	23.04	24.92	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Inner_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	22.45	24.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Outer_Full	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Inner_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	22.28	24.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Outer_Full	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Inner_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Edge_1RB_Left	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Edge_1RB_Right	22.52	24.4	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Outer_Full	23.12	25	PC3	PASS

DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Inner_Full	23.22	25.1	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Edge_1RB_Left	23.39	25.27	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Edge_1RB_Right	22.42	24.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Outer_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Inner_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.45	24.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.71	24.59	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	22.83	24.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Inner_Full	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.50	24.38	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.71	24.59	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	22.83	24.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	22.99	24.87	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.78	24.66	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.92	24.8	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	22.89	24.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	22.95	24.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	22.62	24.5	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	22.72	24.6	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Outer_Full	22.90	24.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Inner_Full	23.05	24.93	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	22.66	24.54	PC3	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	22.82	24.7	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Outer_Full	22.81	24.69	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Inner_Full	23.01	24.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	22.66	24.54	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	22.65	24.53	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	22.84	24.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Inner_Full	22.97	24.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Edge_1RB_Left	22.28	24.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Edge_1RB_Right	22.34	24.22	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Outer_Full	22.86	24.74	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Inner_Full	23.01	24.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Edge_1RB_Left	22.58	24.46	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Edge_1RB_Right	22.69	24.57	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Outer_Full	22.79	24.67	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Inner_Full	22.97	24.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Edge_1RB_Left	22.59	24.47	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Edge_1RB_Right	22.72	24.6	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Outer_Full	22.85	24.73	PC3	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Inner_Full	22.97	24.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Left	23.93	25.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Edge_1RB_Right	23.45	25.33	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	23.93	25.81	PC3	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Inner_Full	23.94	25.82	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.96	25.84	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.46	25.34	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	23.93	25.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	23.90	25.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.93	25.81	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.32	25.2	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	23.95	25.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	24.01	25.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	24.14	26.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Outer_Full	23.87	25.75	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Inner_Full	23.99	25.87	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	24.14	26.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Outer_Full	23.90	25.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Inner_Full	23.60	25.48	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	23.89	25.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	23.51	25.39	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	23.95	25.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Inner_Full	23.95	25.83	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	23.98	25.86	PC3	PASS

DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	23.47	25.35	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	23.96	25.84	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Inner_Full	23.97	25.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Edge_1RB_Left	24.24	26.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Edge_1RB_Right	23.62	25.5	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Outer_Full	23.97	25.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Inner_Full	24.00	25.88	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Edge_1RB_Left	24.15	26.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Edge_1RB_Right	23.77	25.65	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Outer_Full	23.97	25.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Inner_Full	23.98	25.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Left	23.48	25.36	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Edge_1RB_Right	22.83	24.71	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Inner_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.42	25.3	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.91	24.79	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.55	25.43	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.80	24.68	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	23.24	25.12	PC3	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	23.23	25.11	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	23.52	25.4	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Edge_1RB_Right	23.00	24.88	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Outer_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Inner_Full	23.04	24.92	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Edge_1RB_Right	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Outer_Full	23.18	25.06	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Inner_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	23.41	25.29	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	22.84	24.72	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	23.30	25.18	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Inner_Full	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	23.43	25.31	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	22.90	24.78	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	23.23	25.11	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Inner_Full	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Edge_1RB_Left	23.67	25.55	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Edge_1RB_Right	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Outer_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Inner_Full	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Edge_1RB_Left	23.77	25.65	PC3	PASS

DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Edge_1RB_Right	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Outer_Full	23.28	25.16	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Inner_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Left	22.98	24.86	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Edge_1RB_Right	22.97	24.85	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Inner_Full	23.16	25.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.03	24.91	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.09	24.97	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	23.14	25.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.30	25.18	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	23.31	25.19	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	23.14	25.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	23.10	24.98	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	23.07	24.95	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Outer_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Inner_Full	23.19	25.07	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	23.29	25.17	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Outer_Full	22.99	24.87	PC3	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Inner_Full	23.11	24.99	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	23.17	25.05	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	23.01	24.89	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	23.14	25.02	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Inner_Full	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	22.89	24.77	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	22.71	24.59	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	23.21	25.09	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Inner_Full	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	23.12	25	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	23.06	24.94	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Outer_Full	23.24	25.12	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Inner_Full	23.20	25.08	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	23.16	25.04	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	23.15	25.03	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Outer_Full	23.17	25.05	PC3	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Inner_Full	23.20	25.08	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n 77A-3700- 3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	4.51	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-QPSK	M+L	Outer_Full	5.55	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6.38	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-64QAM	M+L	Outer_Full	6.54	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-256QAM	M+L	Outer_Full	6.95	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	CP-QPSK	M+L	Outer_Full	7.43	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	CP-16QAM	M+L	Outer_Full	7.44	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	CP-64QAM	M+L	Outer_Full	7.43	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	CP-256QAM	M+L	Outer_Full	7.43	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.74	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+20	DFT-QPSK	M+M	Outer_Full	5.52	≤13	PASS
DC_2A_n	30	5+20	DFT-16QAM	M+M	Outer_Full	6.44	≤13	PASS

77A-3700-3980								
DC_2A_n 77A-3700-3980	30	5+20	DFT-64QAM	M+M	Outer_Full	7.12	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-256QAM	M+M	Outer_Full	6.81	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	7.35	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	7.32	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-64QAM	M+M	Outer_Full	8.08	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-256QAM	M+M	Outer_Full	8.07	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	4.50	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	5.57	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6.44	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-64QAM	M+H	Outer_Full	6.47	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	DFT-256QAM	M+H	Outer_Full	6.83	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	7.35	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	7.30	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+20	CP-64QAM	M+H	Outer_Full	7.35	≤13	PASS

DC_2A_n 77A-3700- 3980	30	5+20	CP-256QAM	M+H	Outer_Full	7.37	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	4.53	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-QPSK	M+L	Outer_Full	5.59	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6.42	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-64QAM	M+L	Outer_Full	6.57	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-256QAM	M+L	Outer_Full	6.98	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-QPSK	M+L	Outer_Full	7.48	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-16QAM	M+L	Outer_Full	7.67	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-64QAM	M+L	Outer_Full	7.65	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-256QAM	M+L	Outer_Full	7.67	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	4.92	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-QPSK	M+M	Outer_Full	5.46	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6.44	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-64QAM	M+M	Outer_Full	7.12	≤13	PASS
DC_2A_n 77A-3700-	30	5+40	DFT-256QAM	M+M	Outer_Full	7.05	≤13	PASS

3980								
DC_2A_n 77A-3700- 3980	30	5+40	CP-QPSK	M+M	Outer_Full	7.39	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-16QAM	M+M	Outer_Full	7.55	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-64QAM	M+M	Outer_Full	8.23	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-256QAM	M+M	Outer_Full	8.22	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	4.74	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-QPSK	M+H	Outer_Full	5.06	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-16QAM	M+H	Outer_Full	5.98	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-64QAM	M+H	Outer_Full	6.61	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	DFT-256QAM	M+H	Outer_Full	6.78	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-QPSK	M+H	Outer_Full	7.52	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-16QAM	M+H	Outer_Full	7.71	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-64QAM	M+H	Outer_Full	7.70	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+40	CP-256QAM	M+H	Outer_Full	7.67	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	4.63	≤13	PASS
DC_2A_n	30	5+80	DFT-QPSK	M+L	Outer_Full	5.52	≤13	PASS

77A-3700-3980								
DC_2A_n 77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6.25	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-64QAM	M+L	Outer_Full	6.88	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-256QAM	M+L	Outer_Full	7.09	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	7.20	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	7.30	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-64QAM	M+L	Outer_Full	8.09	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-256QAM	M+L	Outer_Full	8.13	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	4.80	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	5.73	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6.55	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-64QAM	M+M	Outer_Full	6.67	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	DFT-256QAM	M+M	Outer_Full	7.10	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	7.63	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	7.63	≤13	PASS

DC_2A_n 77A-3700- 3980	30	5+80	CP-64QAM	M+M	Outer_Full	7.84	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	CP-256QAM	M+M	Outer_Full	7.80	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	4.56	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-QPSK	M+H	Outer_Full	5.55	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6.32	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-64QAM	M+H	Outer_Full	6.48	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	DFT-256QAM	M+H	Outer_Full	6.99	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	CP-QPSK	M+H	Outer_Full	7.35	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	CP-16QAM	M+H	Outer_Full	7.37	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	CP-64QAM	M+H	Outer_Full	7.48	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+80	CP-256QAM	M+H	Outer_Full	7.47	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-16QAM	M+L	Outer_Full	6.25	≤13	PASS
DC_2A_n 77A-3700-	30	5+100	DFT-64QAM	M+L	Outer_Full	6.79	≤13	PASS

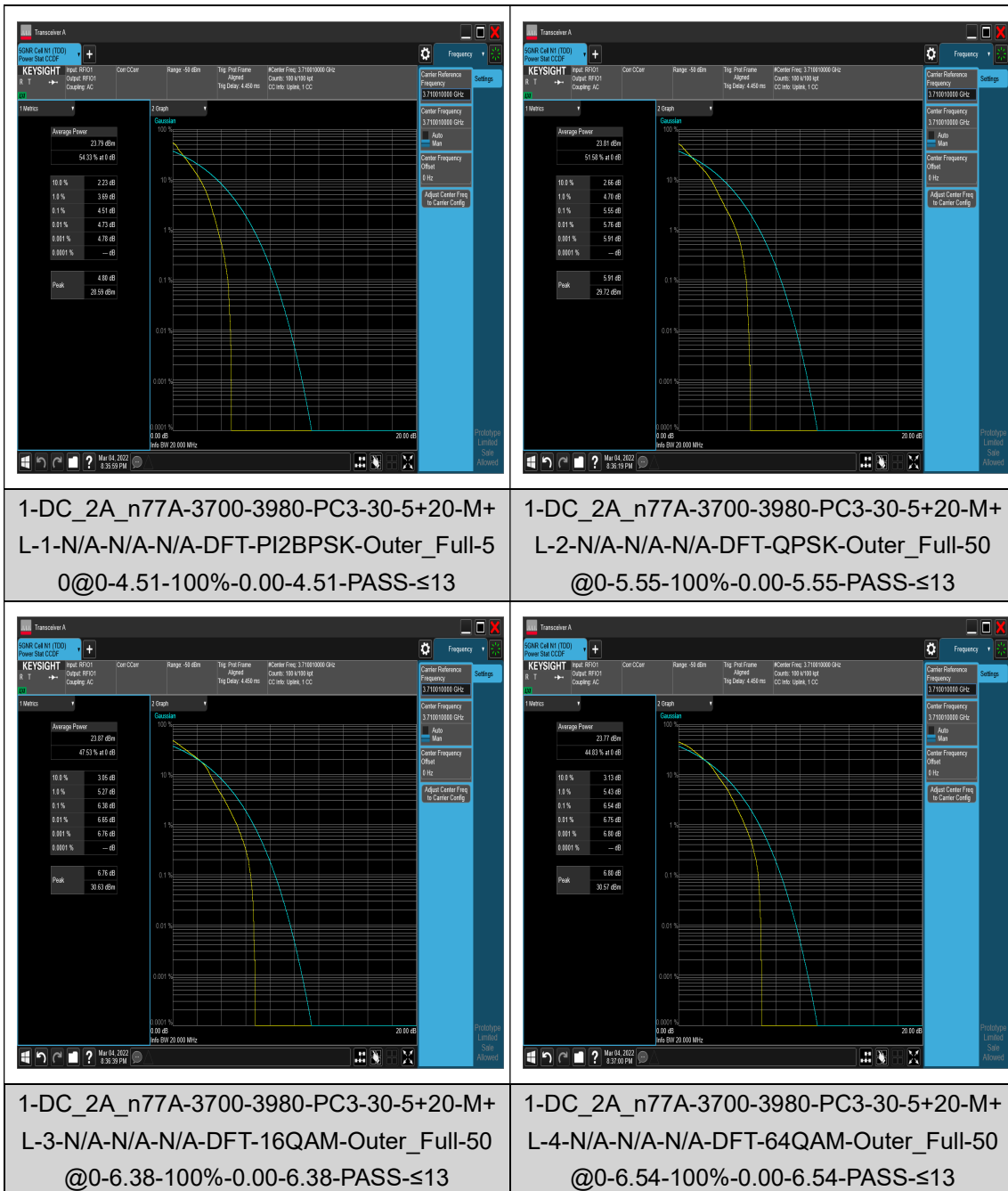
3980								
DC_2A_n 77A-3700- 3980	30	5+100	DFT-256QAM	M+L	Outer_Full	6.96	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-QPSK	M+L	Outer_Full	7.21	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-16QAM	M+L	Outer_Full	7.13	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-64QAM	M+L	Outer_Full	8.00	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-256QAM	M+L	Outer_Full	7.99	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	4.79	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-QPSK	M+M	Outer_Full	5.71	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6.58	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-64QAM	M+M	Outer_Full	6.65	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	DFT-256QAM	M+M	Outer_Full	7.02	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-QPSK	M+M	Outer_Full	7.67	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-16QAM	M+M	Outer_Full	7.59	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-64QAM	M+M	Outer_Full	7.72	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+100	CP-256QAM	M+M	Outer_Full	7.81	≤13	PASS
DC_2A_n	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	4.57	≤13	PASS

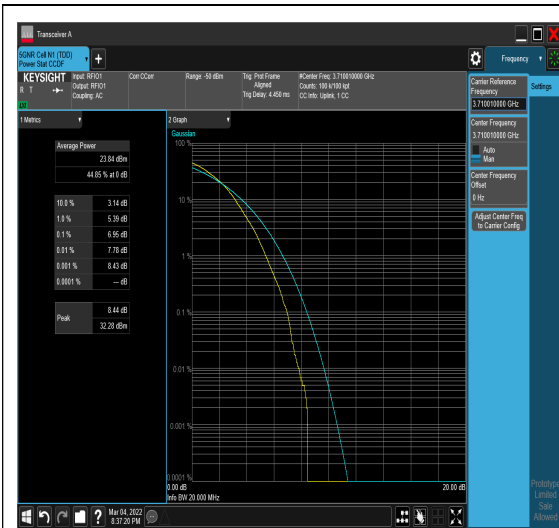
77A-3700-3980								
DC_2A_n 77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	5.48	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	6.35	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	DFT-64QAM	M+H	Outer_Full	6.78	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	DFT-256QAM	M+H	Outer_Full	7.00	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	7.39	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	CP-16QAM	M+H	Outer_Full	7.39	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	CP-64QAM	M+H	Outer_Full	8.19	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+100	CP-256QAM	M+H	Outer_Full	8.24	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	4.64	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6.18	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	DFT-64QAM	M+L	Outer_Full	6.85	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	DFT-256QAM	M+L	Outer_Full	7.01	≤13	PASS
DC_2A_n 77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	7.18	≤13	PASS

DC_2A_n 77A-3700- 3980	30	5+60	CP-16QAM	M+L	Outer_Full	7.19	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-64QAM	M+L	Outer_Full	8.04	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-256QAM	M+L	Outer_Full	8.04	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	4.62	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-QPSK	M+M	Outer_Full	5.59	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6.40	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-64QAM	M+M	Outer_Full	6.60	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-256QAM	M+M	Outer_Full	7.09	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-QPSK	M+M	Outer_Full	7.57	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-16QAM	M+M	Outer_Full	7.54	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-64QAM	M+M	Outer_Full	7.65	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-256QAM	M+M	Outer_Full	7.66	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	4.57	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-QPSK	M+H	Outer_Full	4.96	≤13	PASS
DC_2A_n 77A-3700-	30	5+60	DFT-16QAM	M+H	Outer_Full	5.94	≤13	PASS

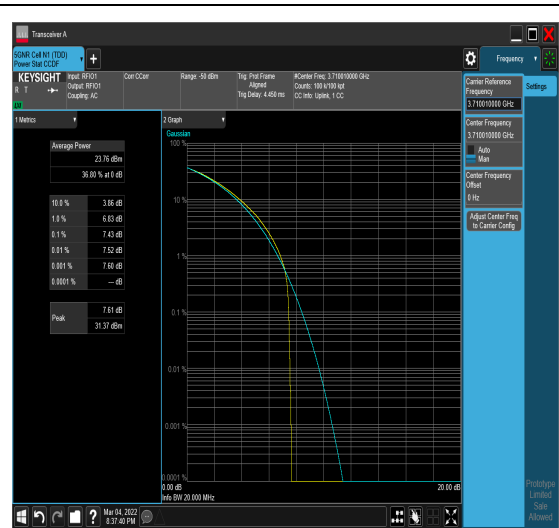
3980								
DC_2A_n 77A-3700- 3980	30	5+60	DFT-64QAM	M+H	Outer_Full	6.55	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	DFT-256QAM	M+H	Outer_Full	6.88	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-QPSK	M+H	Outer_Full	7.55	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-16QAM	M+H	Outer_Full	7.57	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-64QAM	M+H	Outer_Full	7.63	≤13	PASS
DC_2A_n 77A-3700- 3980	30	5+60	CP-256QAM	M+H	Outer_Full	7.64	≤13	PASS

Test Graphs

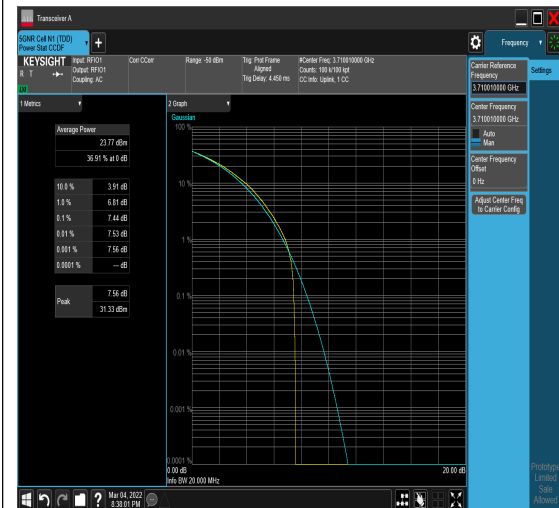




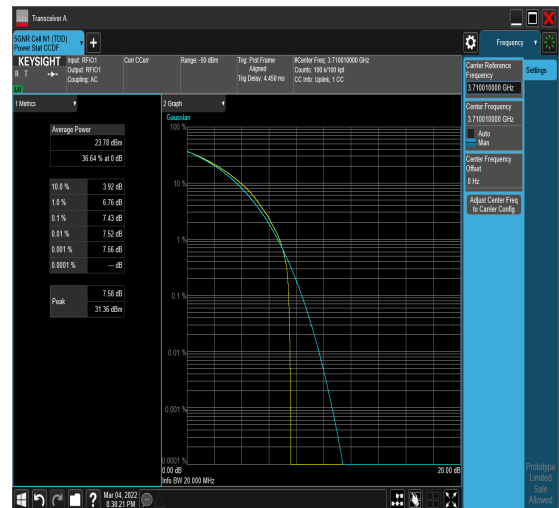
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-5-N/A-N/A-N/A-DFT-256QAM-Outter_Full-5
@0-6.95-100%-0.00-6.95-PASS-≤13



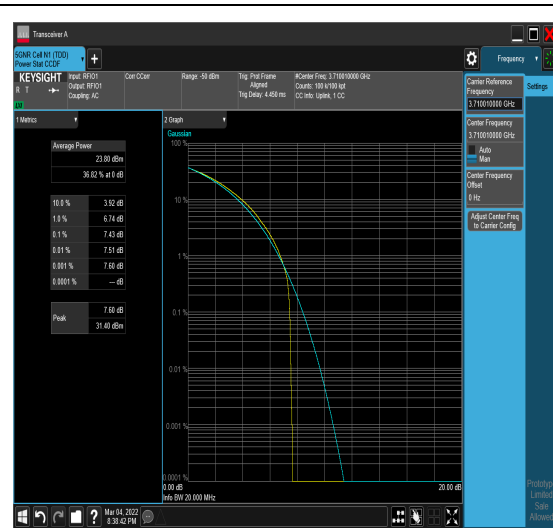
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-6-N/A-N/A-N/A-CP-QPSK-Outter_Full-51@
0-7.43-100%-0.00-7.43-PASS-≤13



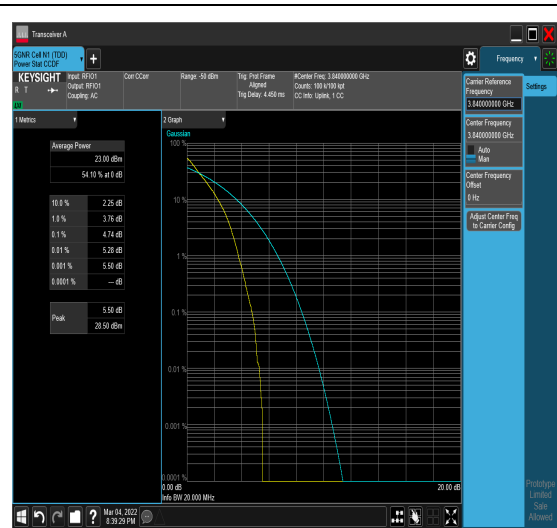
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-7-N/A-N/A-N/A-CP-16QAM-Outter_Full-51
@0-7.44-100%-0.00-7.44-PASS-≤13



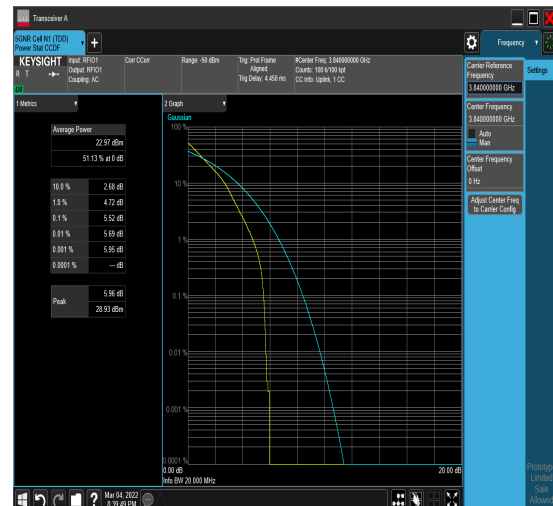
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-8-N/A-N/A-N/A-CP-64QAM-Outter_Full-51
@0-7.43-100%-0.00-7.43-PASS-≤13



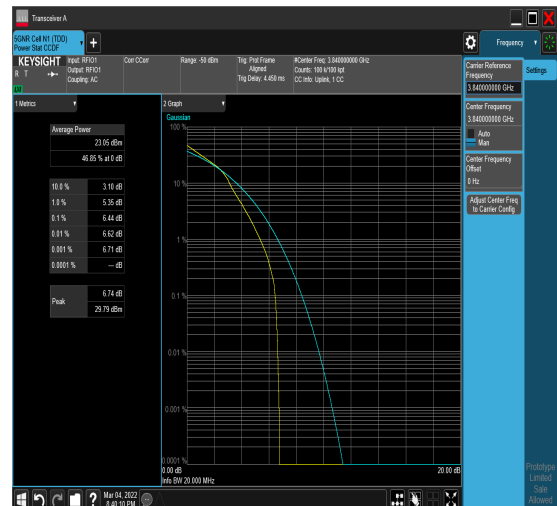
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-7.43-100%-0.00-7.43-PASS-≤13



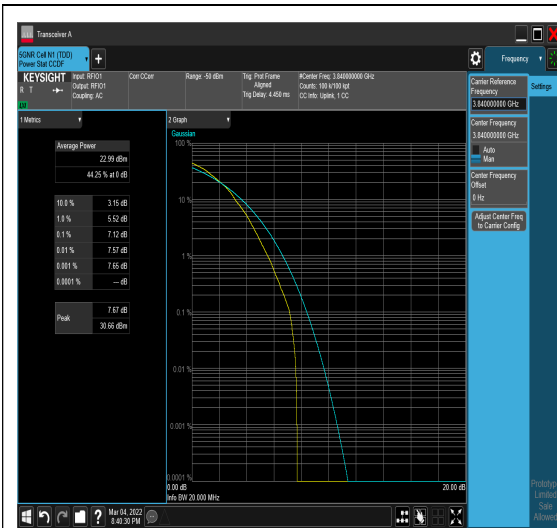
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
50@0-4.74-100%-0.00-4.74-PASS-≤13



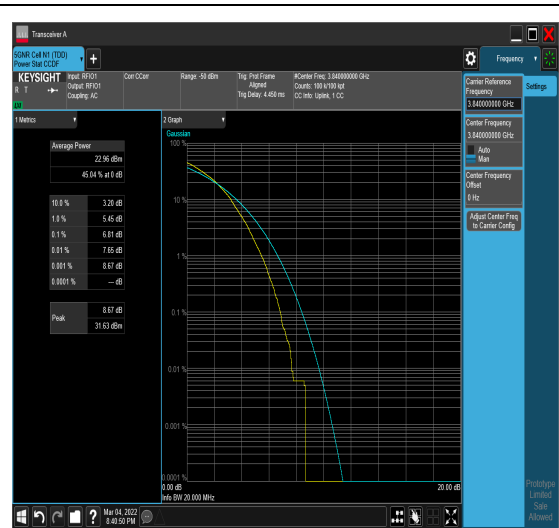
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
@0-5.52-100%-0.00-5.52-PASS-≤13



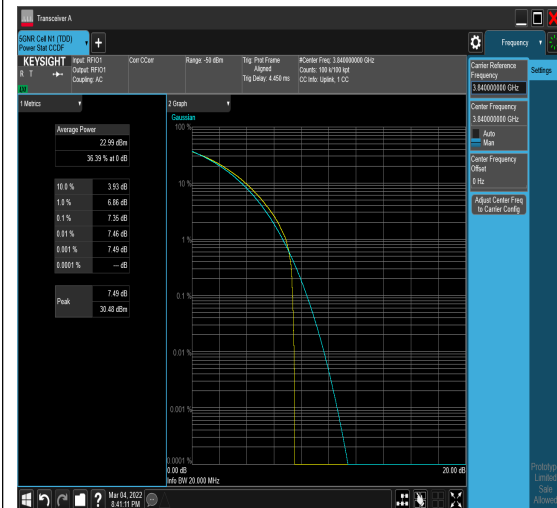
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
@0-6.44-100%-0.00-6.44-PASS-≤13



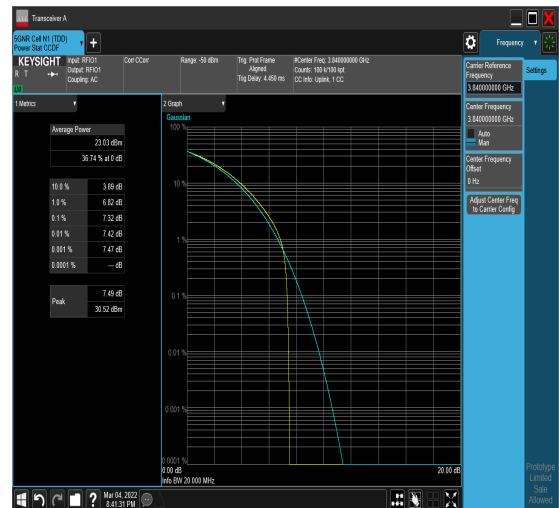
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
@0-7.12-100%-0.00-7.12-PASS-≤13



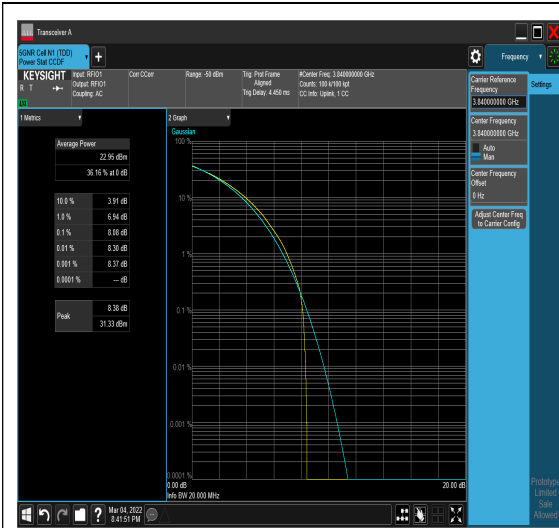
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-5
0@0-6.81-100%-0.00-6.81-PASS-≤13



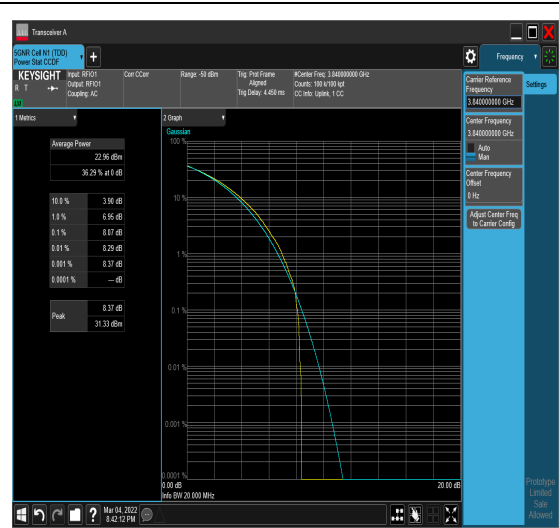
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
0-7.35-100%-0.00-7.35-PASS-≤13



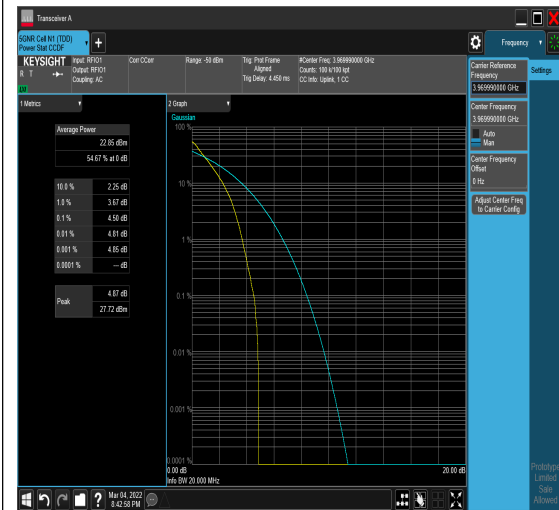
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-7.32-100%-0.00-7.32-PASS-≤13



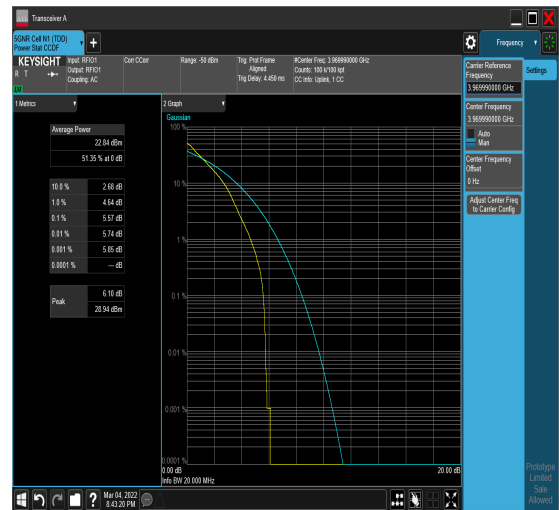
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-8.08-100%-0.00-8.08-PASS≤13



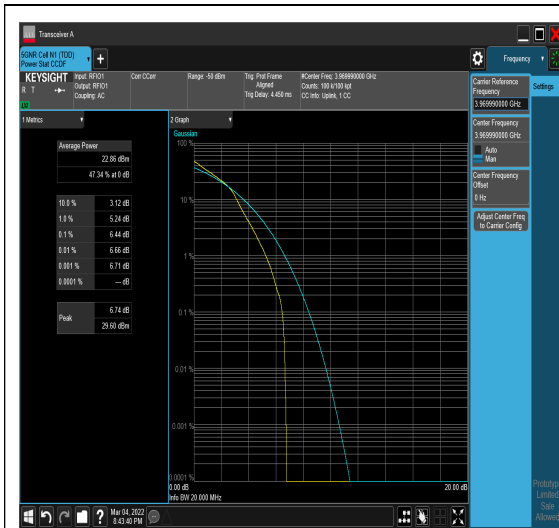
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-8.07-100%-0.00-8.07-PASS≤13



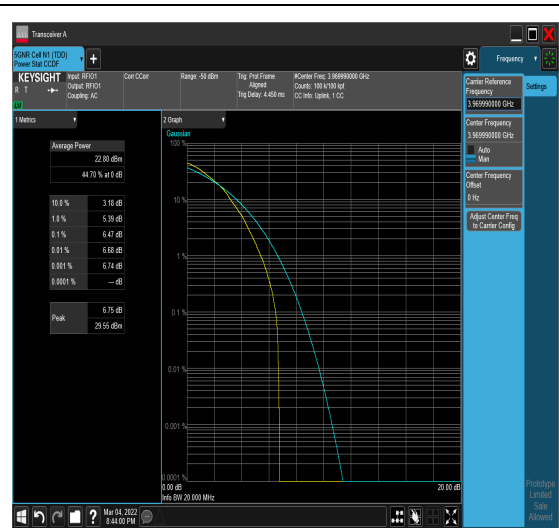
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
50@0-4.50-100%-0.00-4.50-PASS≤13



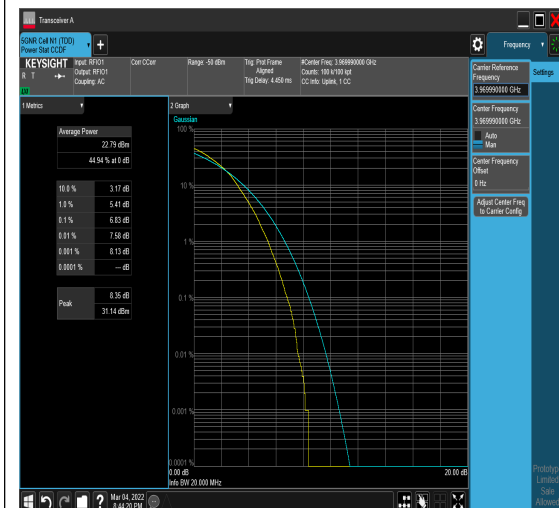
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H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
@0-5.57-100%-0.00-5.57-PASS≤13



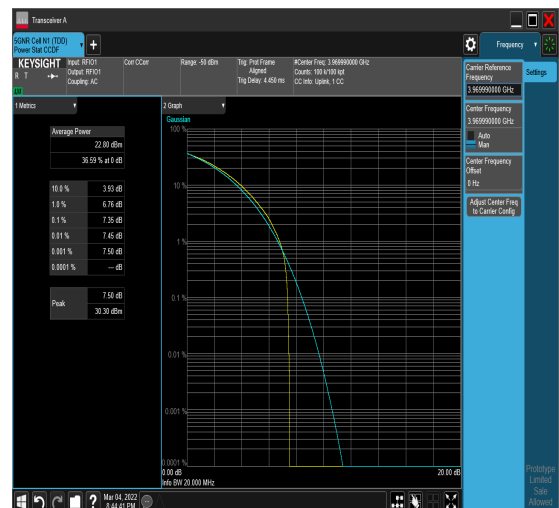
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
@0-6.44-100%-0.00-6.44-PASS-≤13



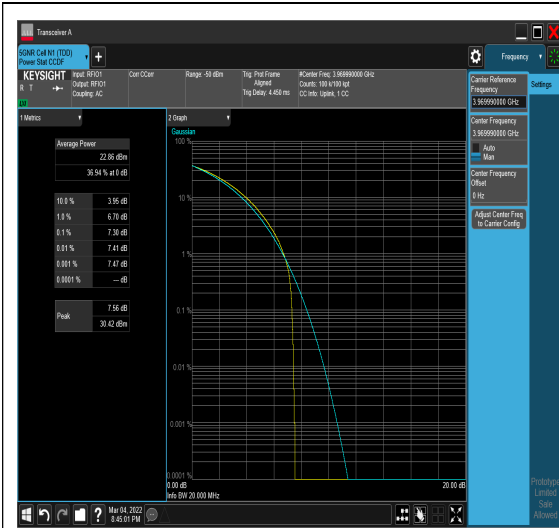
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
@0-6.47-100%-0.00-6.47-PASS-≤13



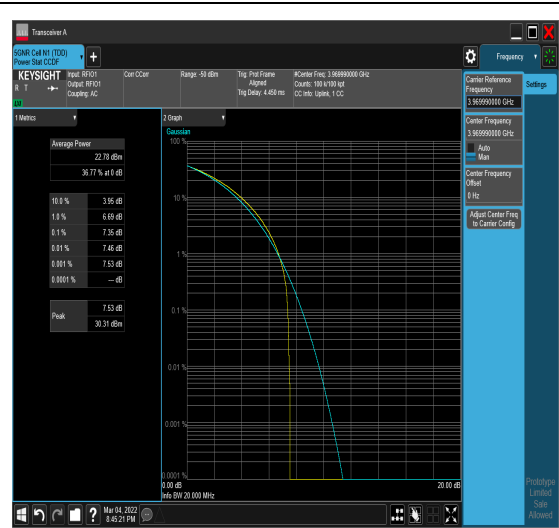
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-5
0@0-6.83-100%-0.00-6.83-PASS-≤13



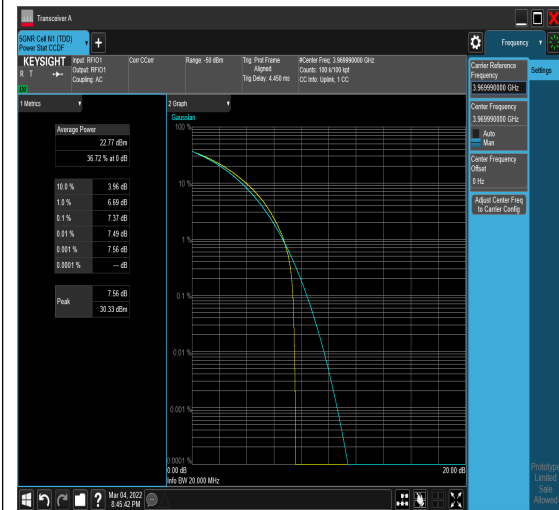
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
0-7.35-100%-0.00-7.35-PASS-≤13



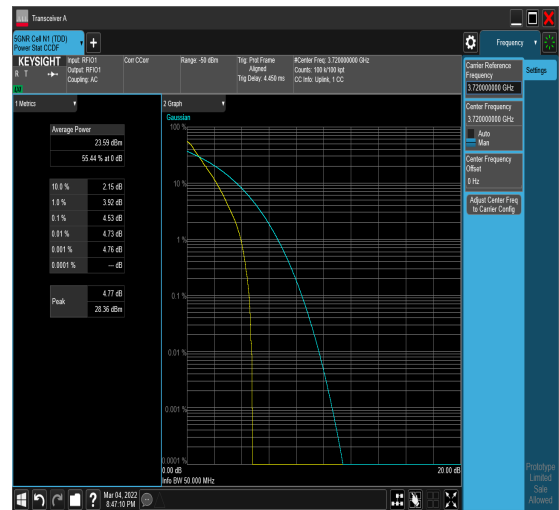
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-7.30-100%-0.00-7.30-PASS≤13



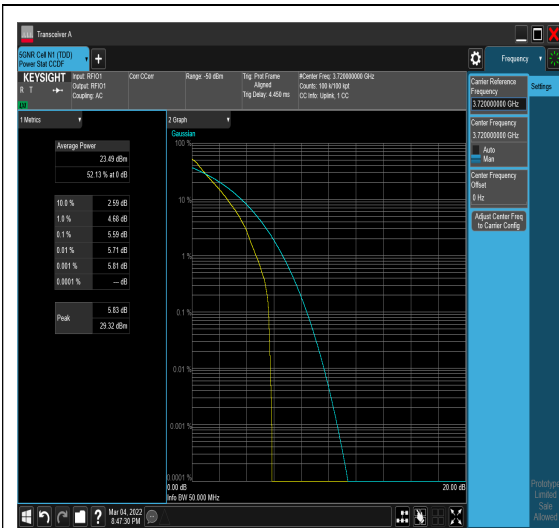
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-7.35-100%-0.00-7.35-PASS≤13



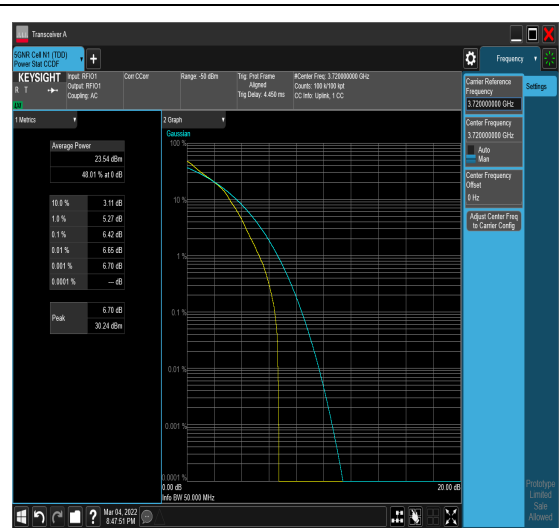
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-7.37-100%-0.00-7.37-PASS≤13



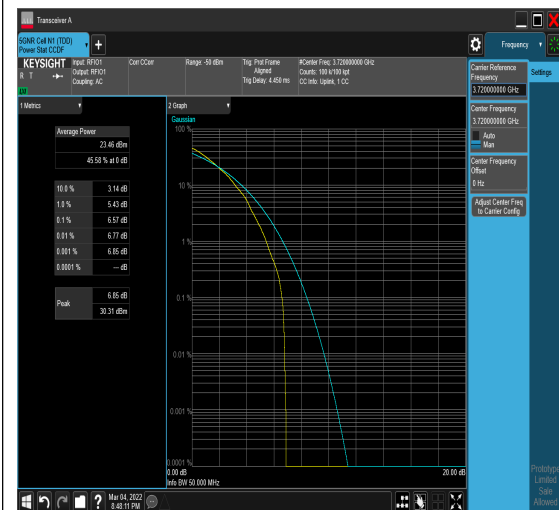
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-1
00@0-4.53-100%-0.00-4.53-PASS≤13



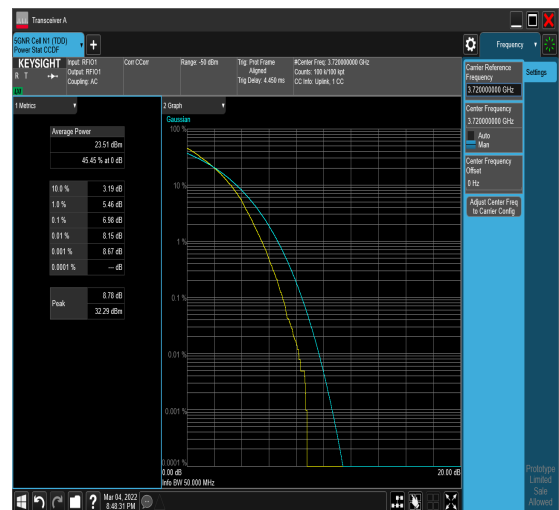
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-2-N/A-N/A-N/A-DFT-QPSK-Outter_Full-100
@0.5.59-100%-0.00-5.59-PASS-≤13



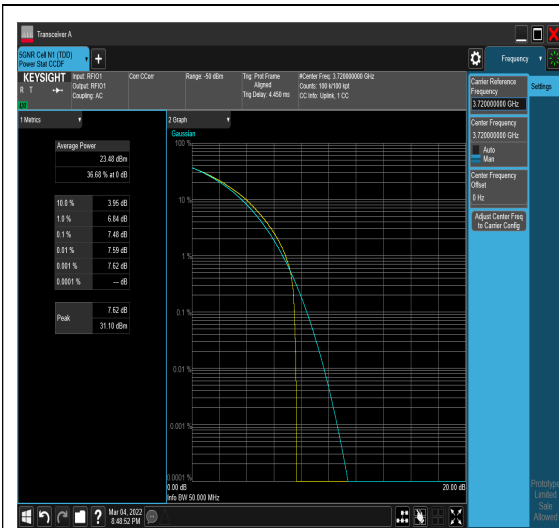
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-3-N/A-N/A-N/A-DFT-16QAM-Outter_Full-10
0@0.6.42-100%-0.00-6.42-PASS-≤13



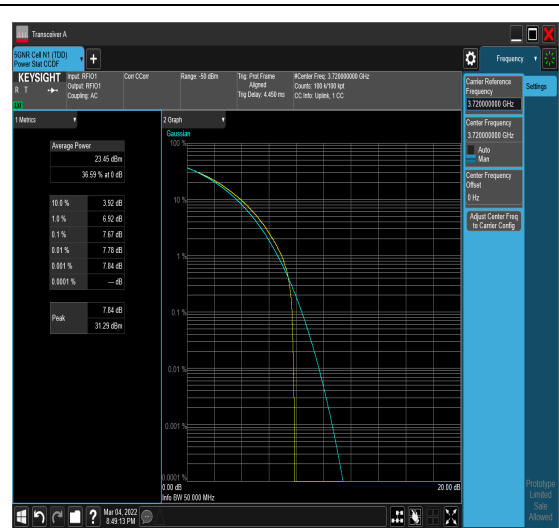
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L-4-N/A-N/A-N/A-DFT-64QAM-Outter_Full-10
0@0.6.57-100%-0.00-6.57-PASS-≤13



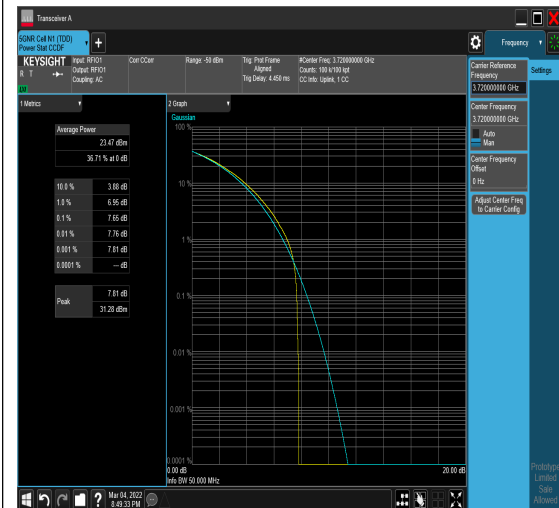
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L-5-N/A-N/A-N/A-DFT-256QAM-Outter_Full-1
00@0.6.98-100%-0.00-6.98-PASS-≤13



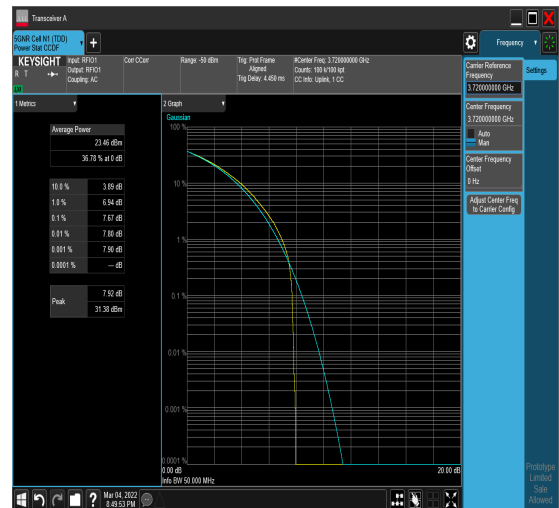
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L-6-N/A-N/A-N/A-CP-QPSK-Outet_Full-106
@0-7.48-100%-0.00-7.48-PASS≤13



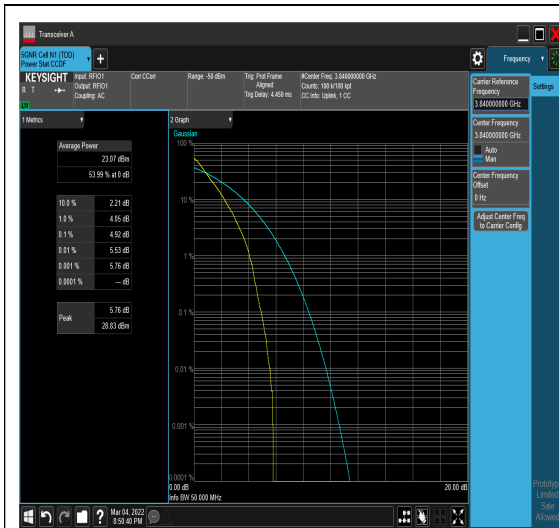
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-7-N/A-N/A-N/A-CP-16QAM-Outet_Full-106
@0-7.67-100%-0.00-7.67-PASS≤13



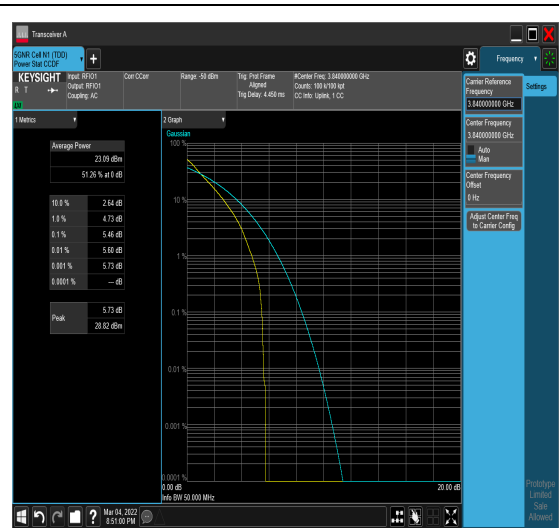
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L-8-N/A-N/A-N/A-CP-64QAM-Outet_Full-106
@0-7.65-100%-0.00-7.65-PASS≤13



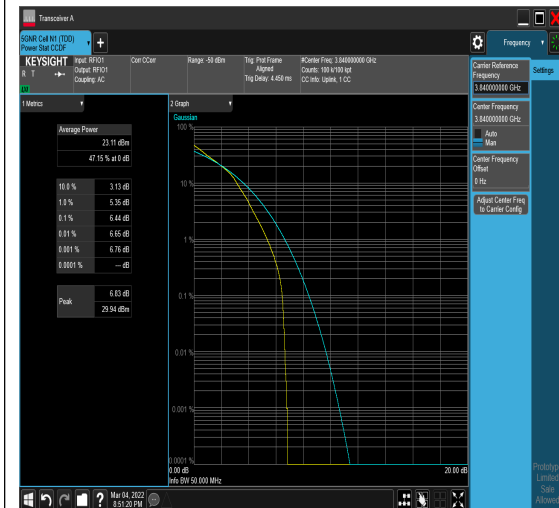
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L-9-N/A-N/A-N/A-CP-256QAM-Outet_Full-10
6@0-7.67-100%-0.00-7.67-PASS≤13



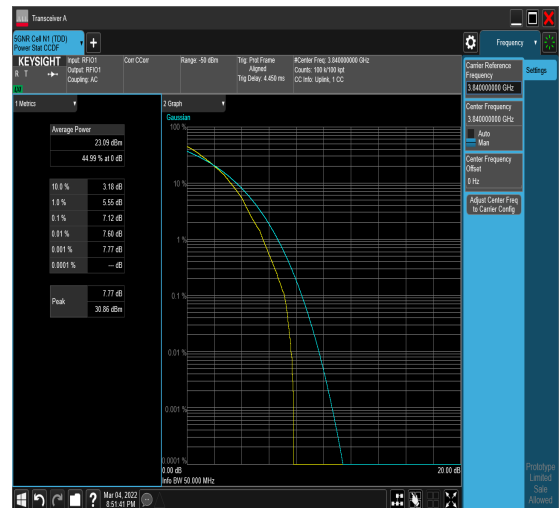
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outter_Full-
100@0-4.92-100%-0.00-4.92-PASS-≤13



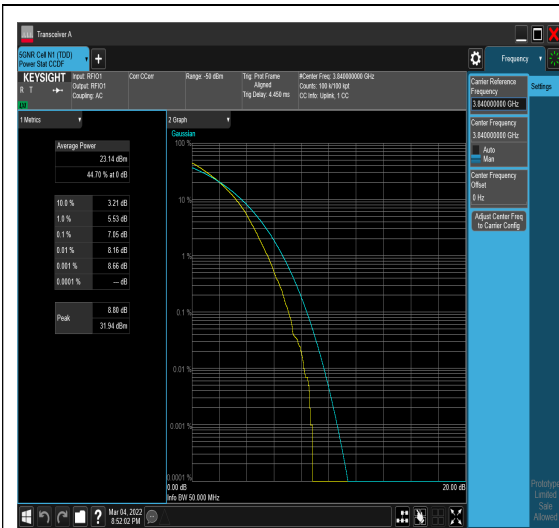
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-2-N/A-N/A-N/A-DFT-QPSK-Outter_Full-100
@0-5.46-100%-0.00-5.46-PASS-≤13



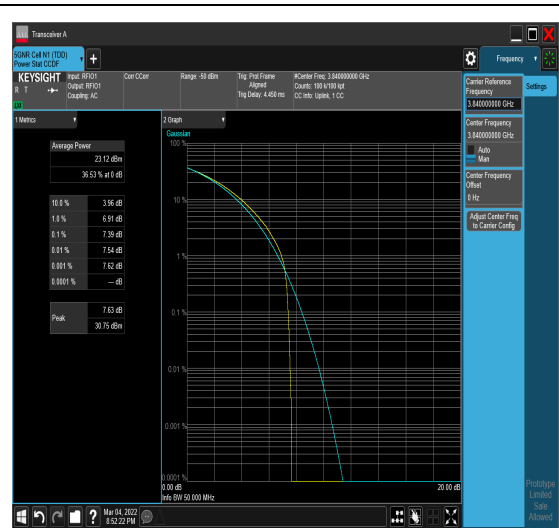
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-3-N/A-N/A-N/A-DFT-16QAM-Outter_Full-10
0@0-6.44-100%-0.00-6.44-PASS-≤13



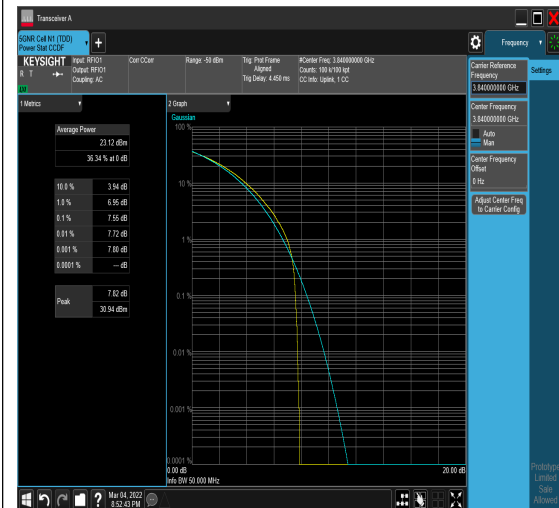
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-4-N/A-N/A-N/A-DFT-64QAM-Outter_Full-10
0@0-7.12-100%-0.00-7.12-PASS-≤13



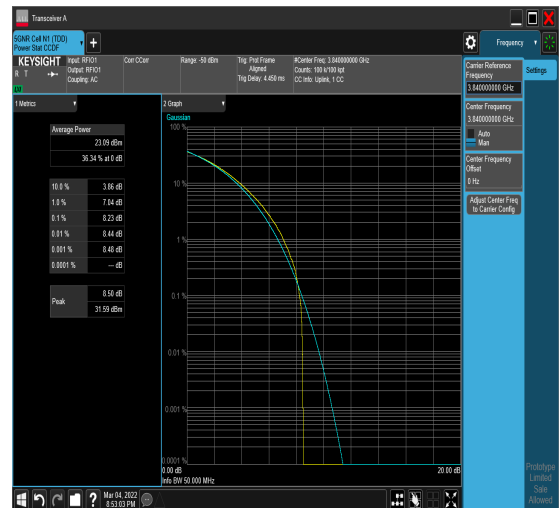
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-5-N/A-N/A-N/A-DFT-256QAM-Outet_Full-1
00@0-7.05-100%-0.00-7.05-PASS-≤13



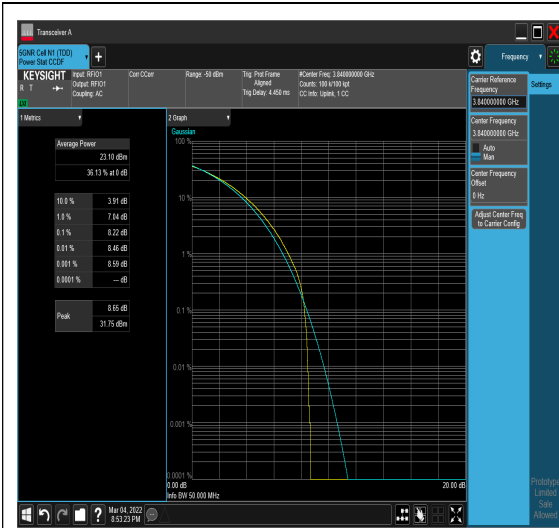
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-6-N/A-N/A-N/A-CP-QPSK-Outet_Full-106
@0-7.39-100%-0.00-7.39-PASS-≤13



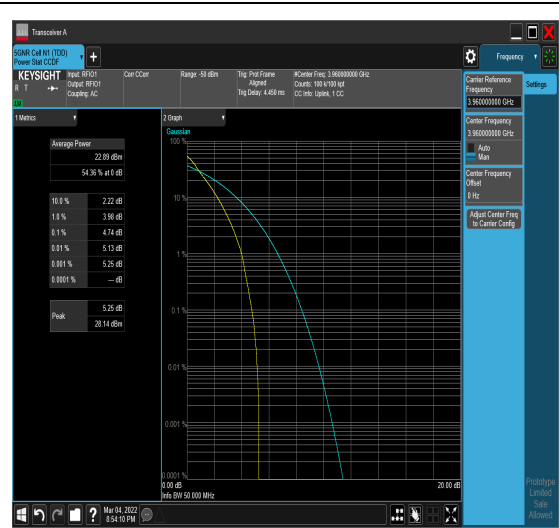
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-7-N/A-N/A-N/A-CP-16QAM-Outet_Full-106
@0-7.55-100%-0.00-7.55-PASS-≤13



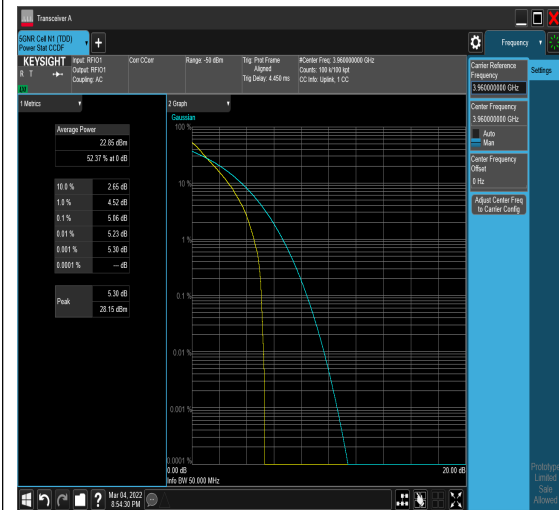
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-8-N/A-N/A-N/A-CP-64QAM-Outet_Full-106
@0-8.23-100%-0.00-8.23-PASS-≤13



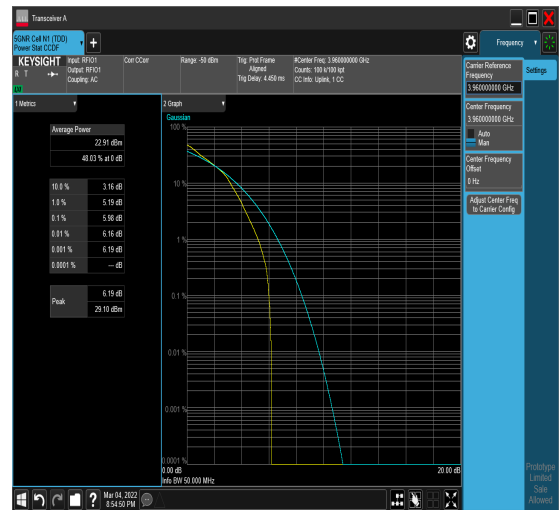
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-10
6@0-8.22-100%-0.00-8.22-PASS-≤13



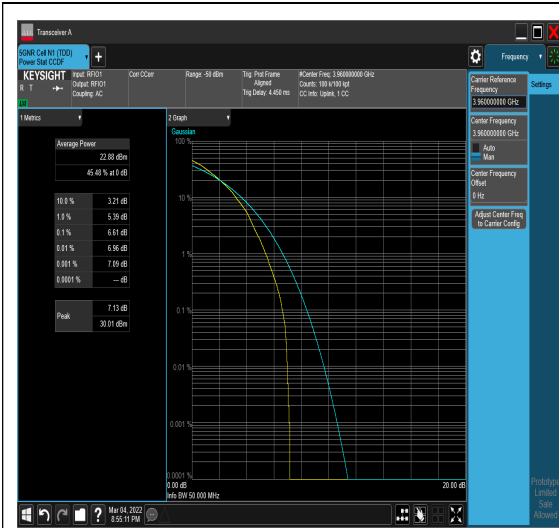
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
100@0-4.74-100%-0.00-4.74-PASS-≤13



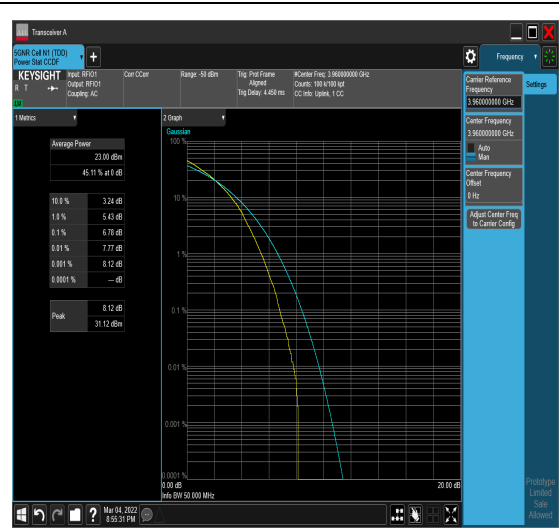
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100
@0-5.06-100%-0.00-5.06-PASS-≤13



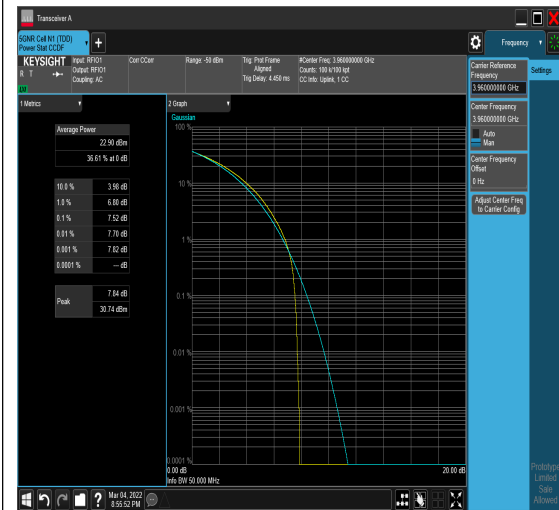
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-10
0@0-5.98-100%-0.00-5.98-PASS-≤13



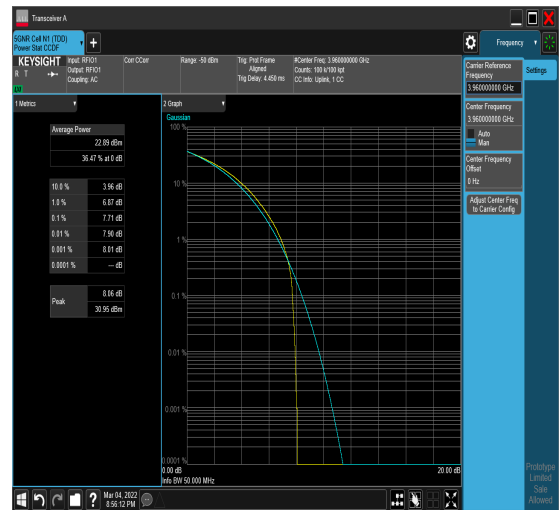
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-4-N/A-N/A-N/A-DFT-64QAM-Outet_Full-10
0@0-6.61-100%-0.00-6.61-PASS-≤13



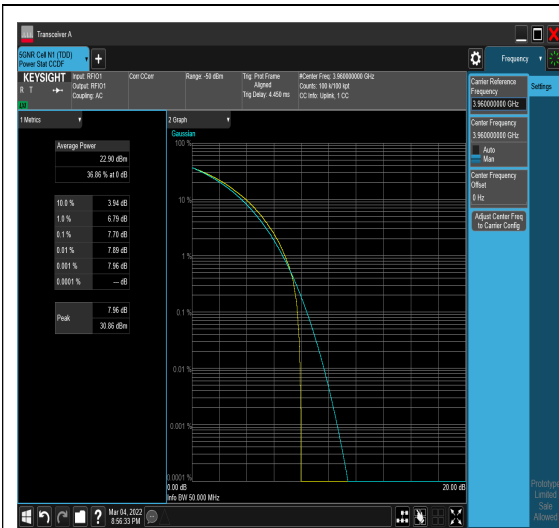
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-5-N/A-N/A-N/A-DFT-256QAM-Outet_Full-1
00@0-6.78-100%-0.00-6.78-PASS-≤13



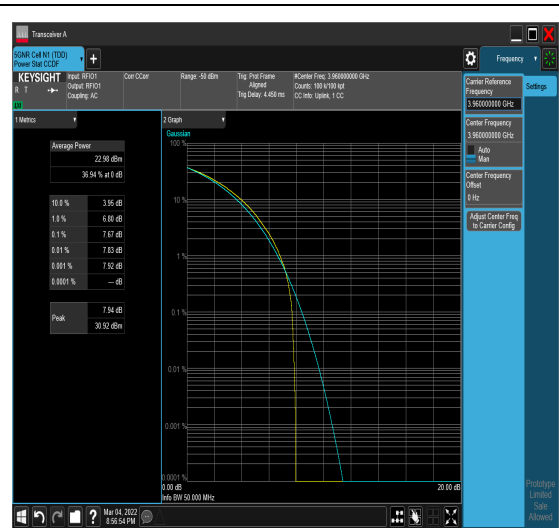
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-6-N/A-N/A-N/A-CP-QPSK-Outet_Full-106
@0-7.52-100%-0.00-7.52-PASS-≤13



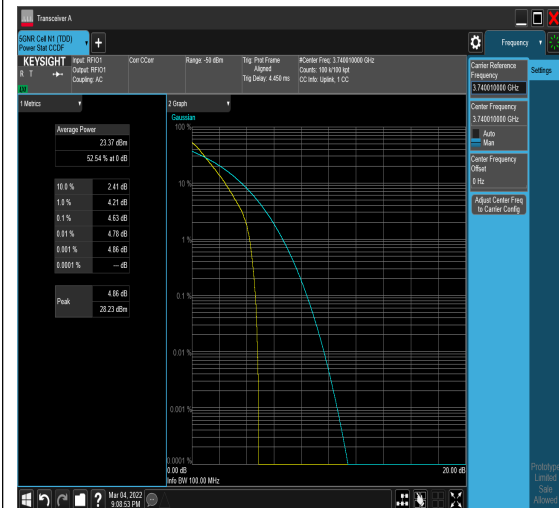
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outet_Full-106
@0-7.71-100%-0.00-7.71-PASS-≤13



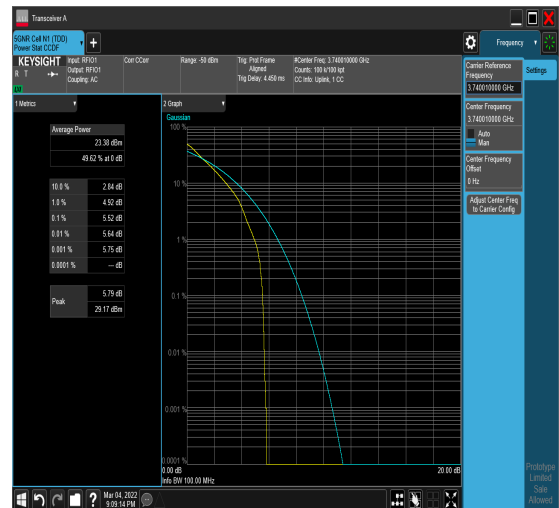
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-8-N/A-N/A-N/A-CP-64QAM-Outet_Full-106
@0-7.70-100%-0.00-7.70-PASS-≤13



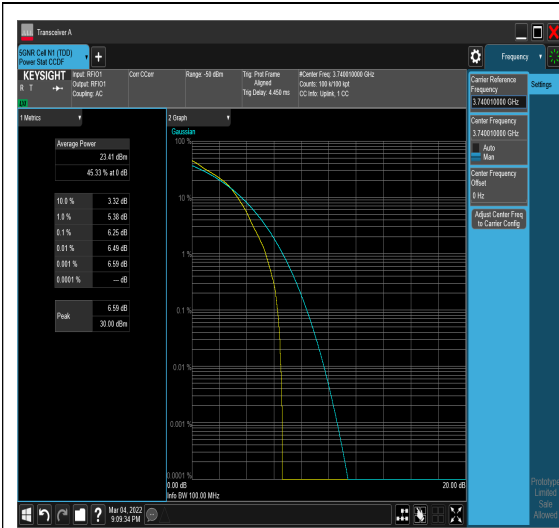
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-9-N/A-N/A-N/A-CP-256QAM-Outet_Full-10
6@0-7.67-100%-0.00-7.67-PASS-≤13



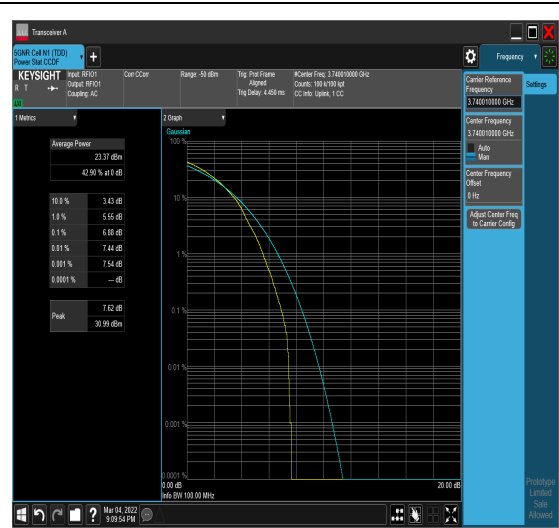
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outet_Full-2
16@0-4.63-100%-0.00-4.63-PASS-≤13



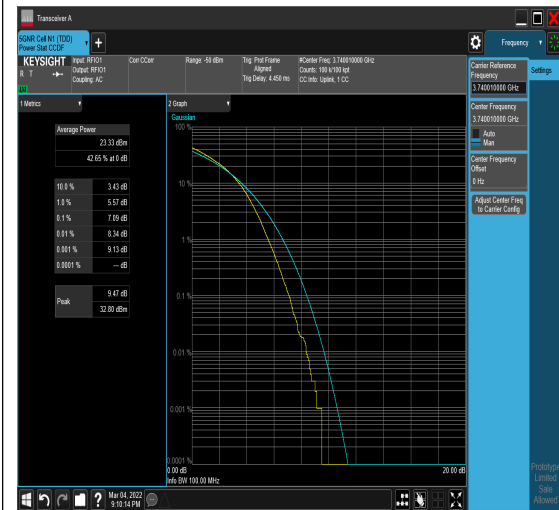
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-2-N/A-N/A-N/A-DFT-QPSK-Outet_Full-216
@0-5.52-100%-0.00-5.52-PASS-≤13



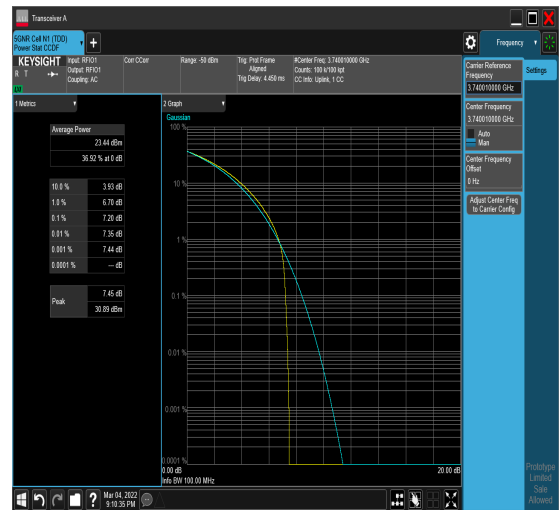
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-21
6@0-6.25-100%-0.00-6.25-PASS-≤13



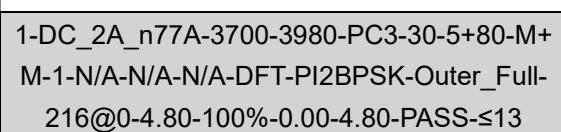
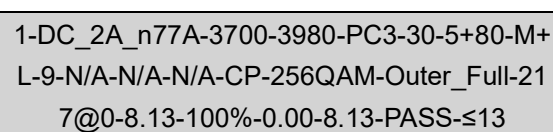
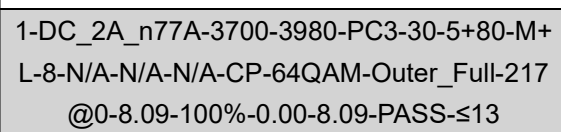
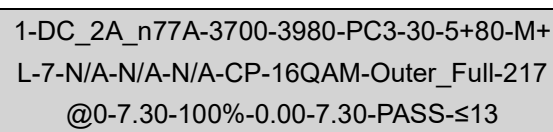
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-21
6@0-6.88-100%-0.00-6.88-PASS-≤13

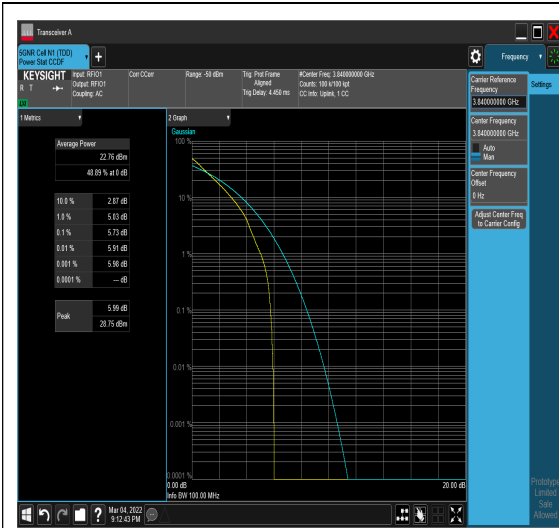


1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-2
16@0-7.09-100%-0.00-7.09-PASS-≤13

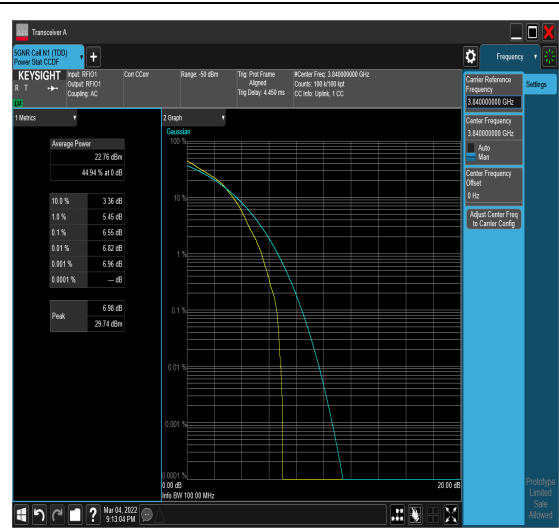


1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
L-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-217
@0-7.20-100%-0.00-7.20-PASS-≤13

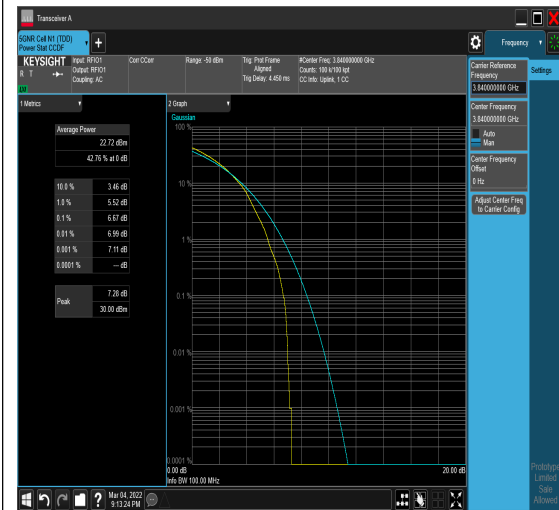




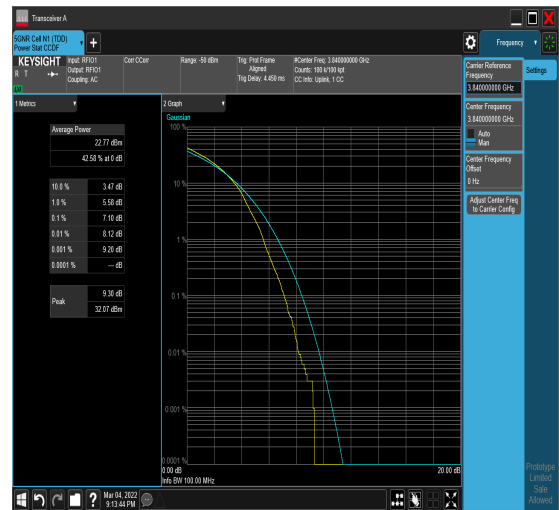
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
M-2-N/A-N/A-N/A-DFT-QPSK-Outet_Full-216
@0-5.73-100%-0.00-5.73-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
M-3-N/A-N/A-N/A-DFT-16QAM-Outet_Full-21
6@0-6.55-100%-0.00-6.55-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
M-4-N/A-N/A-N/A-DFT-64QAM-Outet_Full-21
6@0-6.67-100%-0.00-6.67-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
M-5-N/A-N/A-N/A-DFT-256QAM-Outet_Full-2
16@0-7.10-100%-0.00-7.10-PASS-≤13

