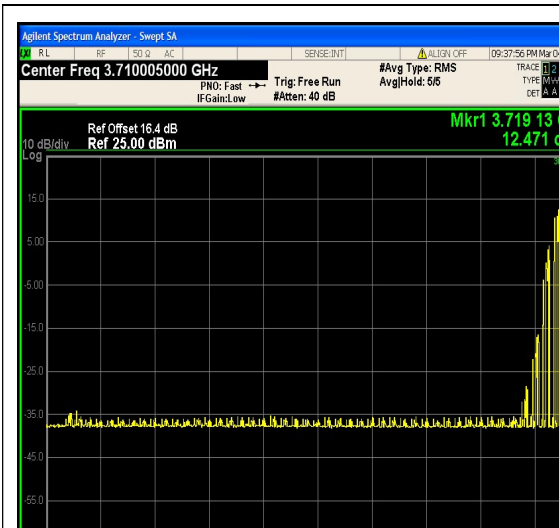
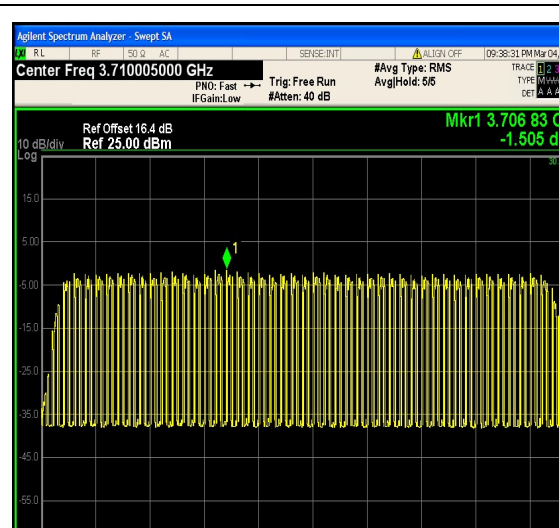
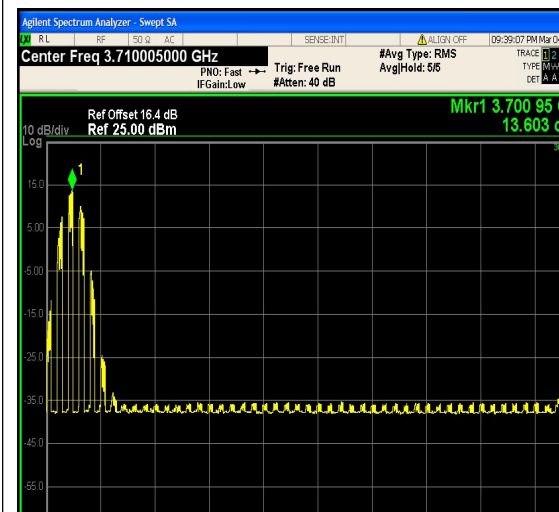




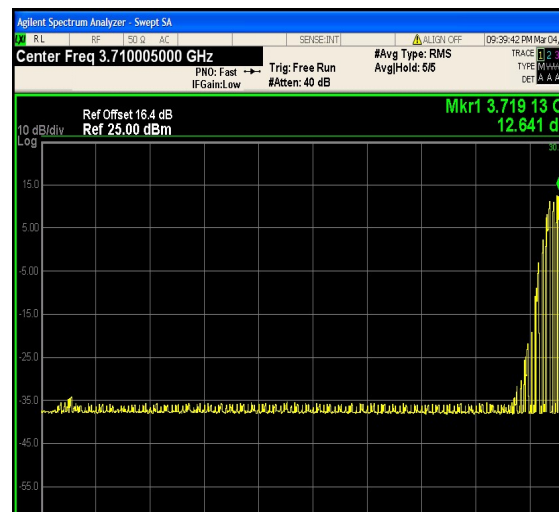
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
ht-1@50-3700.005~3720.005(3719.125MHz,
12.471MHz,Limit30,Margin[17.529])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
@0-3700.005~3720.005(3706.825MHz,-1.50
5MHz,Limit30,Margin[31.505])-PASS-see
graph



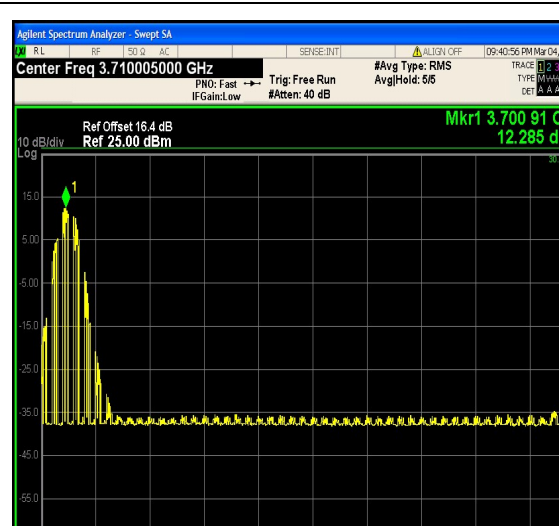
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
eft-1@0-3700.005~3720.005(3700.945MHz,1
3.603MHz,Limit30,Margin[16.397])-PASS-see
graph



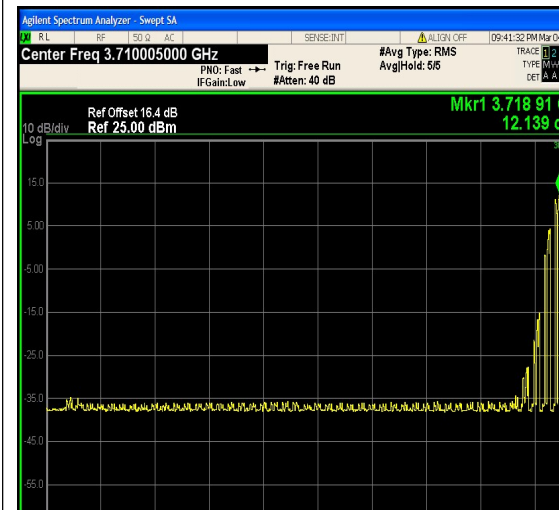
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Ri
ght-1@50-3700.005~3720.005(3719.125MHz
,12.641MHz,Limit30,Margin[17.359])-PASS-s
ee graph



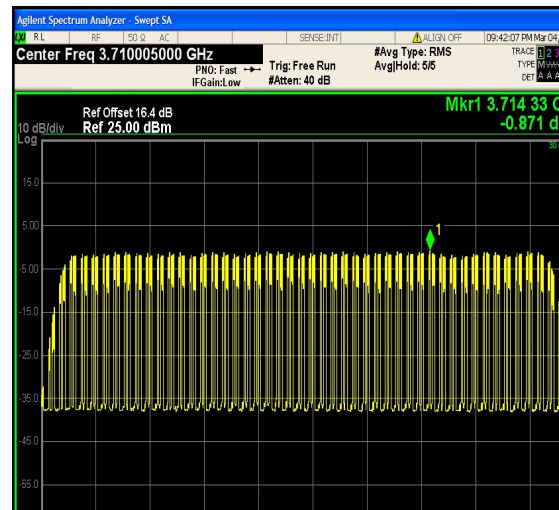
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
@0-3700.005~3720.005(3712.085MHz,-0.75
4MHz,Limit30,Margin[30.754])-PASS-see
graph



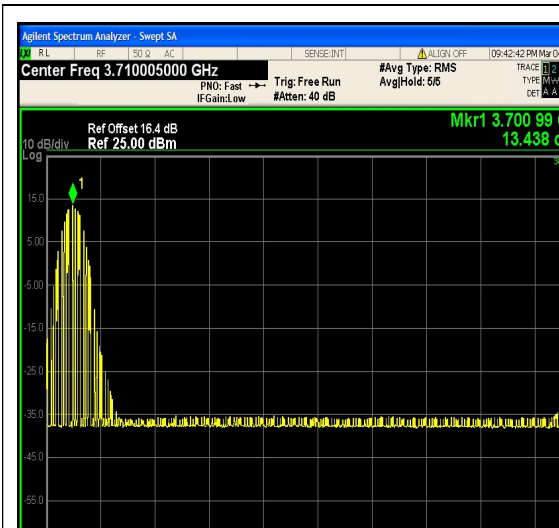
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Left-1@0-3700.005~3720.005(3700.905MHz,
12.285MHz,Limit30,Margin[17.715])-PASS-se
e graph



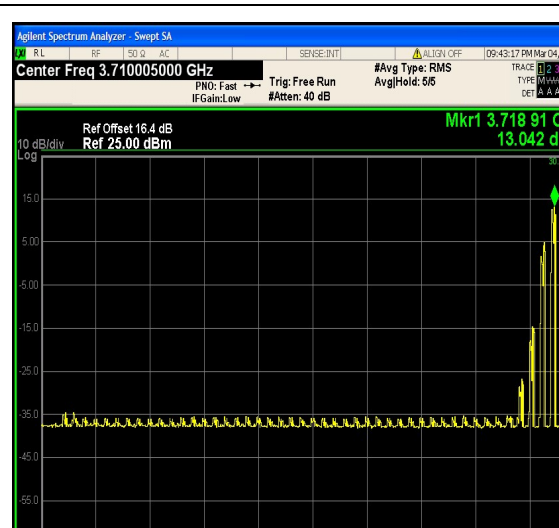
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Right-1@50-3700.005~3720.005(3718.905M
Hz,12.139MHz,Limit30,Margin[17.861])-PAS
S-see graph



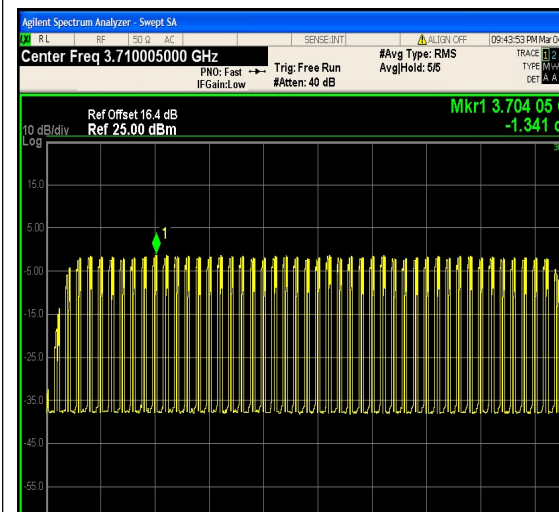
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-5
0@0-3700.005~3720.005(3714.325MHz,-0.8
71MHz,Limit30,Margin[30.871])-PASS-see
graph



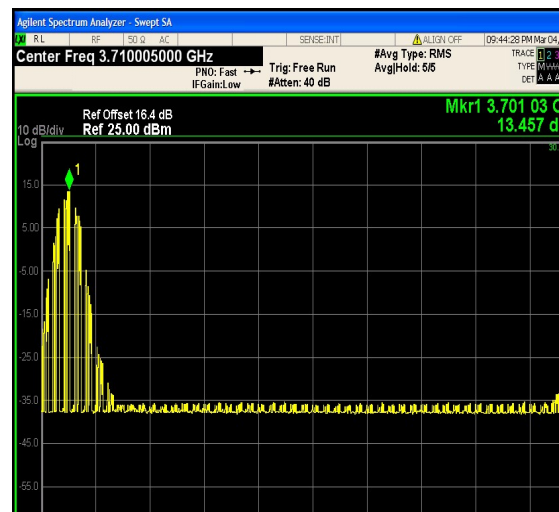
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Left-1@0-3700.005~3720.005(3700.985MH
 z,13.438MHz,Limit30,Margin[16.562])-PASS-
 see graph



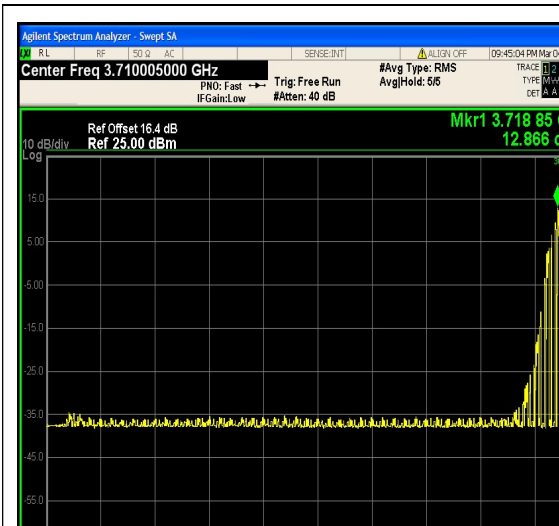
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 L-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Right-1@50-3700.005~3720.005(3718.905
 MHz,13.042MHz,Limit30,Margin[16.958])-PA
 SS-see graph



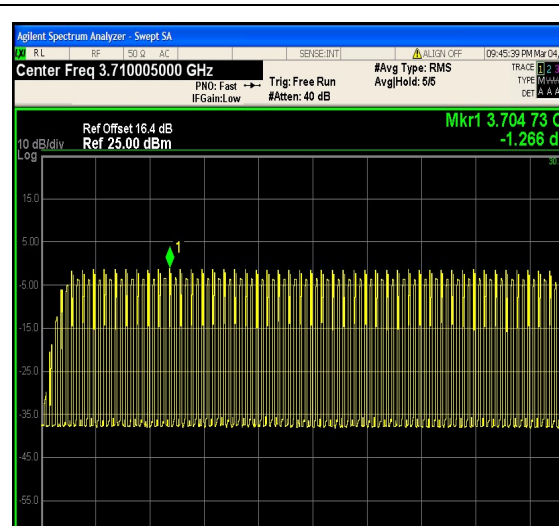
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 L-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
 50@0-3700.005~3720.005(3704.045MHz,-1.
 341MHz,Limit30,Margin[31.341])-PASS-see
 graph



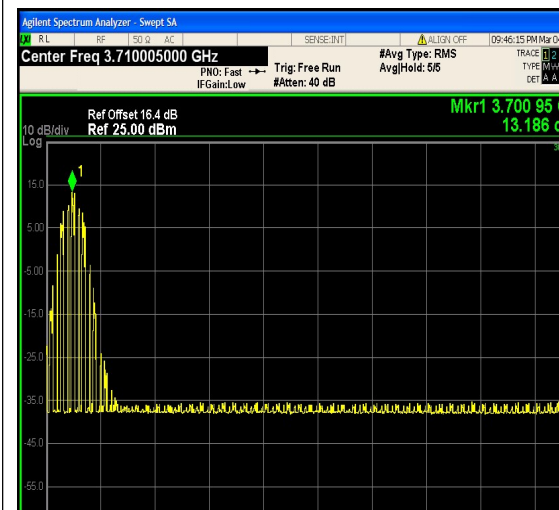
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 L-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
 t-1@0-3700.005~3720.005(3701.025MHz,13.
 457MHz,Limit30,Margin[16.543])-PASS-see
 graph



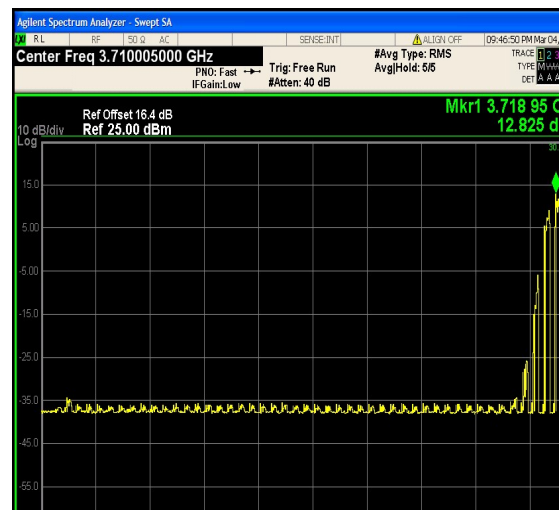
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Rig
ht-1@50-3700.005~3720.005(3718.845MHz,
12.866MHz,Limit30,Margin[17.134])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-51
@0-3700.005~3720.005(3704.725MHz,-1.26
6MHz,Limit30,Margin[31.266])-PASS-see
graph



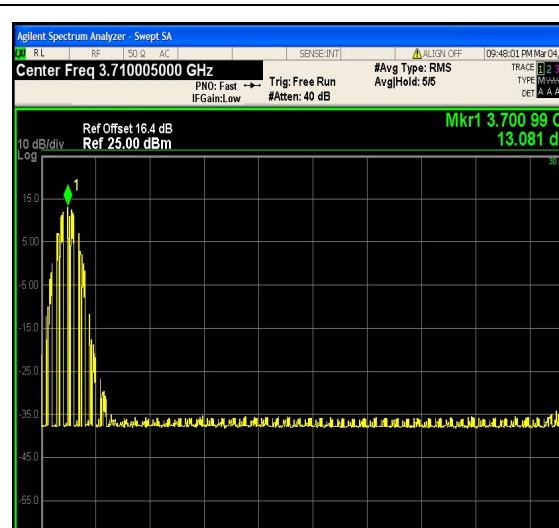
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_L
eft-1@0-3700.005~3720.005(3700.945MHz,1
3.186MHz,Limit30,Margin[16.814])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Ri
ght-1@50-3700.005~3720.005(3718.945MHz
,12.825MHz,Limit30,Margin[17.175])-PASS-s
ee graph



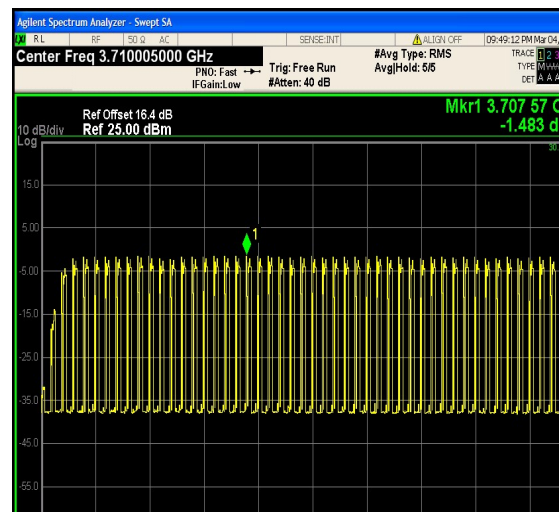
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-3700.005~3720.005(3706.985MHz,-1.91
2MHz,Limit30,Margin[31.912])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
eft-1@0-3700.005~3720.005(3700.985MHz,1
3.081MHz,Limit30,Margin[16.919])-PASS-see
graph



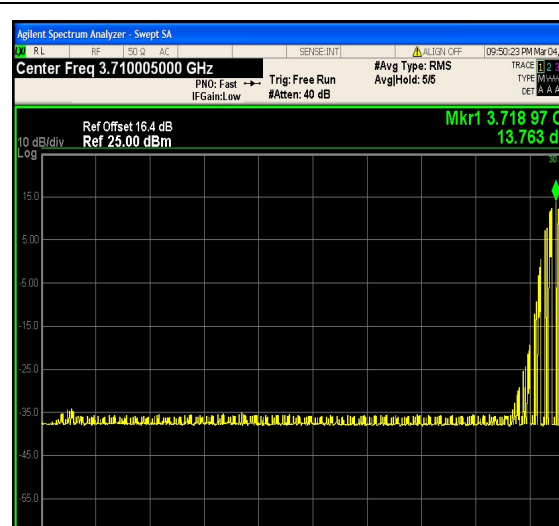
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Ri
ght-1@50-3700.005~3720.005(3719.045MHz
,11.909MHz,Limit30,Margin[18.091])-PASS-s
ee graph



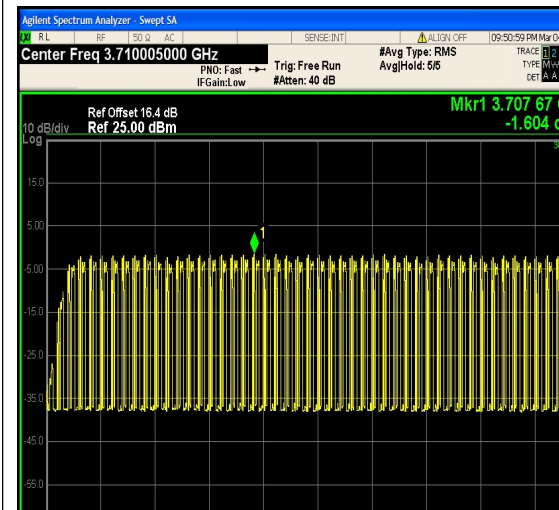
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-3700.005~3720.005(3707.565MHz,-1.48
3MHz,Limit30,Margin[31.483])-PASS-see
graph



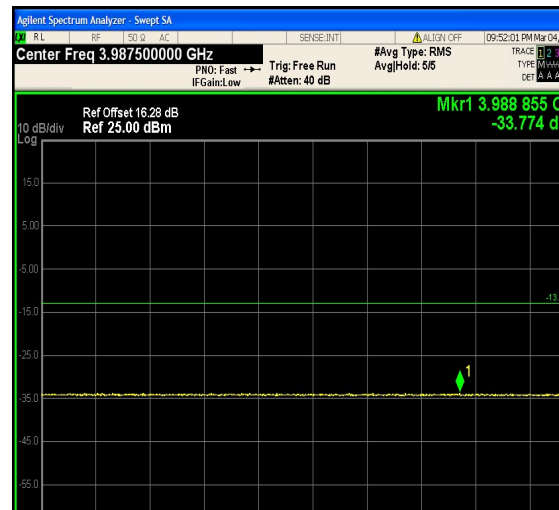
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Left-1@0-3700.005~3720.005(3700.925MHz,
12.807MHz,Limit30,Margin[17.193])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Right-1@50-3700.005~3720.005(3718.965M
Hz,13.763MHz,Limit30,Margin[16.237])-PAS
S-se graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-5
1@0-3700.005~3720.005(3707.665MHz,-1.6
04MHz,Limit30,Margin[31.604])-PASS-see
graph



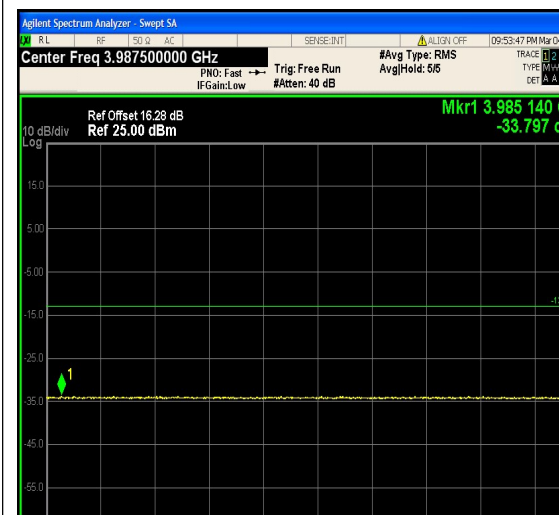
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Left-1@0-3985~3990(3988.855MHz,-33.774
MHz,Limit-13,Margin[20.774])-PASS-see
graph



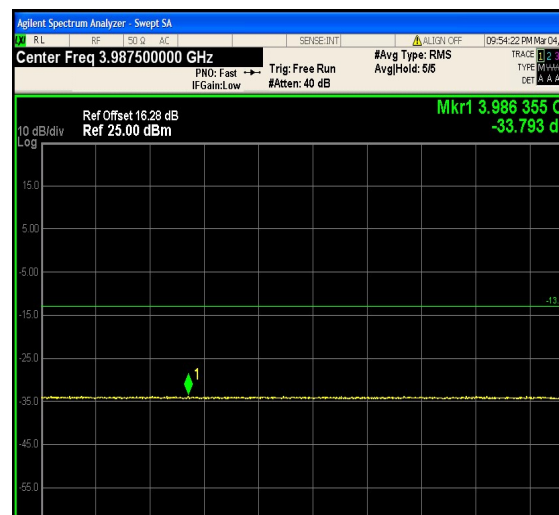
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Right-1@50-3985~3990(3986.39MHz,-33.8M
Hz,Limit-13,Margin[20.8])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
50@0-3985~3990(3986.705MHz,-33.238MH
z,Limit-13,Margin[20.238])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Lef
t-1@0-3985~3990(3985.14MHz,-33.797MHz,
Limit-13,Margin[20.797])-PASS-see graph



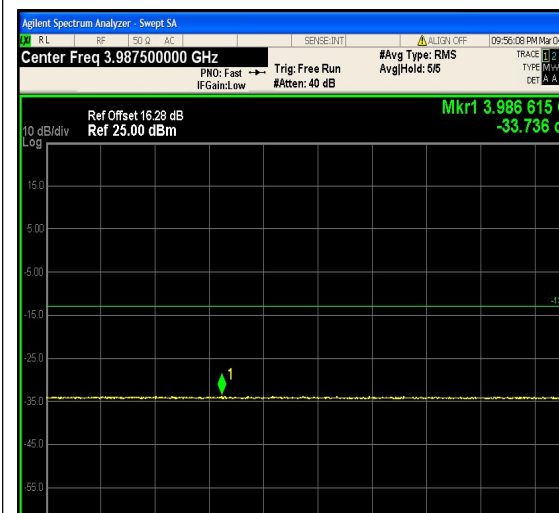
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
ht-1@50-3985~3990(3986.355MHz,-33.793
MHz,Limit-13,Margin[20.793])-PASS-see
graph



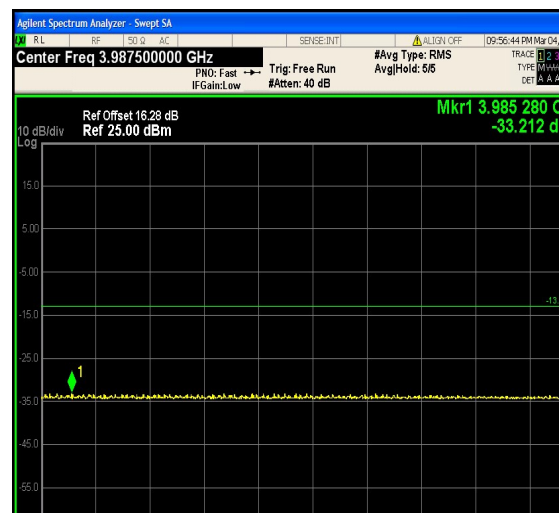
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
@0-3985~3990(3985.075MHz,-33.311MHz,Limit-13,Margin[20.311])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
eft-1@0-3985~3990(3987.225MHz,-33.778MHz,Limit-13,Margin[20.778])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_R
ight-1@50-3985~3990(3986.615MHz,-33.736MHz,Limit-13,Margin[20.736])-PASS-see graph



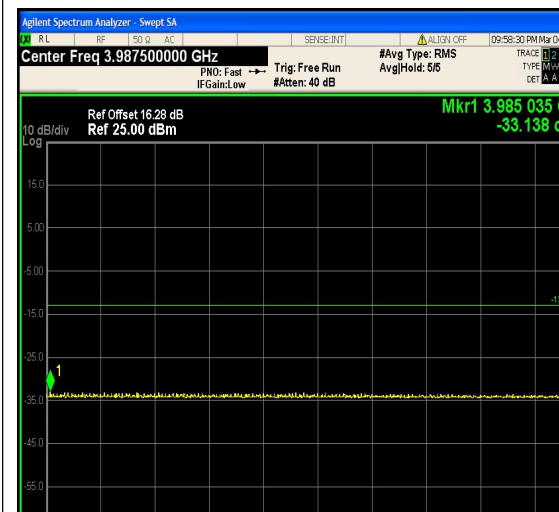
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
@0-3985~3990(3985.28MHz,-33.212MHz,Limit-13,Margin[20.212])-PASS-see graph



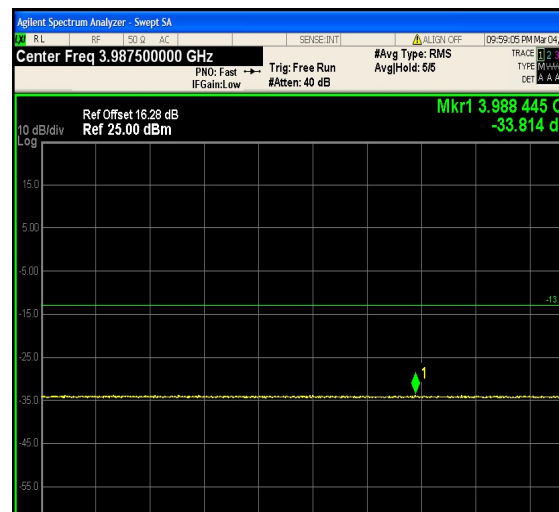
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Left-1@0-3985~3990(3985.24MHz,-33.755M
Hz,Limit-13,Margin[20.755])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Right-1@50-3985~3990(3987.055MHz,-33.6
93MHz,Limit-13,Margin[20.693])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-5
0@0-3985~3990(3985.035MHz,-33.138MHz,
Limit-13,Margin[20.138])-PASS-see graph



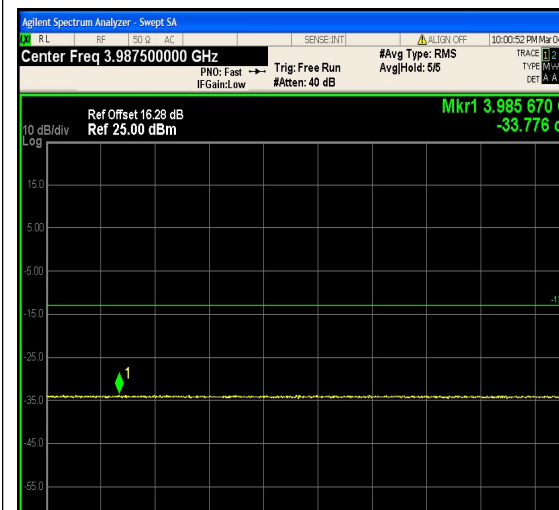
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB_
_Left-1@0-3985~3990(3988.445MHz,-33.81
4MHz,Limit-13,Margin[20.814])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
_Right-1@50-3985~3990(3985.2MHz,-33.81
1MHz,Limit-13,Margin[20.811])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
50@0-3985~3990(3985.205MHz,-32.959MH
z,Limit-13,Margin[19.959])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
t-1@0-3985~3990(3985.67MHz,-33.776MHz,
Limit-13,Margin[20.776])-PASS-see graph



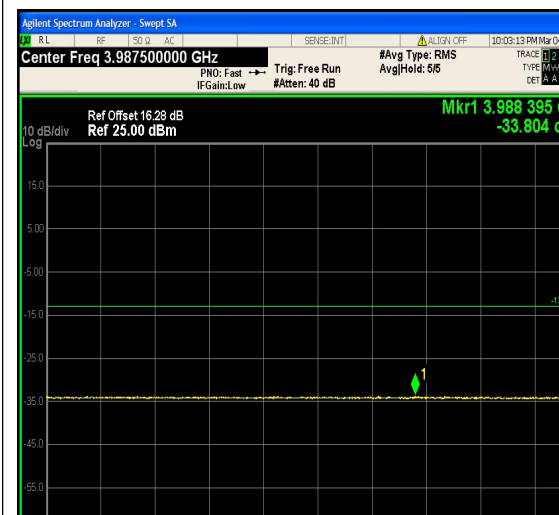
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Ri
ght-1@50-3985~3990(3987.86MHz,-33.729
MHz,Limit-13,Margin[20.729])-PASS-see
graph



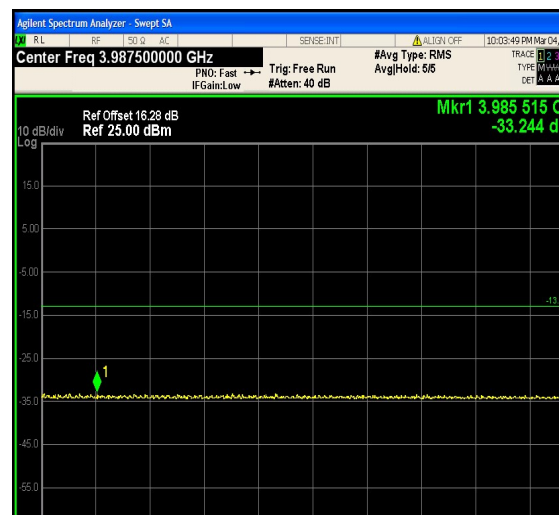
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-51
@0-3985~3990(3985.18MHz,-32.852MHz,Limit-13,Margin[19.852])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_L
eft-1@0-3985~3990(3985.365MHz,-33.751MHz,Limit-13,Margin[20.751])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_R
ight-1@50-3985~3990(3988.395MHz,-33.804MHz,Limit-13,Margin[20.804])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-3985~3990(3985.515MHz,-33.244MHz,Limit-13,Margin[20.244])-PASS-see graph