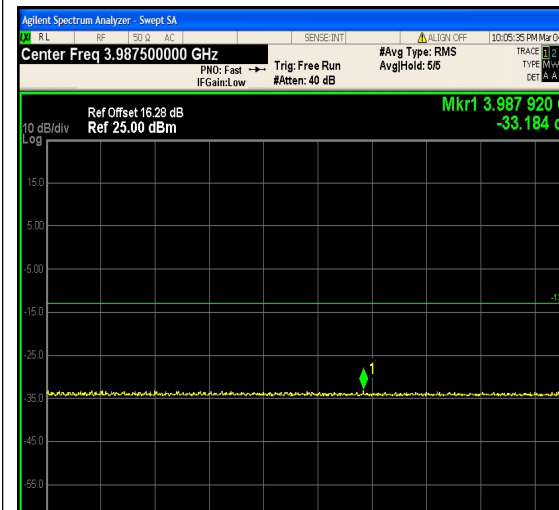




1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
eft-1@0-3985~3990(3988.785MHz,-33.762M
Hz,Limit-13,Margin[20.762])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_R
ight-1@50-3985~3990(3987.485MHz,-33.849
MHz,Limit-13,Margin[20.849])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-3985~3990(3987.92MHz,-33.184MHz,Li
mit-13,Margin[20.184])-PASS-see graph



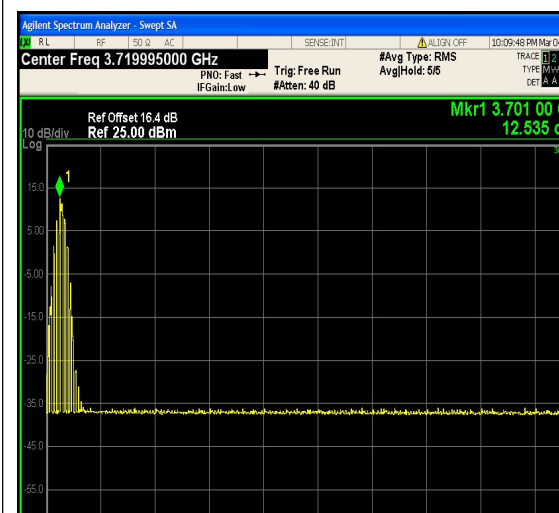
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Left-1@0-3985~3990(3985.28MHz,-33.801M
Hz,Limit-13,Margin[20.801])-PASS-see graph



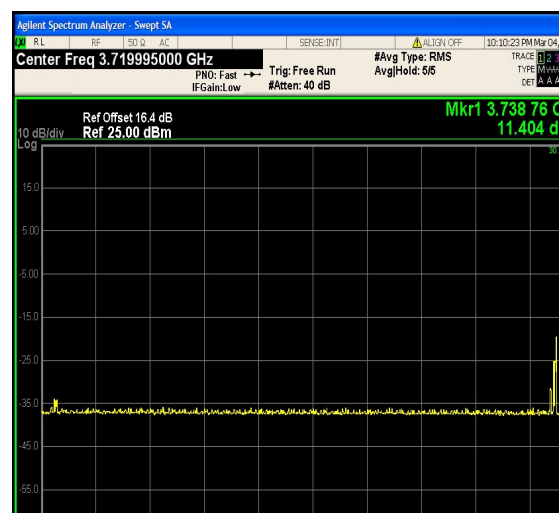
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Right-1@50-3985~3990(3987.165MHz,-33.7
97MHz,Limit-13,Margin[20.797])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-5
1@0-3985~3990(3985.21MHz,-33.066MHz,L
imit-13,Margin[20.066])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Left-1@0-3699.995~3739.995(3700.995MHz,
12.535MHz,Limit30,Margin[17.465])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Right-1@105-3699.995~3739.995(3738.755
MHz,11.404MHz,Limit30,Margin[18.596])-PA
SS-see graph



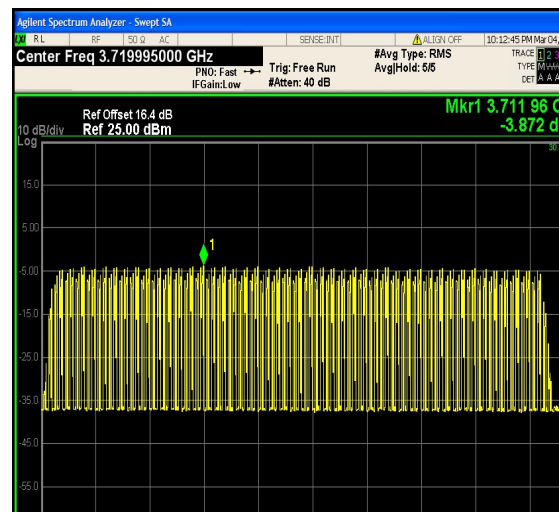
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-1
 00@0-3699.995~3739.995(3718.515MHz,-4.
 333MHz,Limit30,Margin[34.333])-PASS-see
 graph



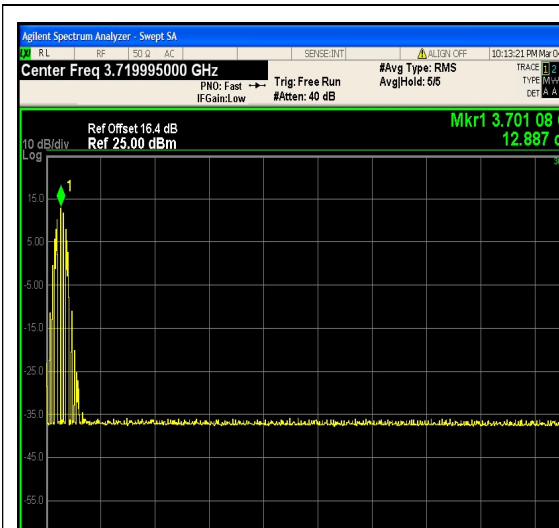
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Left
 -1@0-3699.995~3739.995(3700.915MHz,11.
 947MHz,Limit30,Margin[18.053])-PASS-see
 graph



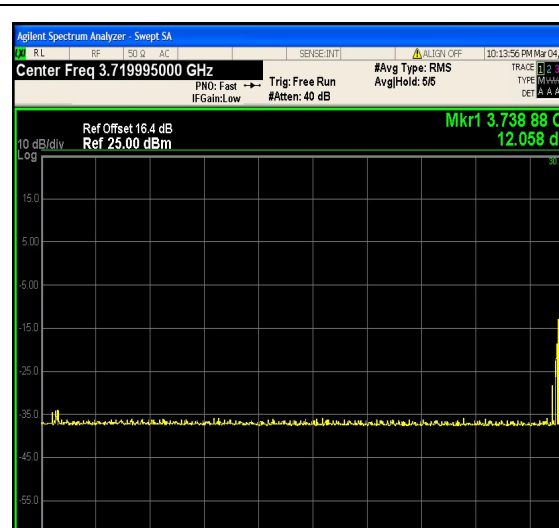
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
 ht-1@105-3699.995~3739.995(3739.035MHz
 ,10.436MHz,Limit30,Margin[19.564])-PASS-s
 ee graph



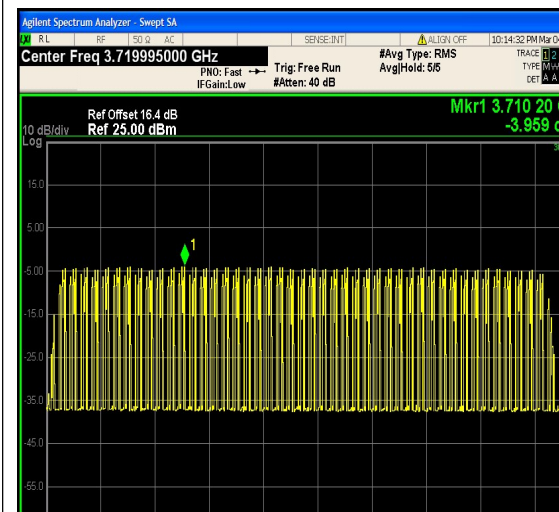
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100
 @0-3699.995~3739.995(3711.955MHz,-3.87
 2MHz,Limit30,Margin[33.872])-PASS-see
 graph



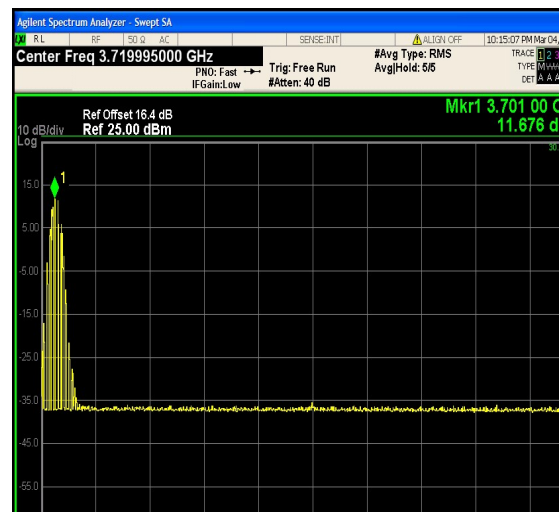
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
eft-1@0-3699.995~3739.995(3701.075MHz,1
2.887MHz,Limit30,Margin[17.113])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Ri
ght-1@105-3699.995~3739.995(3738.875M
Hz,12.058MHz,Limit30,Margin[17.942])-PAS
S-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-10
0@0-3699.995~3739.995(3710.195MHz,-3.9
59MHz,Limit30,Margin[33.959])-PASS-see
graph



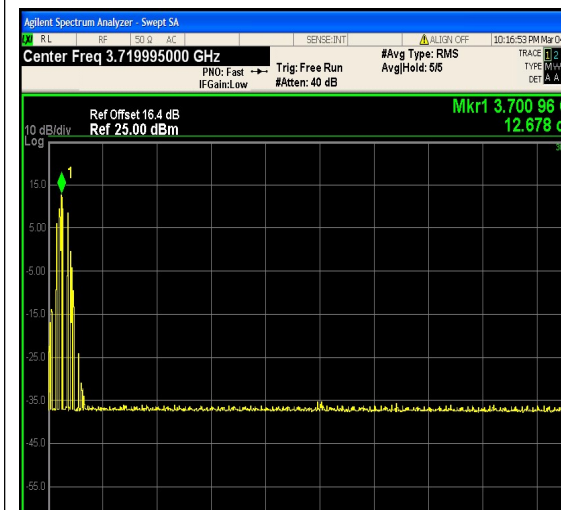
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Left-1@0-3699.995~3739.995(3700.995MHz,
11.676MHz,Limit30,Margin[18.324])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Right-1@105-3699.995~3739.995(3738.875
MHz,12.961MHz,Limit30,Margin[17.039])-PA
SS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-1
00@0-3699.995~3739.995(3708.315MHz,-4.
045MHz,Limit30,Margin[34.045])-PASS-see
graph



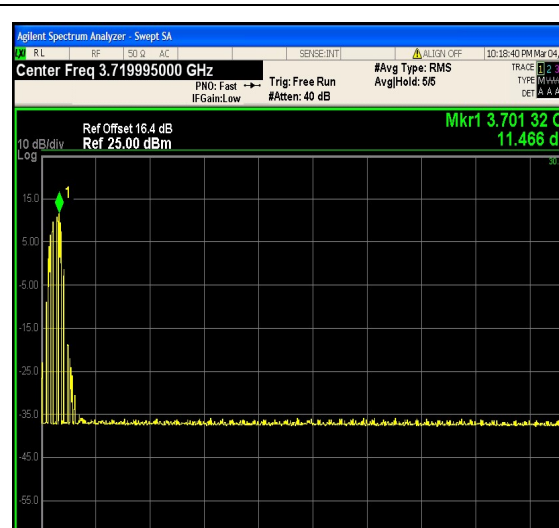
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
_Left-1@0-3699.995~3739.995(3700.955MH
z,12.678MHz,Limit30,Margin[17.322])-PASS-
see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
_Right-1@105-3699.995~3739.995(3738.95
5MHz,12.116MHz,Limit30,Margin[17.884])-P
ASS-see graph



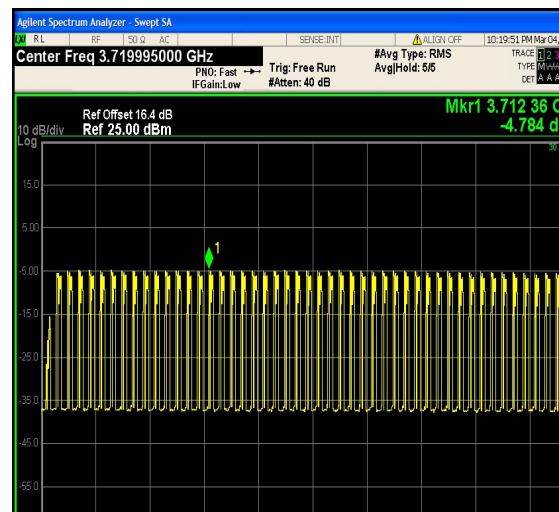
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
 100@0-3699.995~3739.995(3721.835MHz,-4
 .888MHz,Limit30,Margin[34.888])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
 t-1@0-3699.995~3739.995(3701.315MHz,11.
 466MHz,Limit30,Margin[18.534])-PASS-see
 graph



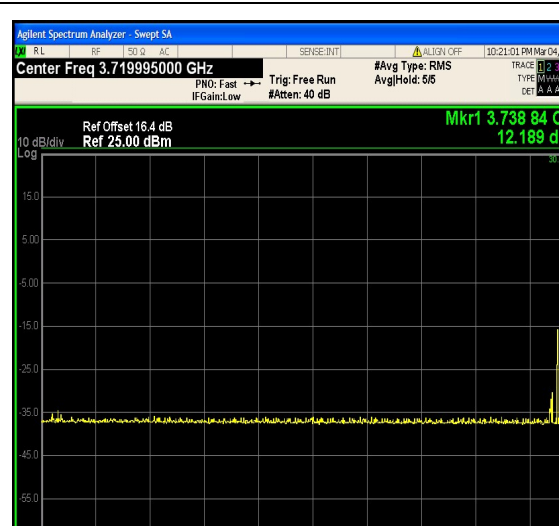
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Rig
 ht-1@105-3699.995~3739.995(3738.875MHz
 ,11.411MHz,Limit30,Margin[18.589])-PASS-s
 ee graph



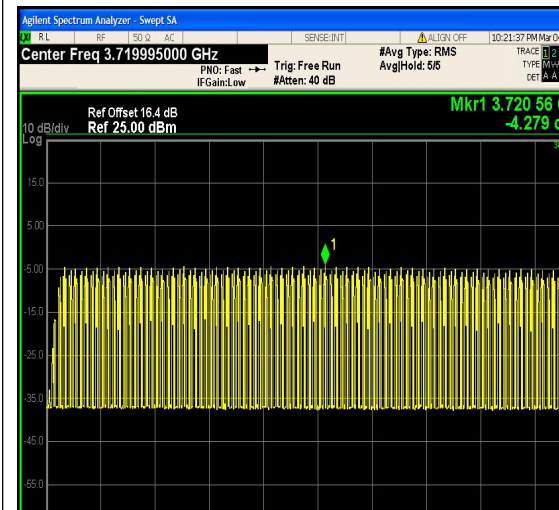
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-106
 @0-3699.995~3739.995(3712.355MHz,-4.78
 4MHz,Limit30,Margin[34.784])-PASS-see
 graph



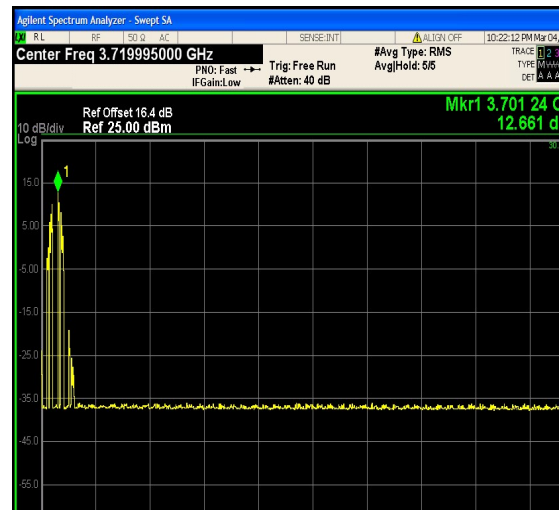
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_L
 eft-1@0-3699.995~3739.995(3700.955MHz,1
 2.669MHz,Limit30,Margin[17.331])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Ri
 ght-1@105-3699.995~3739.995(3738.835M
 Hz,12.189MHz,Limit30,Margin[17.811])-PAS
 S-see graph



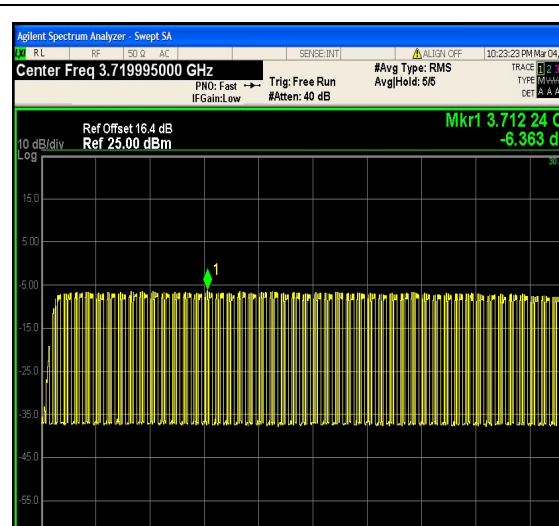
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-10
 6@0-3699.995~3739.995(3720.555MHz,-4.2
 79MHz,Limit30,Margin[34.279])-PASS-see
 graph



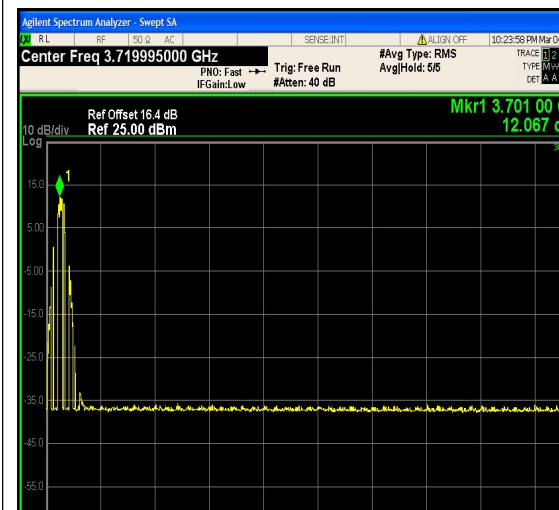
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
 eft-1@0-3699.995~3739.995(3701.235MHz,1
 2.661MHz,Limit30,Margin[17.339])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Ri
ght-1@105-3699.995~3739.995(3738.795M
Hz,11.51MHz,Limit30,Margin[18.49])-PASS-s
ee graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-10
6@0-3699.995~3739.995(3712.235MHz,-6.3
63MHz,Limit30,Margin[36.363])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Left-1@0-3699.995~3739.995(3700.995MHz,
12.067MHz,Limit30,Margin[17.933])-PASS-se
e graph



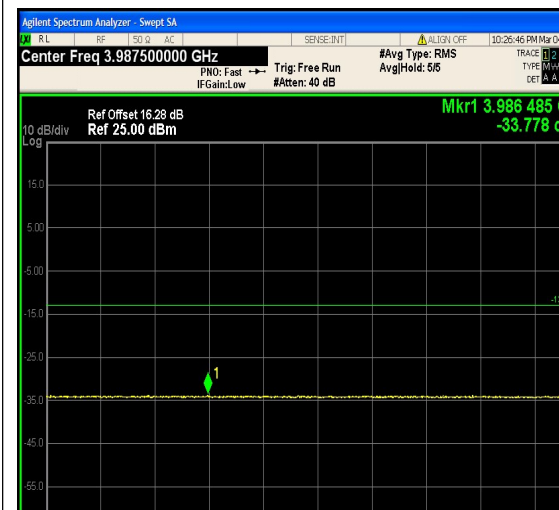
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
Right-1@105-3699.995~3739.995(3738.755
MHz,12.038MHz,Limit30,Margin[17.962])-PA
SS-see graph



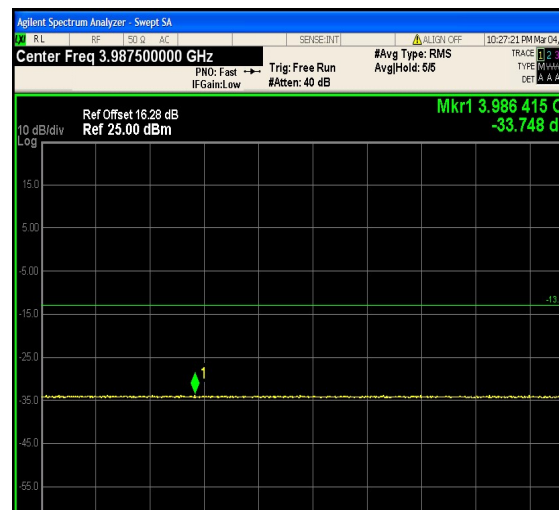
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-1
06@0-3699.995~3739.995(3712.395MHz,-4.
739MHz,Limit30,Margin[34.739])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Left-1@0-3985~3990(3986.145MHz,-33.841
MHz,Limit-13,Margin[20.841])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Right-1@105-3985~3990(3986.485MHz,-33.
778MHz,Limit-13,Margin[20.778])-PASS-see
graph



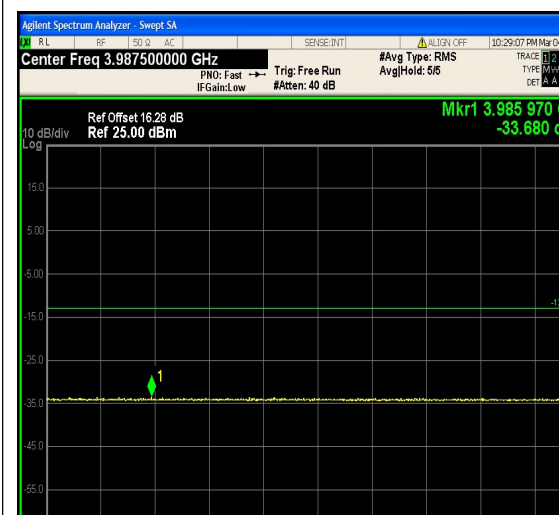
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H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
100@0-3985~3990(3986.415MHz,-33.748M
Hz,Limit-13,Margin[20.748])-PASS-see graph



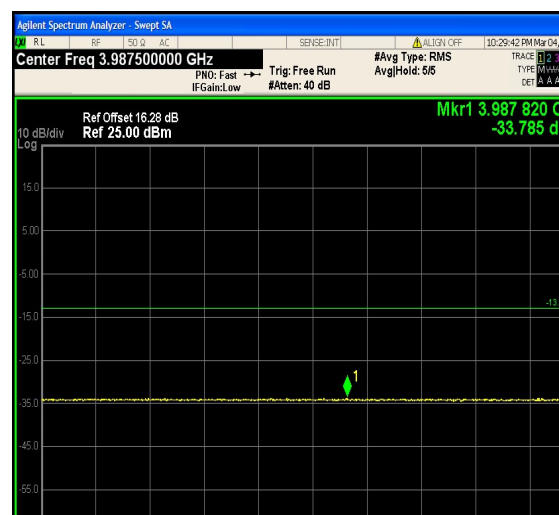
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Left-1@0-3985~3990(3985.4MHz,-33.826MHz,Limit-13,Margin[20.826])-PASS-see graph



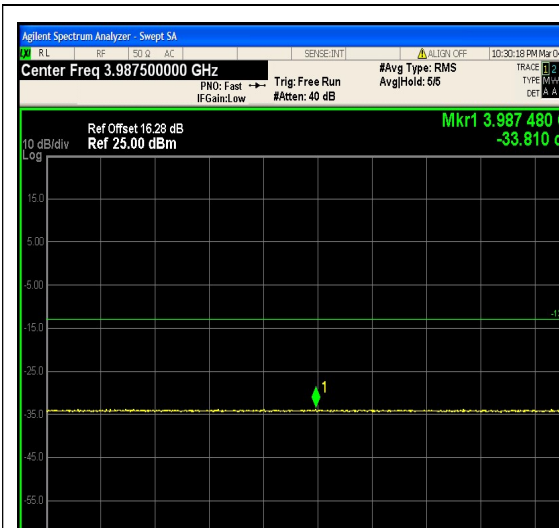
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Right-1@105-3985~3990(3987.78MHz,-33.745 MHz,Limit-13,Margin[20.745])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100@0-3985~3990(3985.97MHz,-33.68MHz,Limit-13,Margin[20.68])-PASS-see graph



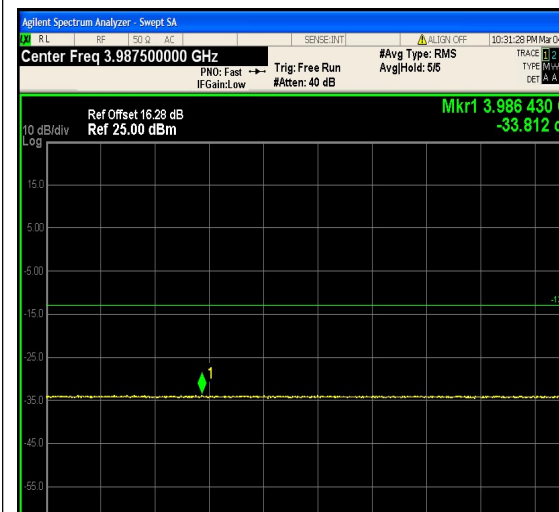
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Left-1@0-3985~3990(3987.82MHz,-33.785 MHz,Limit-13,Margin[20.785])-PASS-see graph



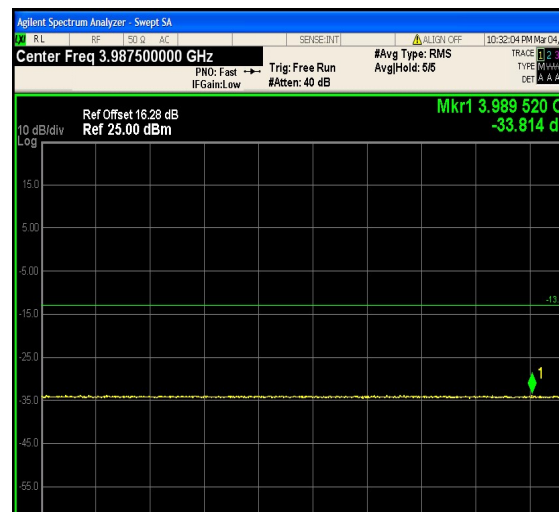
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_R
ight-1@105-3985~3990(3987.48MHz,-33.81
MHz,Limit-13,Margin[20.81])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-10
0@0-3985~3990(3986.54MHz,-33.46MHz,Li
mit-13,Margin[20.46])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Left-1@0-3985~3990(3986.43MHz,-33.812M
Hz,Limit-13,Margin[20.812])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
Right-1@105-3985~3990(3989.52MHz,-33.8
14MHz,Limit-13,Margin[20.814])-PASS-see
graph