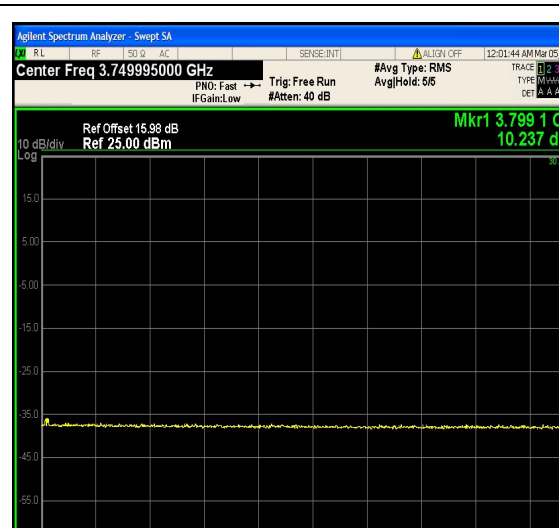
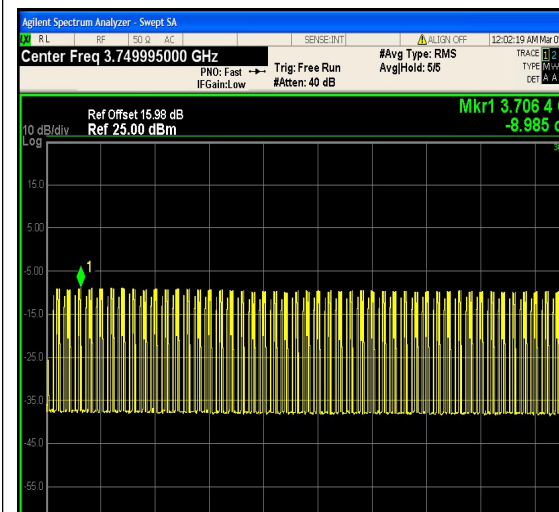




1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_
 Left-1@0-3699.995~3799.995(3701.295MHz,
 7.327MHz,Limit30,Margin[22.673])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_
 Right-1@272-3699.995~3799.995(3799.095
 MHz,10.237MHz,Limit30,Margin[19.763])-PA
 SS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-2
 73@0-3699.995~3799.995(3706.395MHz,-8.
 985MHz,Limit30,Margin[38.985])-PASS-see
 graph



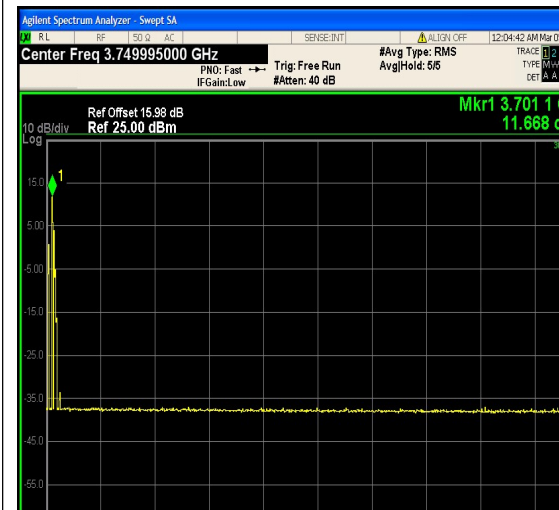
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_
 Left-1@0-3699.995~3799.995(3700.895MHz,
 11.804MHz,Limit30,Margin[18.196])-PASS-se
 e graph



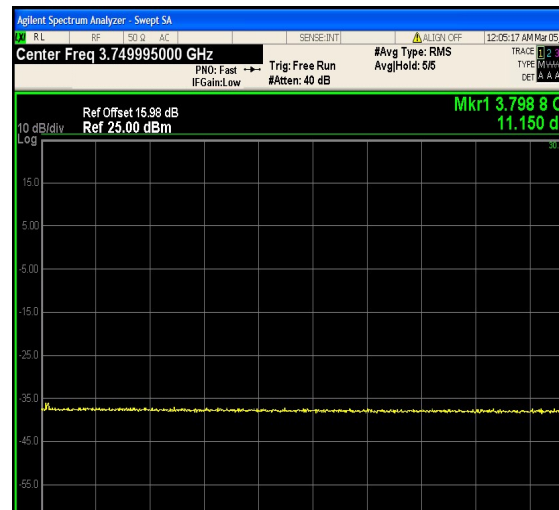
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_
Right-1@272-3699.995~3799.995(3798.795
MHz,11.686MHz,Limit30,Margin[18.314])-PA
SS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-2
73@0-3699.995~3799.995(3702.495MHz,-9.
337MHz,Limit30,Margin[39.337])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
_Left-1@0-3699.995~3799.995(3701.095MH
z,11.668MHz,Limit30,Margin[18.332])-PASS-
see graph



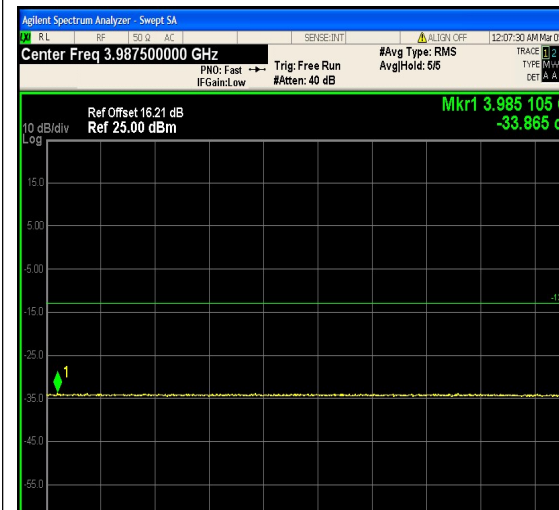
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
_Right-1@272-3699.995~3799.995(3798.79
5MHz,11.15MHz,Limit30,Margin[18.85])-PAS
S-see graph



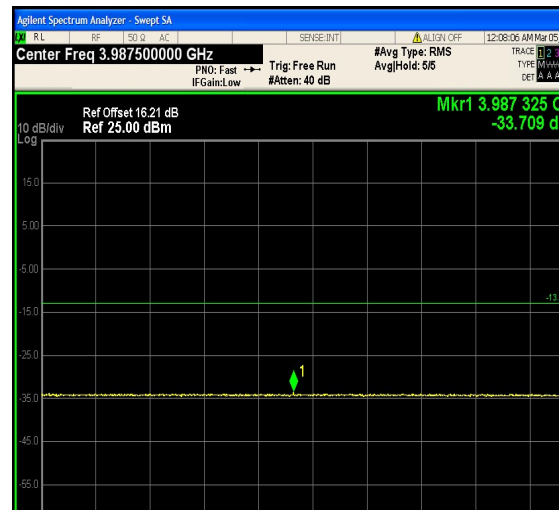
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-
 273@0-3699.995~3799.995(3709.995MHz,-9
 .682MHz,Limit30,Margin[39.682])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +H-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB
 _Left-1@0-3985~3990(3985.39MHz,-33.857
 MHz,Limit-13,Margin[20.857])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB
 _Right-1@272-3985~3990(3985.105MHz,-33
 .865MHz,Limit-13,Margin[20.865])-PASS-see
 graph



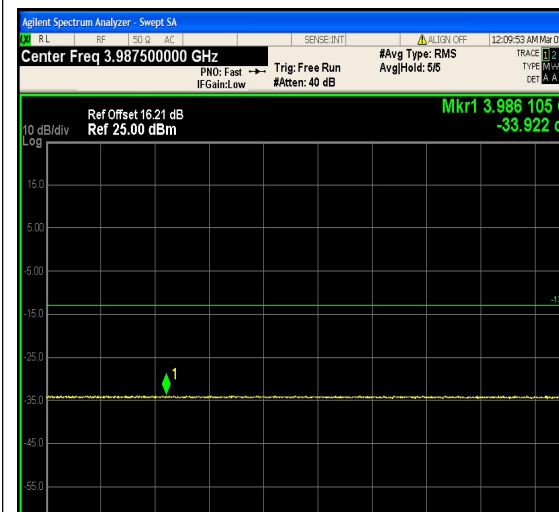
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full
 -270@0-3985~3990(3987.325MHz,-33.709M
 Hz,Limit-13,Margin[20.709])-PASS-see graph



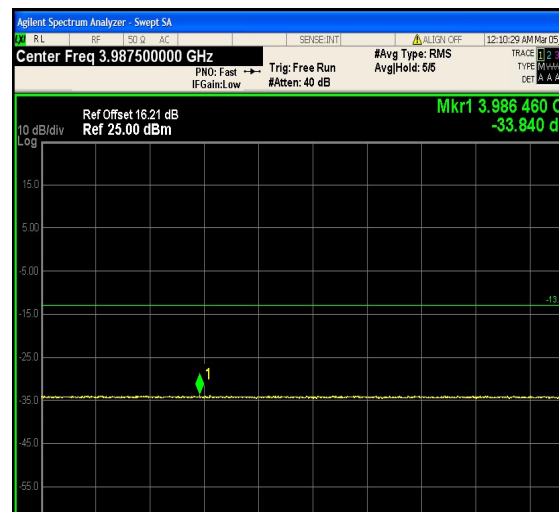
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_L
eft-1@0-3985~3990(3985.14MHz,-33.897MH
z,Limit-13,Margin[20.897])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Ri
ght-1@272-3985~3990(3985.84MHz,-33.818
MHz,Limit-13,Margin[20.818])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-27
0@0-3985~3990(3986.105MHz,-33.922MHz,
Limit-13,Margin[20.922])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_
Left-1@0-3985~3990(3986.46MHz,-33.84MH
z,Limit-13,Margin[20.84])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_
Right-1@272-3985~3990(3985.725MHz,-33.
742MHz,Limit-13,Margin[20.742])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-2
70@0-3985~3990(3989.085MHz,-33.86MHz,
Limit-13,Margin[20.86])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB
_Left-1@0-3985~3990(3985.225MHz,-33.88
9MHz,Limit-13,Margin[20.889])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB
_Right-1@272-3985~3990(3985.845MHz,-33
.9MHz,Limit-13,Margin[20.9])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-
270@0-3985~3990(3985.82MHz,-33.895MH
z,Limit-13,Margin[20.895])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-13-N/A-N/A-N/A-DFT-256QAM-Edge_1R
B_Left-1@0-3985~3990(3987.335MHz,-33.9
09MHz,Limit-13,Margin[20.909])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-14-N/A-N/A-N/A-DFT-256QAM-Edge_1R
B_Right-1@272-3985~3990(3985.525MHz,-3
3.857MHz,Limit-13,Margin[20.857])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-15-N/A-N/A-N/A-DFT-256QAM-Outer_Ful
l-270@0-3985~3990(3989.275MHz,-33.924M
Hz,Limit-13,Margin[20.924])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_L
eft-1@0-3985~3990(3985MHz,-33.858MHz,L
imit-13,Margin[20.858])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_R
ight-1@272-3985~3990(3985.565MHz,-33.74
7MHz,Limit-13,Margin[20.747])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-27
3@0-3985~3990(3986.415MHz,-33.823MHz,
Limit-13,Margin[20.823])-PASS-see graph



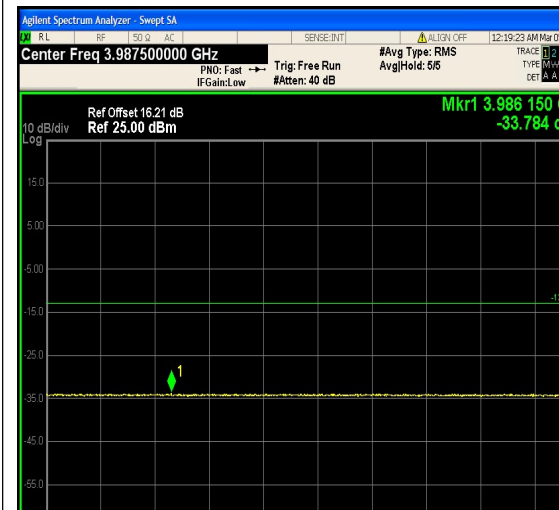
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+H-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_
Left-1@0-3985~3990(3989.695MHz,-33.903
MHz,Limit-13,Margin[20.903])-PASS-see
graph



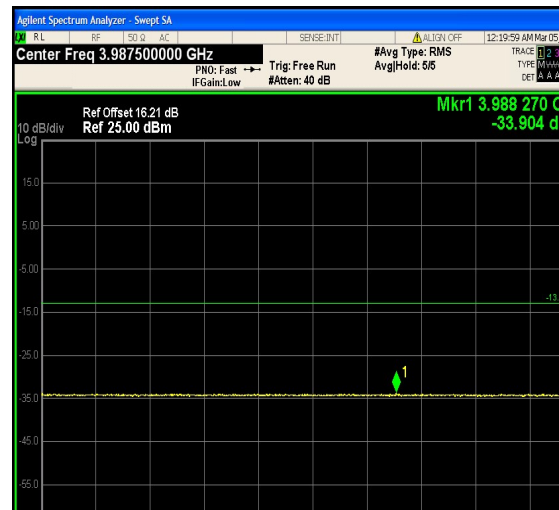
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+H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_
Right-1@272-3985~3990(3985.415MHz,-33.
886MHz,Limit-13,Margin[20.886])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-2
73@0-3985~3990(3985.75MHz,-33.874MHz,
Limit-13,Margin[20.874])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_
Left-1@0-3985~3990(3986.15MHz,-33.784M
Hz,Limit-13,Margin[20.784])-PASS-see graph



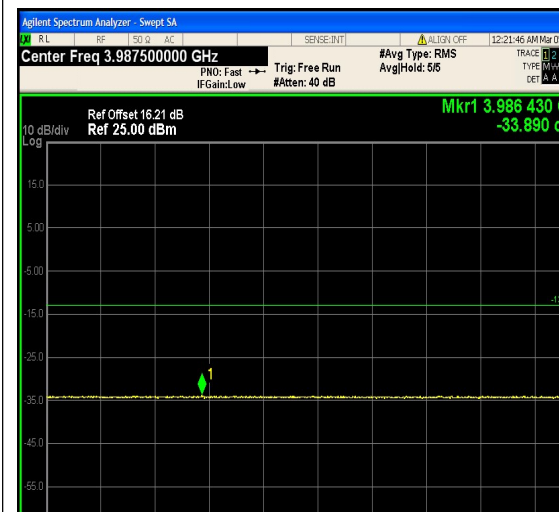
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_
Right-1@272-3985~3990(3988.27MHz,-33.9
04MHz,Limit-13,Margin[20.904])-PASS-see
graph



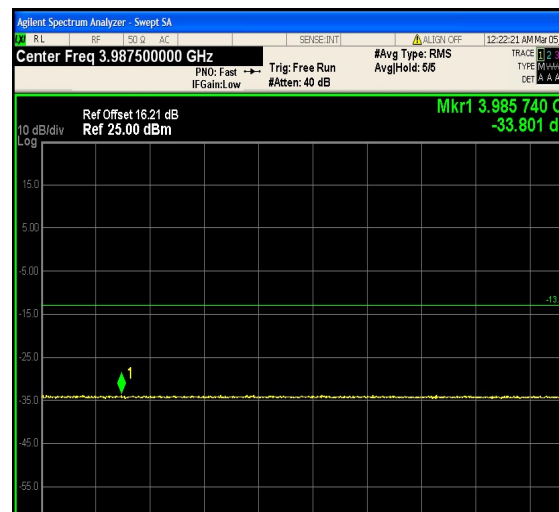
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-2
73@0-3985~3990(3987.41MHz,-33.85MHz,L
imit-13,Margin[20.85])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB
_Left-1@0-3985~3990(3987.405MHz,-33.86
9MHz,Limit-13,Margin[20.869])-PASS-see
graph



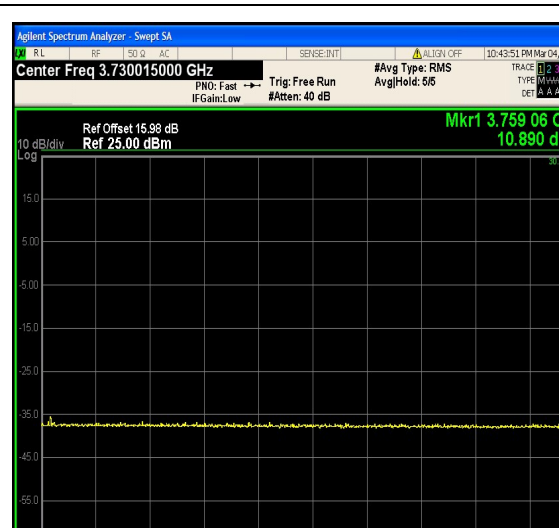
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB
_Right-1@272-3985~3990(3986.43MHz,-33.
89MHz,Limit-13,Margin[20.89])-PASS-see
graph



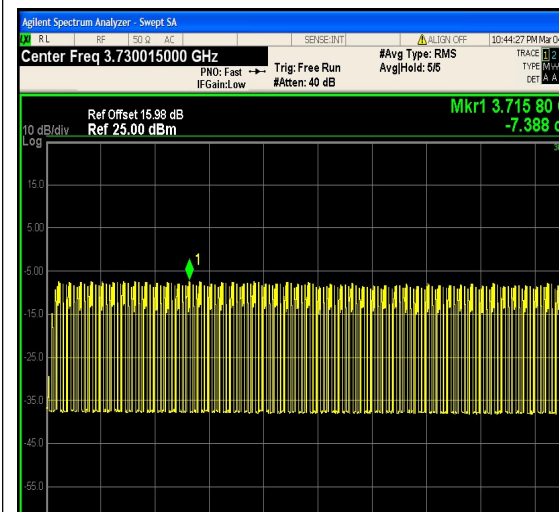
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+H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-
273@0-3985~3990(3985.74MHz,-33.801MH
z,Limit-13,Margin[20.801])-PASS-see graph



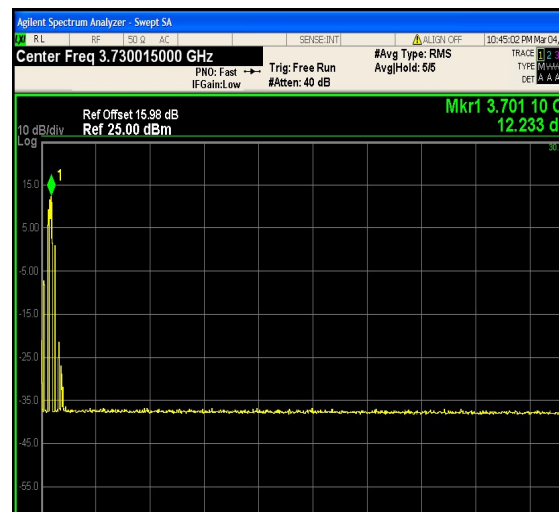
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Left-1@0-3700.015~3760.015(3701.095MHz,
11.345MHz,Limit30,Margin[18.655])-PASS-se
e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
Right-1@161-3700.015~3760.015(3759.055
MHz,10.89MHz,Limit30,Margin[19.11])-PASS
-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-1
62@0-3700.015~3760.015(3715.795MHz,-7.
388MHz,Limit30,Margin[37.388])-PASS-see
graph



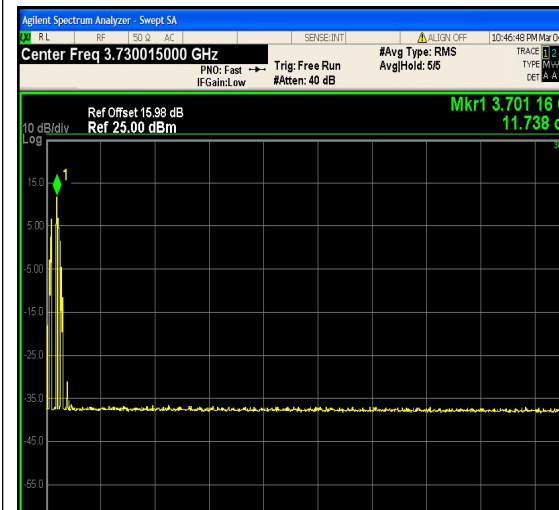
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Left
-1@0-3700.015~3760.015(3701.095MHz,12.
233MHz,Limit30,Margin[17.767])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
ht-1@161-3700.015~3760.015(3759.055MHz
,11.914MHz,Limit30,Margin[18.086])-PASS-s
ee graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
@0-3700.015~3760.015(3714.835MHz,-7.03
2MHz,Limit30,Margin[37.032])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
eft-1@0-3700.015~3760.015(3701.155MHz,1
1.738MHz,Limit30,Margin[18.262])-PASS-see
graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_Ri
ght-1@161-3700.015~3760.015(3758.995M
Hz,12.513MHz,Limit30,Margin[17.487])-PAS
S-see graph