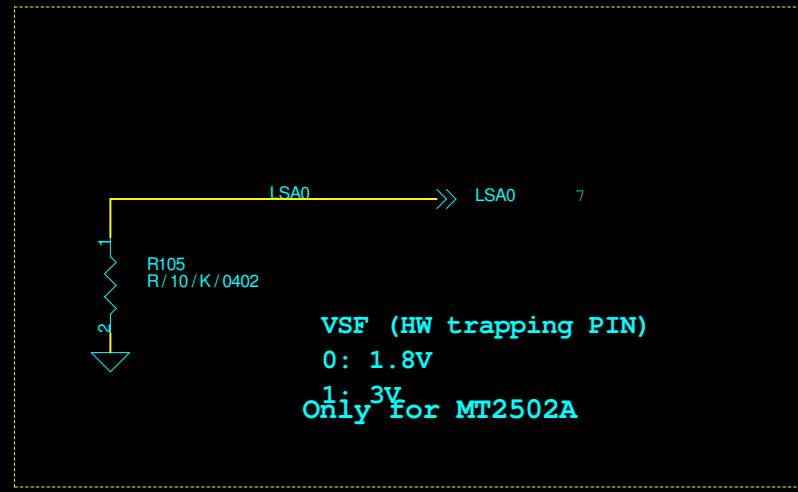
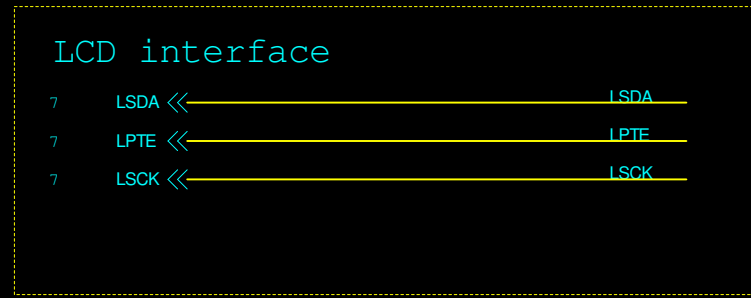
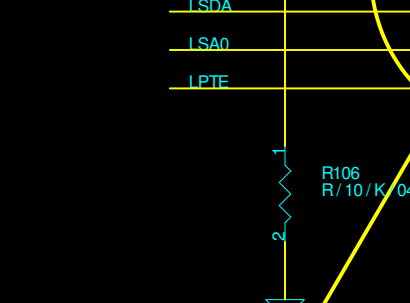


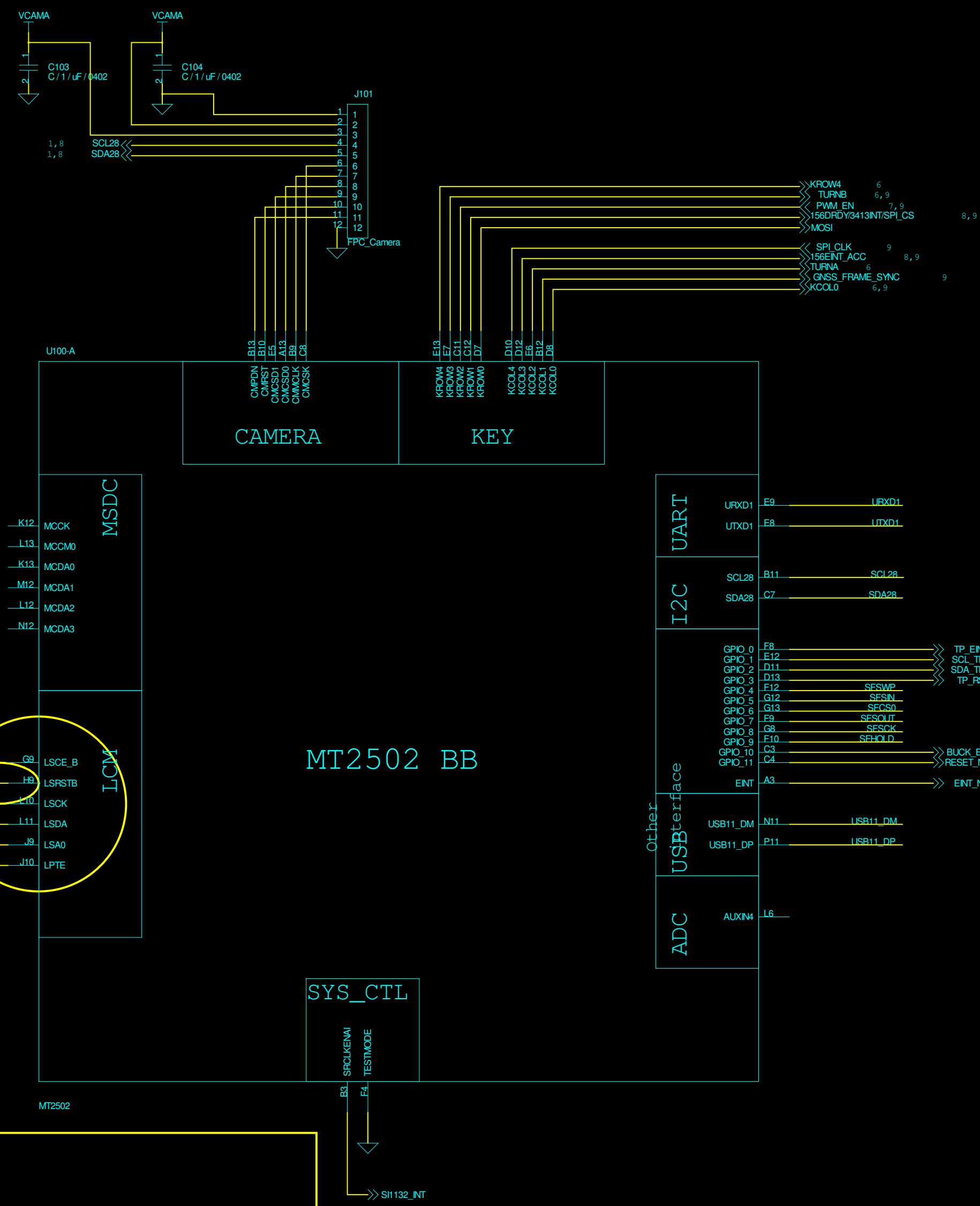


LSRSTB only for test mode,
can't use for any part

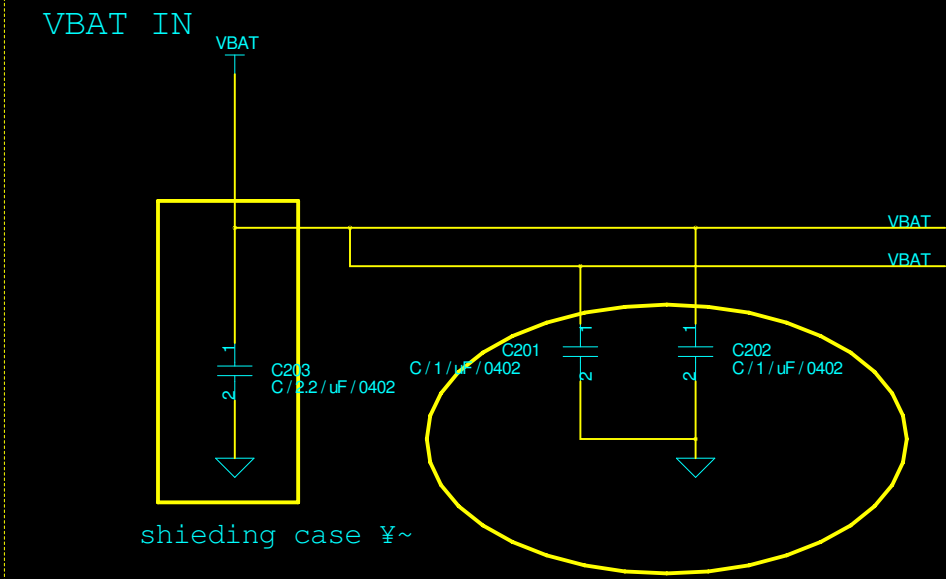
LSCK can't have any pull up
circuit



Lcm I/O power domain is VI018 ,In sleep mode ,VI018 will
shut off, if use LCM I/O for external device, please make
sure , in sleep mode with shut off VI018 ,the device is no
leakage , and once again wake up system , can normal
work .If have any question,please call MTK



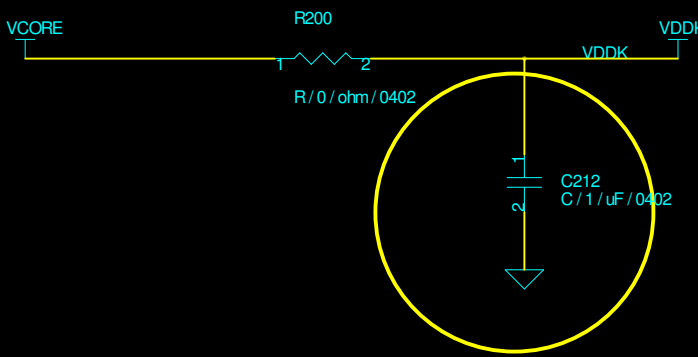
Audio



close to MT2502 each power in ball (M10,E1)

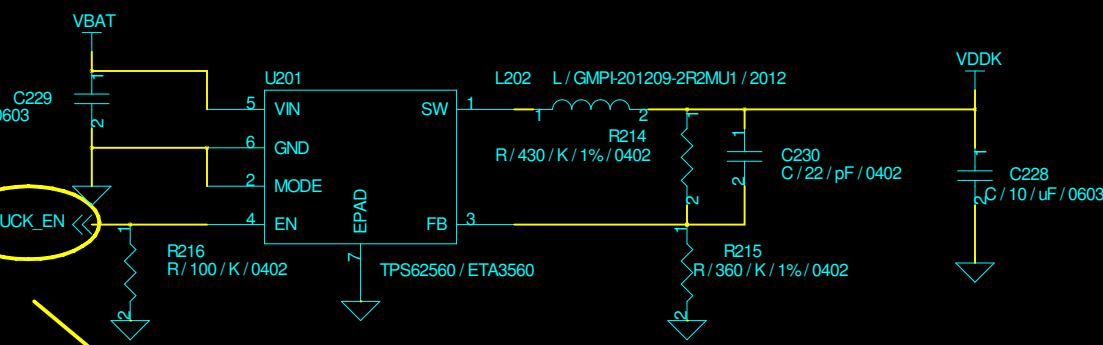
POWER IN

With or without use of the external buckf
R200 must SMT



close to MT2502 VDDK Pin (A11)

DC-DC Backup



Material Determination of single BUCK1*		
PART NUMBER:	PACKAGE:	Notes:
ETA3406		
ETA3415	ETA3406/08MT8B518 Pin to Pin	* Design native external buck part

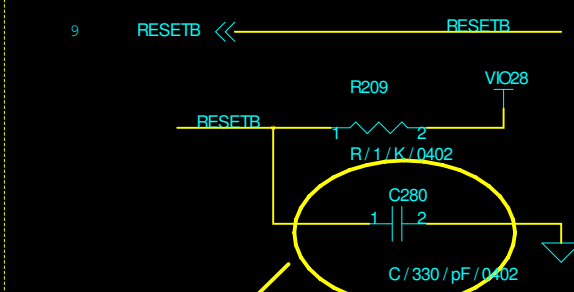
RTC_XOSC32_ENB

XTAL_SEL: VRTC domain
1: f32k_ck src INT clock source
0: f32k_ck src EXT clock source

LCD IF:

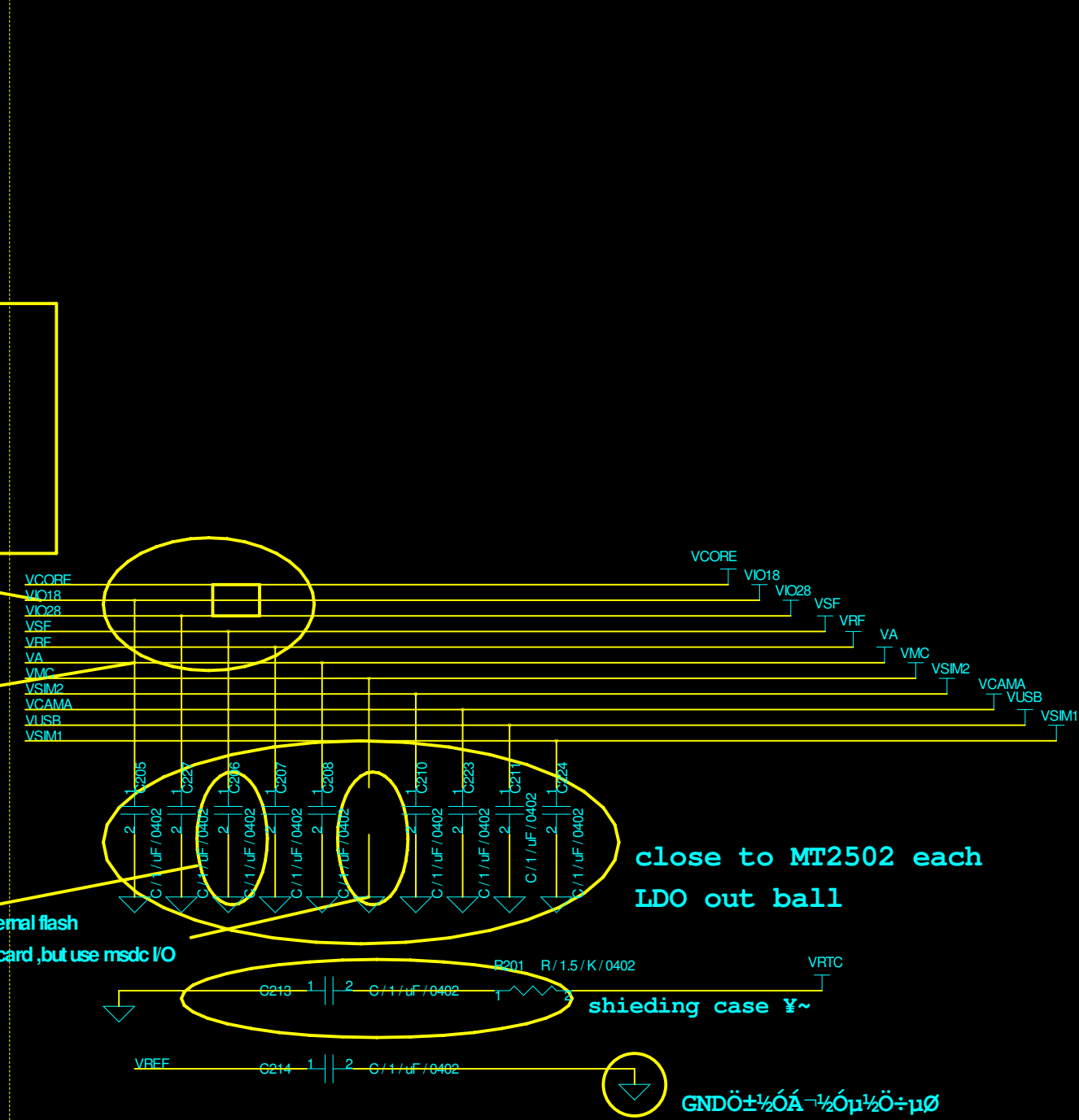


SYSTEM CONTROL



This CAP(330pF) is optional for better ESD performance
This Cap should be placed near BB

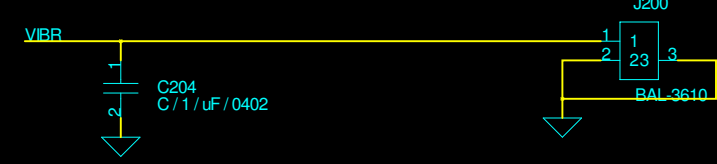
PMU LDO



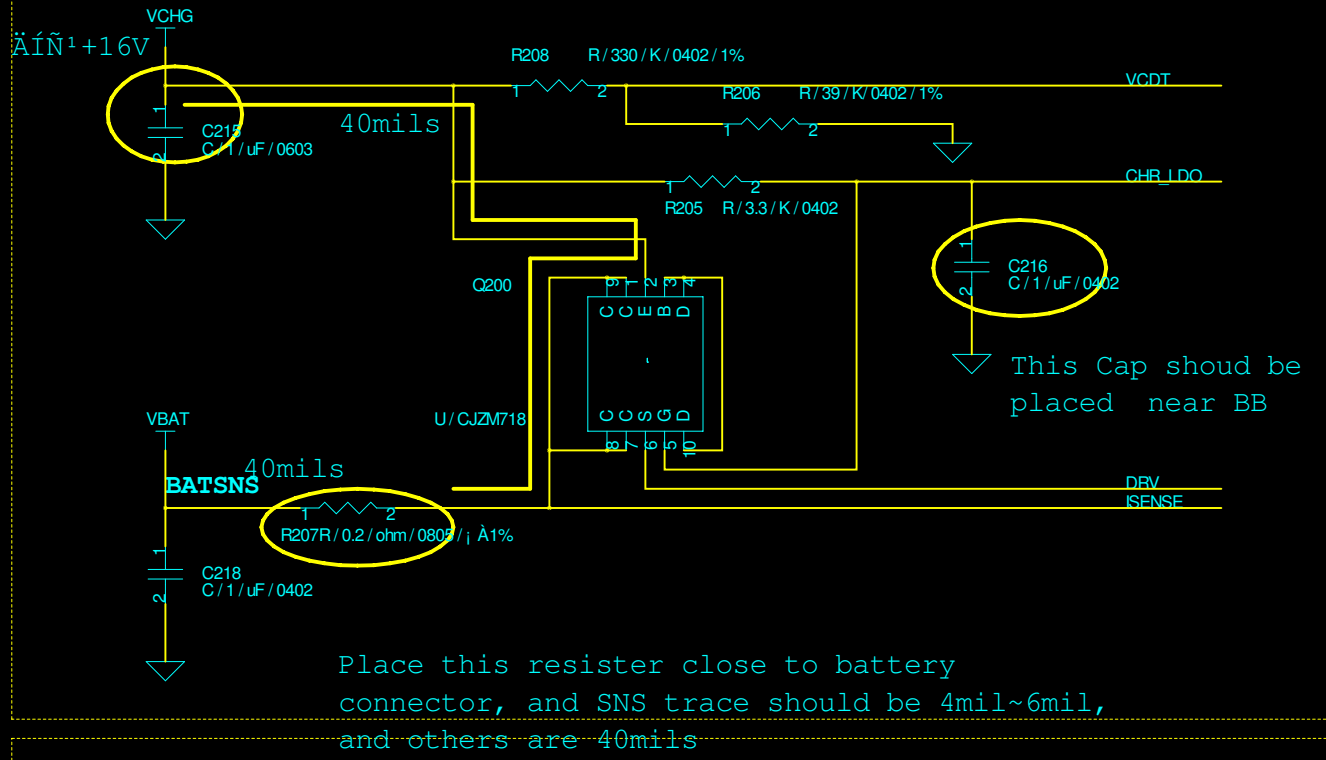
C214 close to MT2502

LDO out ball

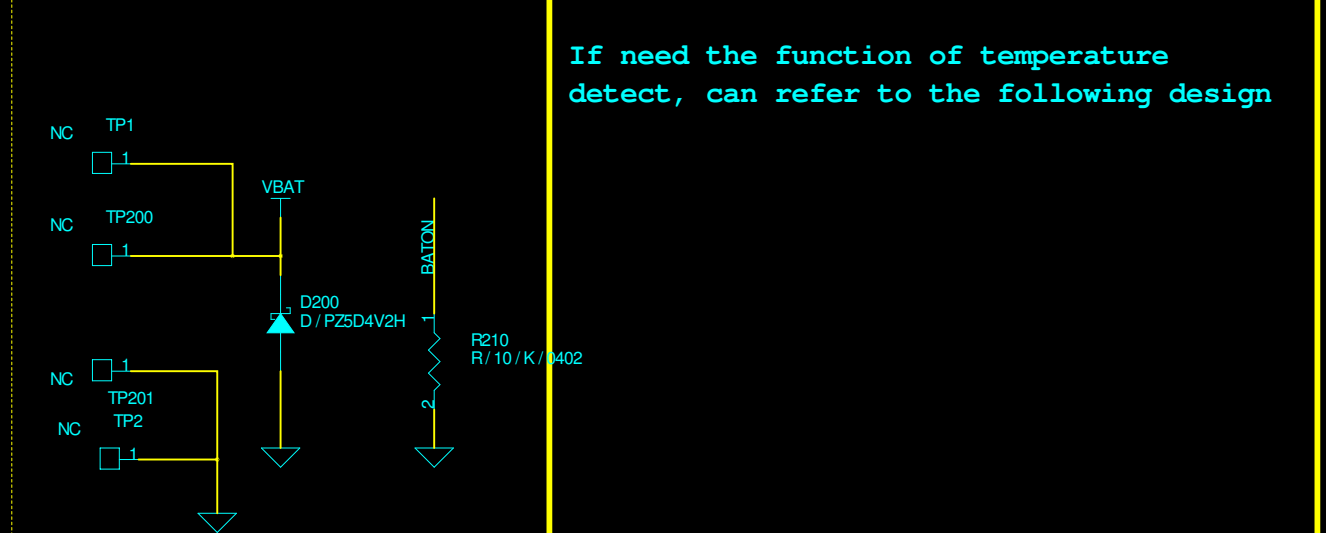
Vibrator

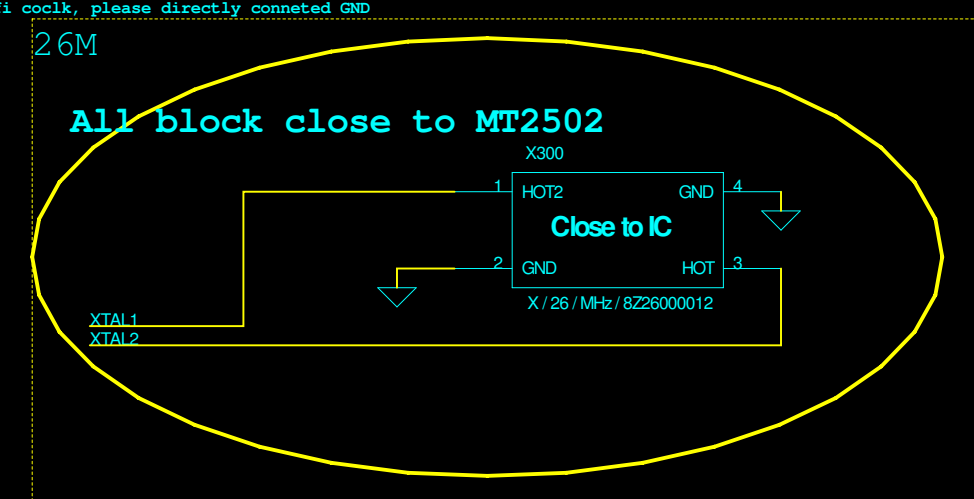


Charge

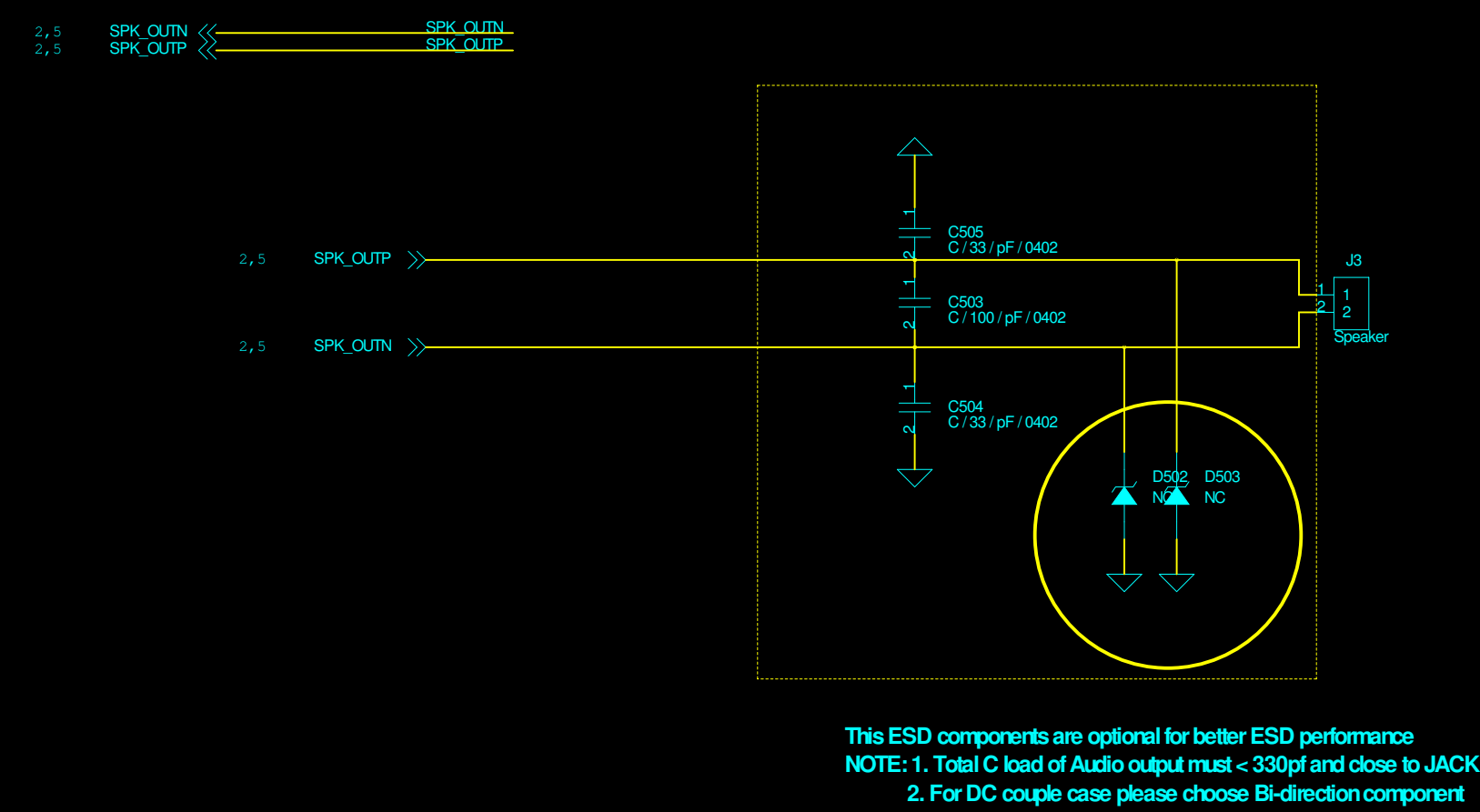


Battery connector

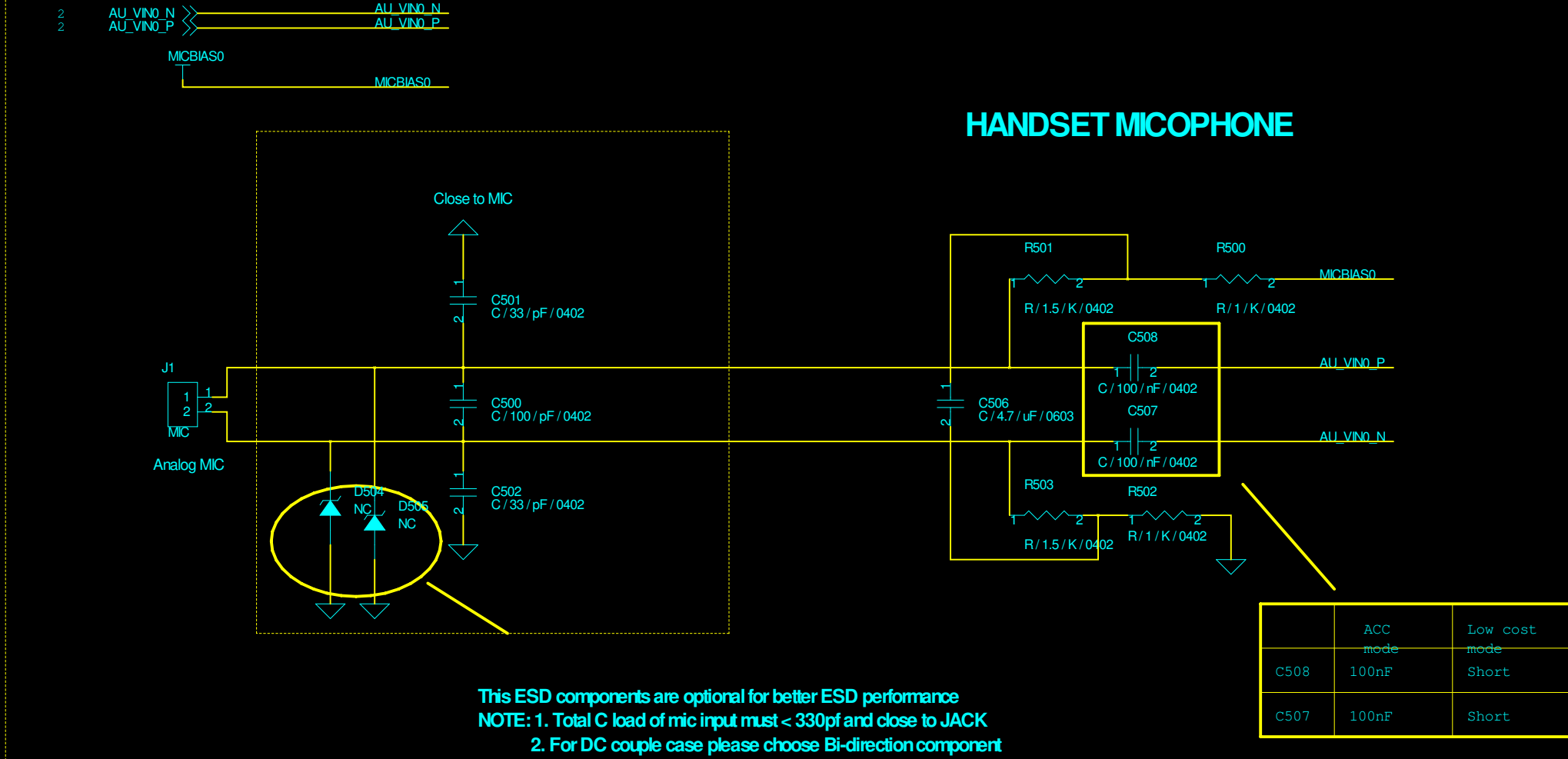




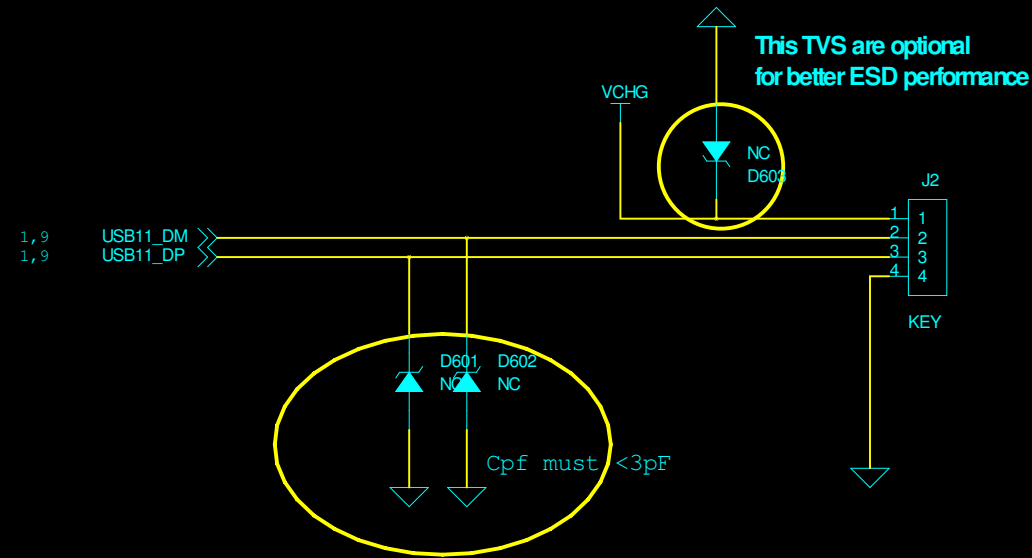
Speaker



MIC

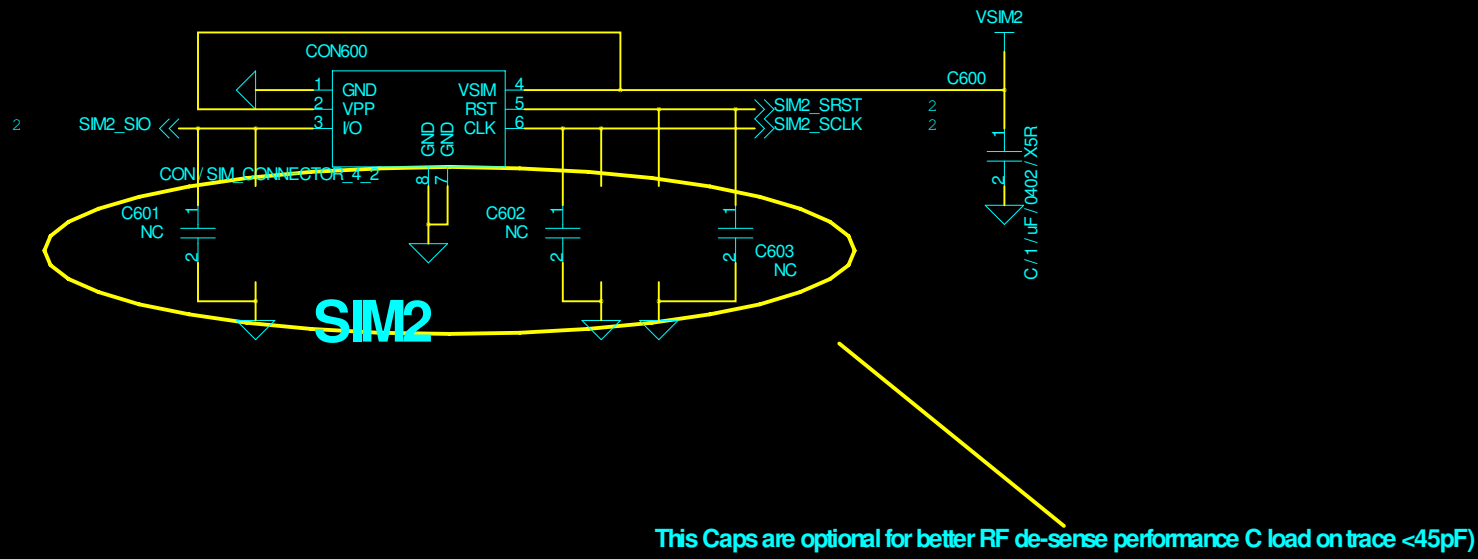


USB



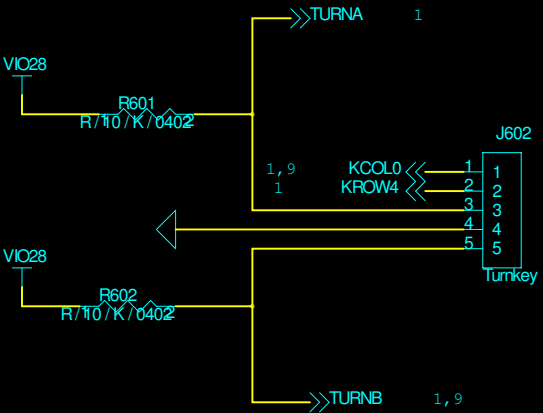
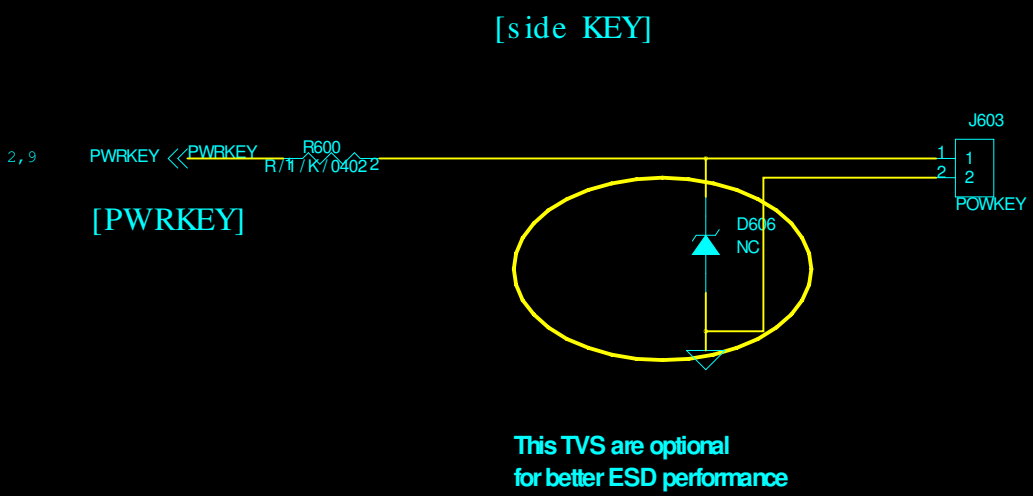
This ESD components are optional for better ESD performance
NOTE: 1. C load must < 3pf and close to JACK

SIM

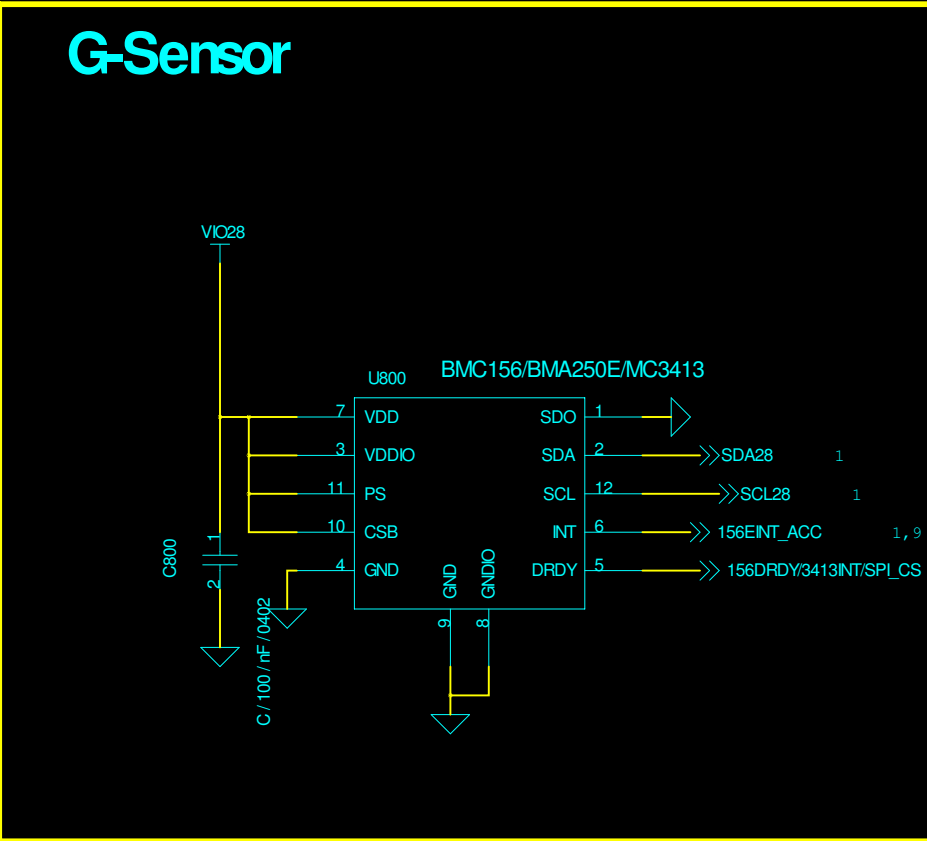


This Caps are optional for better RF de-sense performance C load on trace <45pF

KEY



[illegible][illegible][illegible]



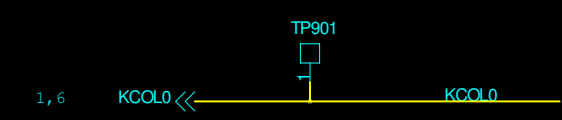
The diagram illustrates the pin connections for the ATmega328P microcontroller. The chip is mounted on a breadboard. The following connections are shown:

- TP903** and **TP911** are connected to the **LUTXD1** and **LUKXD1** pins of the ATmega328P.
- TP912** is connected to the **VBAT** pin of the ATmega328P.
- TP900** and **TP902** are connected to the **PWIRKEY** pin of the ATmega328P.
- A **2x6** header is connected to the **PWIRKEY** pin.

TP913 TP914

1,6 USB11_DM USB11_DM

1,6 USB11_DP USB11_DP



JTAG trap
 00:X(BPI_BUS1,BPI_BUS2)
 01:KEYPAD(BPI_BUS1,BPI_BUS2)
 10:Reserve
 11:CAM(BPI_BUS1,BPI_BUS2)

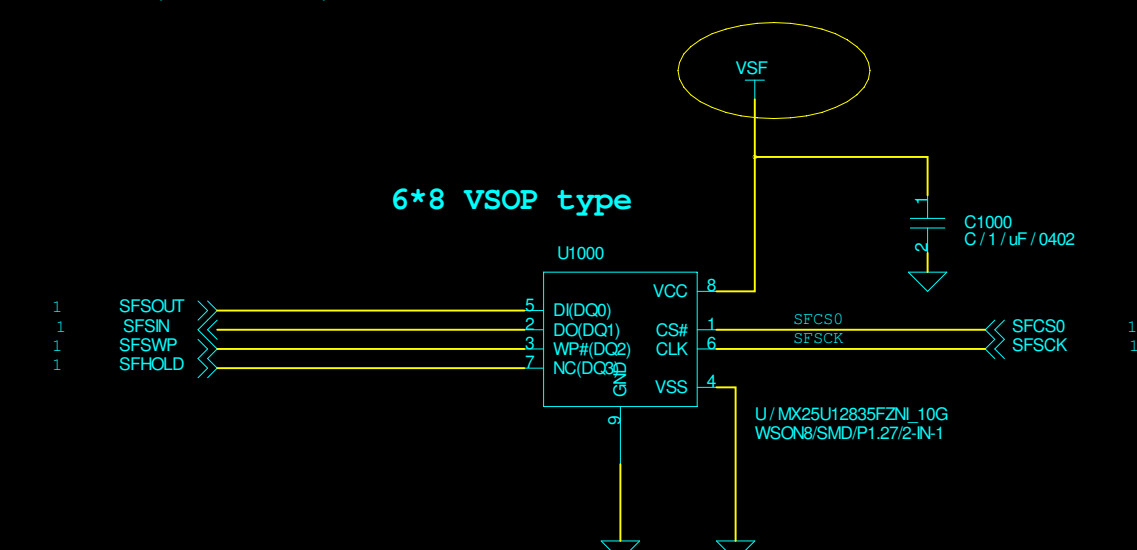
MT2502 JTAG MUX Keypad / CAM by HW trapping , Full high use 10K , pull down use 10K

JTAG Pin mux with keypad , Please reserve test point on related GPIO / Keypad interface

Serial Flash

Shielding case all sheet

MXIC MX25U12835FZNI-10G,
1.8V,128Mb,WSO6 6*8:



```
SHOLD : SIO3
SWP   : SIO2
SOUT  : SQI_SI : DQ0
SIN   : SQI_SO : DQ1
```

Option only for MT2502A