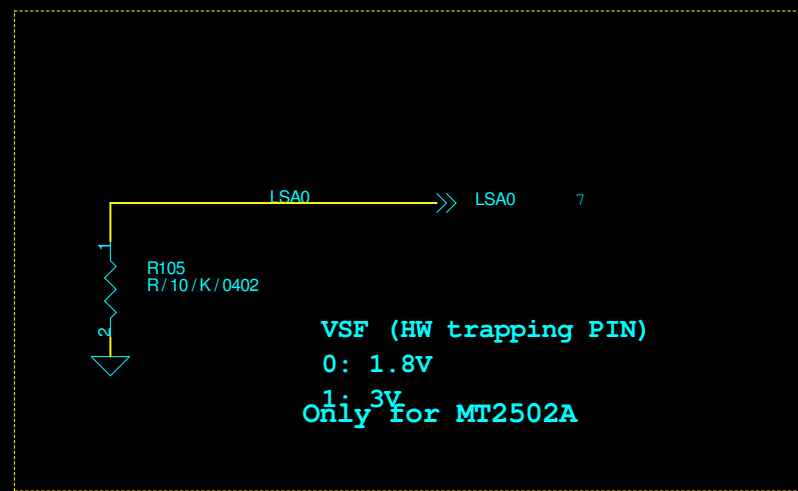
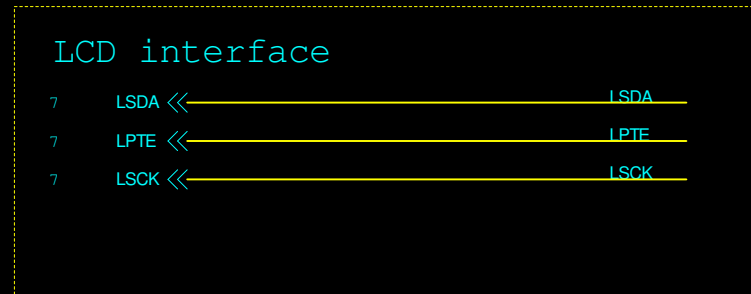
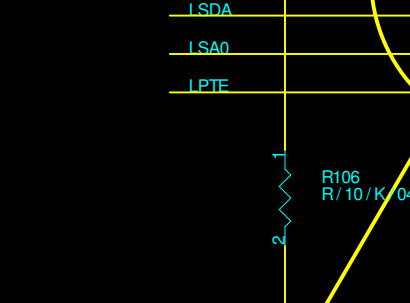


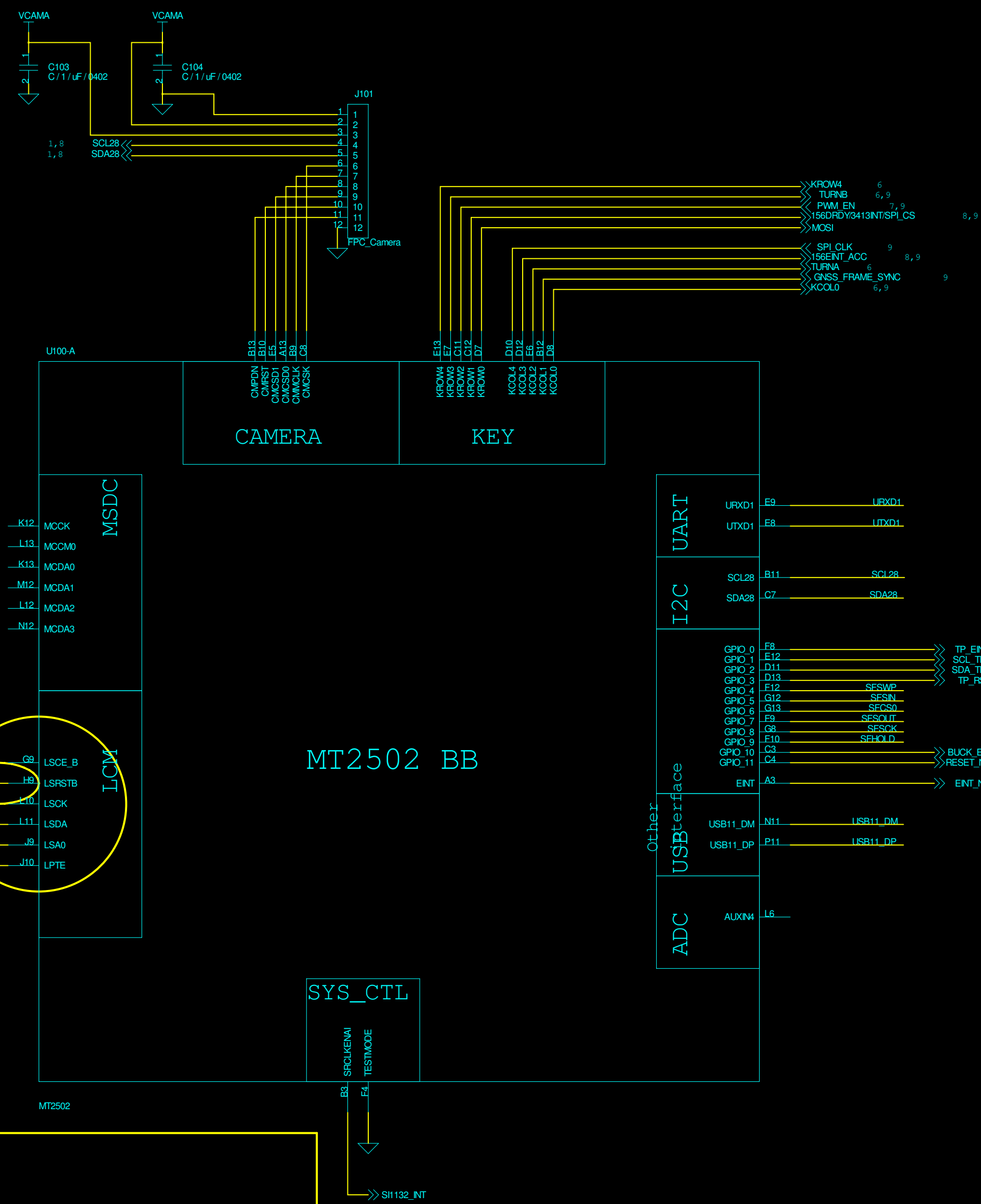


LSRSTB only for test mode ,
can't use for any part

LSCK can't have any pull up
circuit



Lcm I/O power domain is VI018 ,In sleep mode ,VI018 will
shut off, if use LCM I/O for external device, please make
sure , in sleep mode with shut off VI018 ,the device is no
leakage , and once again wake up system , can normal
work .If have any question,please call MTK

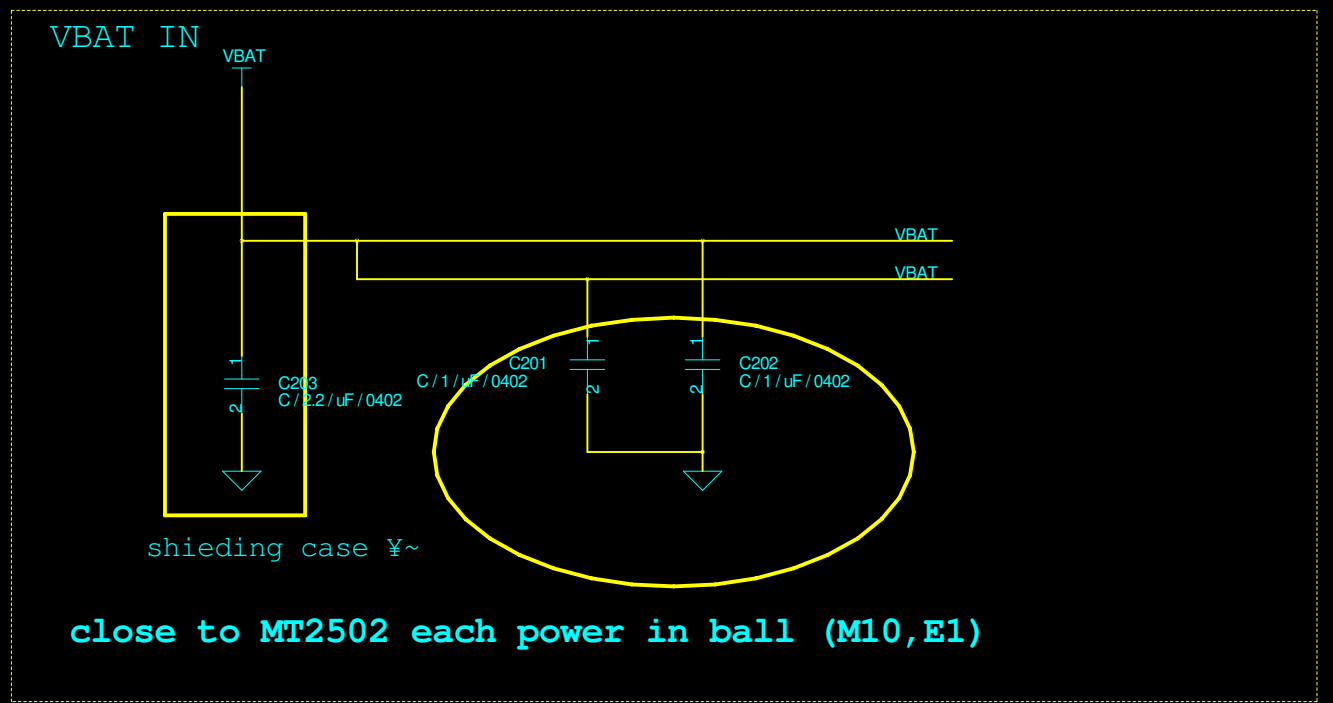


Audio

The diagram illustrates the audio interface connections between a processor and an external audio device. The processor side (left) includes:

- SPK_OUTN** and **SPK_OUTP**: Speaker output pins, connected to the external **SPK_OUTN** and **SPK_OUTP** pins via a double-line bus.
- AU_VMD_N** and **AU_VMD_P**: Audio VMD pins, connected to the external **AU_VMD_N** and **AU_VMD_P** pins via a double-line bus.
- MCBIAS0**: A microphone bias pin, connected to the external **MCBIAS0** pin via a single line.

The external audio device (right) has corresponding pins: **SPK_OUTN**, **SPK_OUTP**, **AU_VMD_N**, **AU_VMD_P**, and **MCBIAS0**.

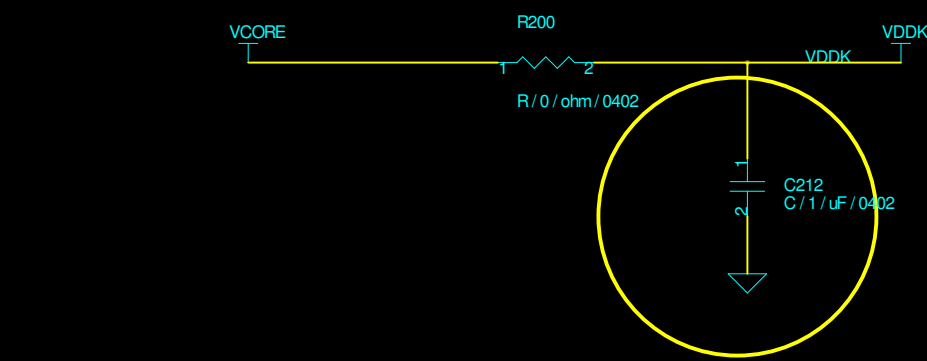


POWER IN

With or without use of the external buckf

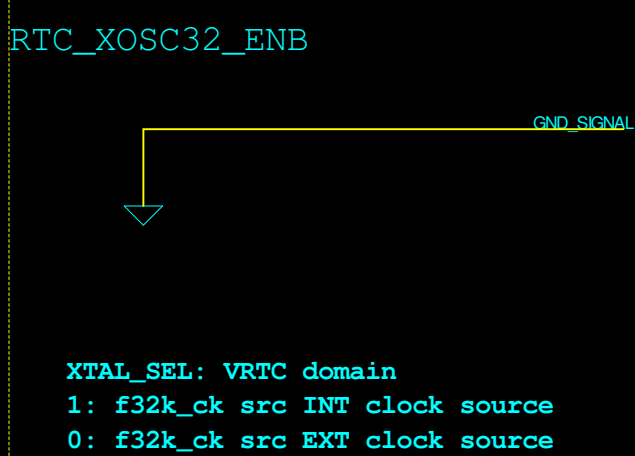
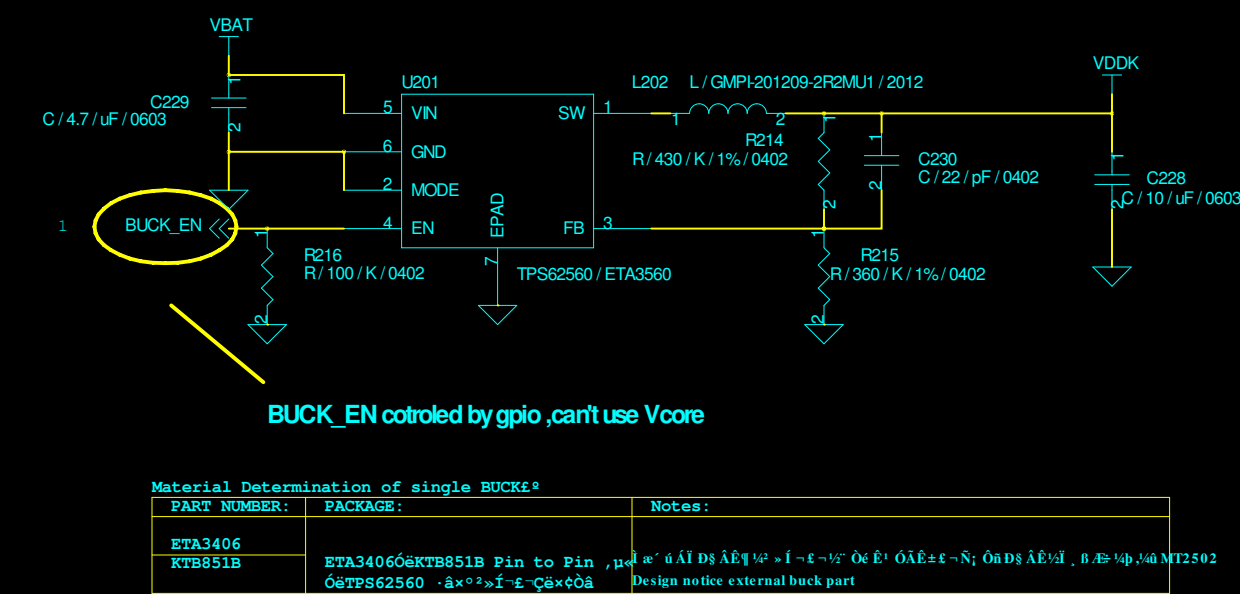
R200 must SMT

close to MT2502 VDDK Pin (A11)



BUCK_EN controlled by gpio, can't use Vcore

Material Determination of single BUCK#		
PART NUMBER:	PACKAGE:	Notes:
ETA3406		
ETB351B	ETA3406&ETB351B Pin to Pin QWTP62560 .As 0754-4 Q&X084	Design notice external back part



SYSTEM CONTROL

3

RESETB

R209

VCC28

RESETB

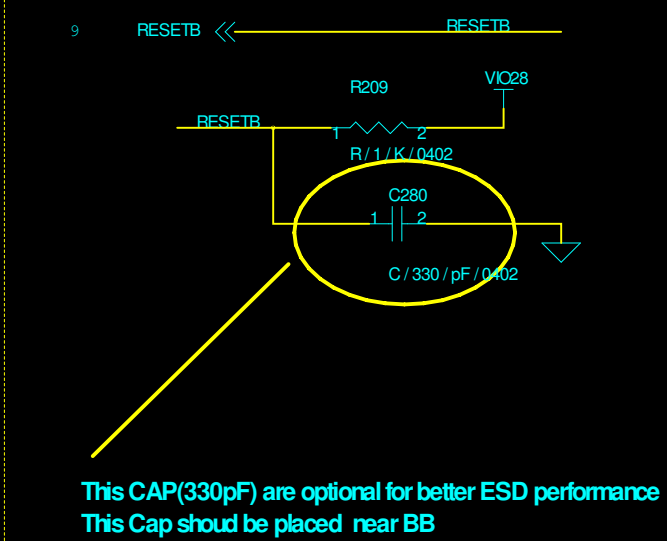
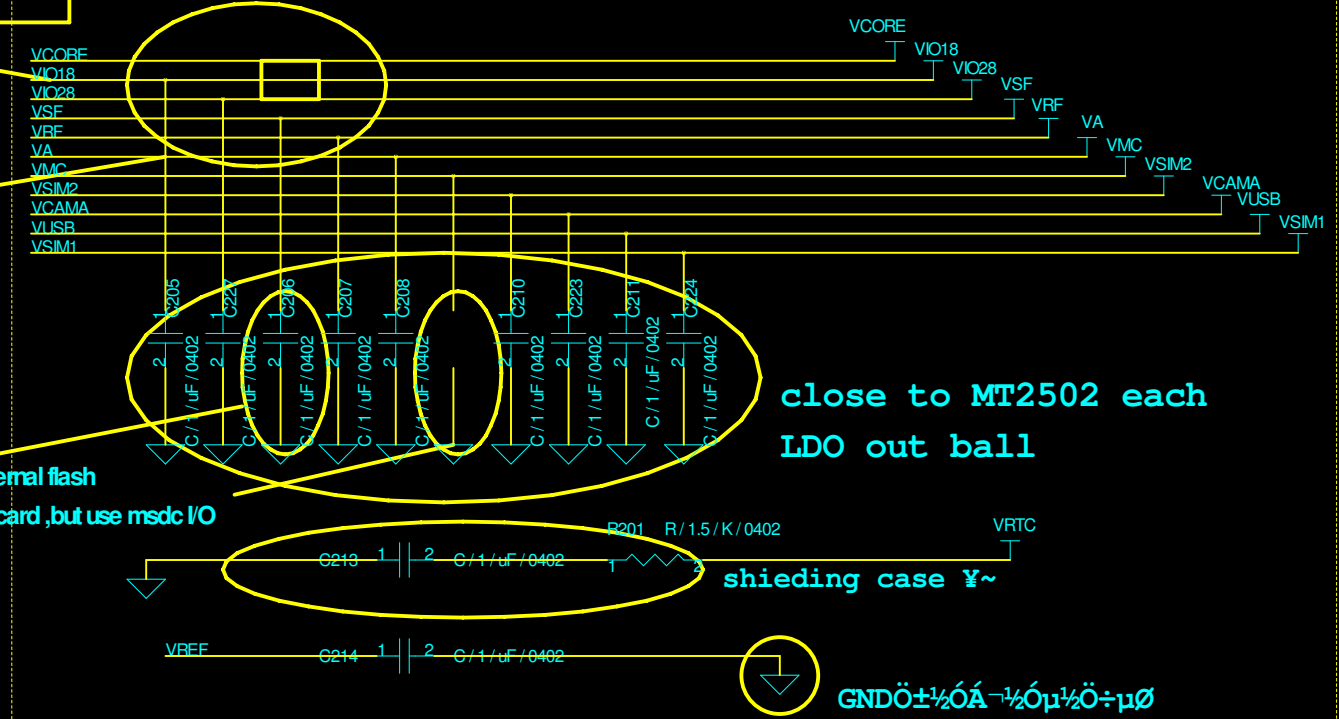
R1 1K/10k

C330

1 2

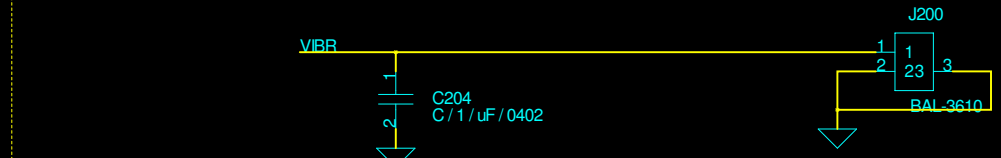
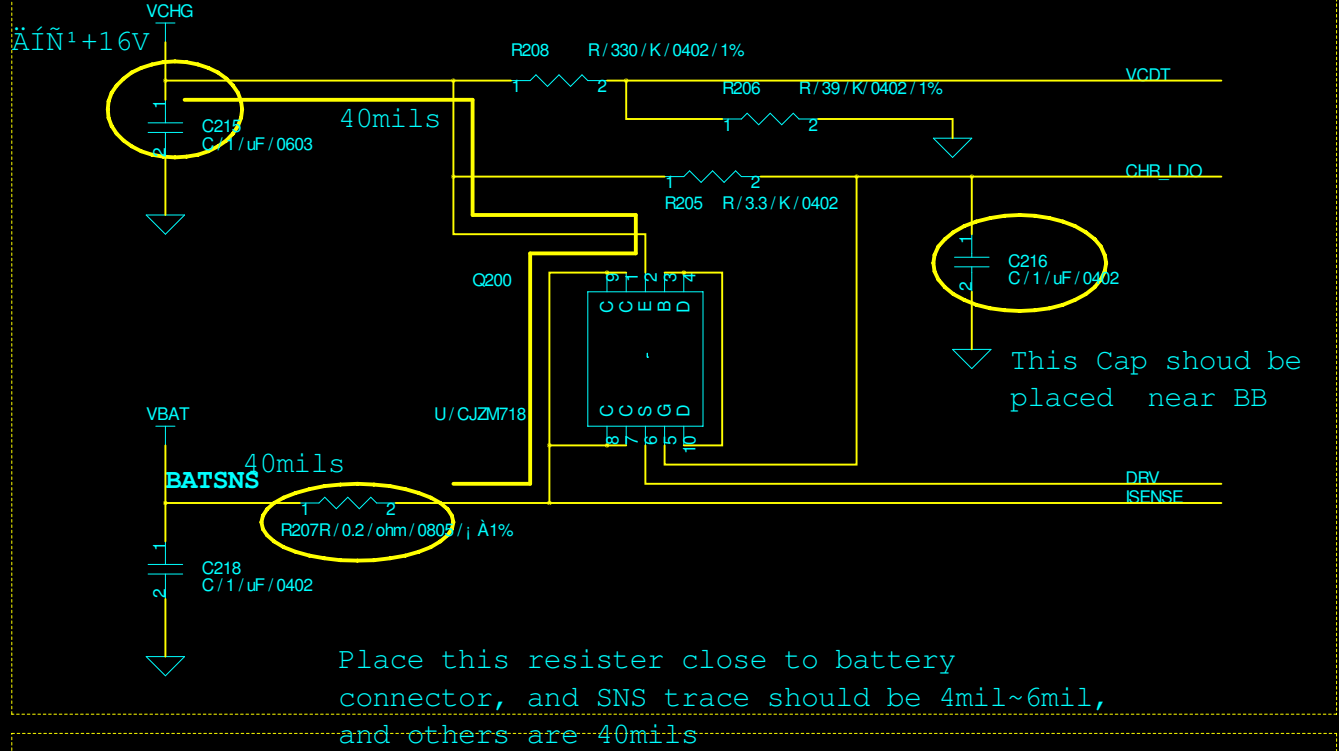
C/330/pF/10nF

This CAP(330pF) are optional for better ESD performance
This Cap should be placed near BB

[illegible]

Vibrator

Diagram of a vibrator circuit. The circuit includes a yellow line labeled VBR, a capacitor C204 (0.1 uF/0402), and a transformer BAL-3610. The VBR line is connected to the capacitor and the transformer. The transformer has three pins: pin 1 is connected to the VBR line, pin 2 is connected to ground, and pin 3 is connected to a component labeled J200.

[illegible]

Battery connector

If need the function of temperature detect, can refer to the following design

NC TP1

NC TP200

NC TP201

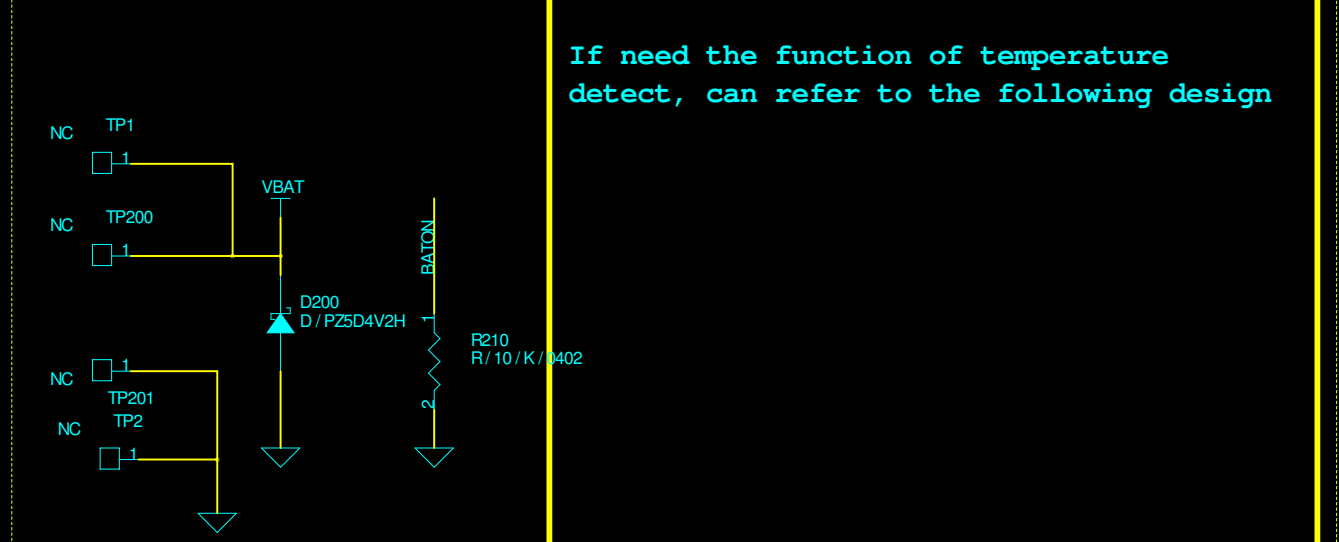
NC TP2

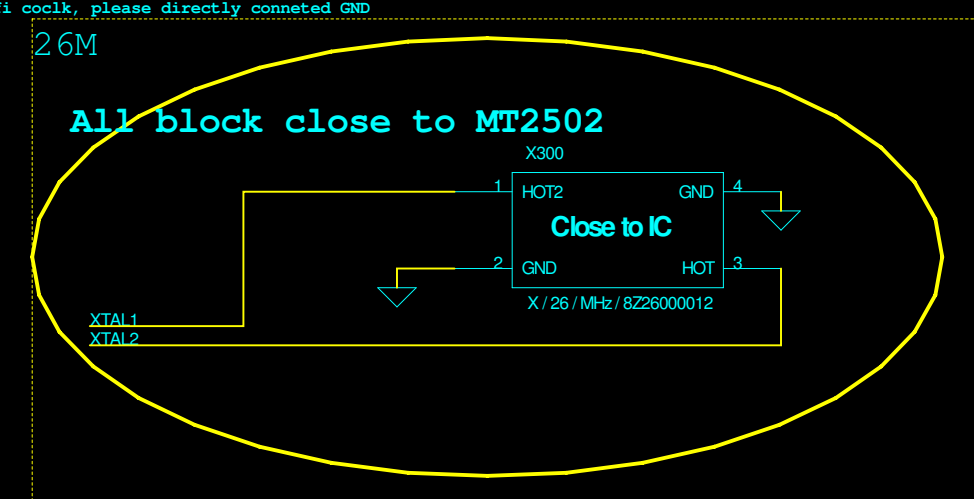
VBAT

D200
D/P2SD4V2H

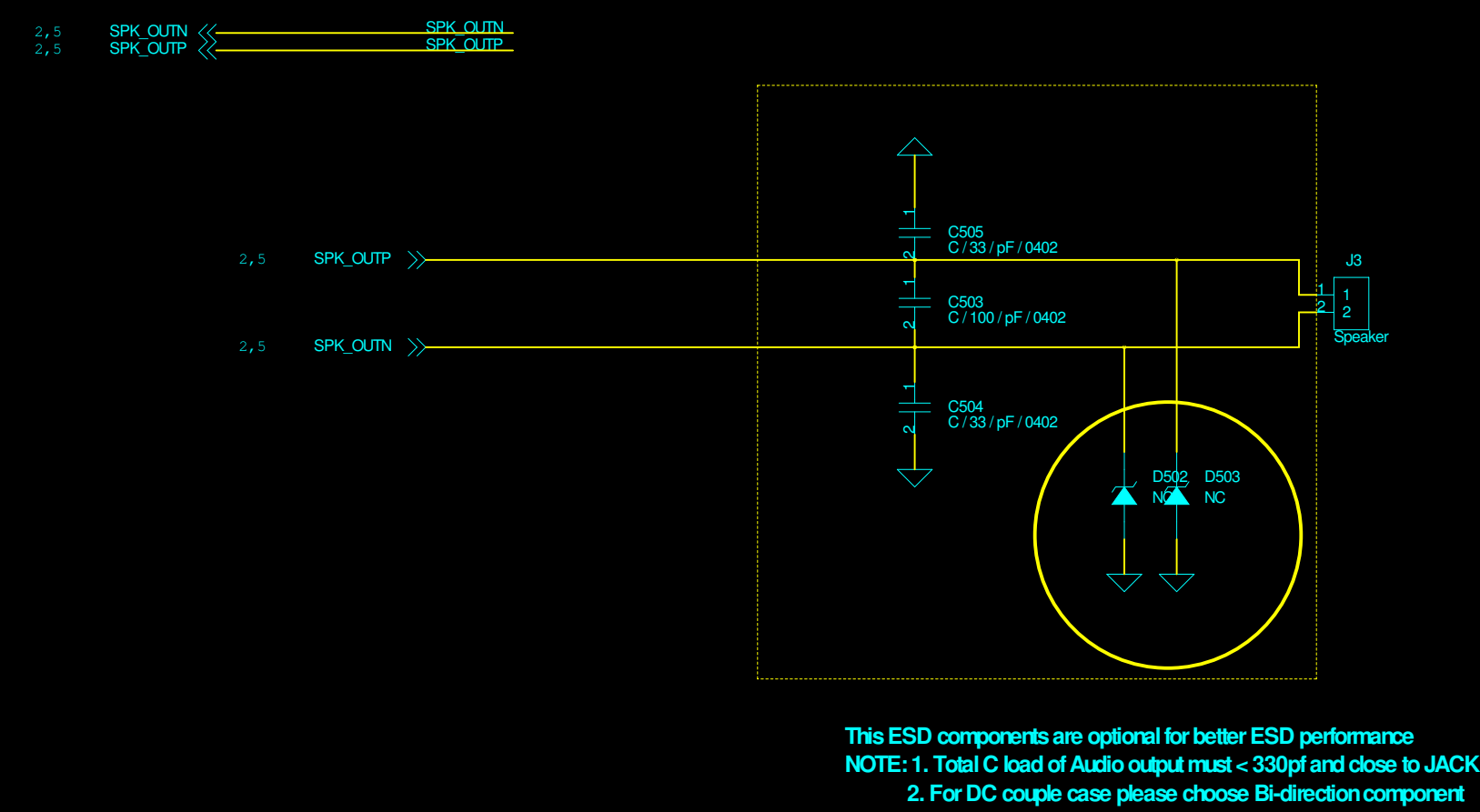
BATON

R210
R/10/K/1402

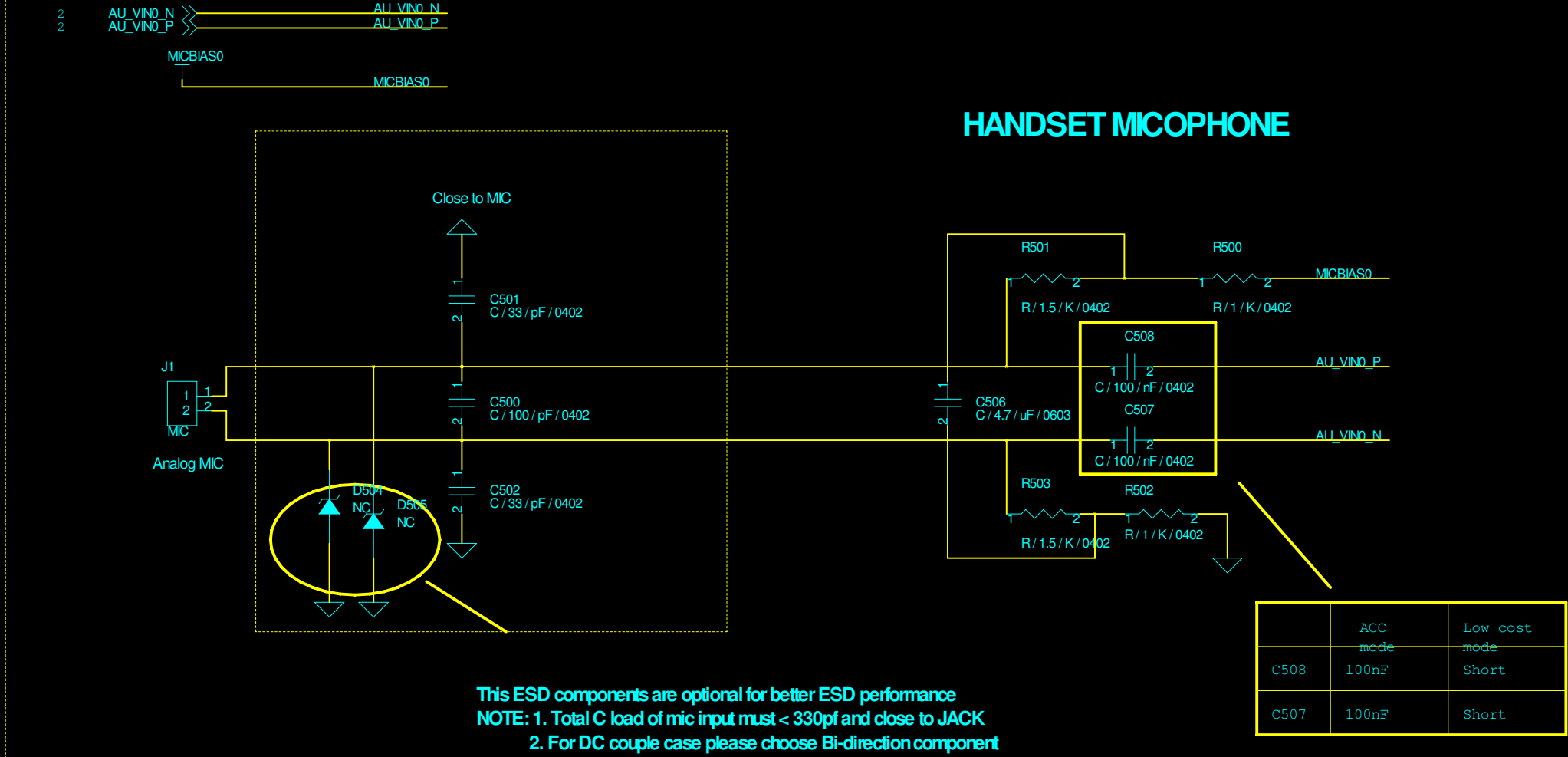




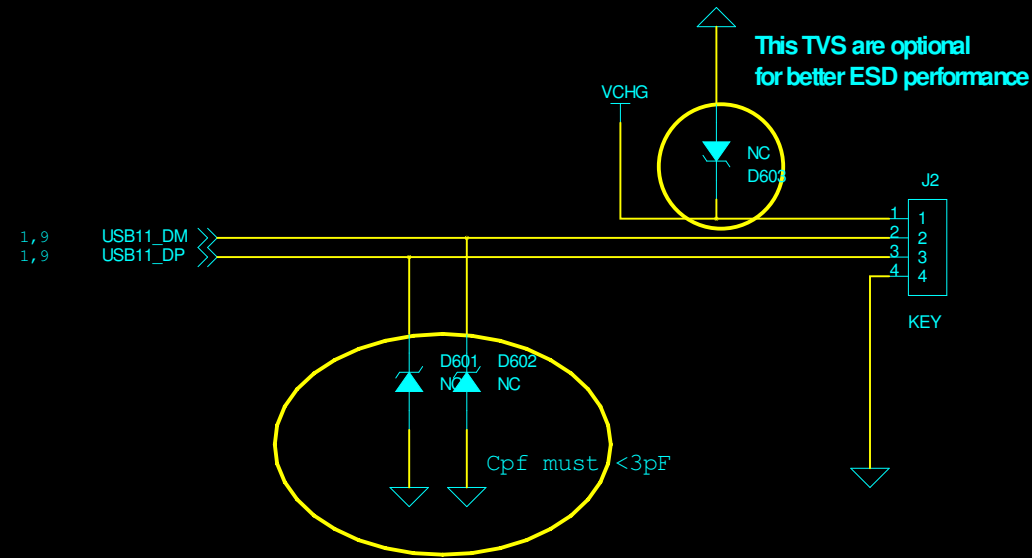
Speaker



MIC

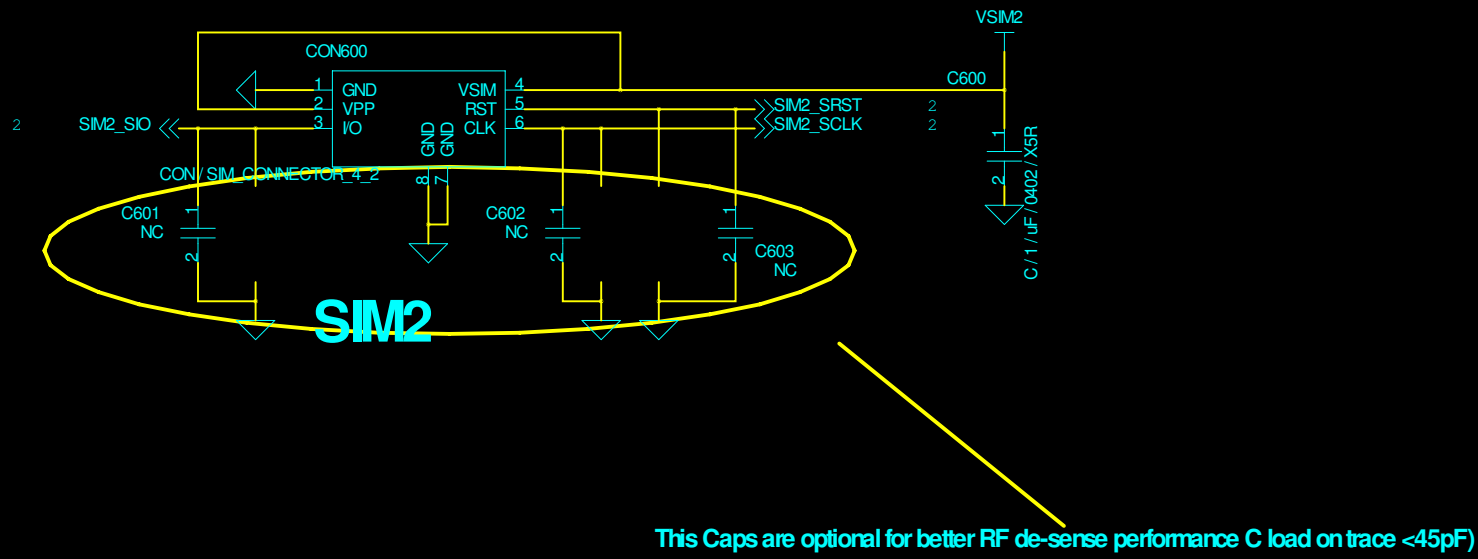


USB

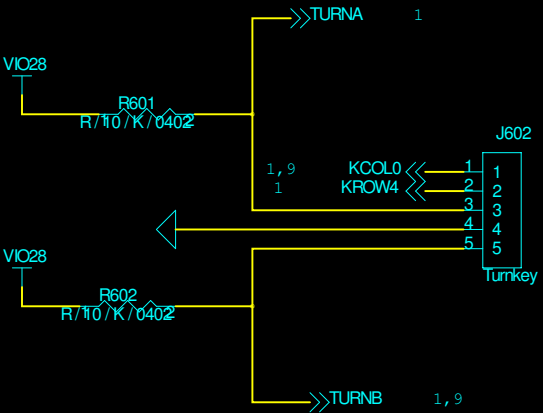
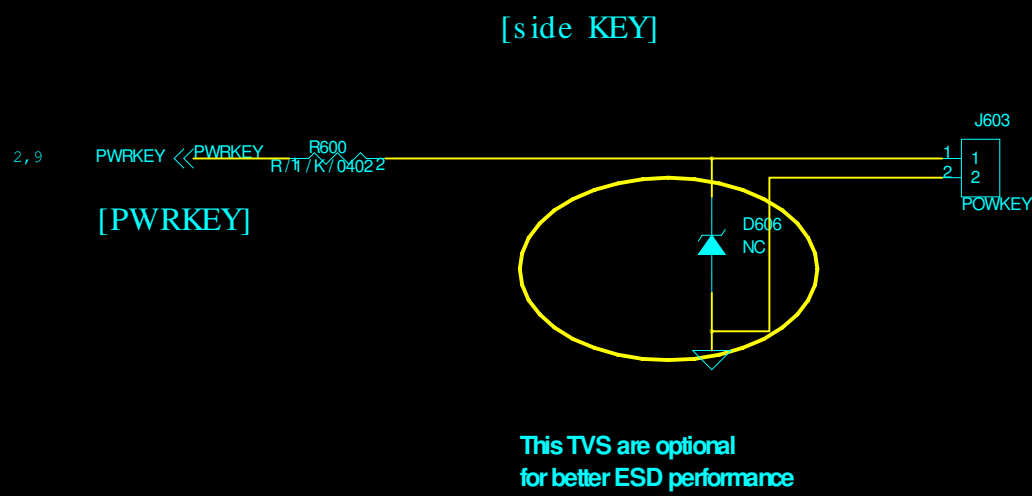


This ESD components are optional for better ESD performance
NOTE: 1. C load must < 3pf and close to JACK

SIM



KEY



[illegible][illegible]

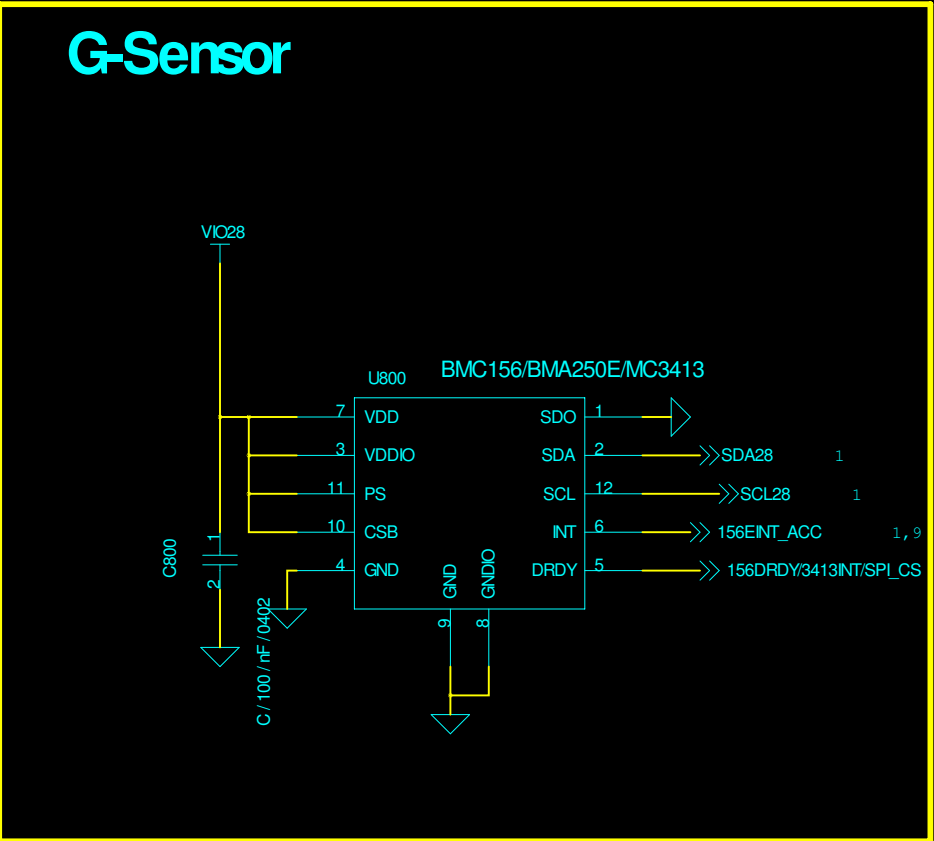


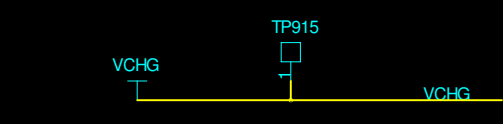
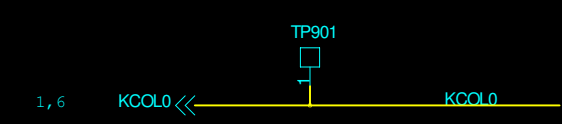
Figure 10 illustrates the pin connections for the ATmega328P microcontroller. The chip is shown with its pins numbered 1 through 28. The connections are as follows:

- TP903** is connected to **LUTXD1**.
- TP911** is connected to **LRXD1**.
- TP912** is connected to **VBAT**.
- TP900** and **TP902** are connected to **PWIRKEY**.

TP913 TP914

1,6 USB11_DM USB11_DM

1,6 USB11_DP USB11_DP



MK1 MK2 MK3 MK4


JTAG trap
 00:X(BPI_BUS1,BPI_BUS2)
 01:KEYPAD(BPI_BUS1,BPI_BUS2)
 10:Reserve
 11:CAM(BPI_BUS1,BPI_BUS2)

MT2502 JTAG MUX Keypad / CAM by HW trapping , Pull high use 10K , pull down use 10K

1 SPI CLK >> JTD1

1,8 158ENT_A0 >> JTAG

1 QDS_FRAME_SPI0 >> JTAG0

1,4 158ENT_A1 >> JTAG1

1,2 158ENT_A2 >> JTAG2

1,8 158RDV3413INTSP_LCS >> JTAG3

2 RESETB >> JTAG3

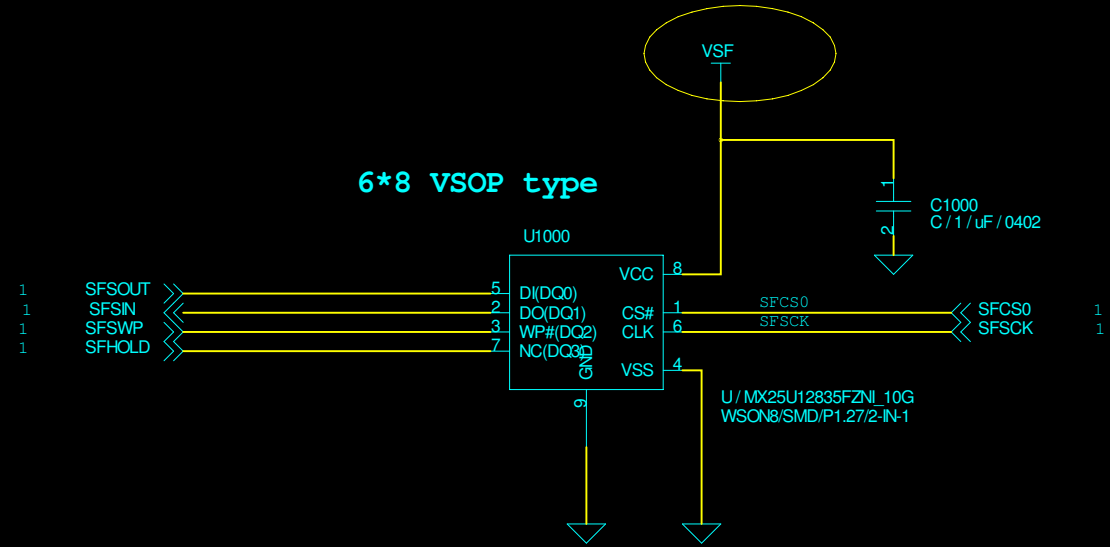
TP904 TP905 TP906 TP907 TP908 TP909 TP910

JTAG Pin mux with keypad , Please reserve test point on related GPIO / Keypad interface

Serial Flash

Shieding case all sheet

MXIC MX25U12835FZNI-10G,
1.8V,128Mb,WSON 6*8:



SHOLD : SIO3
SWP : SIO2
SOUT : SQI_SI : DQ0
SIN : SQI_SO : DQ1

Option only for MT2502A