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18.	LPDDR+EMMC			
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20.	Subboard Connector/Audio PA			
21.	MIC and Receiver			
22.	Headset			
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30.	WTR4905			

Revision History

SCHEMATIC MCN	HW VERSION	VERSION	DESCRIPTION OF CHANGE
	PRE_EVT	A	PRE_EVT Initial release

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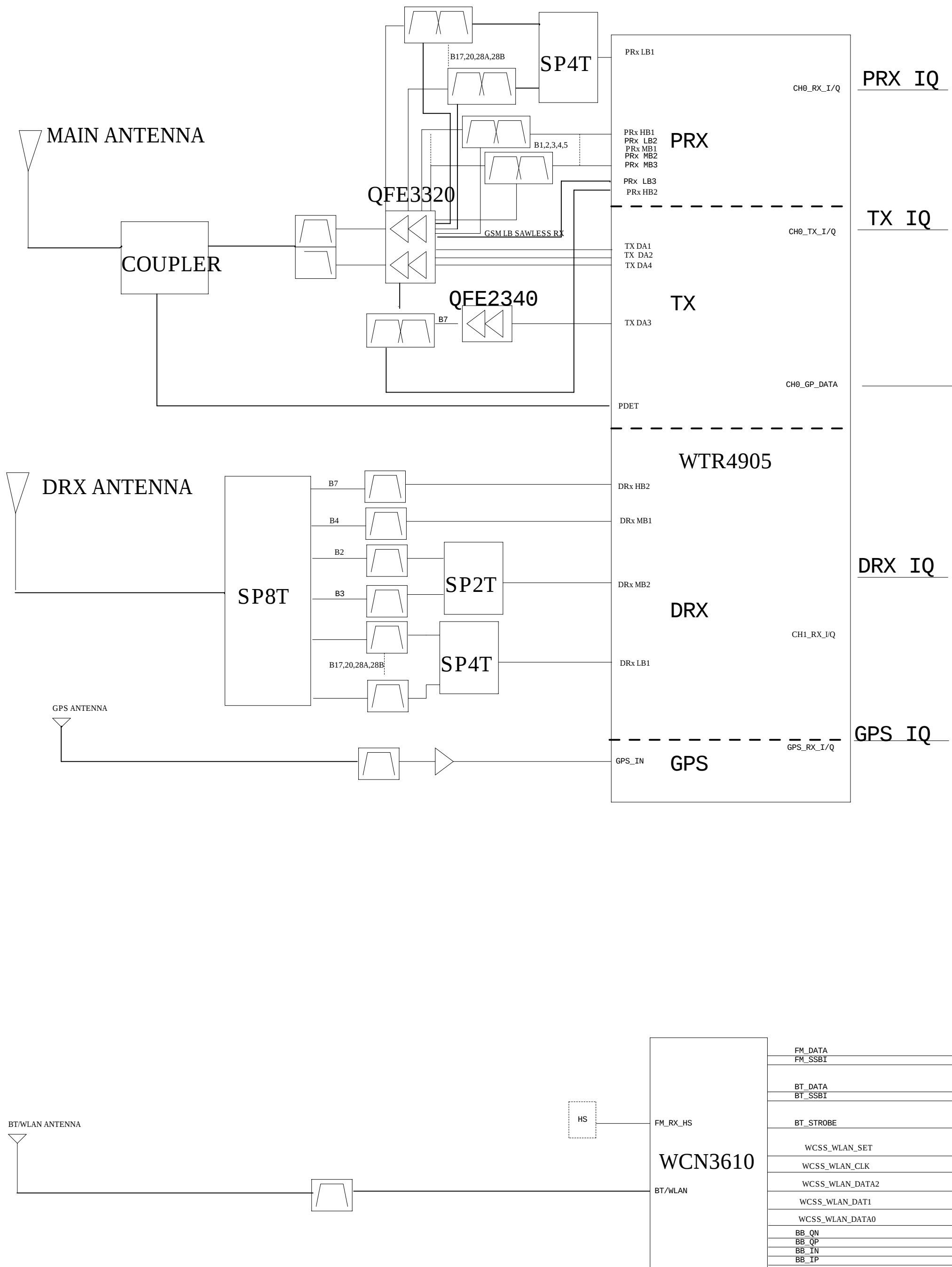
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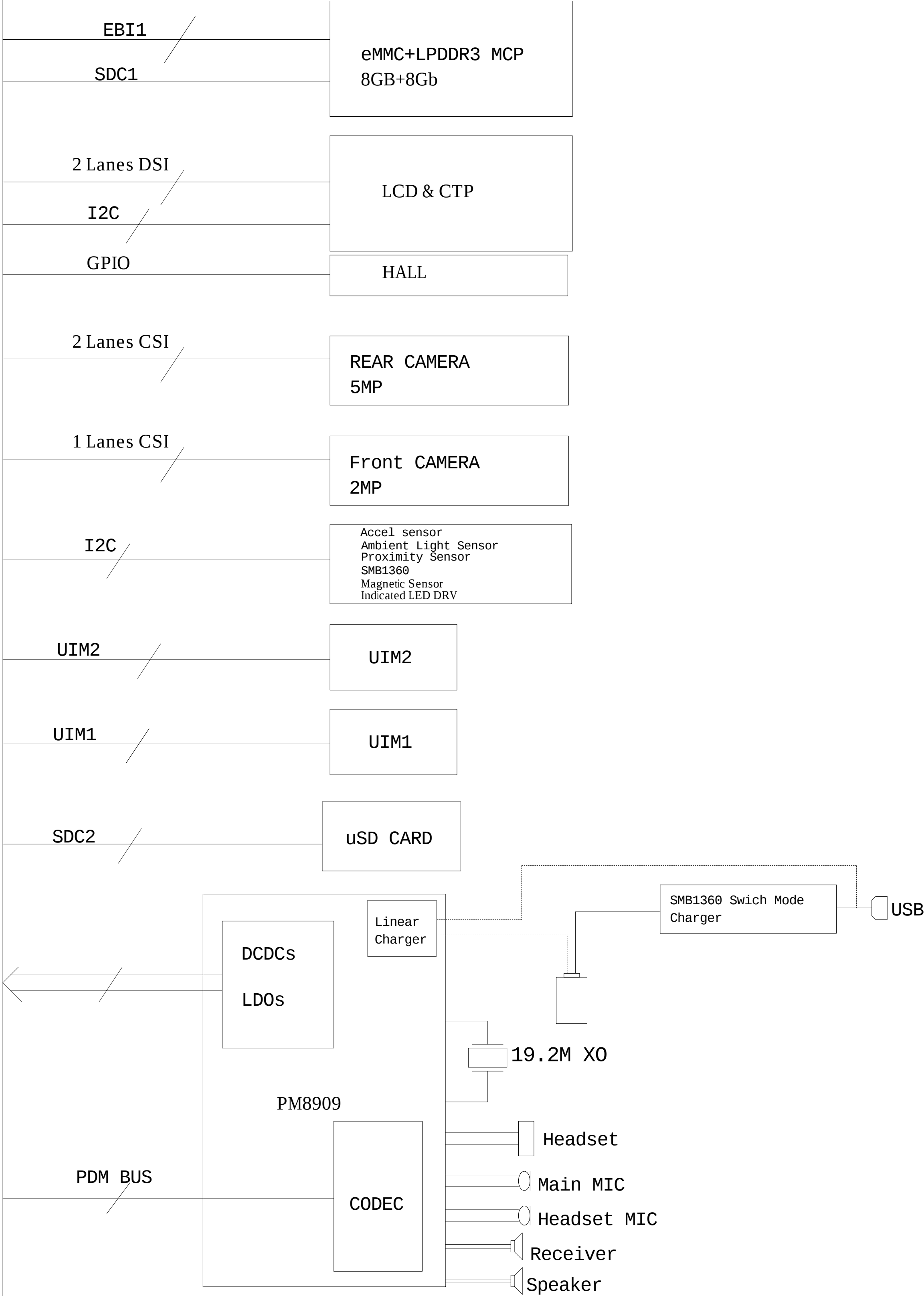
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MSM8909



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MSM8909 GPIO Configuration For CM231					
GPIO_0	LCD_LANSEL	GPIO_41	WCSS_WLAN_DATA_1	GPIO_82	PRX_SW_LB_VC1
GPIO_1	BOARD_ID2	GPIO_42	WCSS_WLAN_DATA_0	GPIO_83	DRX_ANT_SEL1
GPIO_2	BOARD_ID0	GPIO_43	WCSS_WLAN_SET	GPIO_84	EXT_GPS_LNA_EN
GPIO_3	GPIO_LED	GPIO_44	WCSS_WLAN_CLK	GPIO_85	CH0_GSM_TX_PHASE_D0
GPIO_4	UART_MSM_TX	GPIO_45	WCSS_FM_SSBI	GPIO_86	RFFE5_CLK
GPIO_5	UART_MSM_RX	GPIO_46	WCSS_FM_SDI	GPIO_87	RFFE5_DATA
GPIO_6	SENSOR_I2C_SDA	GPIO_47	WCSS_BT_CTRL	GPIO_88	NC
GPIO_7	SENSOR_I2C_SCL	GPIO_48	WCSS_BT_DAT_STB	GPIO_89	NC
GPIO_8	BOARD_ID1	GPIO_49	UIM2_DATA	GPIO_90	KEY_VOL_UP_N
GPIO_9	HAC_EN	GPIO_50	UIM2_CLK	GPIO_91	NC
GPIO_10	GPIO10_FLASH_EN1	GPIO_51	UIM2_RESET	GPIO_92	NC
GPIO_11	GPIO11_CAMERA_INT	GPIO_52	UIM2_PRESENT	GPIO_93	NC
GPIO_12	TP_RST_N	GPIO_53	UIM1_DATA	GPIO_94	ALPS_INT_N
GPIO_13	TP_INT_N	GPIO_54	UIM1_CLK	GPIO_95	MAG_RST [†] (for 2M ID)
GPIO_14	BUCK_SMB_I2C_SDA	GPIO_55	UIM1_RESET	GPIO_96	ACCL_INT1_N
GPIO_15	BUCK_SMB_I2C_SCL	GPIO_56	NC	GPIO_97	NC
GPIO_16	LCD_LDO_EN	GPIO_57	UIM_BATT_ALM	GPIO_98	EXT_AUDIO_PA_EN
GPIO_17	GPIO_KEYBL	GPIO_58	SMB_INT	GPIO_99	NC
GPIO_18	TP_I2C_SDA	GPIO_59	CDC_PDM_CLK	GPIO_100	SDC1_CLK
GPIO_19	TP_I2C_SCL	GPIO_60	CDC_PDM_SYNC	GPIO_101	SDC1_DATA7
GPIO_20	NC	GPIO_61	CDC_PDM_TX	GPIO_102	SDC1_DATA6
GPIO_21	NC	GPIO_62	CDC_PDM_RX0	GPIO_103	SDC1_DATA5
GPIO_22	BOARD_ID3	GPIO_63	CDC_PDM_RX1	GPIO_104	SDC1_DATA4
GPIO_23	USB_ID_DL9008	GPIO_64	CDC_PDM_RX2	GPIO_105	SDC1_DATA3
GPIO_24	LCD_TE0	GPIO_65	ACCL_INT2_N	GPIO_106	SDC1_DATA2
GPIO_25	LCD_RST_N	GPIO_66	RFFE1_CLK	GPIO_107	SDC1_DATA1
GPIO_26	CAM0_MCLK	GPIO_67	RFFE1_DATA	GPIO_108	SDC1_DATA0
GPIO_27	CAM1_MCLK	GPIO_68	RFFE2_CLK	GPIO_109	SDC1_CMD
GPIO_28	SCAM_RST_N	GPIO_69	RFFE2_DATA	GPIO_110	REARCAM_DVDD_EN
GPIO_29	CAM_I2C_SDA	GPIO_70	SW_B17_B28A	GPIO_111	LED_I2C_SDA
GPIO_30	CAM_I2C_SCL	GPIO_71	SW_LB_SEL0	GPIO_112	LED_I2C_SCL
GPIO_31	CAM_TORCH_MODE1	GPIO_72	SW_LB_SEL1		
GPIO_32	GPI051_FLASH_TORCH	GPIO_73	DRX_ANT_SEL2		
GPIO_33	SCAM_PWDN	GPIO_74	PRX_SW_B5B27		
GPIO_34	MCAM_PWDN	GPIO_75	PRX_SW_LB_VC2		
GPIO_35	MCAM_RST_N	GPIO_76	PRX_SW_LB_VC3		
GPIO_36	HALL_INT_N	GPIO_77	ANT_TUNER_CTL1		
GPIO_37	FORCED_USB_BOOT	GPIO_78	DRX_ANT_SEL_VC3		
GPIO_38	NC	GPIO_79	ANT_TUNER_CTL2		
GPIO_39	WCSS_BT_SSBI	GPIO_80	DRX_ANT_SEL_VC2		
GPIO_40	WCSS_WLAN_DATA_2	GPIO_81	DRX_ANT_SEL_VC1		

PM8909 GPIO/MPP Configuration For QRD8909			
GPIO_1	NC	MPP_1	VDD_PX_BIAS_MPP
GPIO_2	NC	MPP_2	PWM_OUT
GPIO_3	UIM_BATT_ALM	MPP_3	VREF_DAC_MPP
GPIO_4	NC	MPP_4	MPP4_SINK

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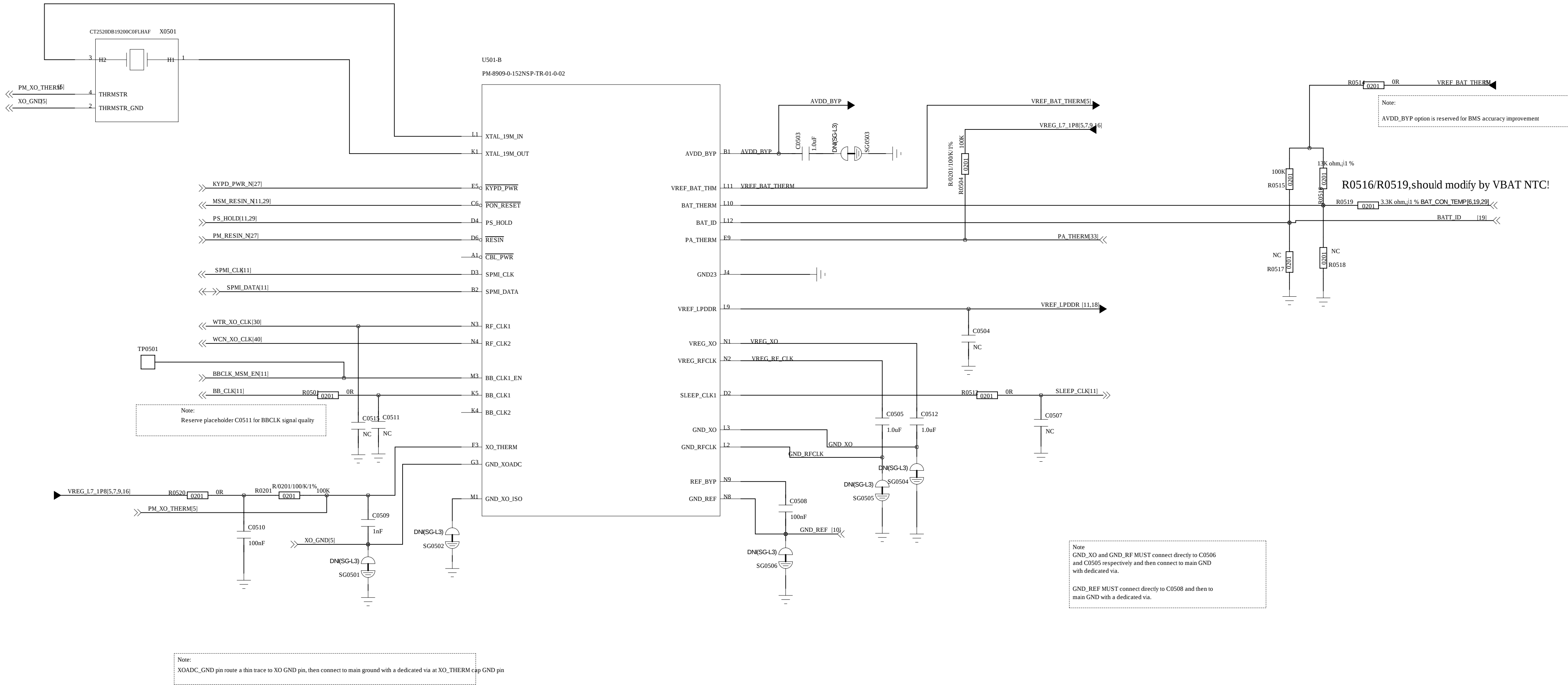
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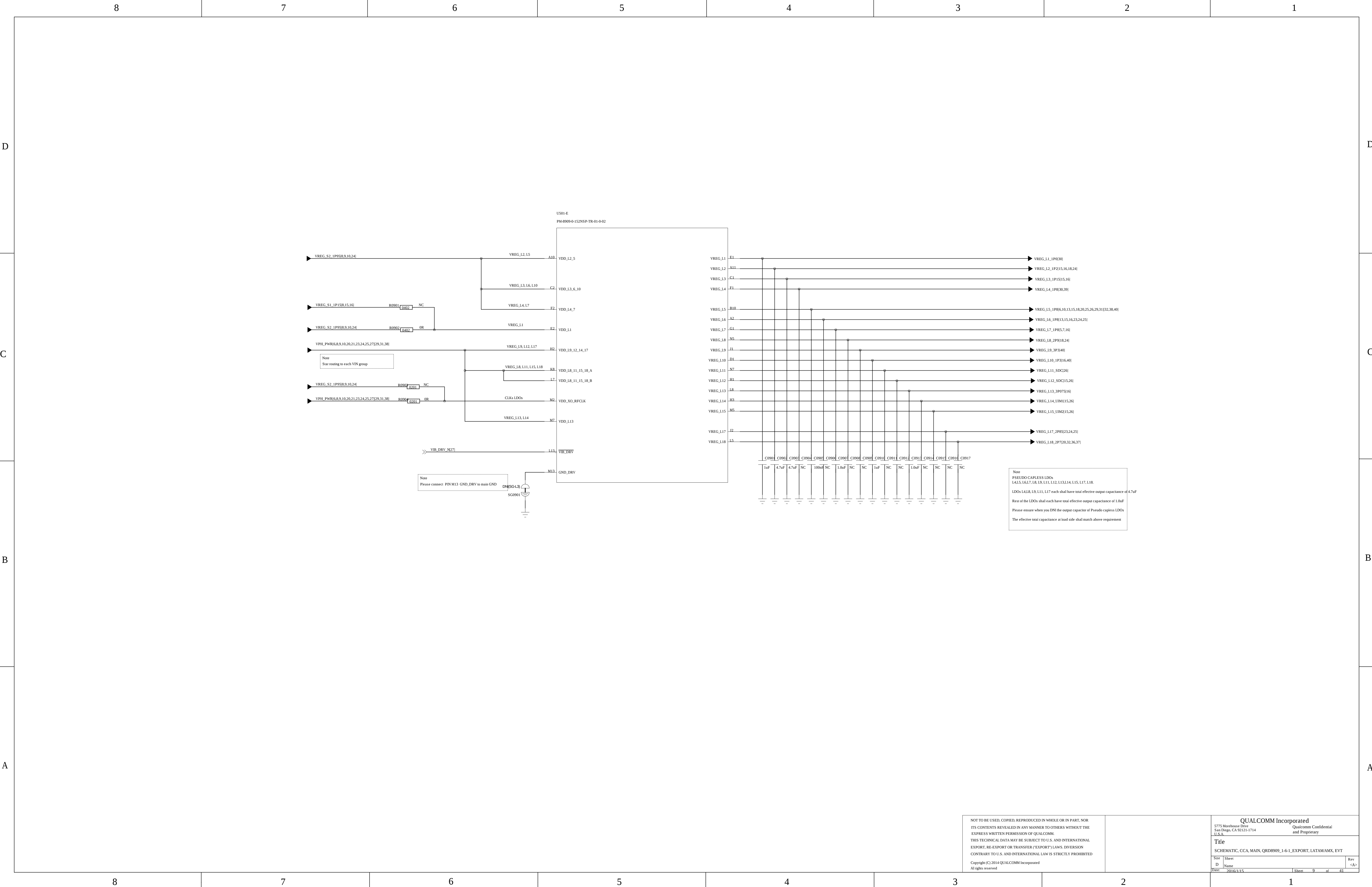
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Note:
XO GND should connect to XOADC_GND pin using thin trace, then via to main ground plane under the XO_THERM cap ground



Note: SG*** in SCH is for single-end GND lib for better layout, not as a component



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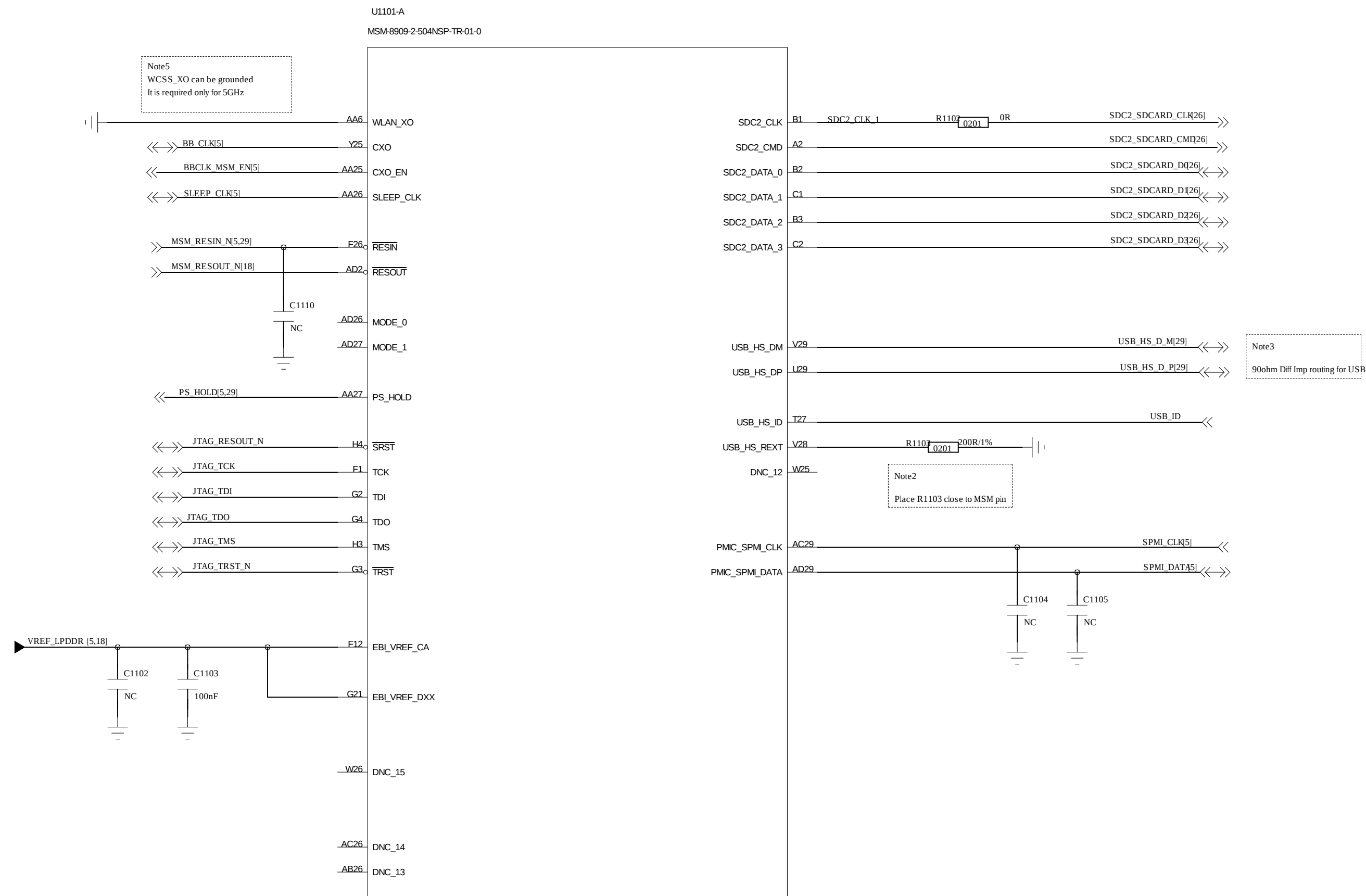
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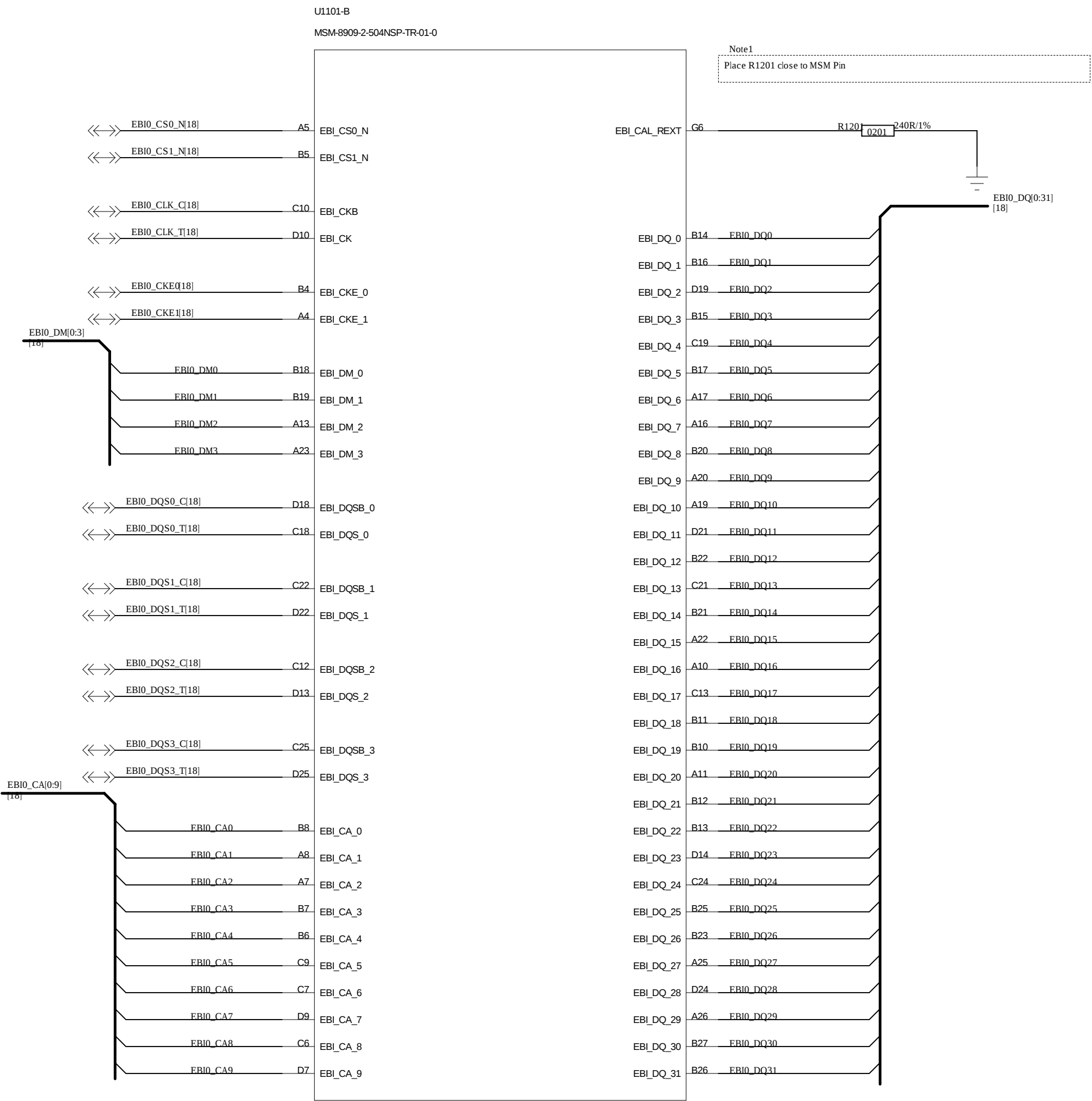
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Date: 2016/1/15
Sheet 11 of 41
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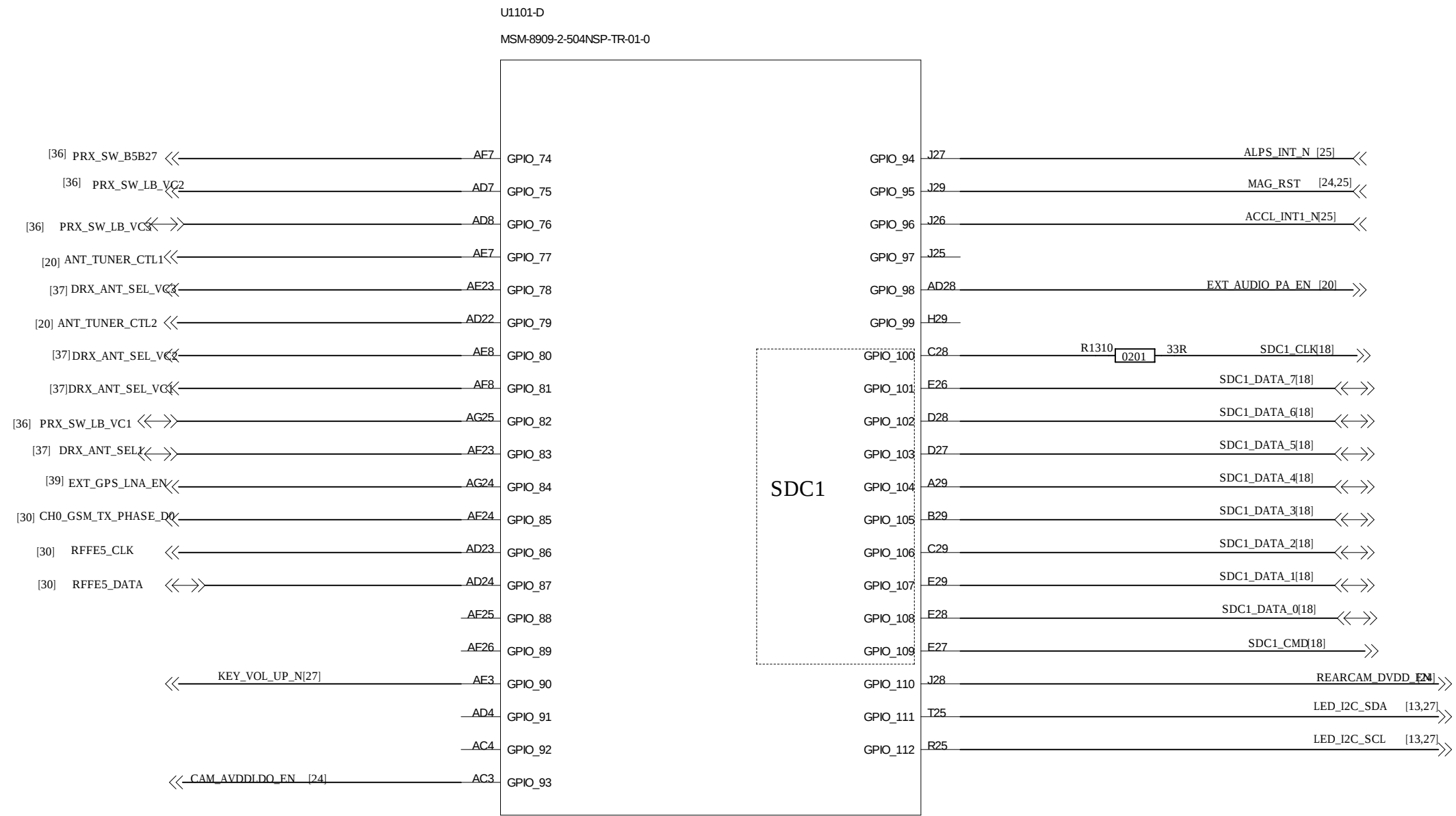
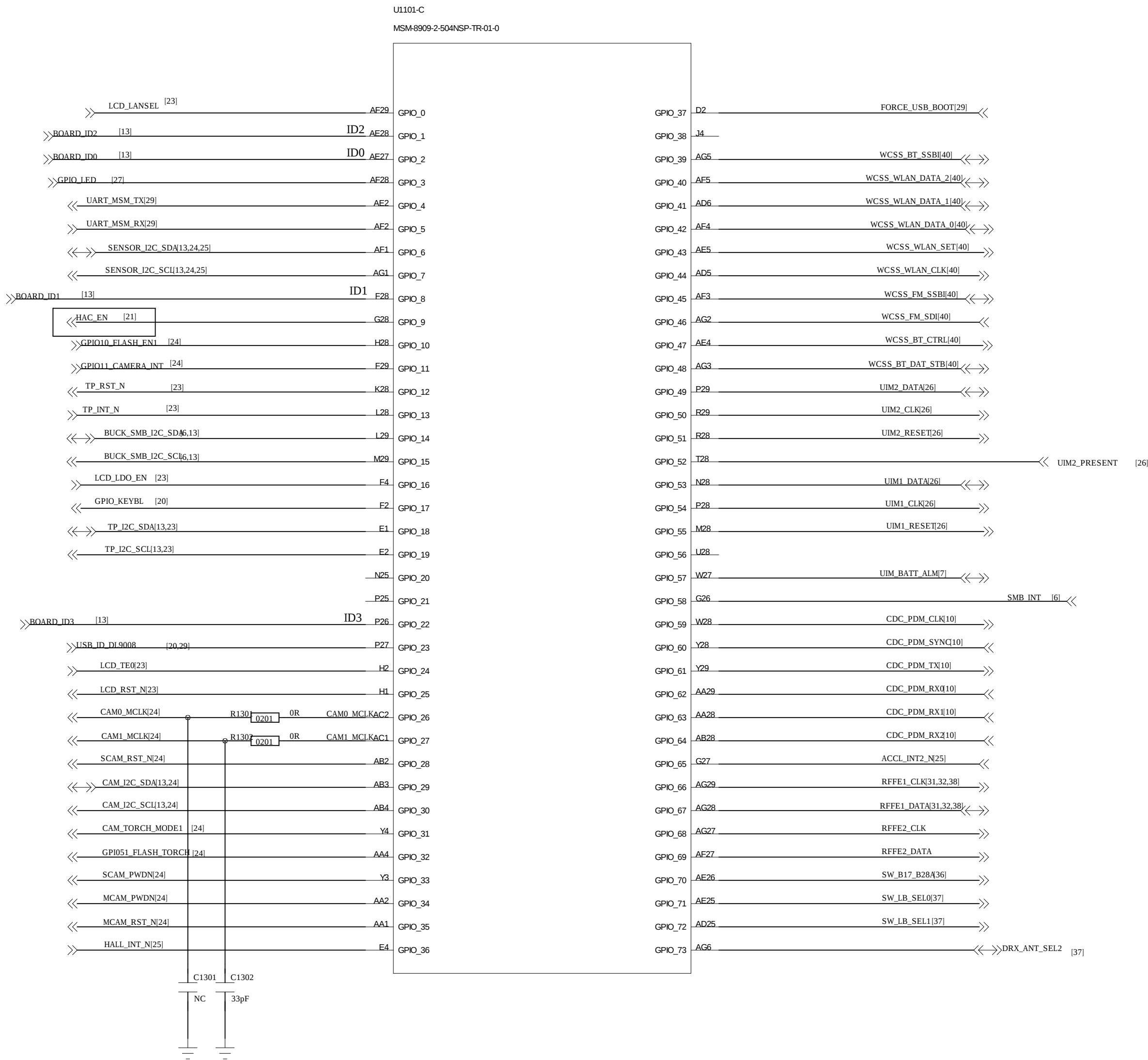
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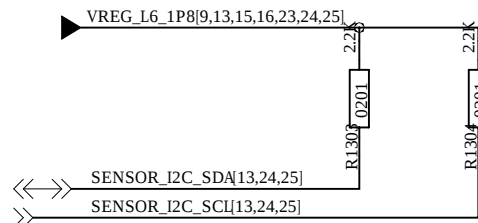
GPIOs	BOOT_CONFIG
GPIO_76	BOOT_CONFIG[0], WDOG
GPIO_77	BOOT_CONFIG[1]
GPIO_78	BOOT_CONFIG[2]
GPIO_79	BOOT_CONFIG[3]

BOOT_CONFIG[3:1]	BOOT_CONFIG
0b000	Nand -> SD @ SD2 -> USB2.0
0b001	SD @ SD2 -> USB2.0
0b010	EMMC
0b011	USB2.0

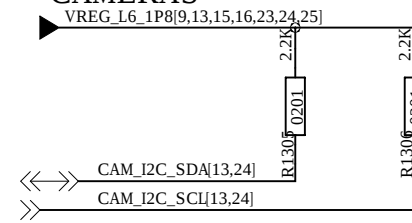
Default Boot Config (0b010) is eMMC

I2C PULL-UP RESISTORS

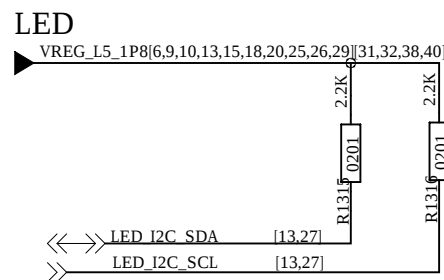
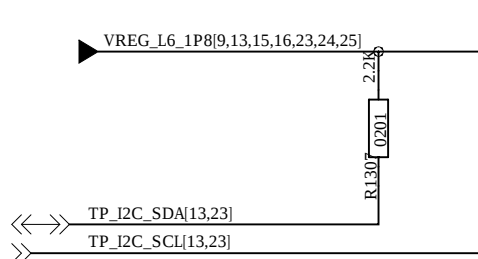
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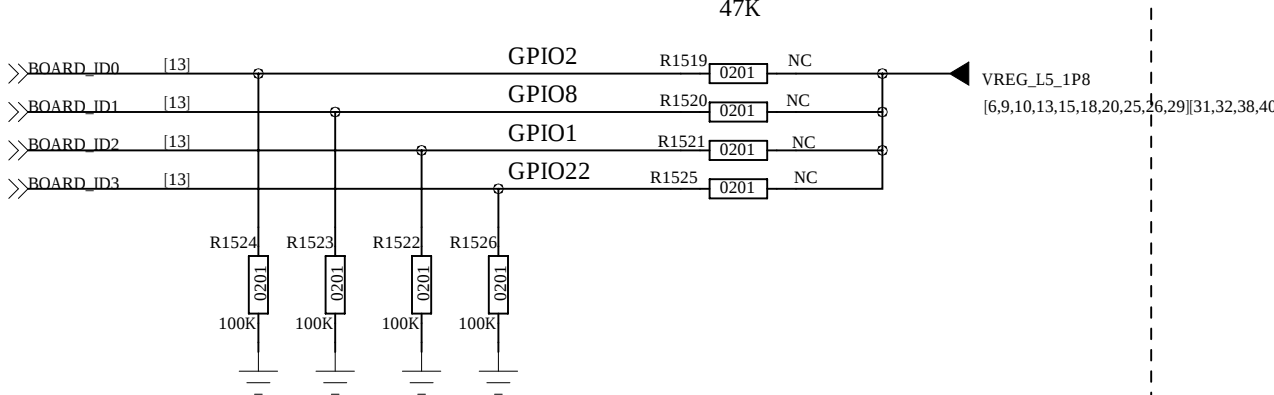
CAMERAS



TOUCHSCREEN



BOARD_ID



		R1524	R1523	R1522	R1526	R1519	R1520	R1521	R1525
Code	ID0 ID1 ID2 ID3	100K	100K	100K	100K	47K	47K	47K	47K
5051X	0 0 0 0	R1524	R1523	R1522	R1526				
5051D	0 0 0 1	R1524	R1523	R1522				R1525	
5051A	0 0 1 0	R1524	R1523		R1526			R1521	
5051J	0 0 1 1	R1524	R1523					R1521	R1525
5051M	0 1 0 0	R1524		R1522	R1526		R1520		
5051T	0 1 0 1	R1524		R1522			R1520		R1525
5051W	0 1 1 0	R1524			R1526		R1520	R1521	
5051E	0 1 1 1	R1524					R1520	R1521	R1525

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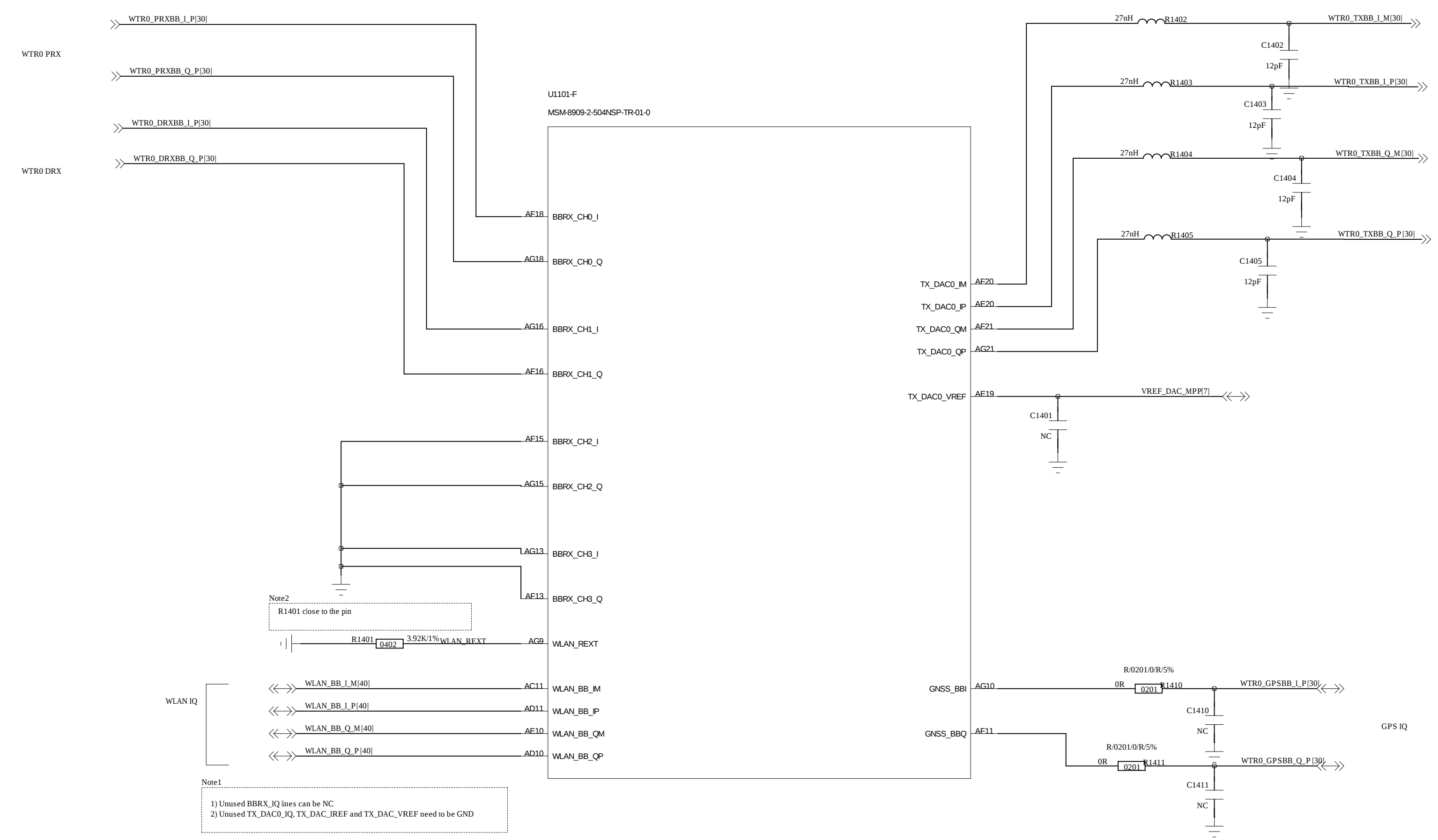
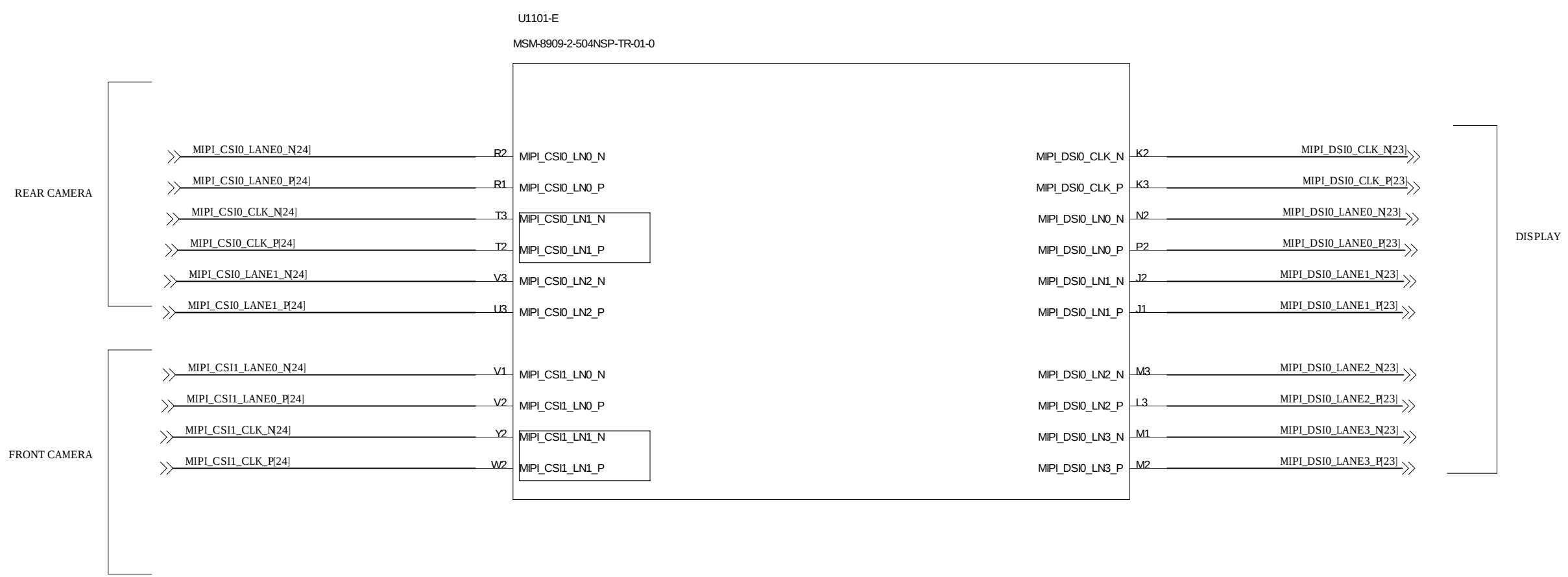
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Note2
R1401 close to the pin

Note1
1) Unused BBRX_IQ lines can be NC
2) Unused TX_DAC0_IQ, TX_DAC_IREF and TX_DAC_VREF need to be GND

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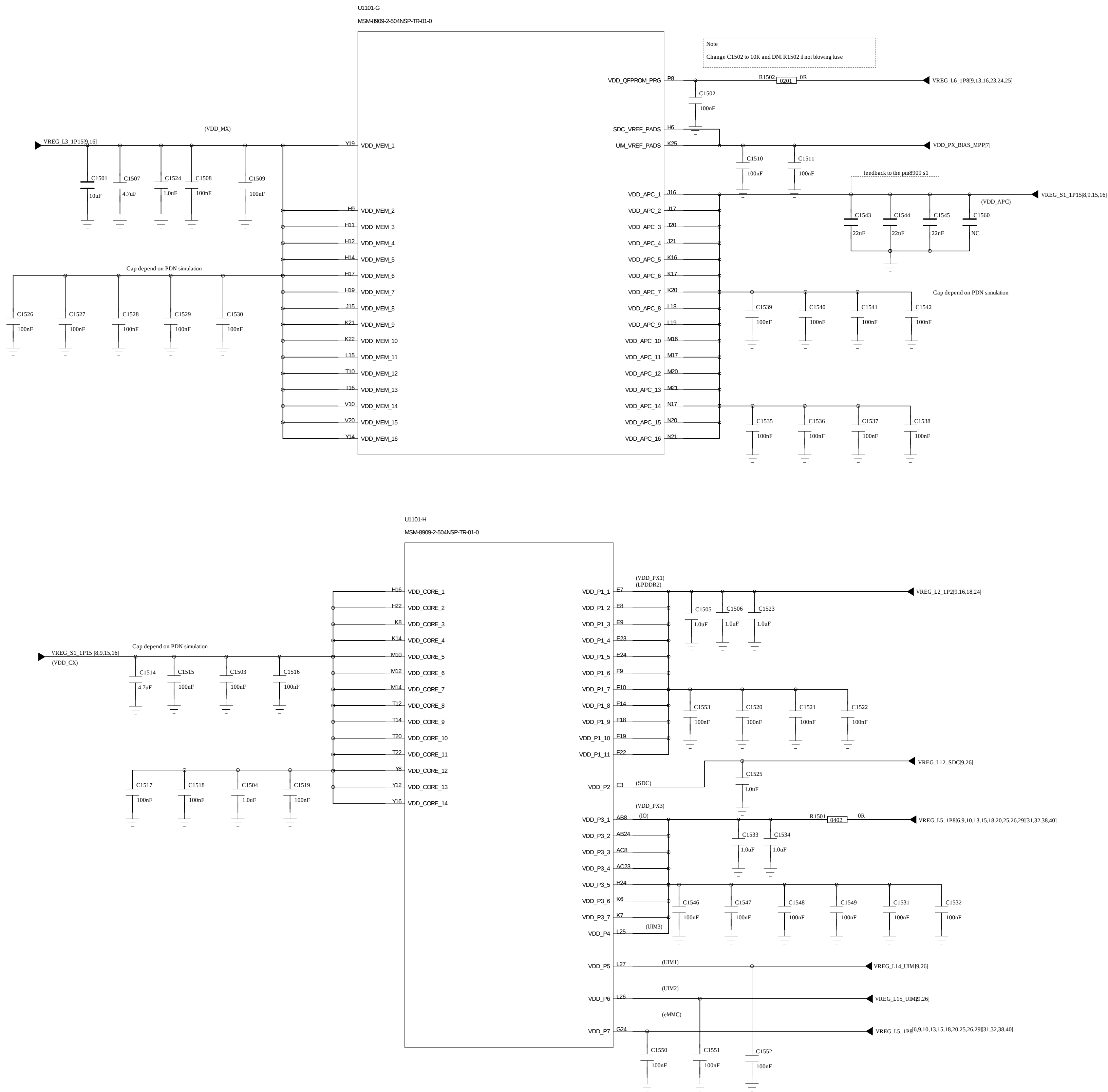
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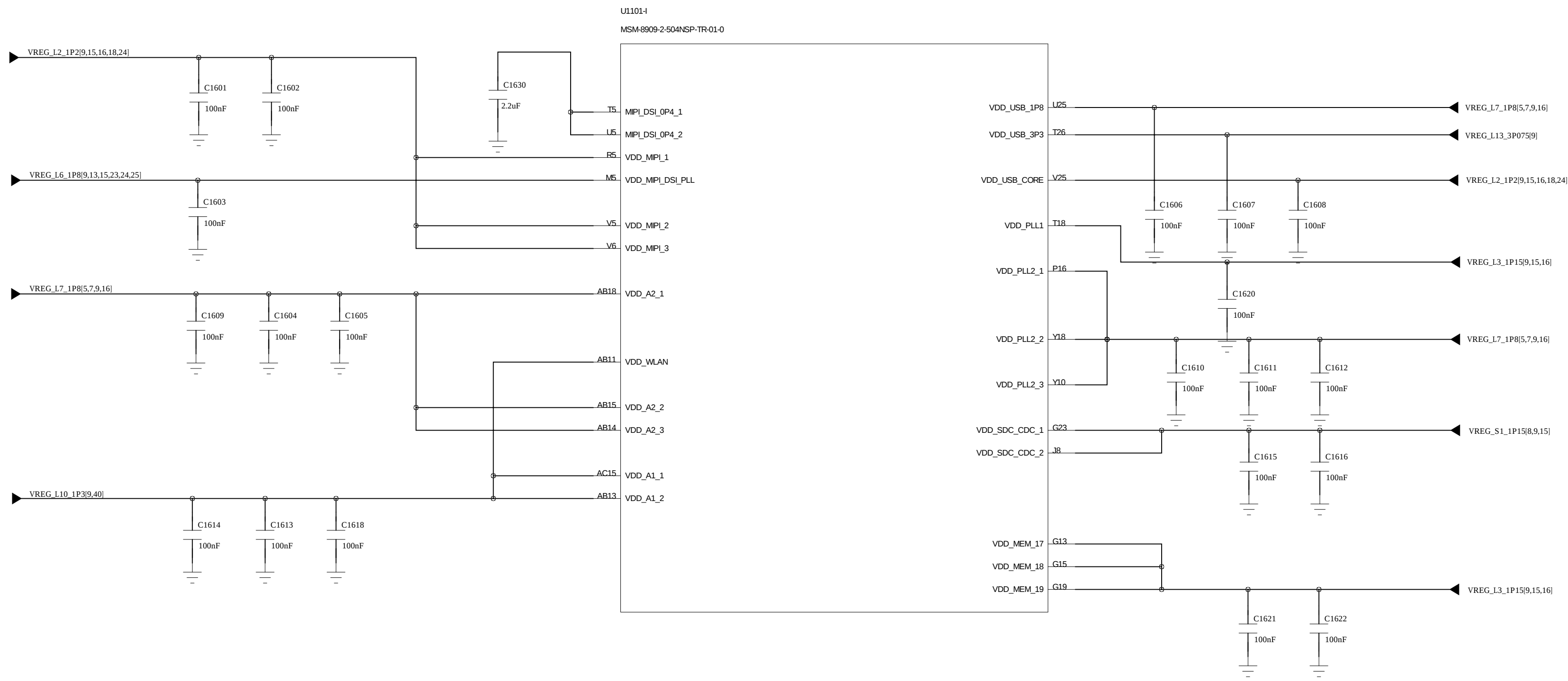
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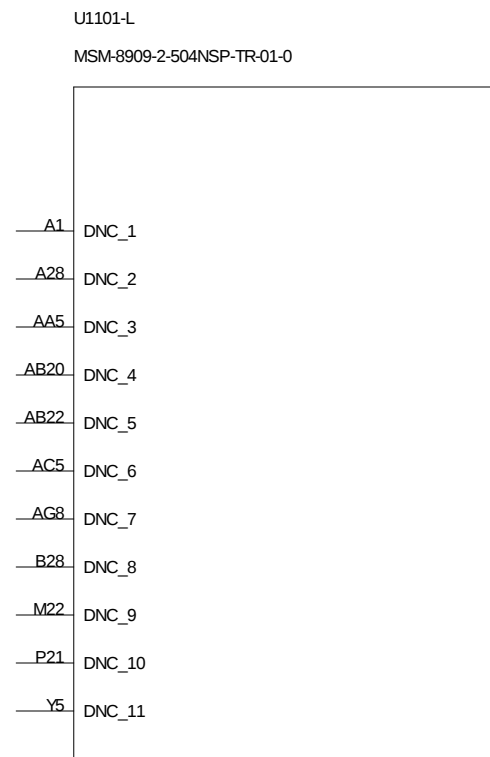
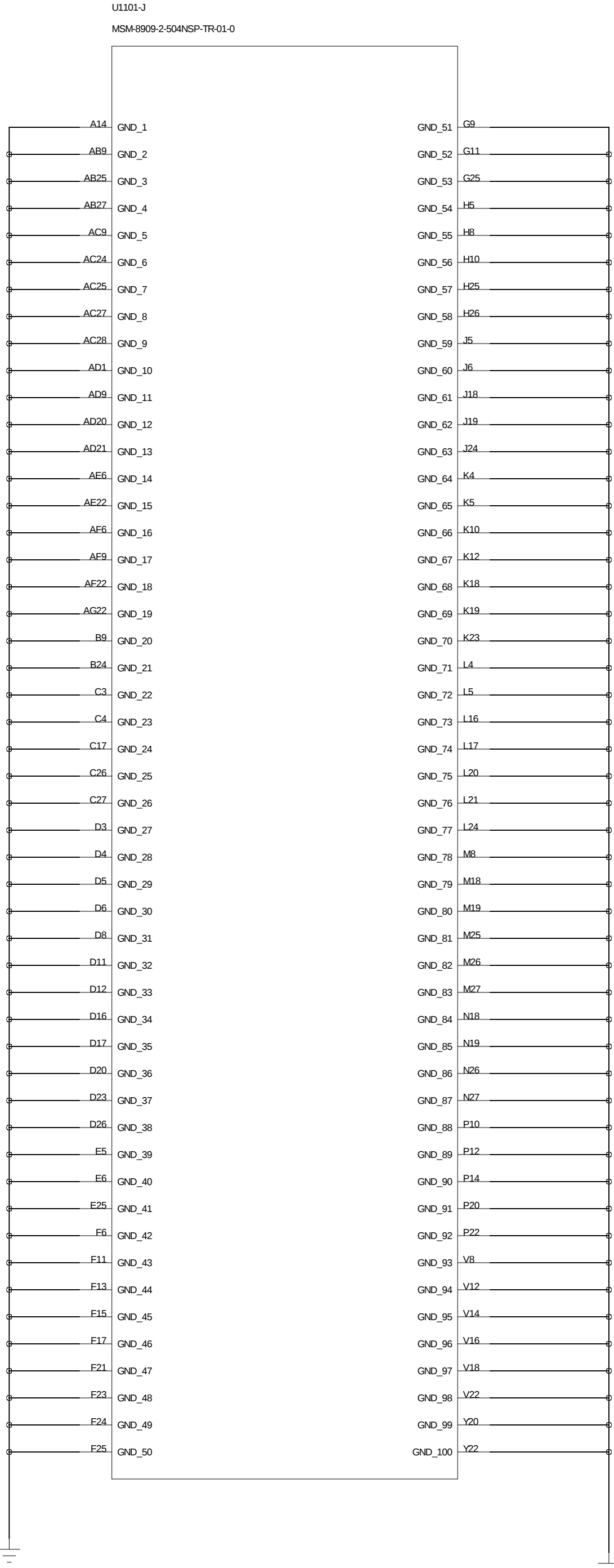
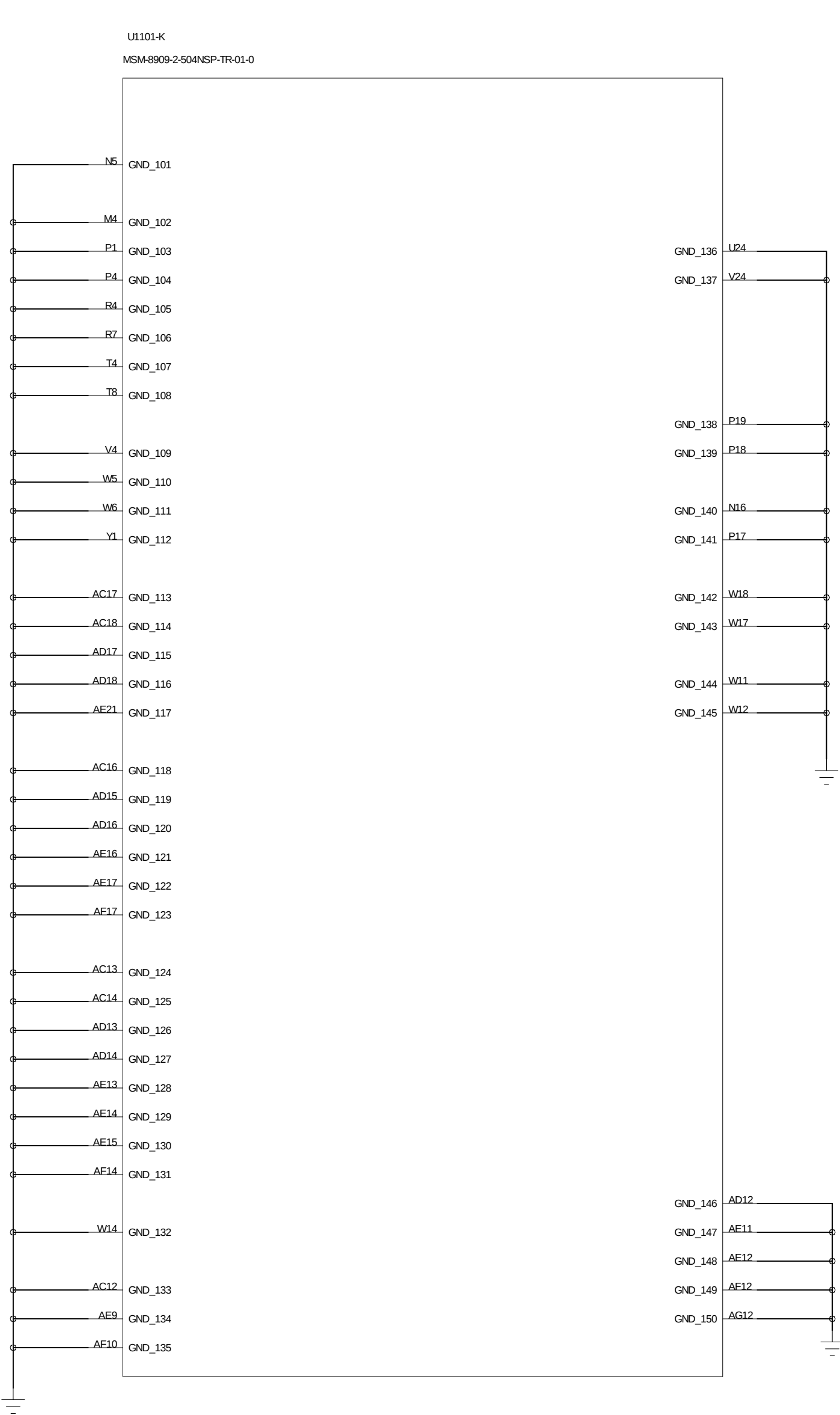
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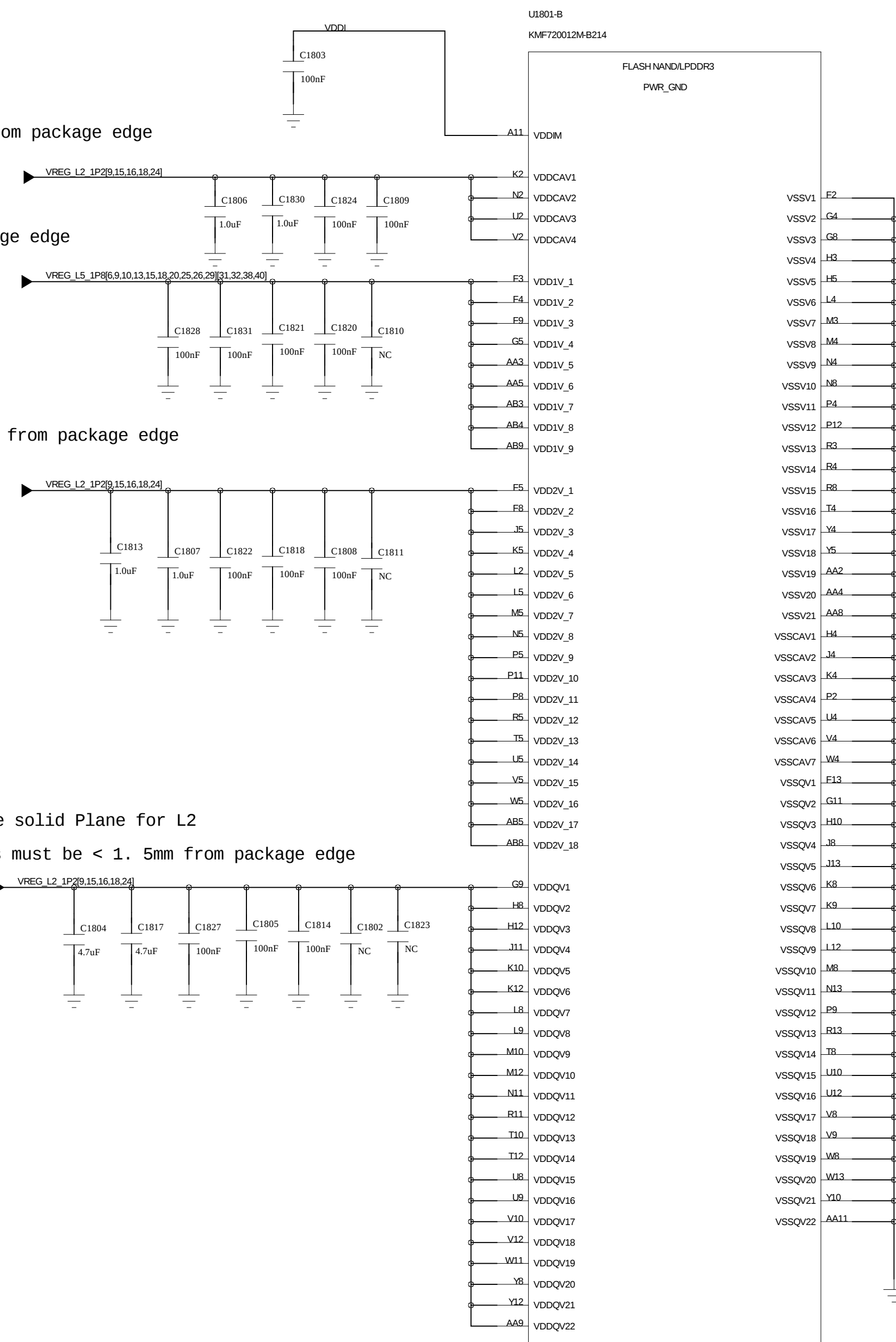
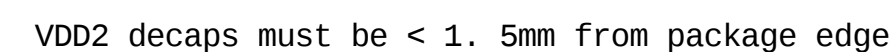
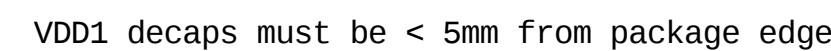
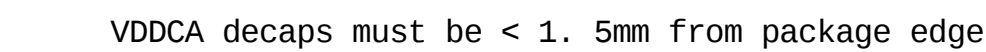
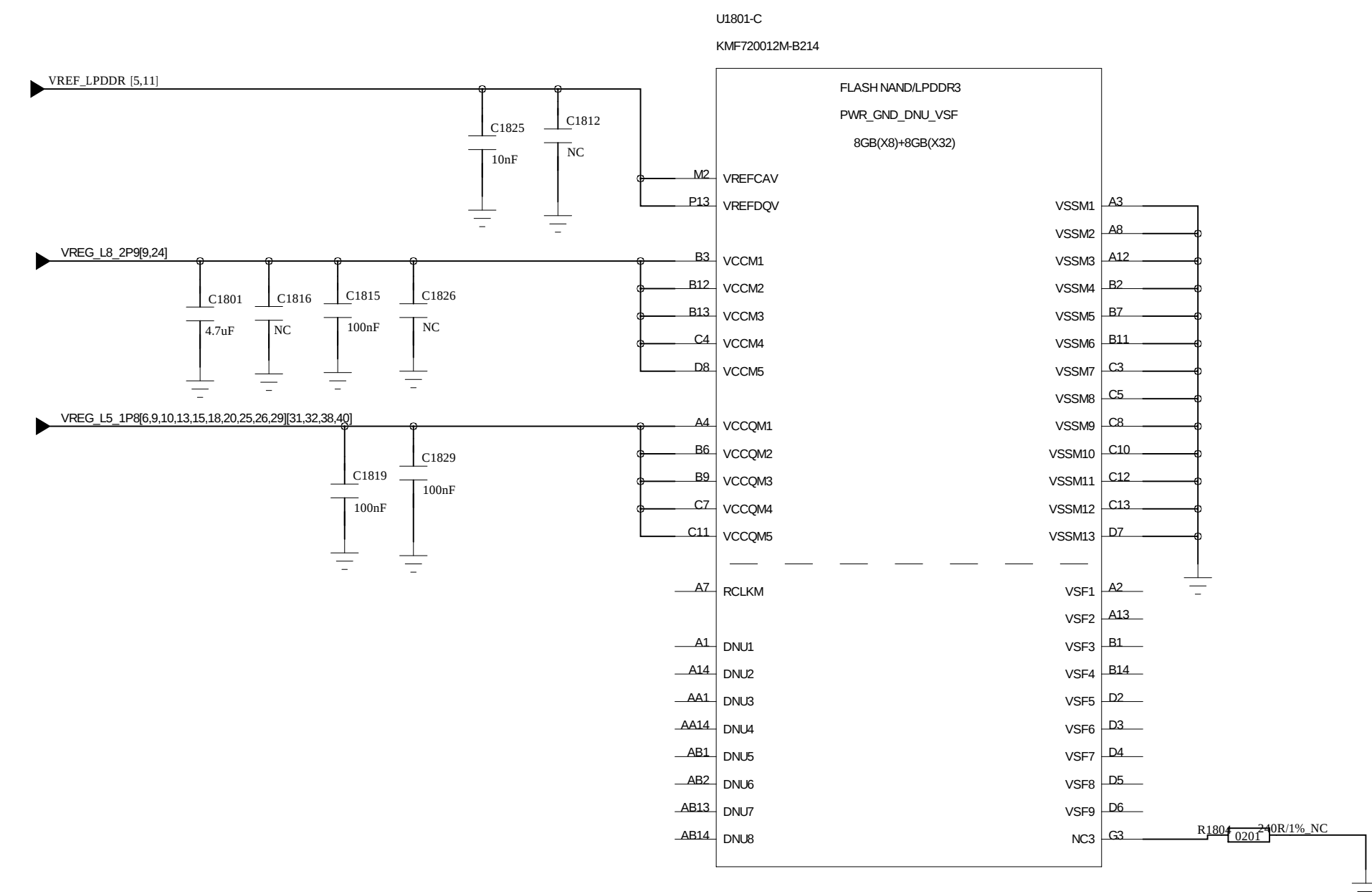
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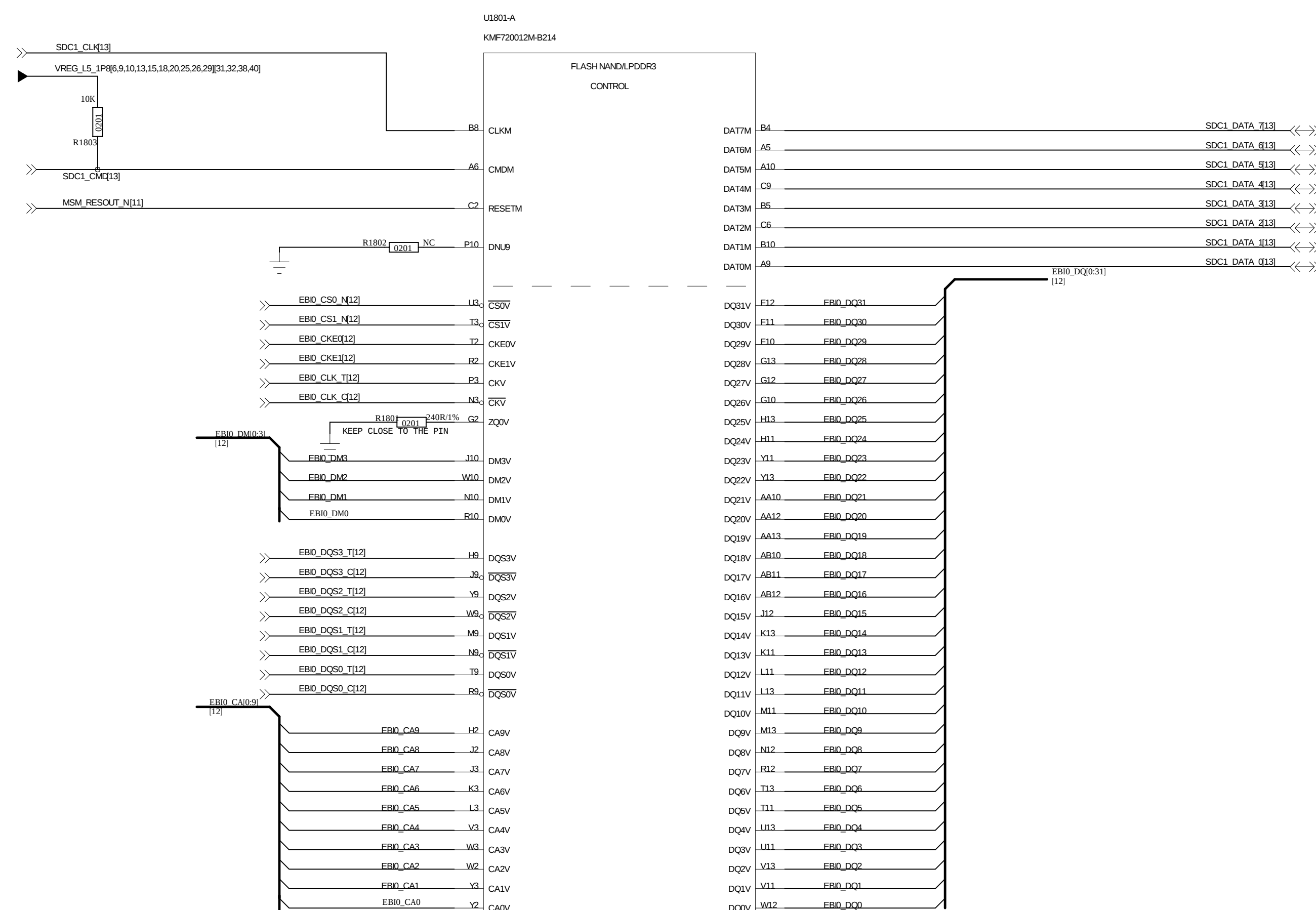
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Use solid Plane for L2

VDD2 decaps must be < 1.5mm from package edge



Note: Pull-up resistors on SDC1_DATA are PCB and eMMC vender dependent

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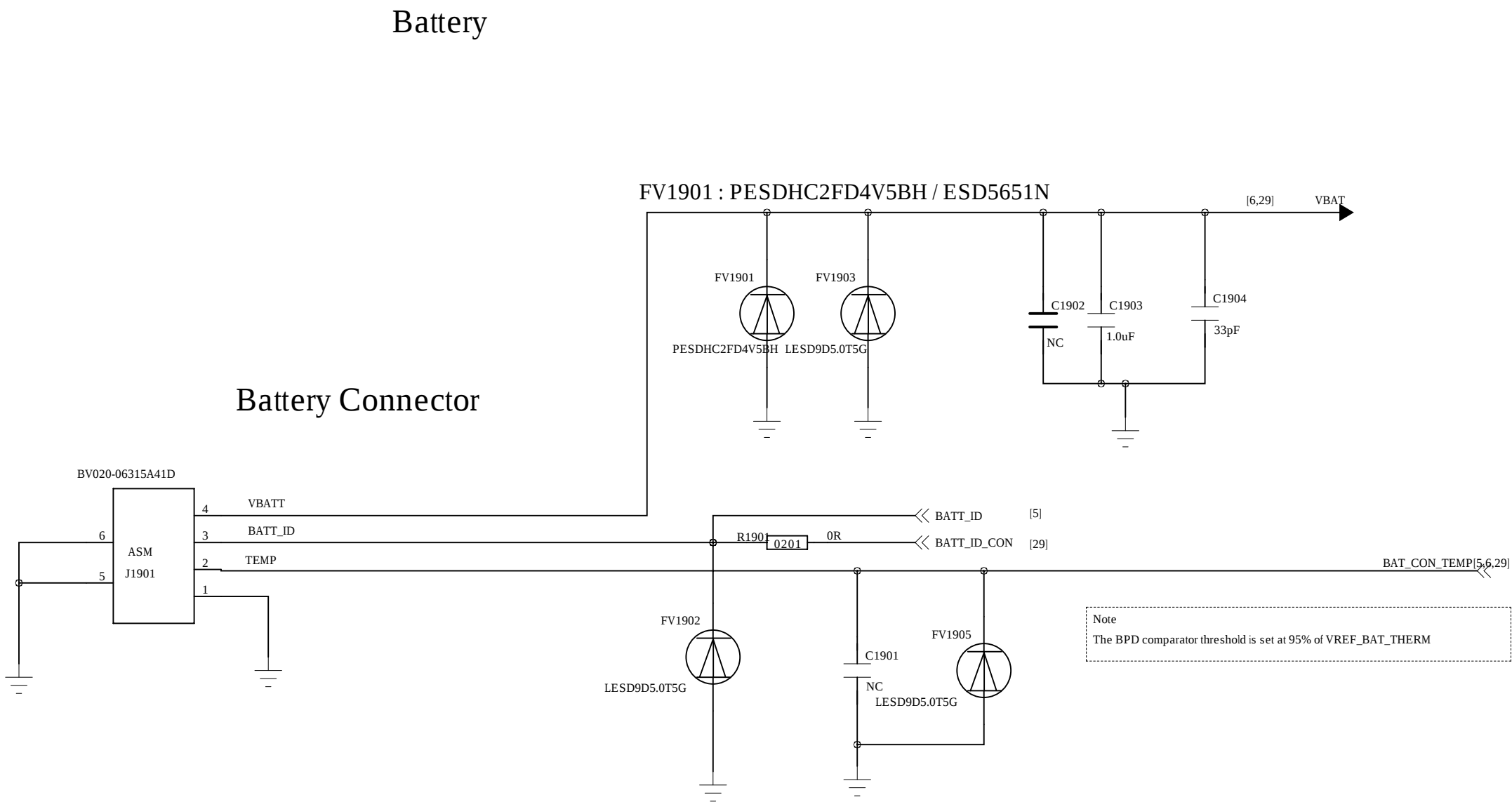
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Subboard Connector

SUB FPC connector

The schematic diagram illustrates the electrical connections for the Subboard Connector. It shows the following components and connections:

- Resistors:**
 - R2002: 10K, connected to VREG_I5_1PB (pins 16, 8, 9, 10, 13, 15, 18, 25, 26, 29, 31, 33, 36, 40).
 - R2001: NC, connected to GPIO_KEYBL (pin 11).
 - R2003: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
- Capacitors:**
 - C2021: 33pF, connected to VBUS_USB_IN (pins 6, 29).
 - C2004: 100nF, connected to VREG_I18_2P (pins 9, 32, 36, 37).
 - C2002: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
 - C2003: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
- Signal Lines:**
 - VBUS_USB_IN (pins 6, 29)
 - USB_HS_D_P_CON (pin 29)
 - USB_HS_D_M_CON (pin 29)
 - MIC_L_P_O (pin 21)
 - MIC_L_N_O (pin 21)
 - USB_ID_DL9008 (pins 13, 29)
 - ANT_TUNER_CTL1_13P (pin 13)
 - ANT_TUNER_CTL2_13P (pin 13)
 - VREG_I18_2P (pins 9, 32, 36, 37)
 - MPF4_SINK (pin 7)
 - VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38)
 - SPKR_OUT_N (pins 10, 20)
 - SPKR_OUT_P (pins 10, 20)
- Subboard Connector (J2001):**
 - 1: GND
 - 2: AF_GND
 - 3: SID_D
 - 4: AF_POWER
 - 5: SID_C
 - 6: DOVDD
 - 7: PWDN
 - 8: AGND
 - 9: AVDD
 - 10: DIVDD
 - 11: RESET
 - 12: STROBE
 - 13: DGND
 - 14: MCLK
 - 15: DGND
 - 16: MDP1
 - 17: MDN1
 - 18: DGND
 - 19: MCP
 - 20: MCN
 - 21: DGND
 - 22: MDP9
 - 23: MDN9
 - 24: DGND
 - 25: DGND

USB_ID_DL9008 for TCL 9008 firehose

Subboard Connector

SUB FPC connector

The schematic diagram illustrates the electrical connections for the Subboard Connector. It shows the following components and connections:

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- Capacitors:**
 - C2021: 33pF, connected to VBUS_USB_IN (pins 6, 29).
 - C2004: 100nF, connected to VREG_I18_2P (pins 9, 32, 36, 37).
 - C2002: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
 - C2003: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
- Signal Lines:**
 - VBUS_USB_IN (pins 6, 29)
 - USB_HS_D_P_CON (pin 29)
 - USB_HS_D_M_CON (pin 29)
 - MIC_L_P_O (pin 21)
 - MIC_L_N_O (pin 21)
 - USB_ID_DL9008 (pins 13, 29)
 - ANT_TUNER_CTL1_13P (pin 13)
 - ANT_TUNER_CTL2_13P (pin 13)
 - VREG_I18_2P (pins 9, 32, 36, 37)
 - MPF4_SINK (pin 7)
 - VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38)
 - SPKR_OUT_N (pins 10, 20)
 - SPKR_OUT_P (pins 10, 20)
- Subboard Connector (J2001):**
 - 1: GND
 - 2: AF_GND
 - 3: SID_D
 - 4: AF_POWER
 - 5: SID_C
 - 6: DOVDD
 - 7: PWDN
 - 8: AGND
 - 9: AVDD
 - 10: DIVDD
 - 11: RESET
 - 12: STROBE
 - 13: DGND
 - 14: MCLK
 - 15: DGND
 - 16: MDP1
 - 17: MDN1
 - 18: DGND
 - 19: MCP
 - 20: MCN
 - 21: DGND
 - 22: MDP9
 - 23: MDN9
 - 24: DGND
 - 25: DGND

USB_ID_DL9008 for TCL 9008 firehose

Subboard Connector

SUB FPC connector

The schematic diagram illustrates the electrical connections for the Subboard Connector. It shows the following components and connections:

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 - C2004: 100nF, connected to VREG_I18_2P (pins 9, 32, 36, 37).
 - C2002: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
 - C2003: 33pF, connected to VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38).
- Signal Lines:**
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 - USB_HS_D_P_CON (pin 29)
 - USB_HS_D_M_CON (pin 29)
 - MIC_L_P_O (pin 21)
 - MIC_L_N_O (pin 21)
 - USB_ID_DL9008 (pins 13, 29)
 - ANT_TUNER_CTL1_13P (pin 13)
 - ANT_TUNER_CTL2_13P (pin 13)
 - VREG_I18_2P (pins 9, 32, 36, 37)
 - MPF4_SINK (pin 7)
 - VPH_PWR (pins 6, 8, 9, 10, 20, 21, 23, 24, 25, 27, 29, 31, 38)
 - SPKR_OUT_N (pins 10, 20)
 - SPKR_OUT_P (pins 10, 20)
- Subboard Connector (J2001):**
 - 1: GND
 - 2: AF_GND
 - 3: SID_D
 - 4: AF_POWER
 - 5: SID_C
 - 6: DOVDO
 - 7: PWDN
 - 8: AGND
 - 9: AVDD
 - 10: DIVDD
 - 11: RESET
 - 12: STROBE
 - 13: DGND
 - 14: MCLK
 - 15: DGND
 - 16: MDP1
 - 17: MDN1
 - 18: DGND
 - 19: MCP
 - 20: MCN
 - 21: DGND
 - 22: MDP9
 - 23: MDN9
 - 24: DGND
 - 25: DGND

USB_ID_DL9008 for TCL 9008 firehose

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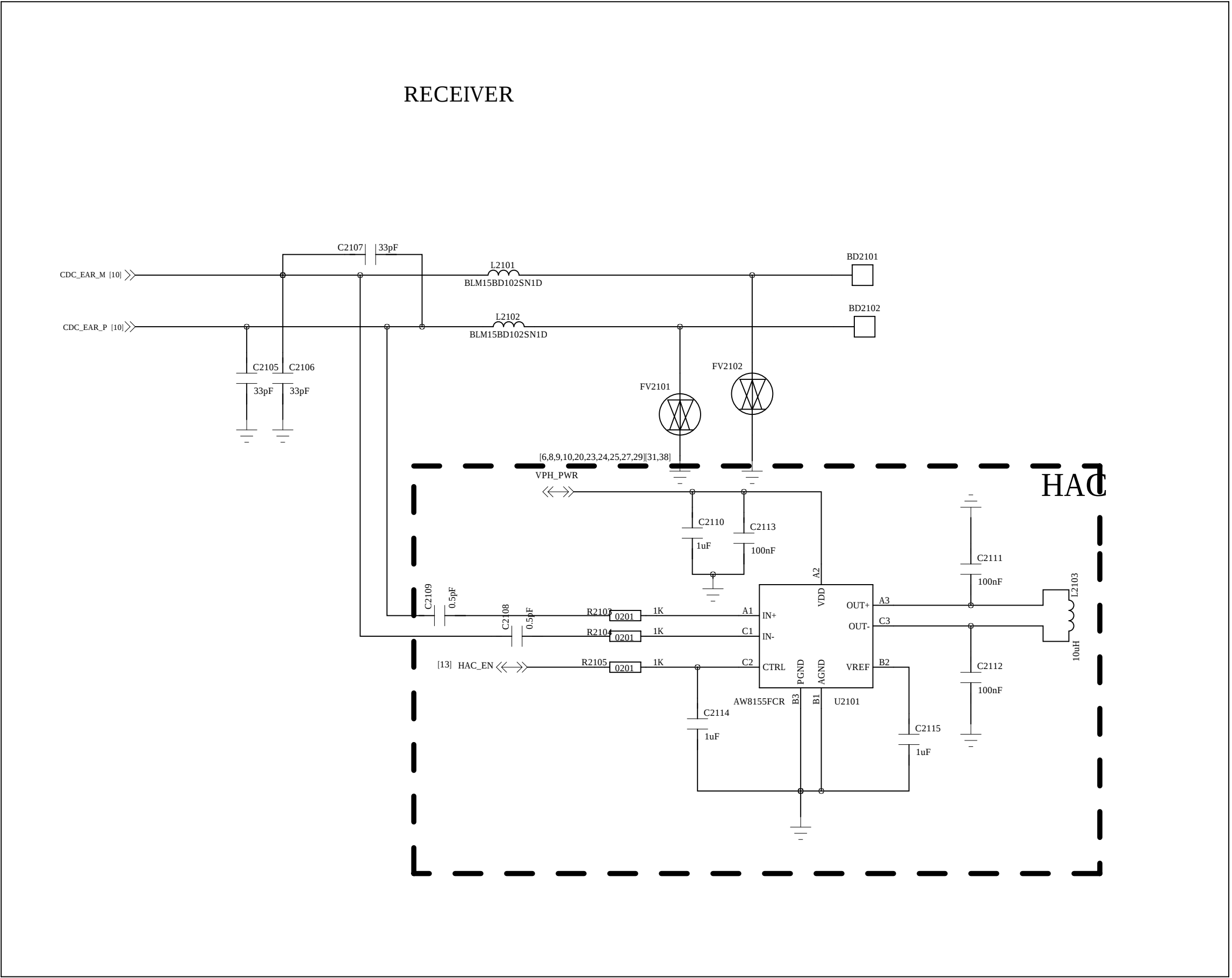
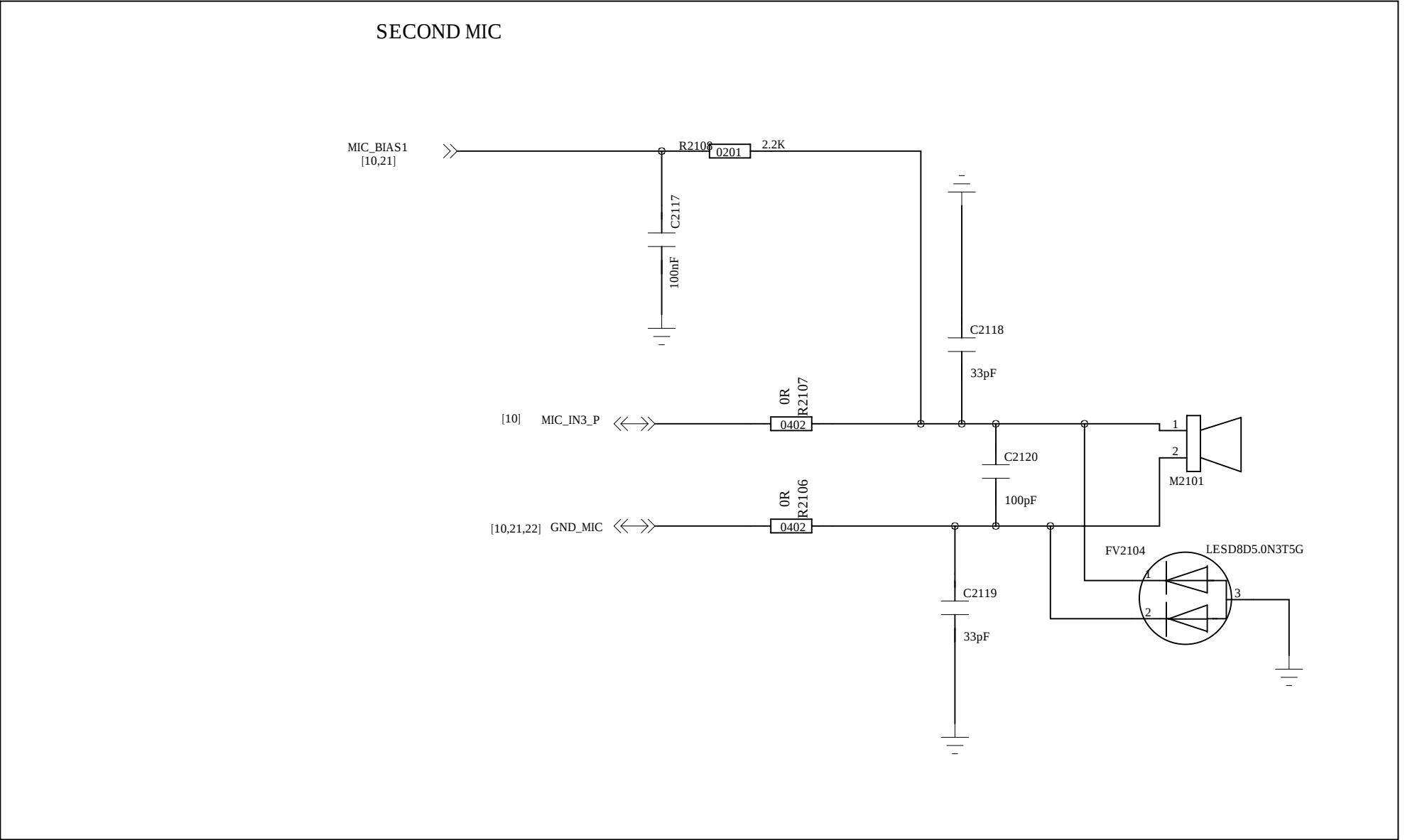
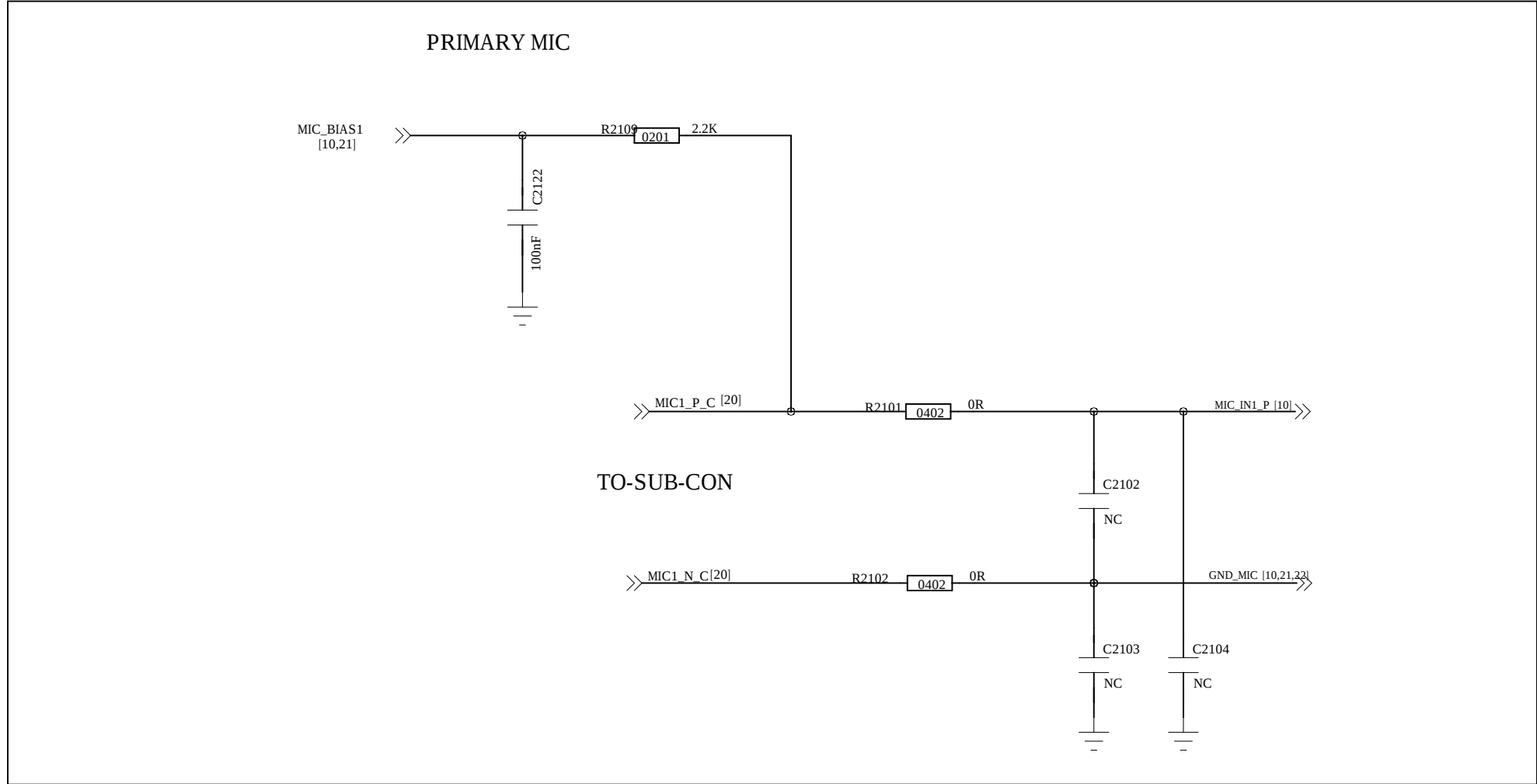
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Size Sheet
D Name
Date: 2016/1/15 Sheet 21 of 41 Rev <A>

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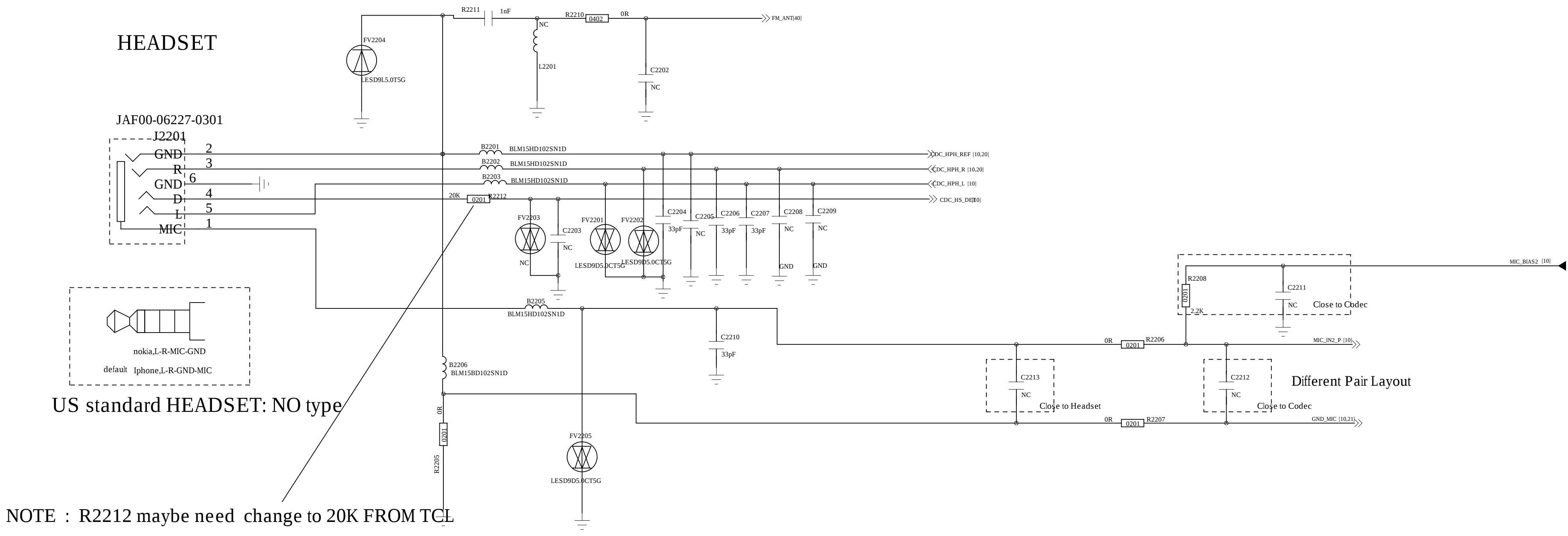
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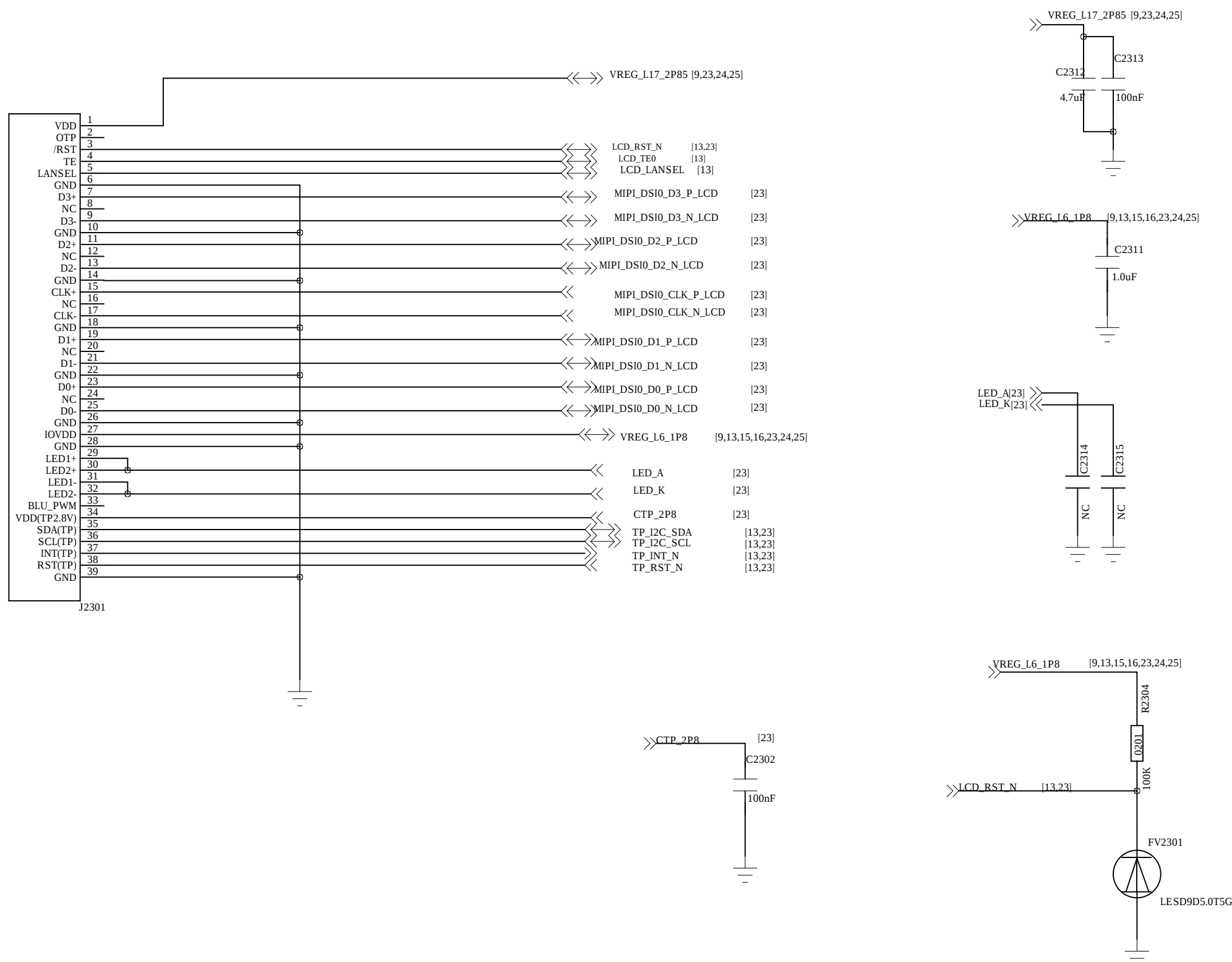
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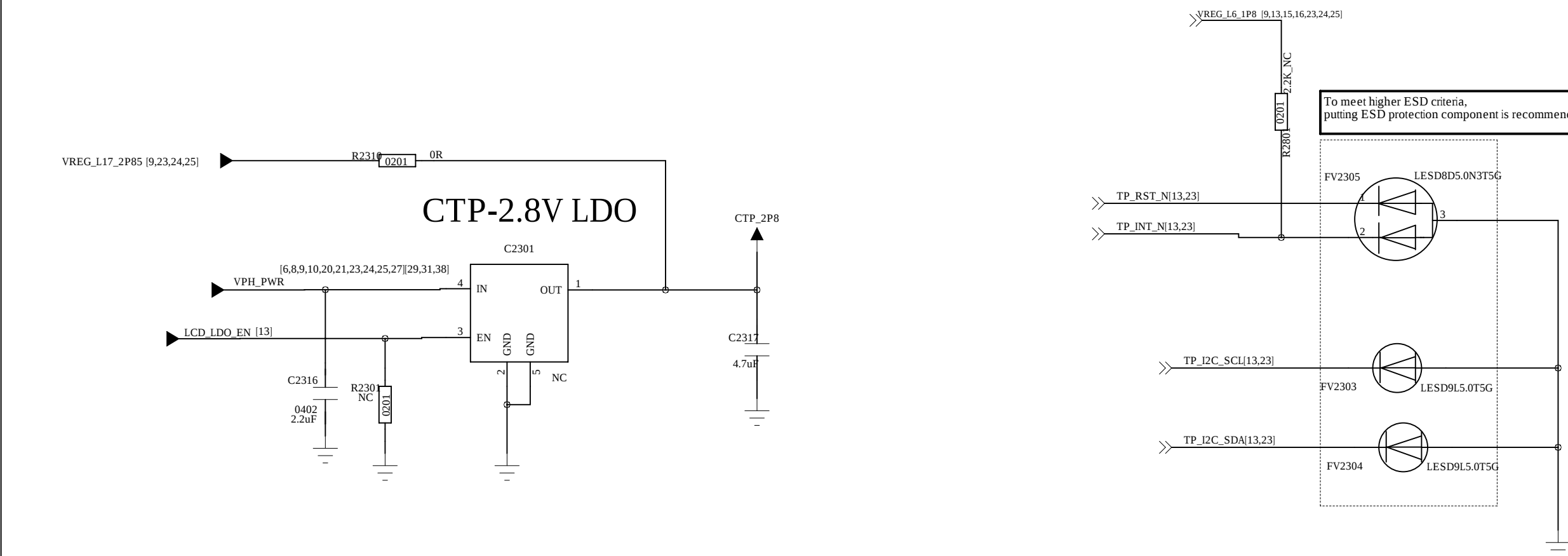
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on-cell LCD FOR ON CELL LCD ;TBD

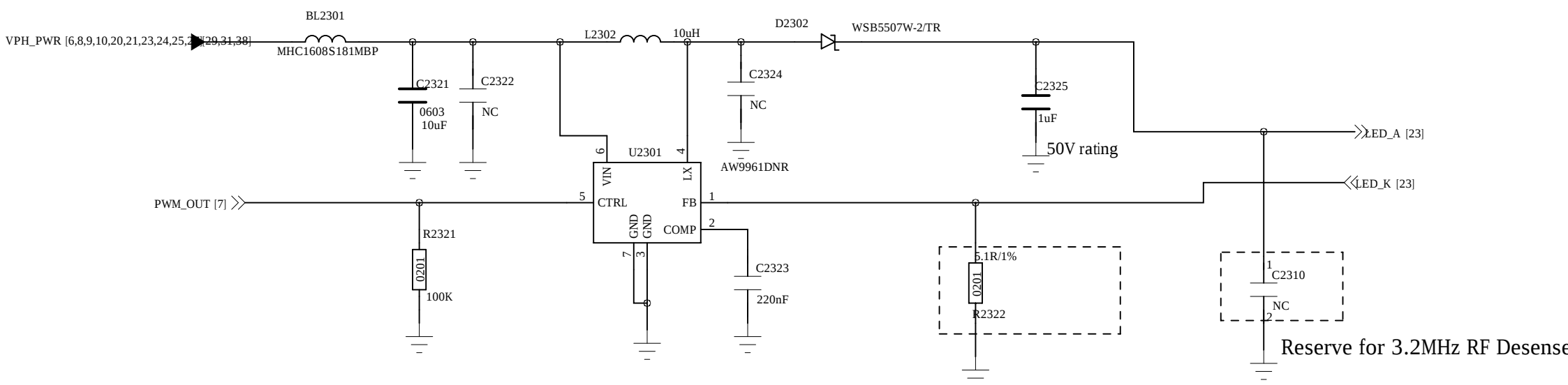
LCD-Connector



CTP



LCD-Backlight Driver

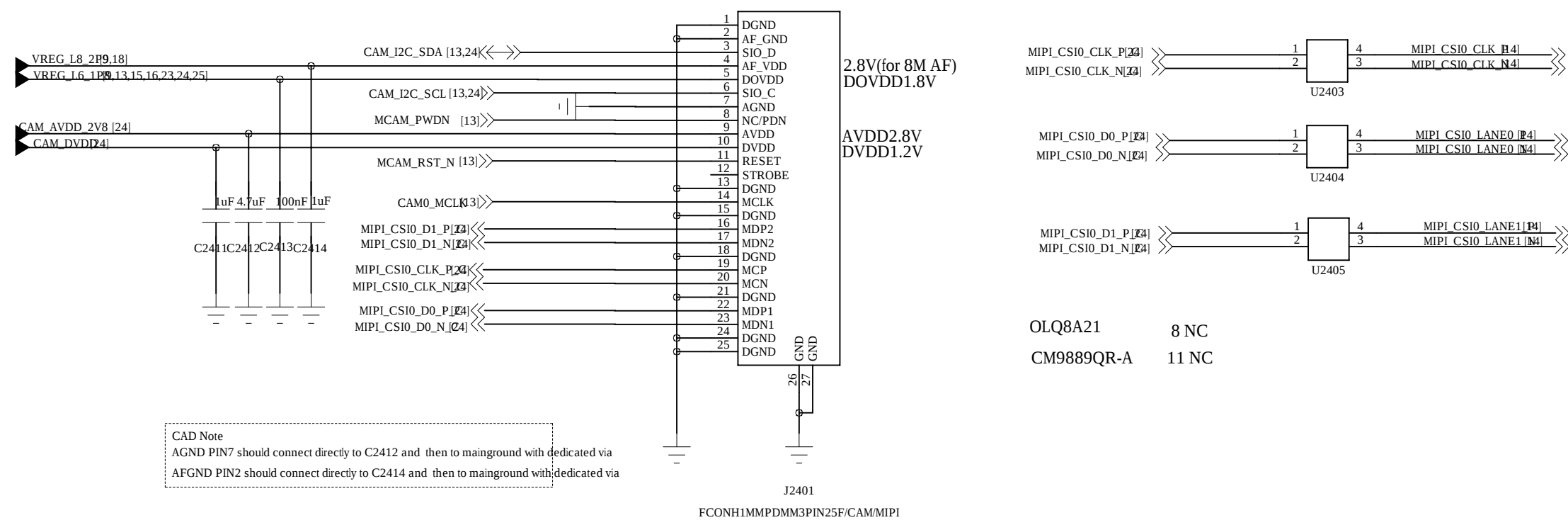


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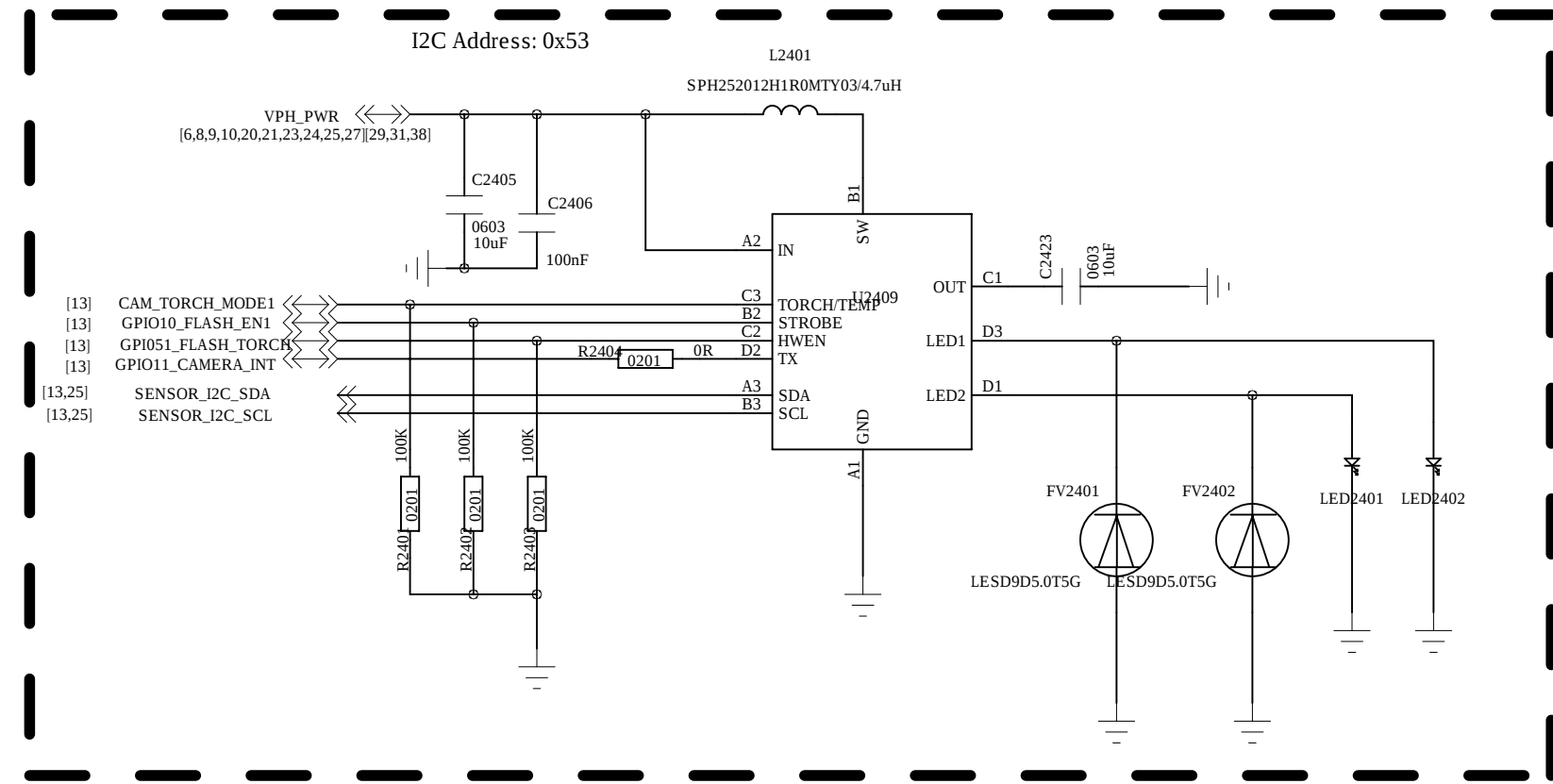
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Main Camera



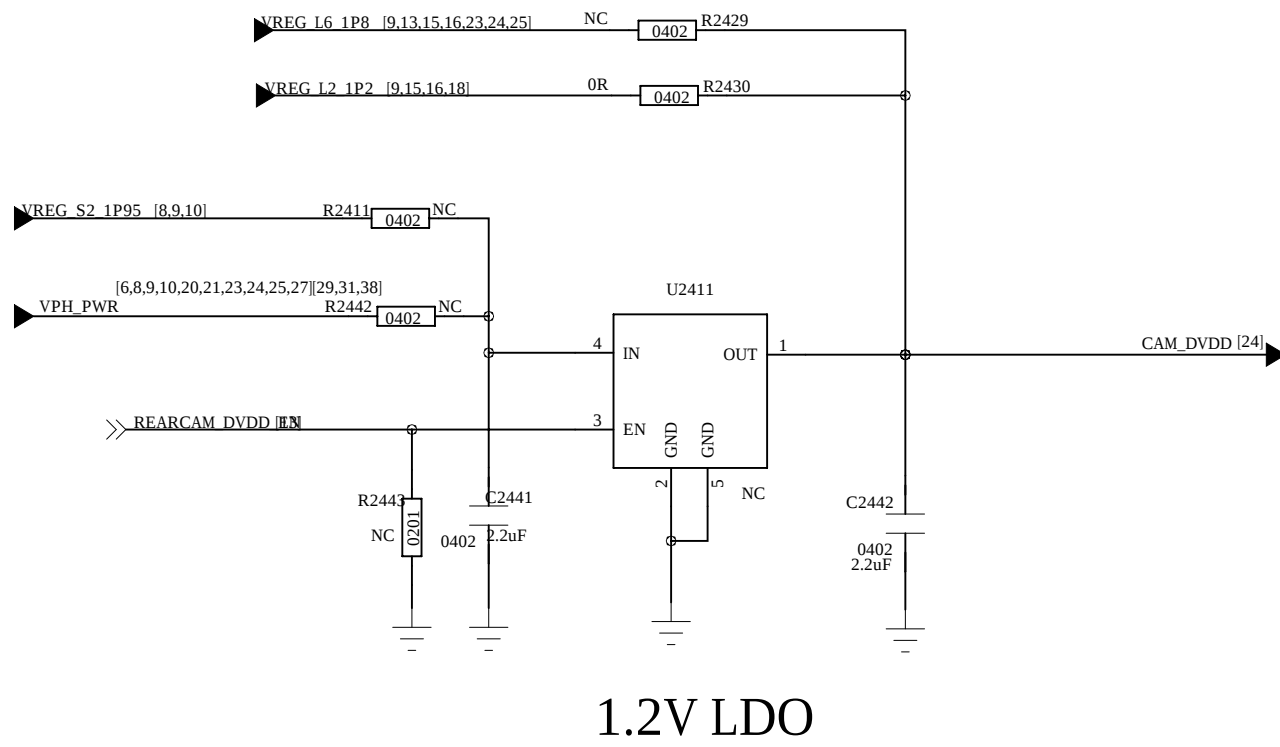
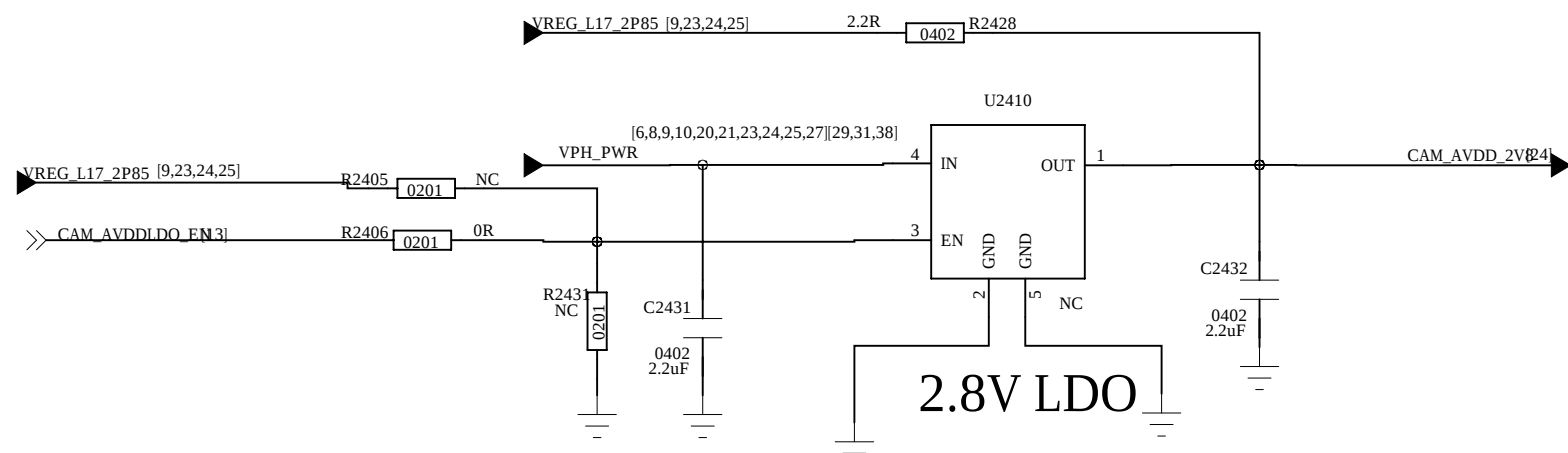
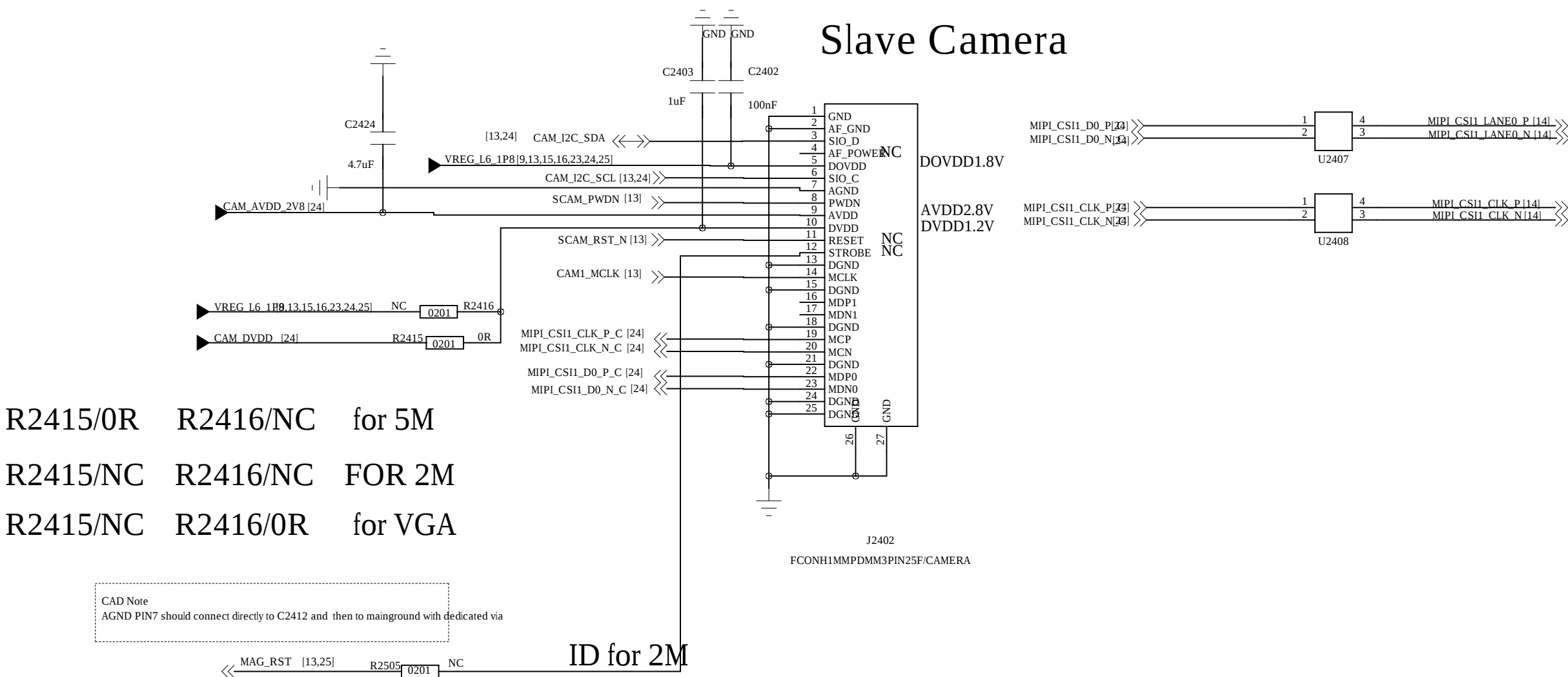
Main/Slave Camera and Flash LED



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Slave Camera



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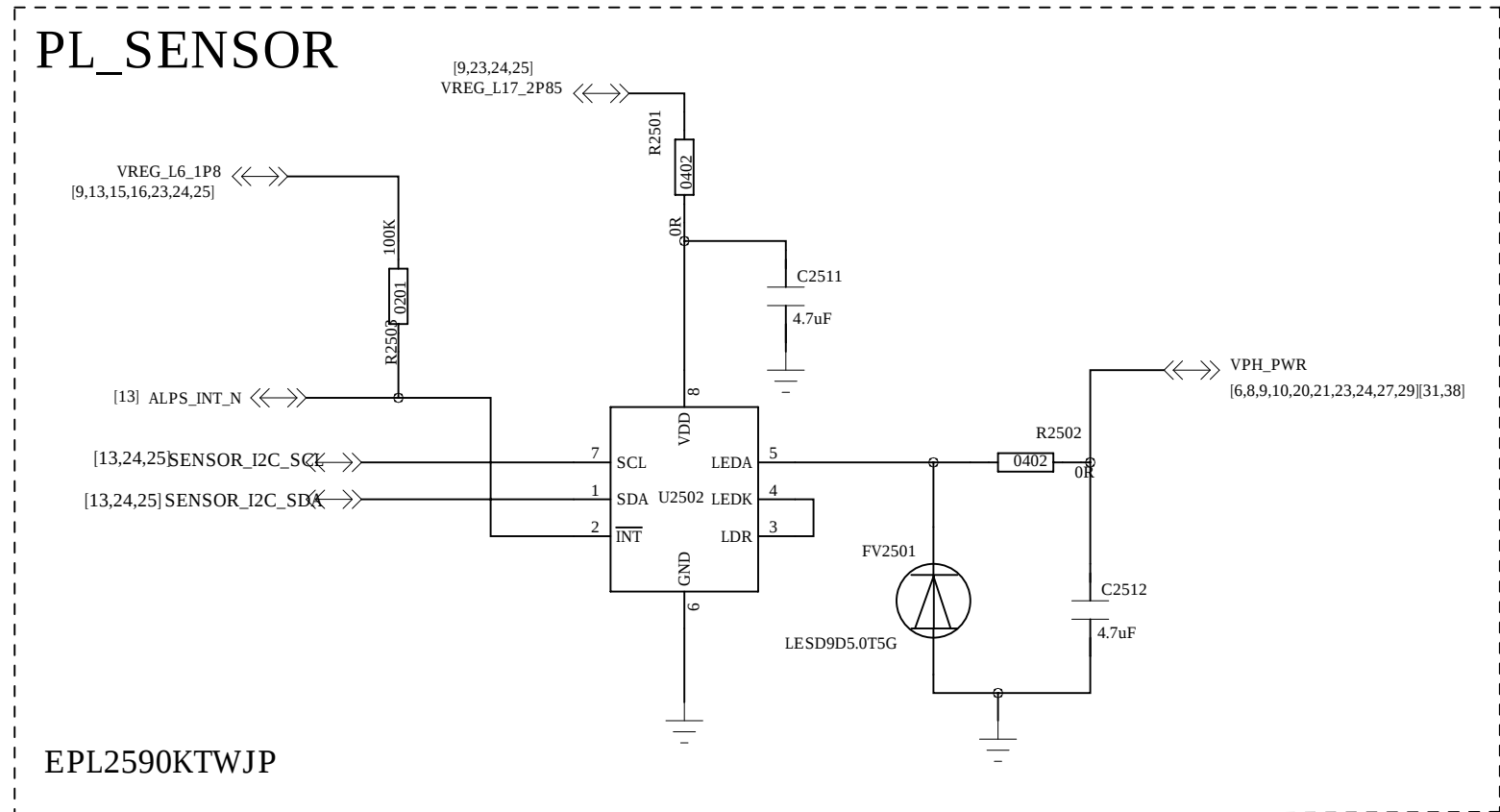
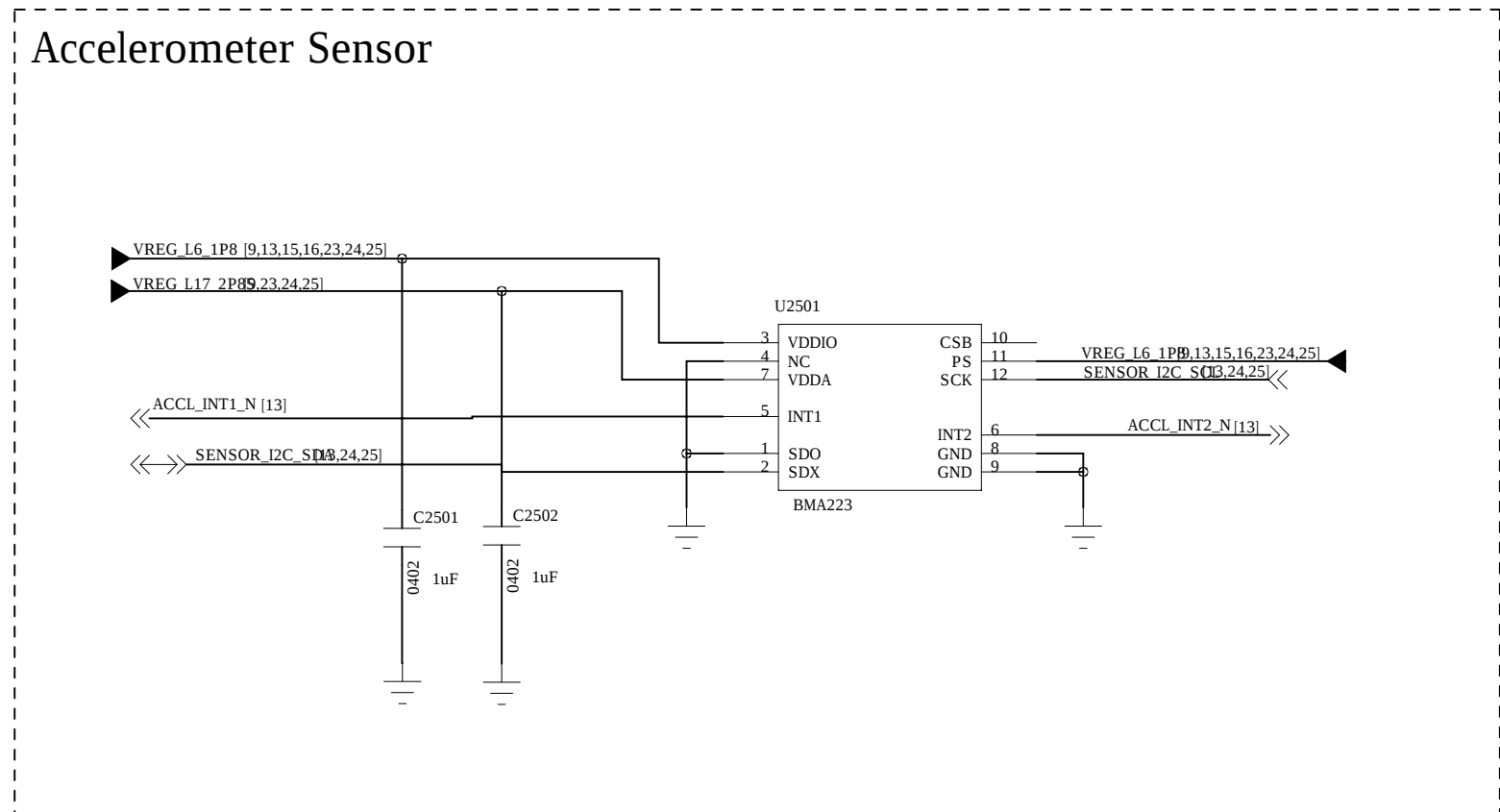
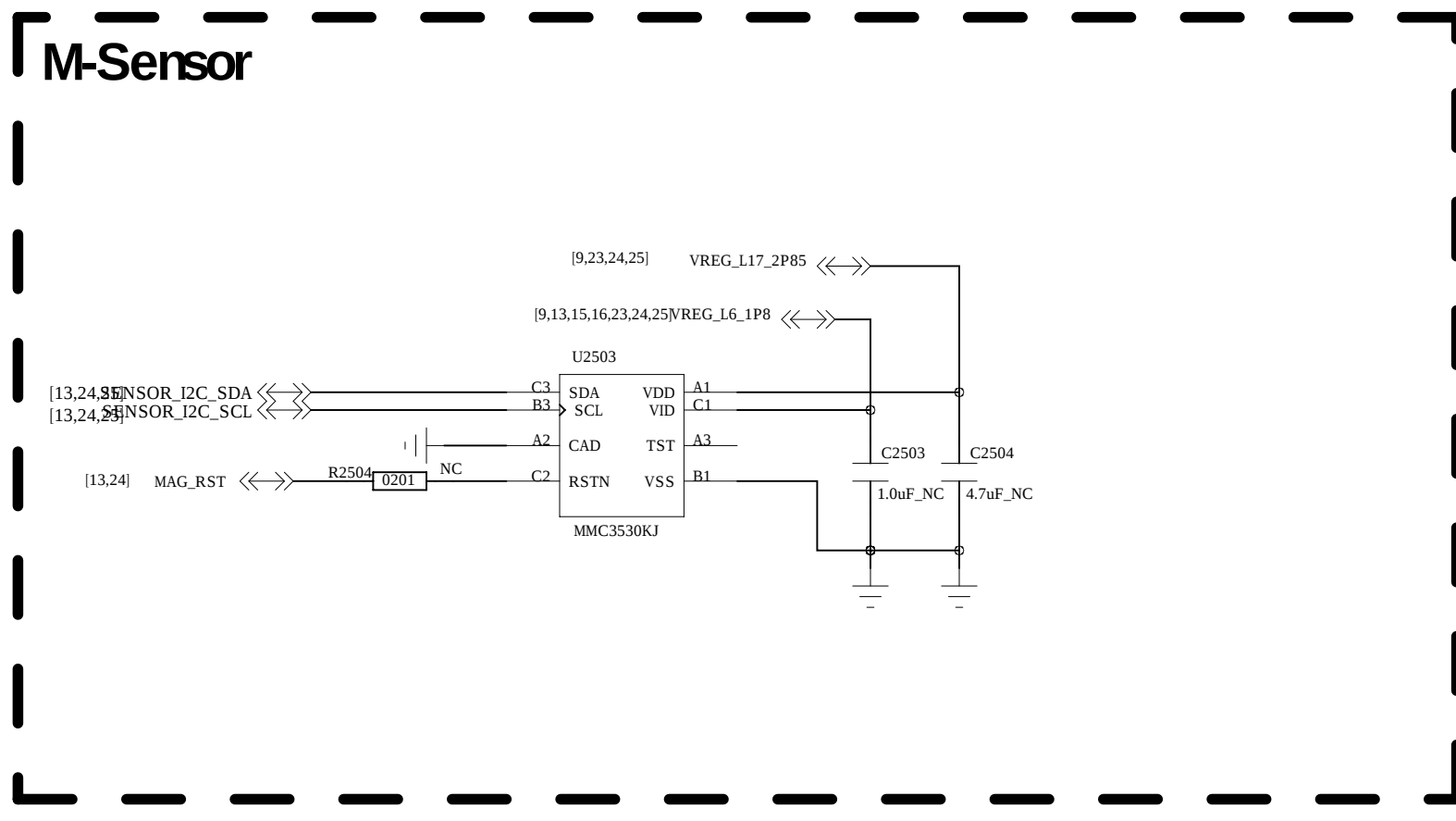
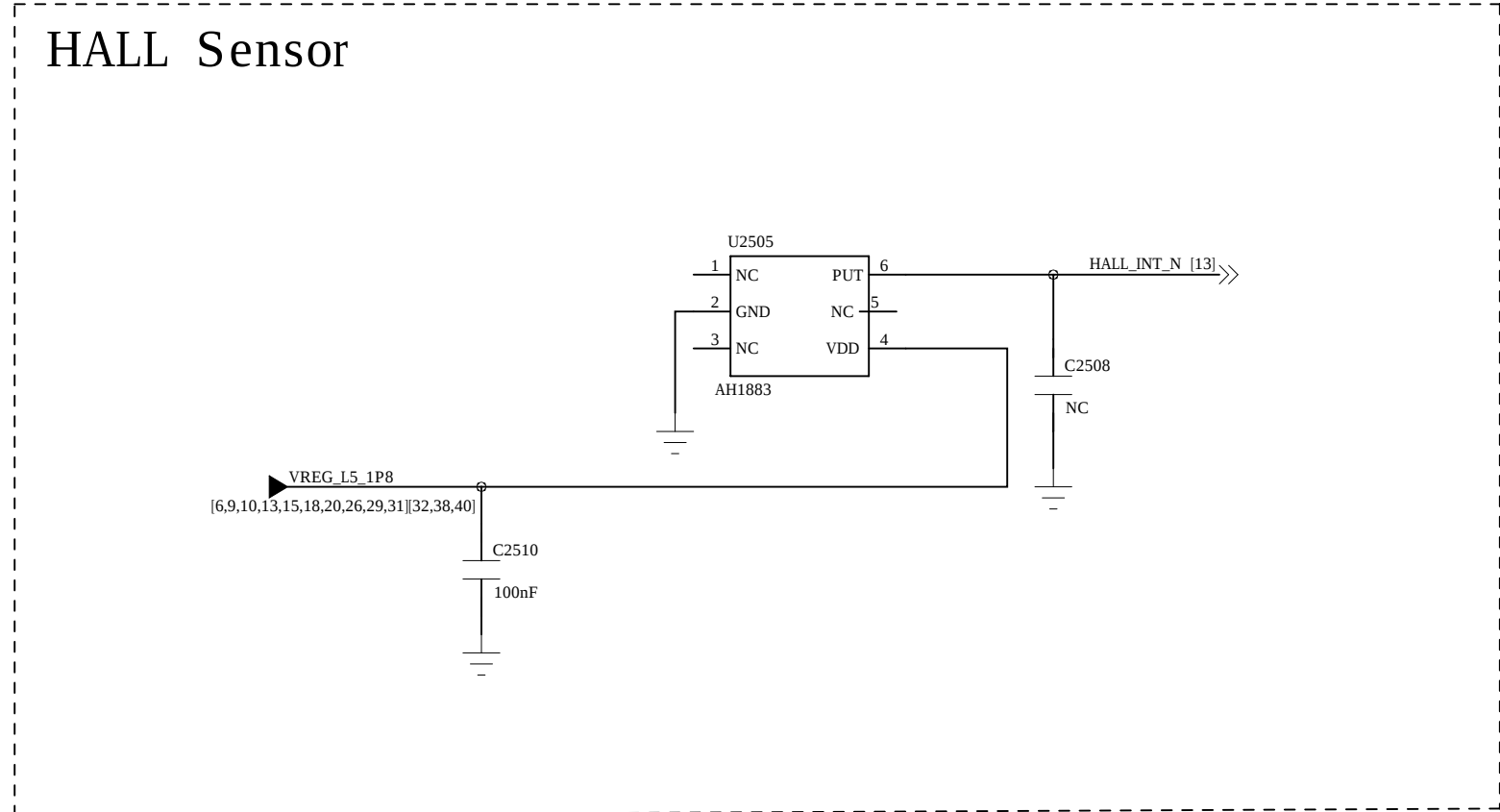
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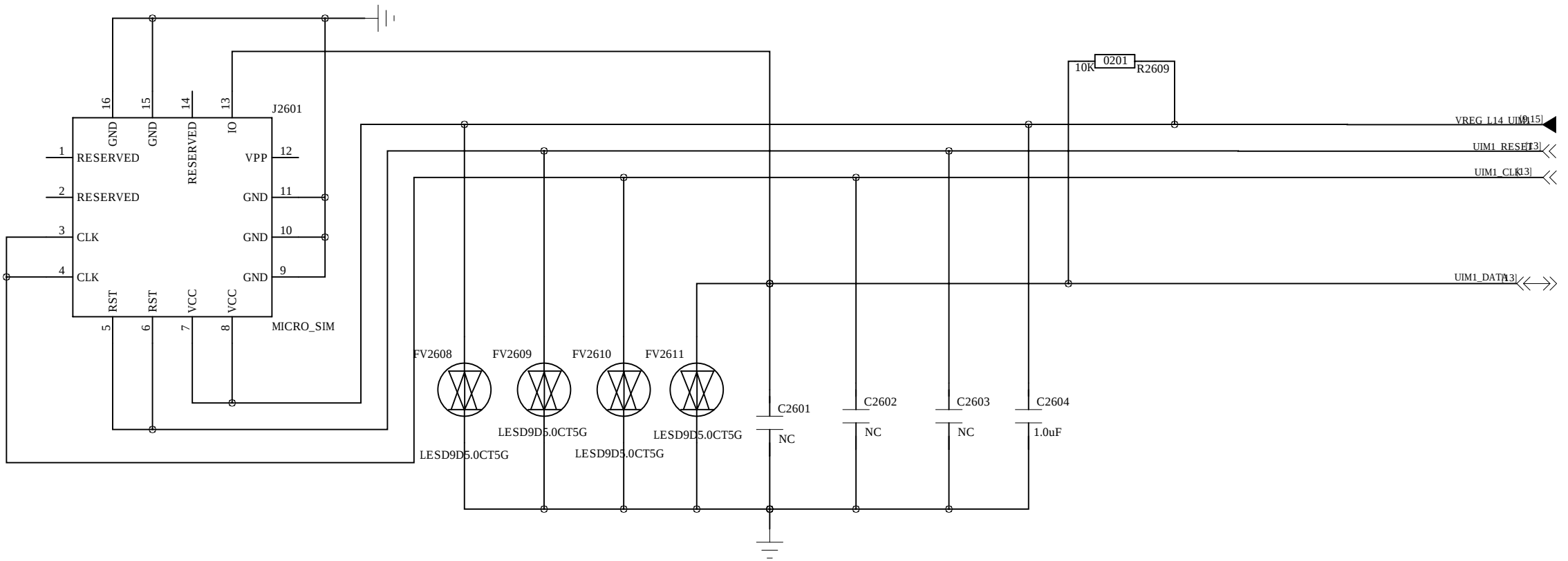
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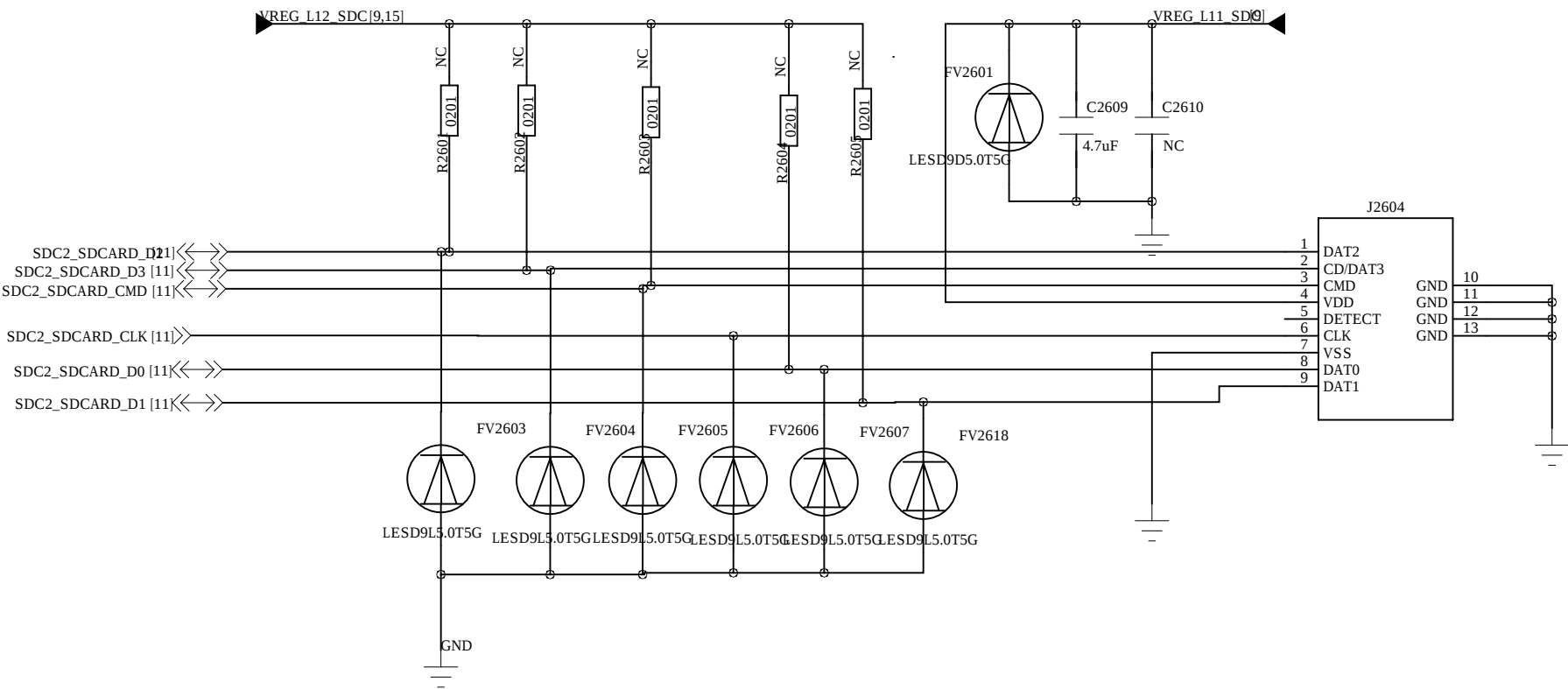
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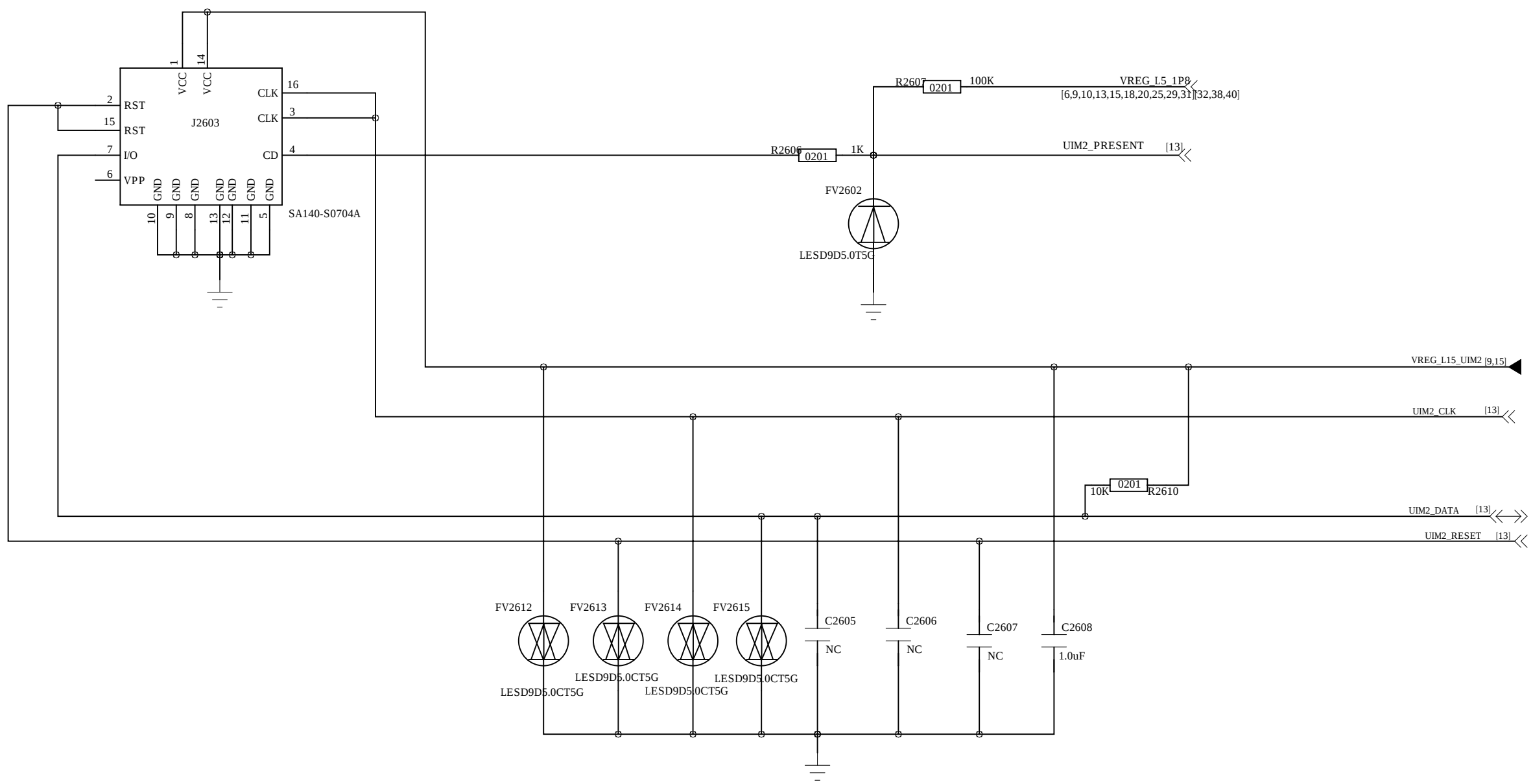
MICRO SIM



TF CARD



NANO SIM



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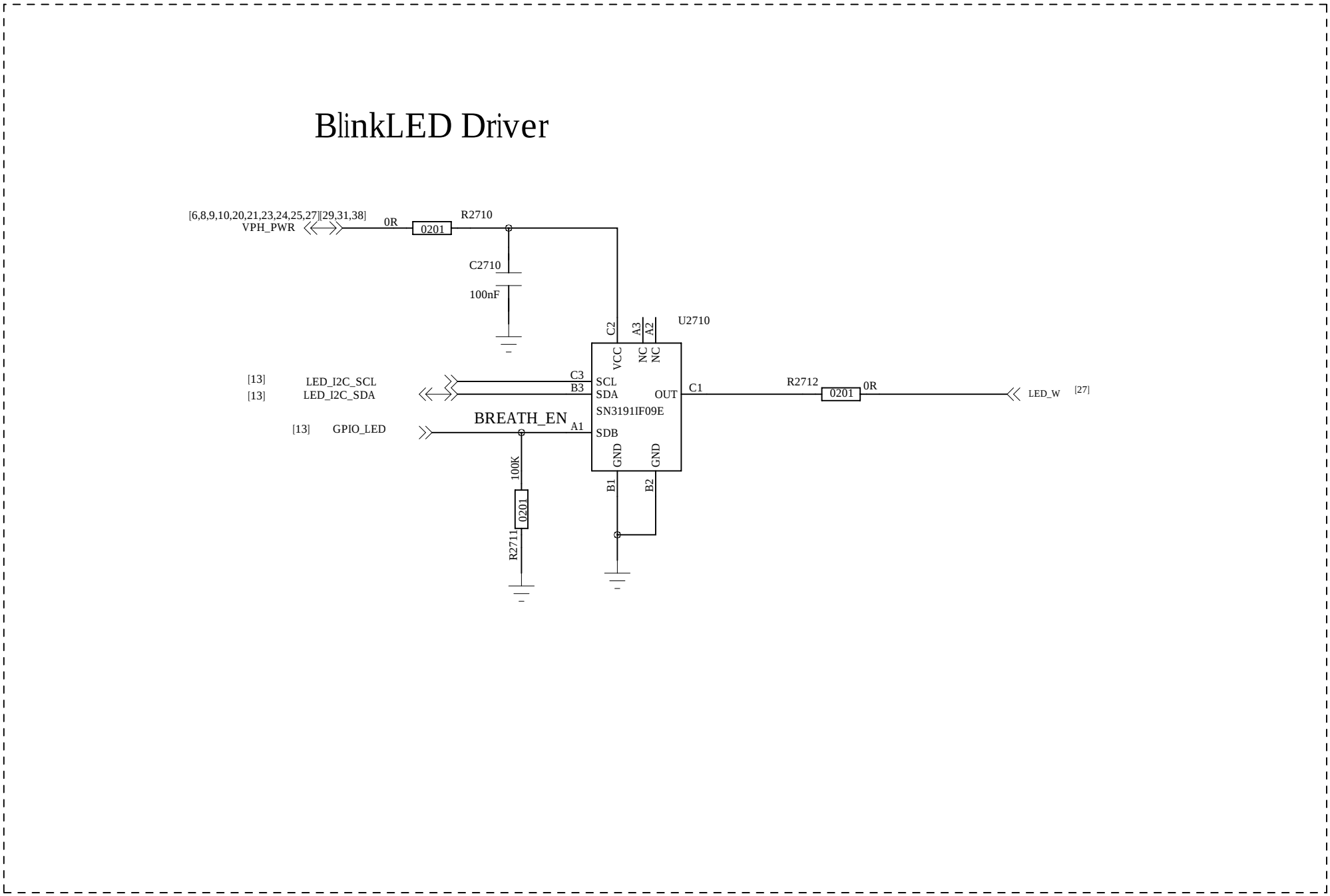
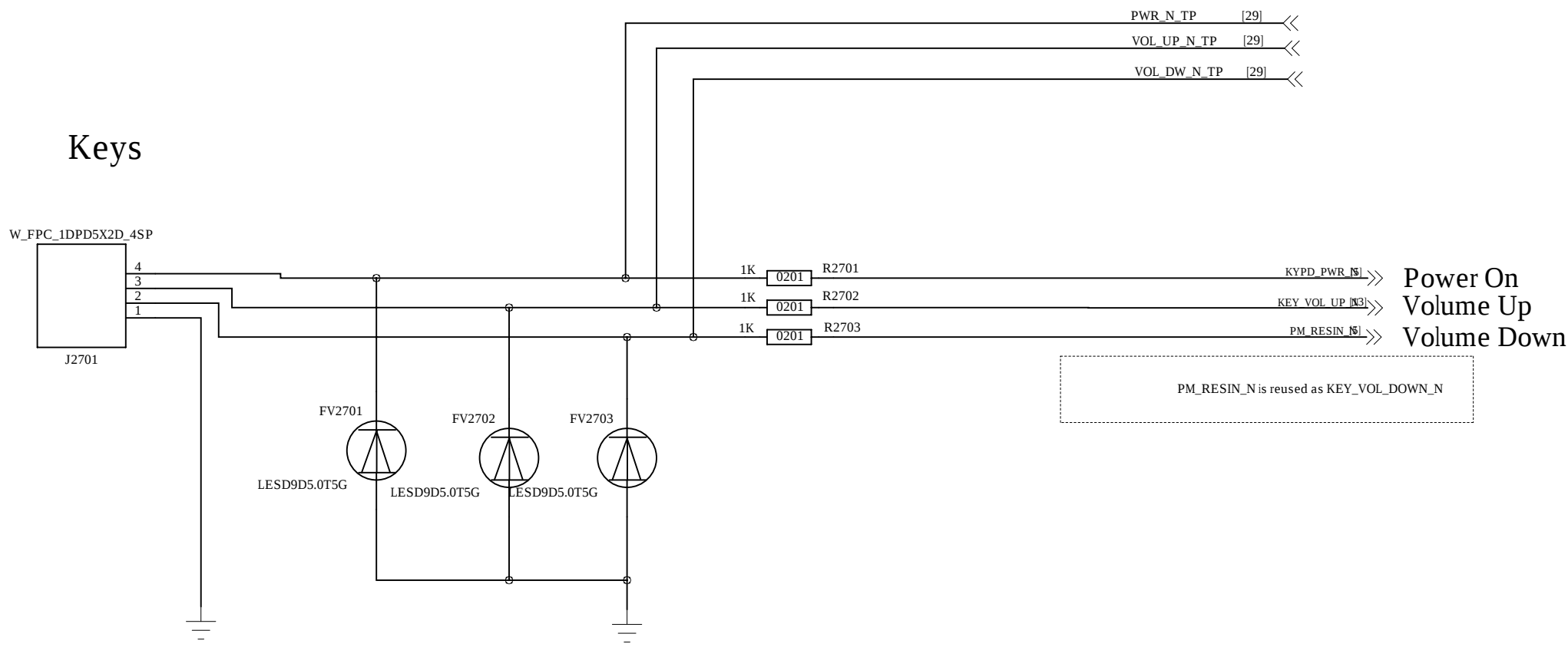
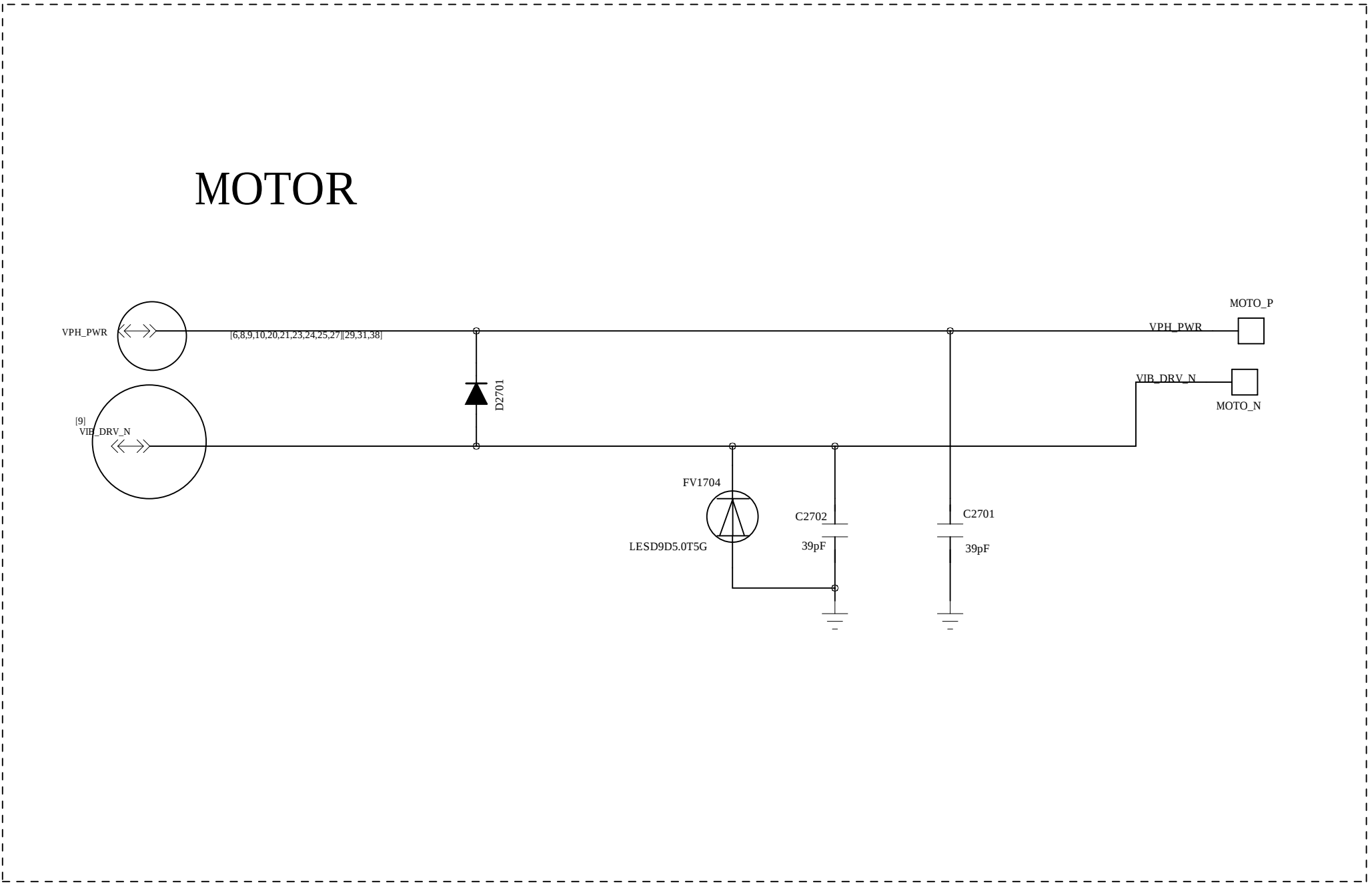
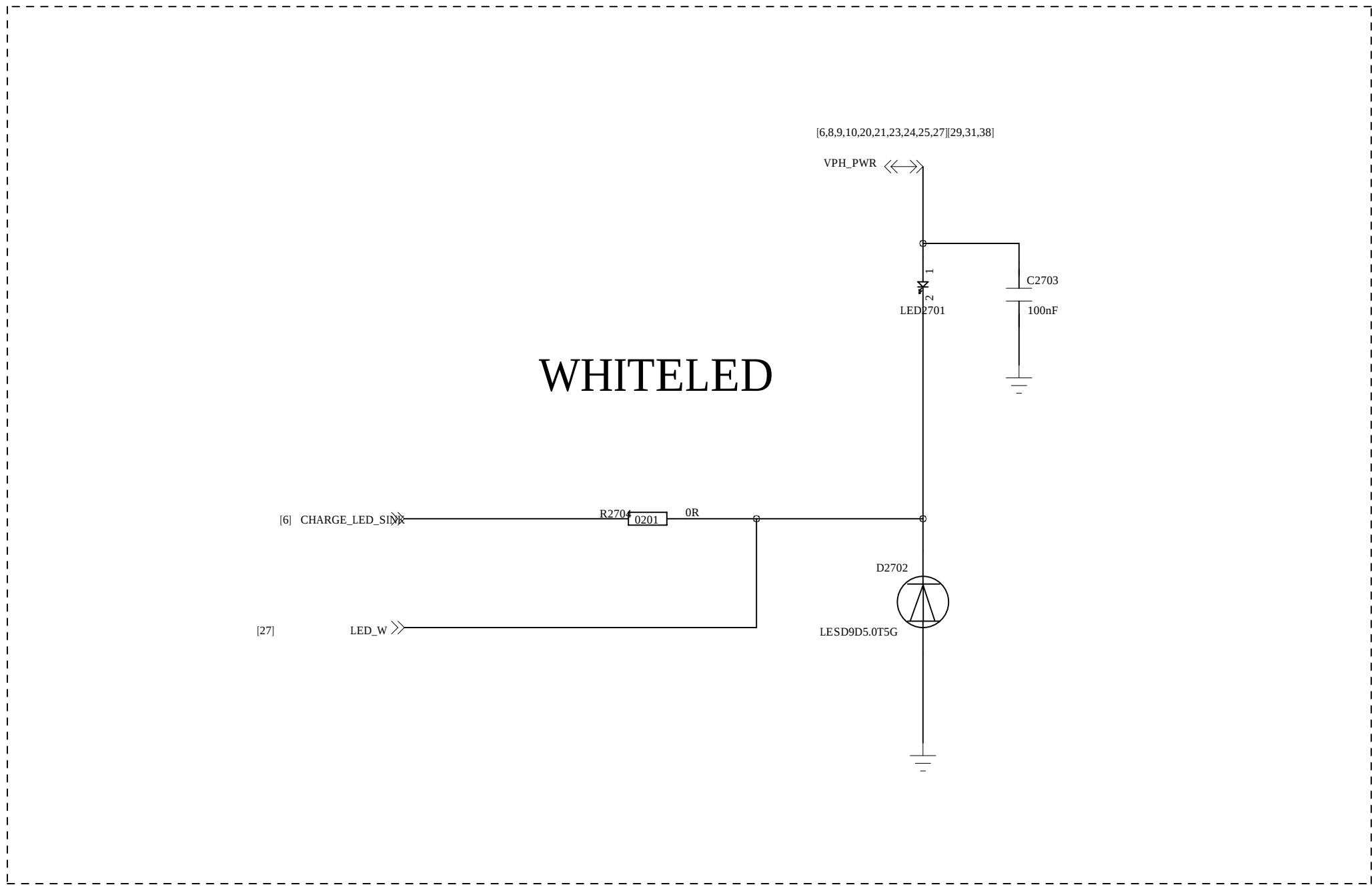
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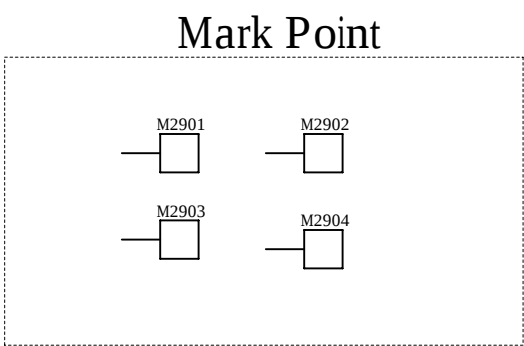
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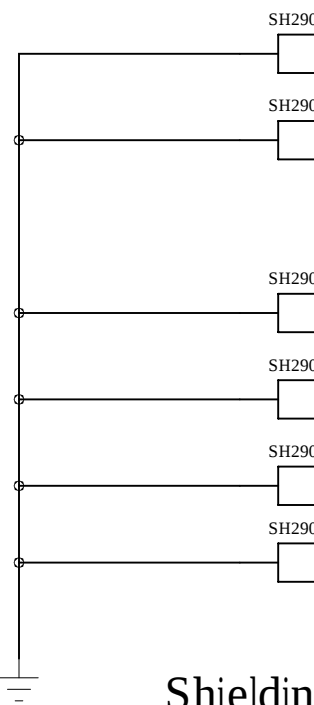
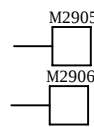
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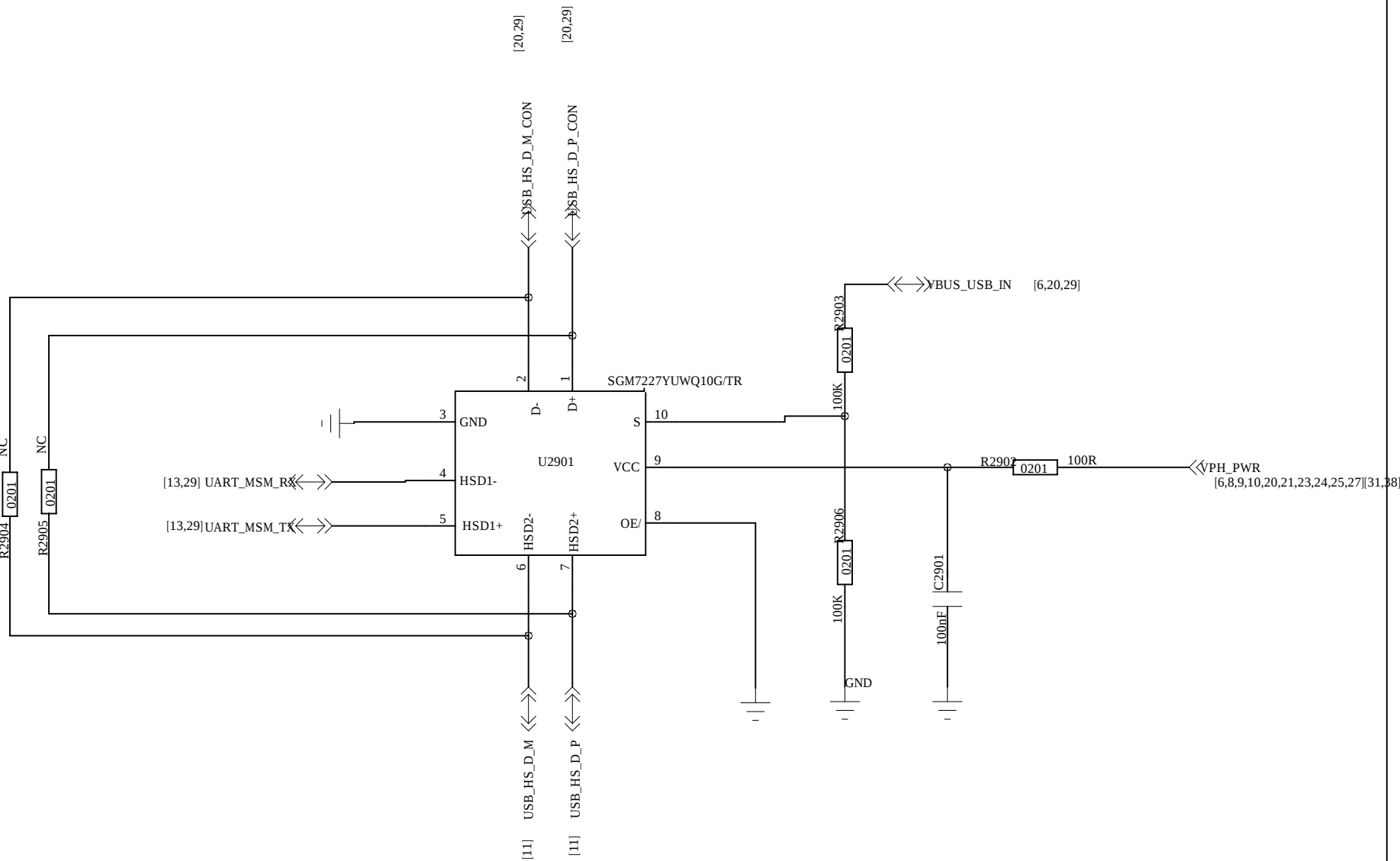


Square

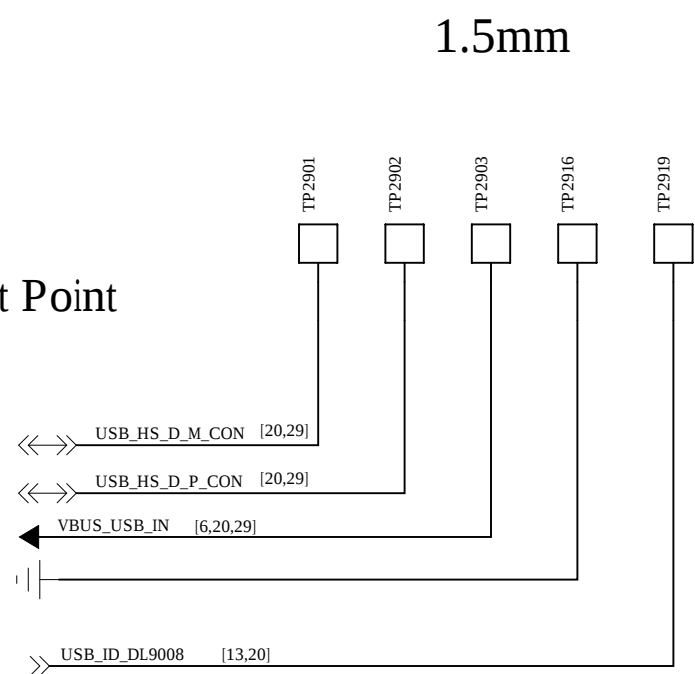


Shielding Frame

USB&UART

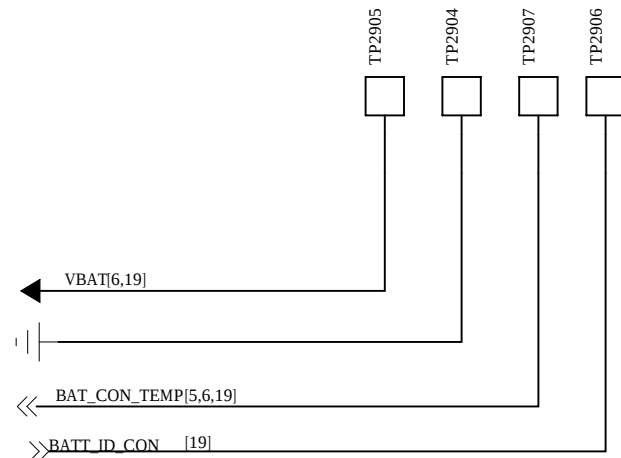


Test Point



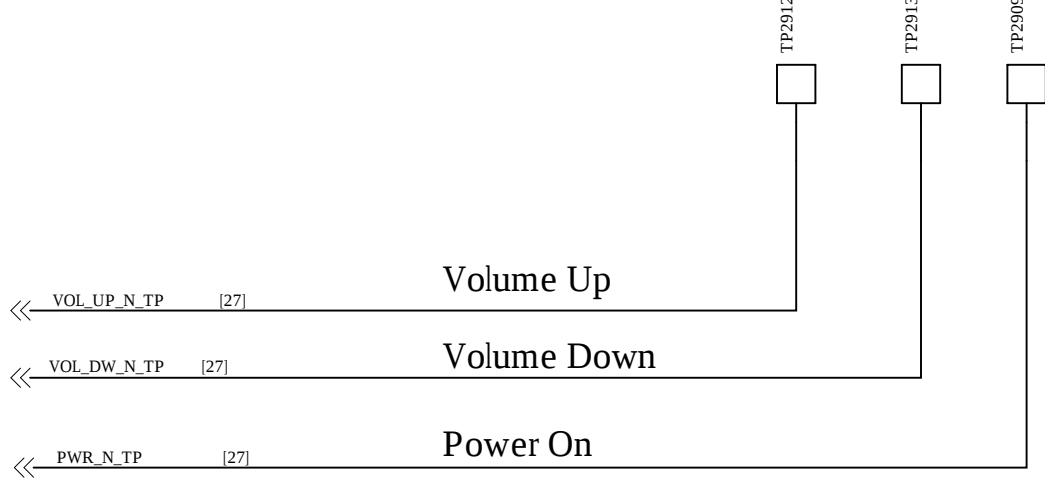
Add a sperate GND close to USBUSB interface if space allowable

1.5mm

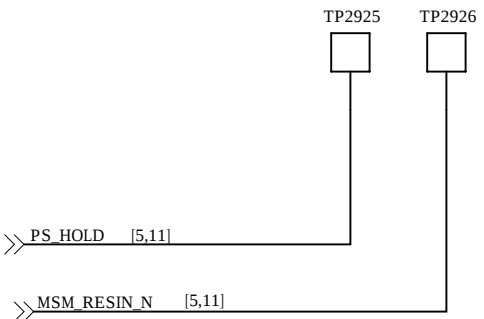


Add BAT_CON_ID of TP if battery has ID pin

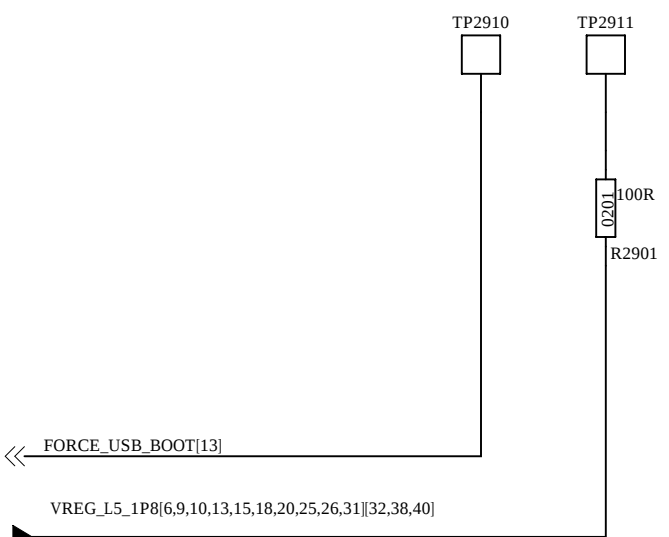
1.5mm



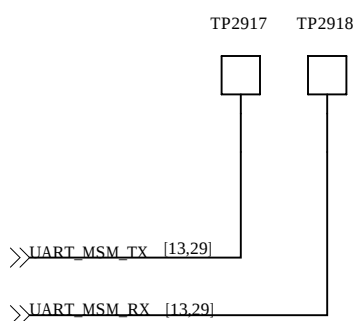
0.5mm



1mm



0.7mm



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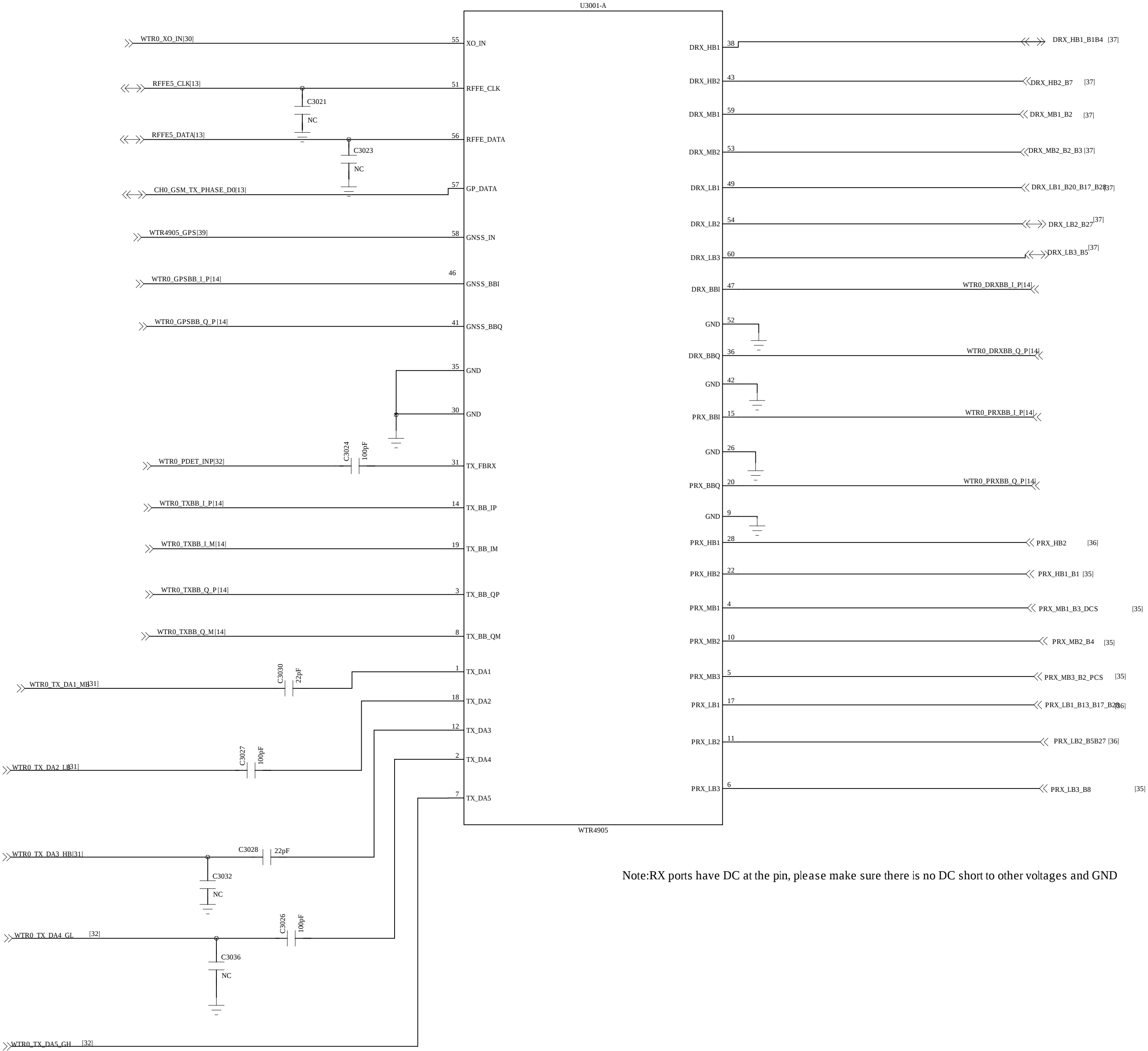
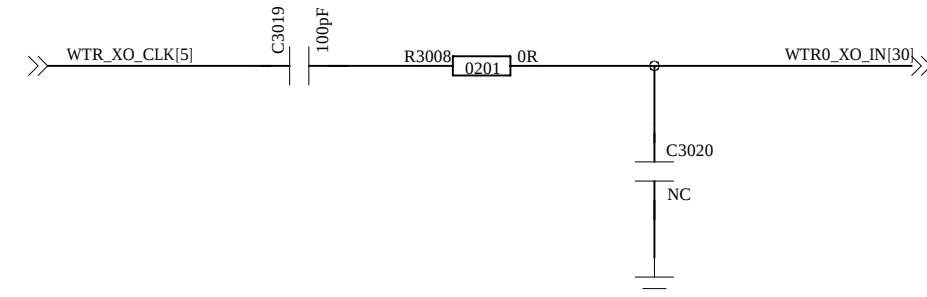
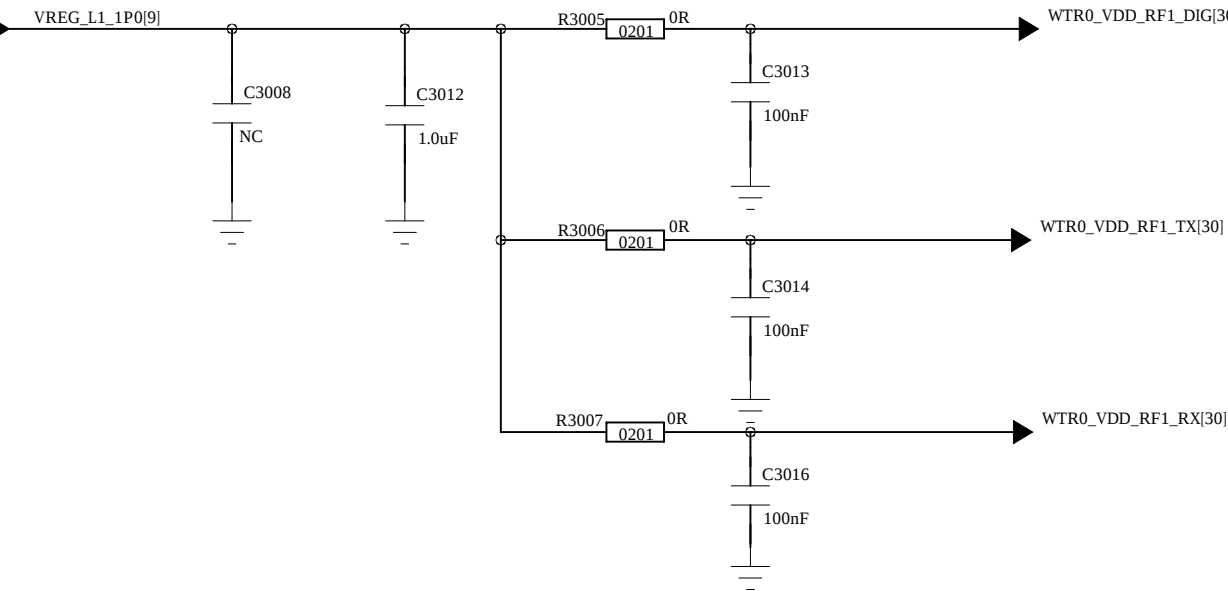
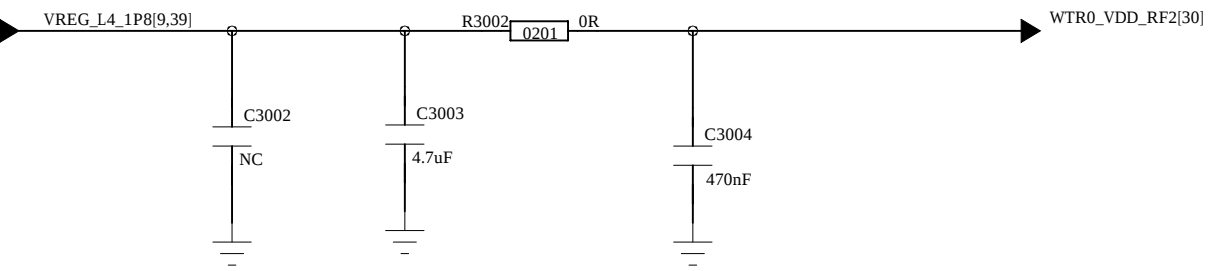
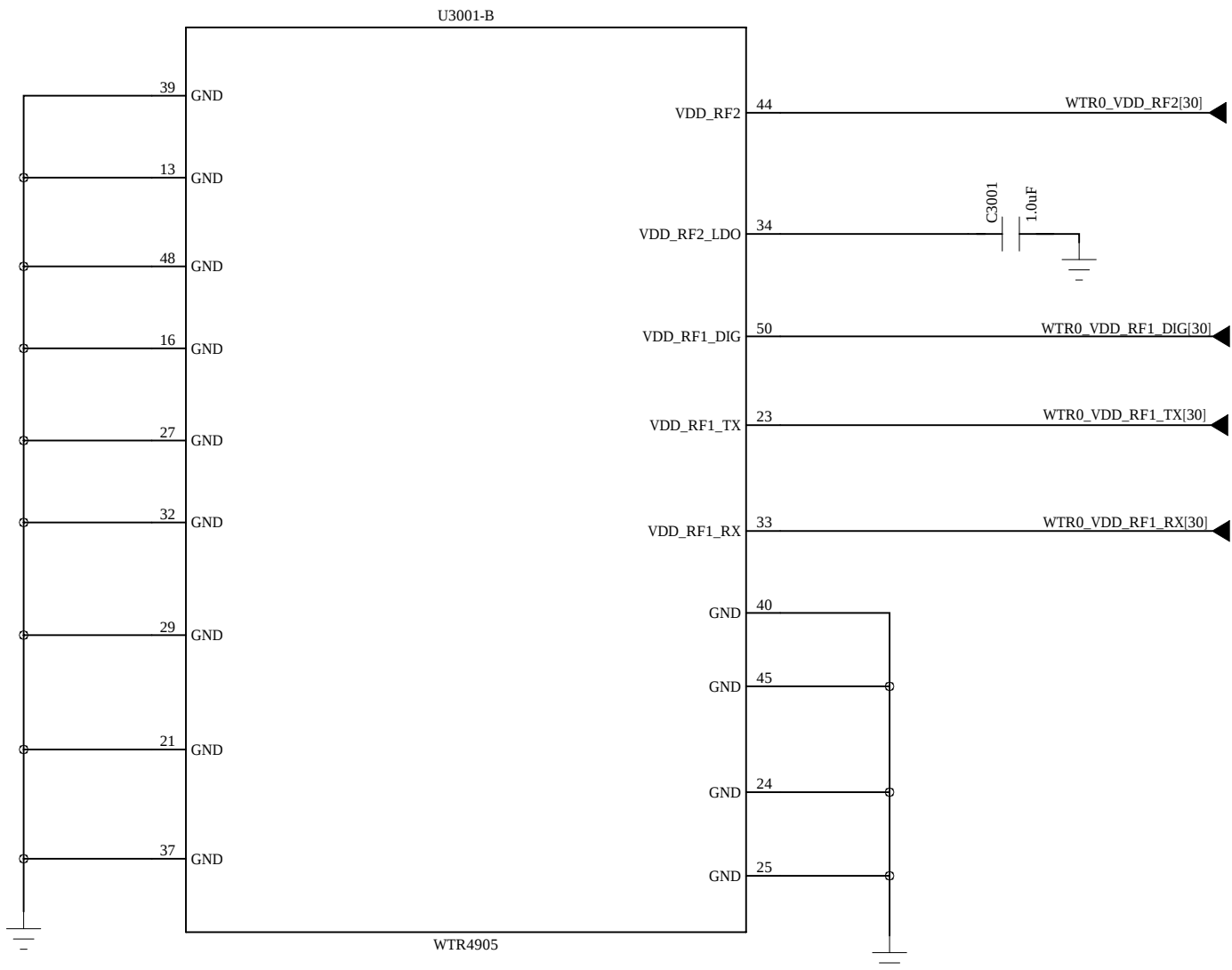
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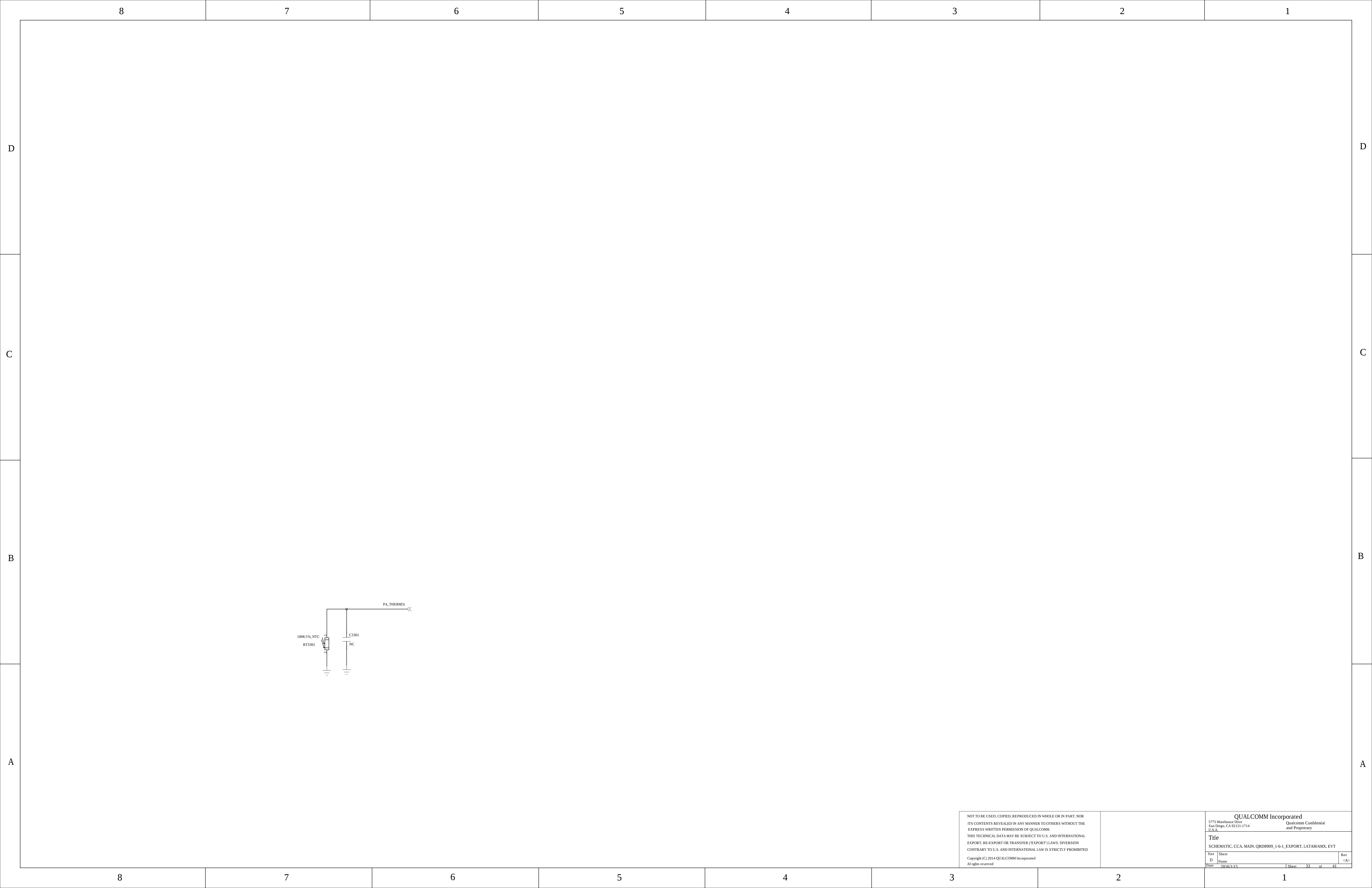
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Note:RX ports have DC at the pin, please make sure there is no DC short to other voltages and GND

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	8	7	6	5	4	3	2	1

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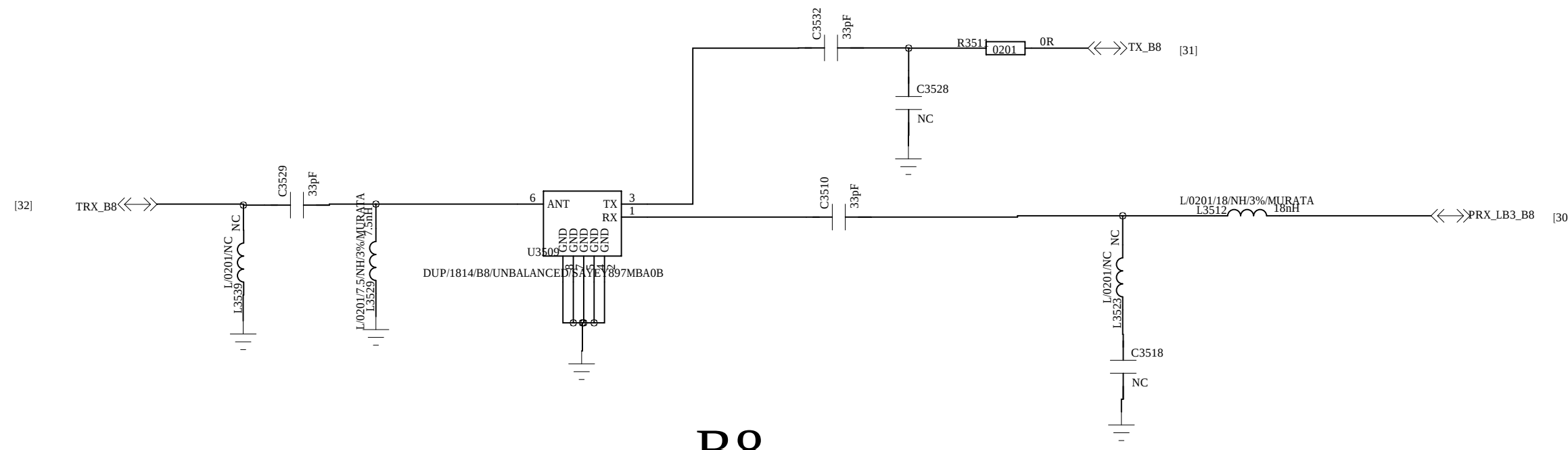
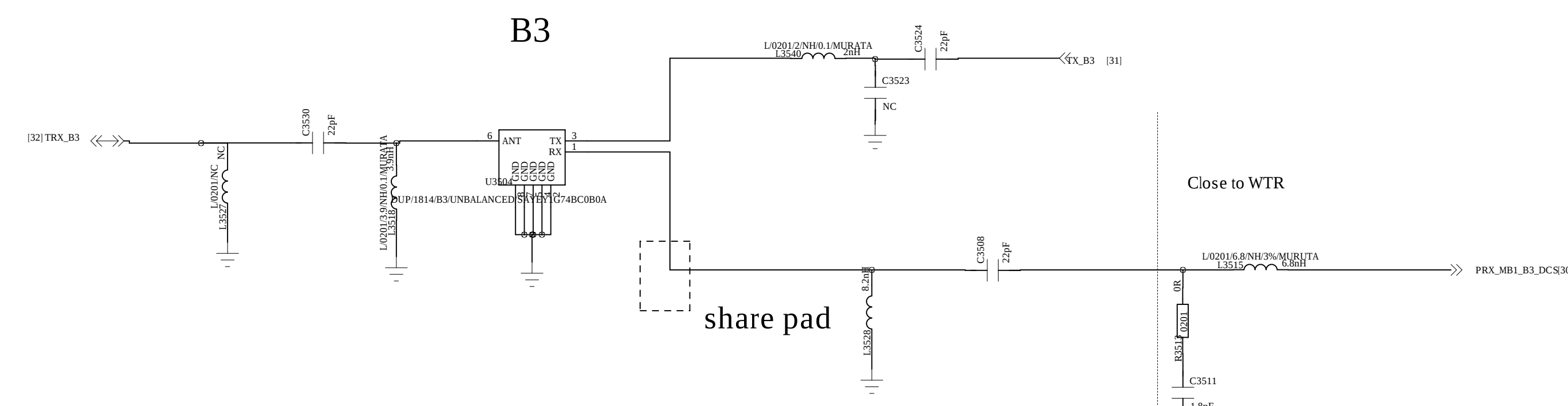
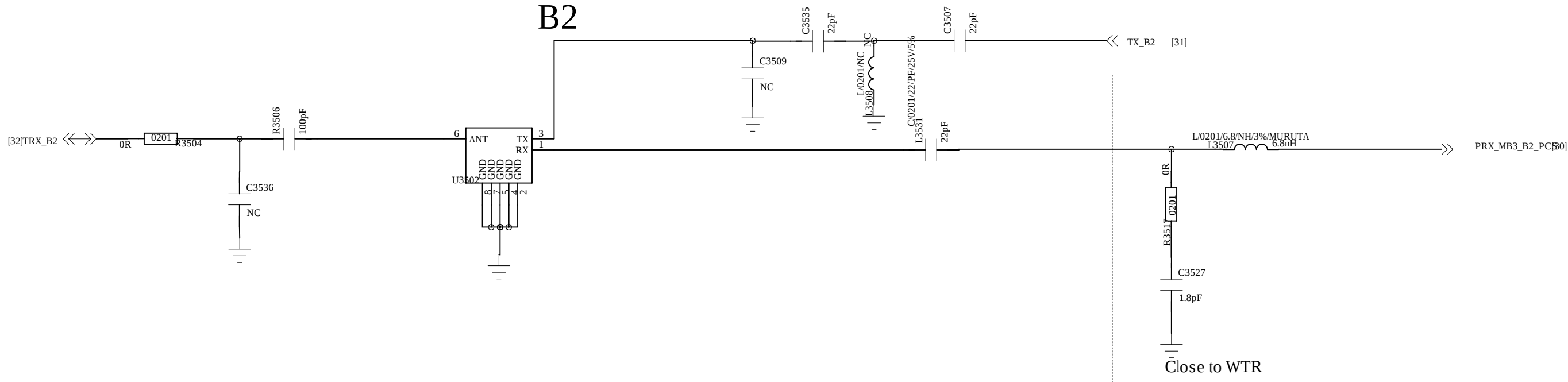
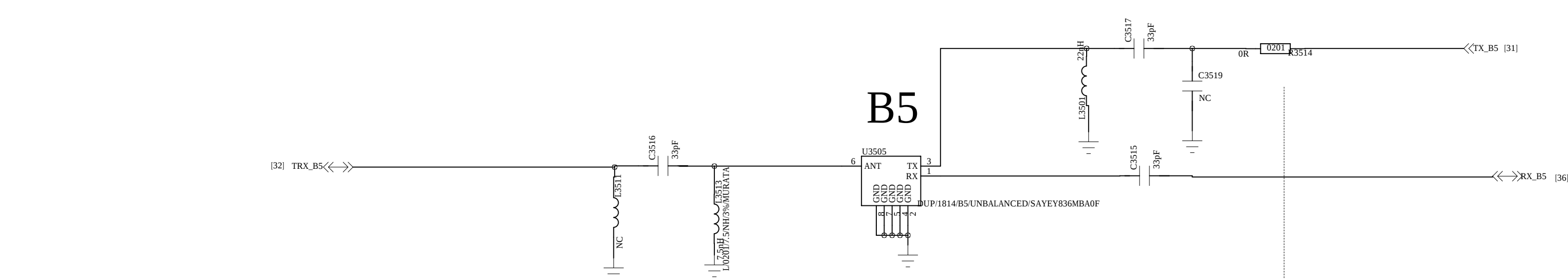
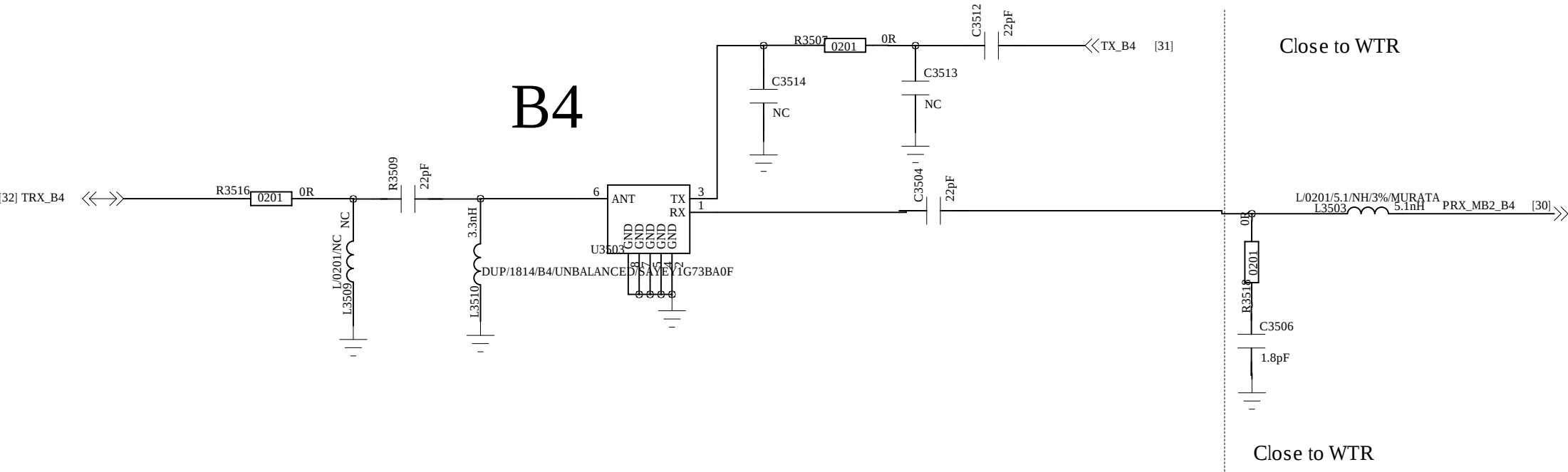
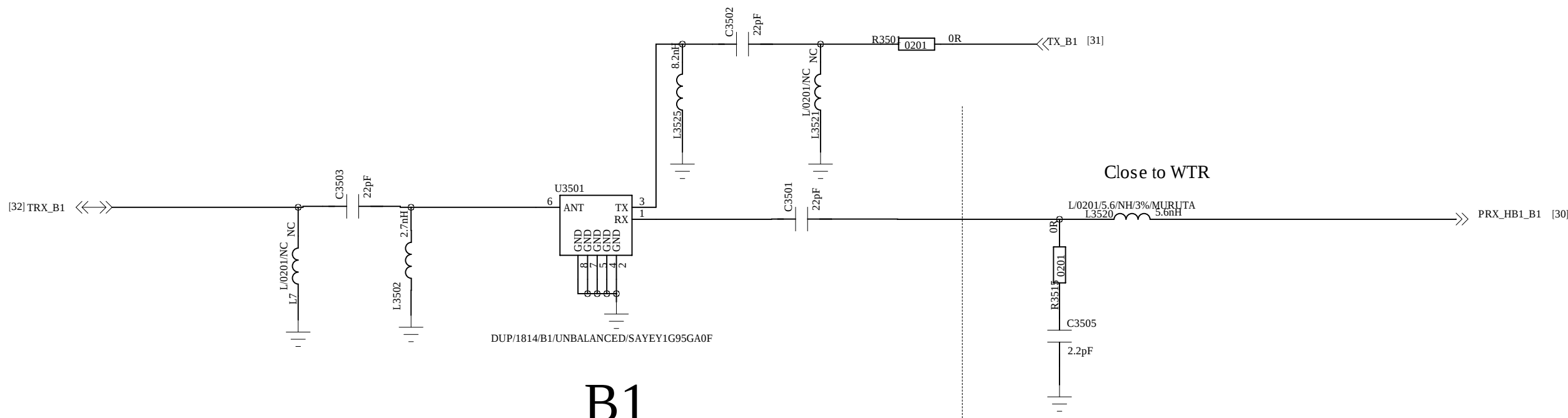
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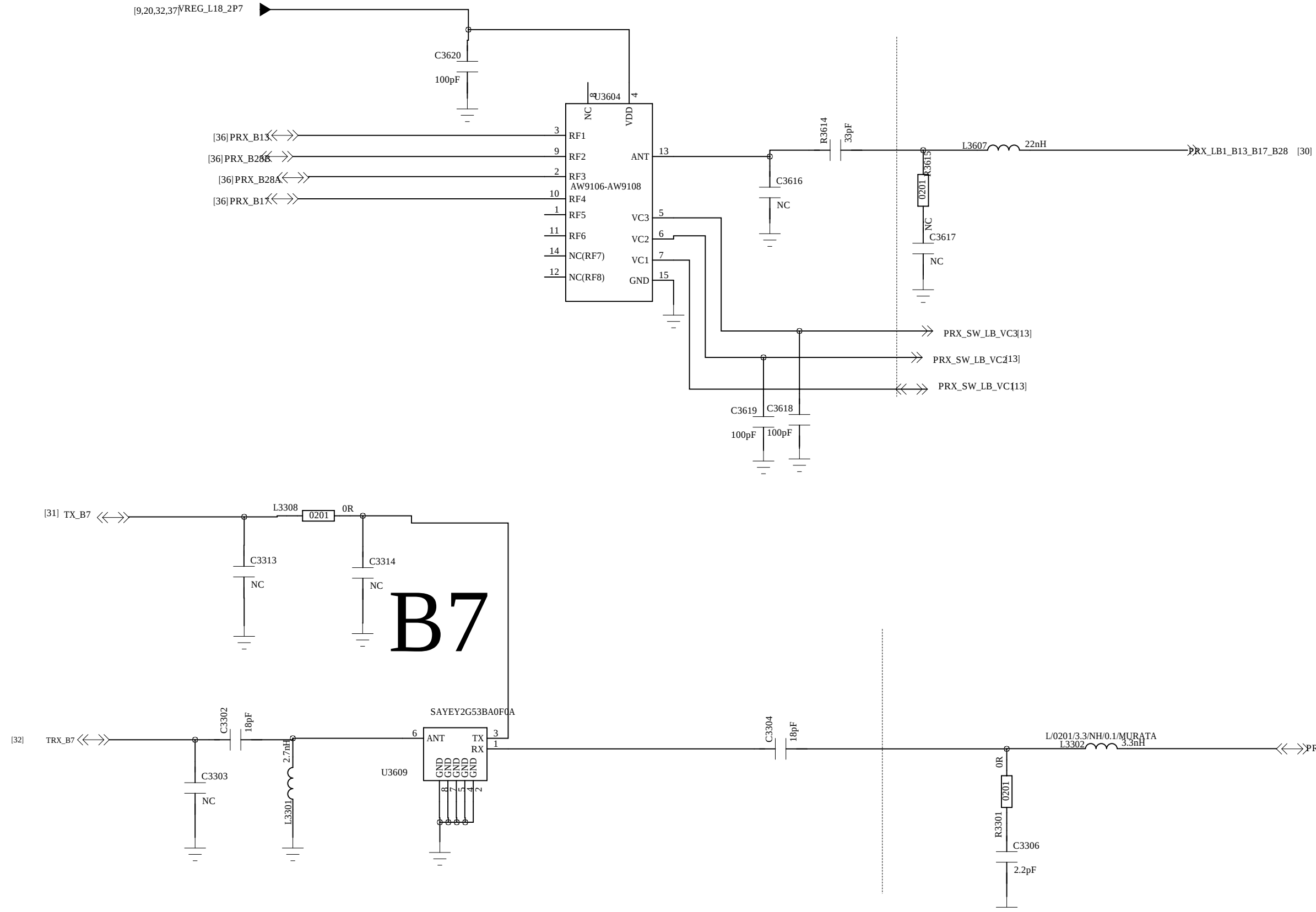
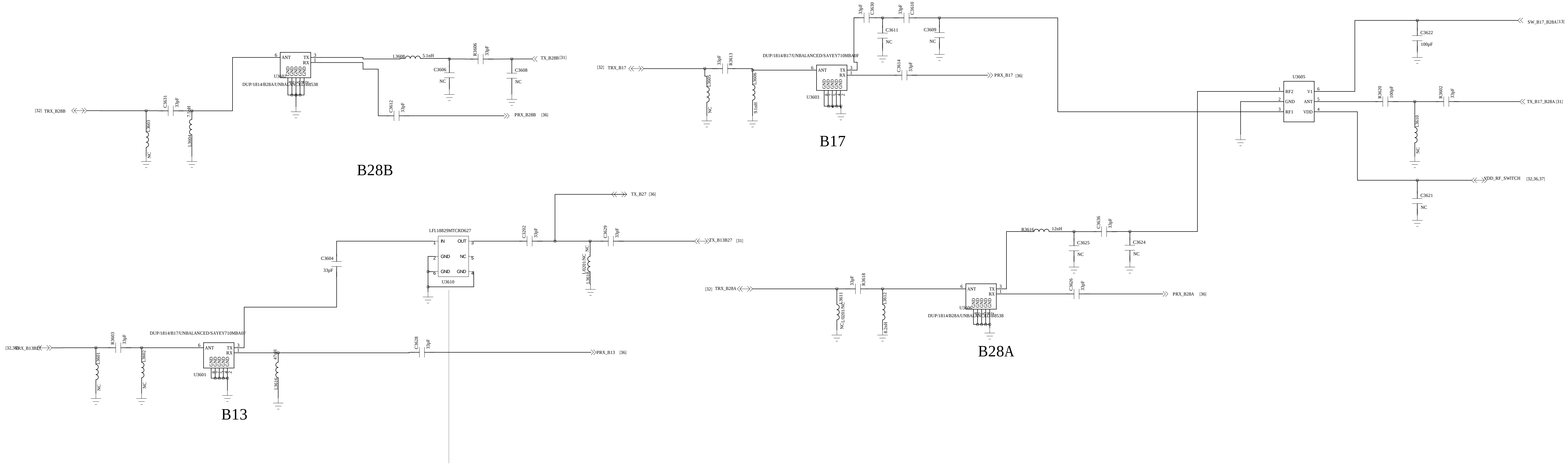
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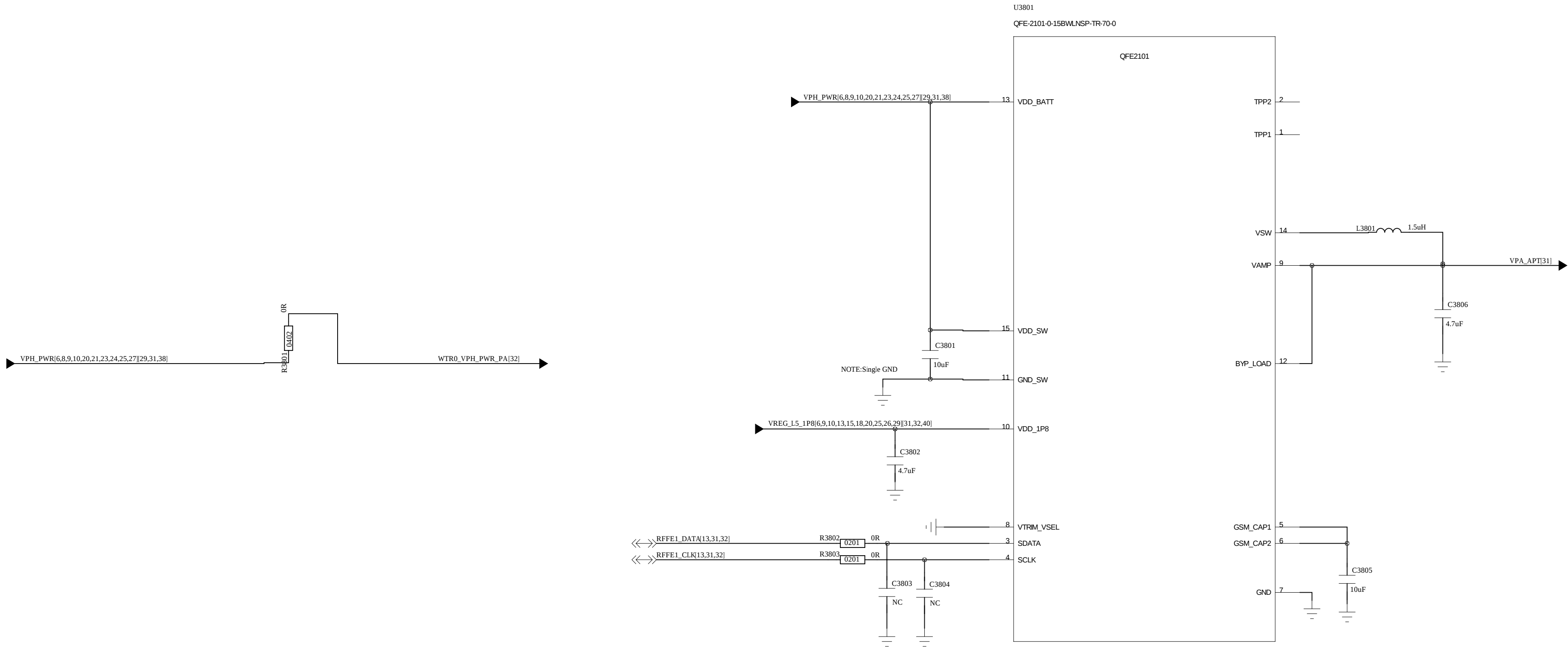
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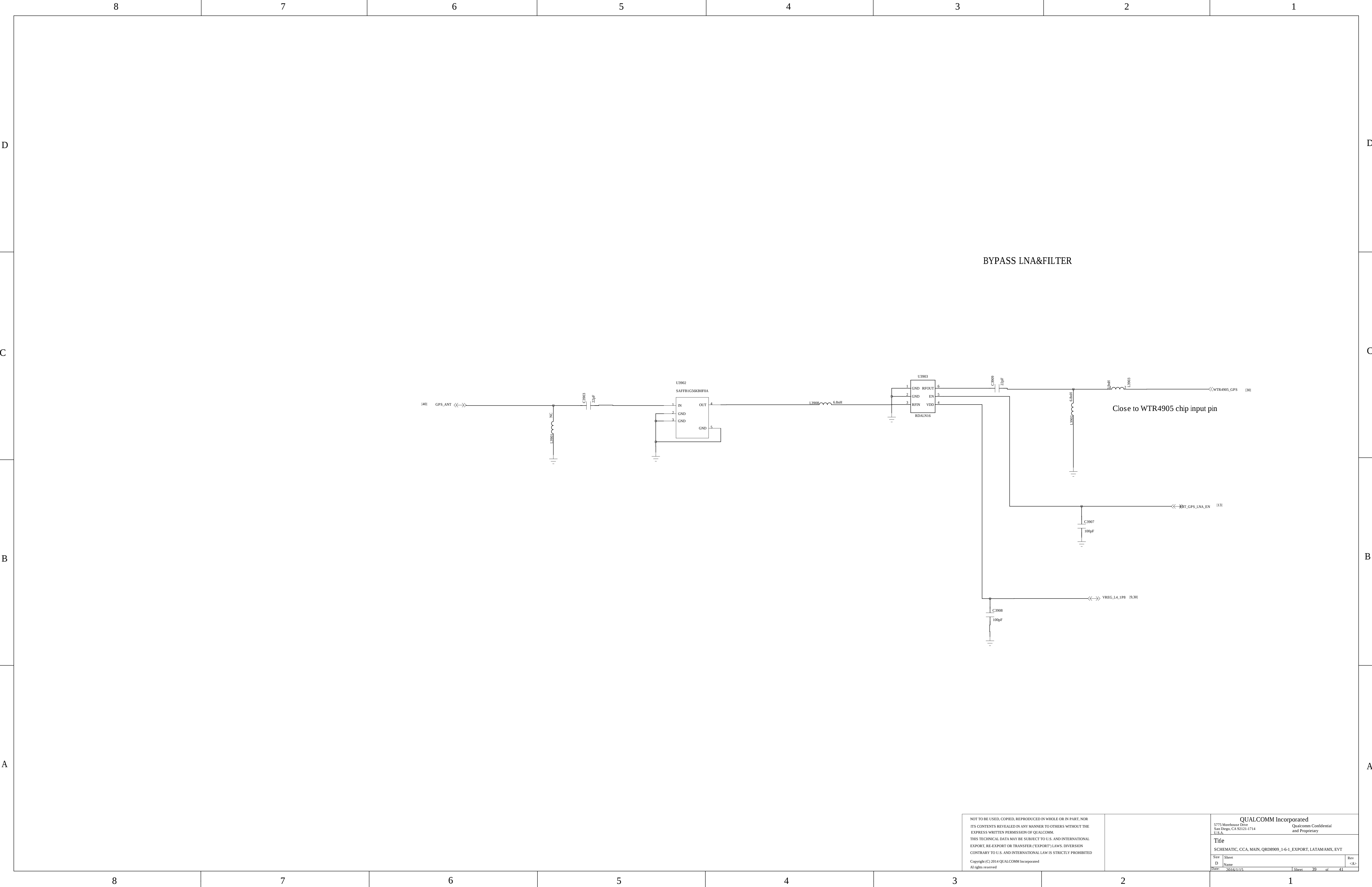
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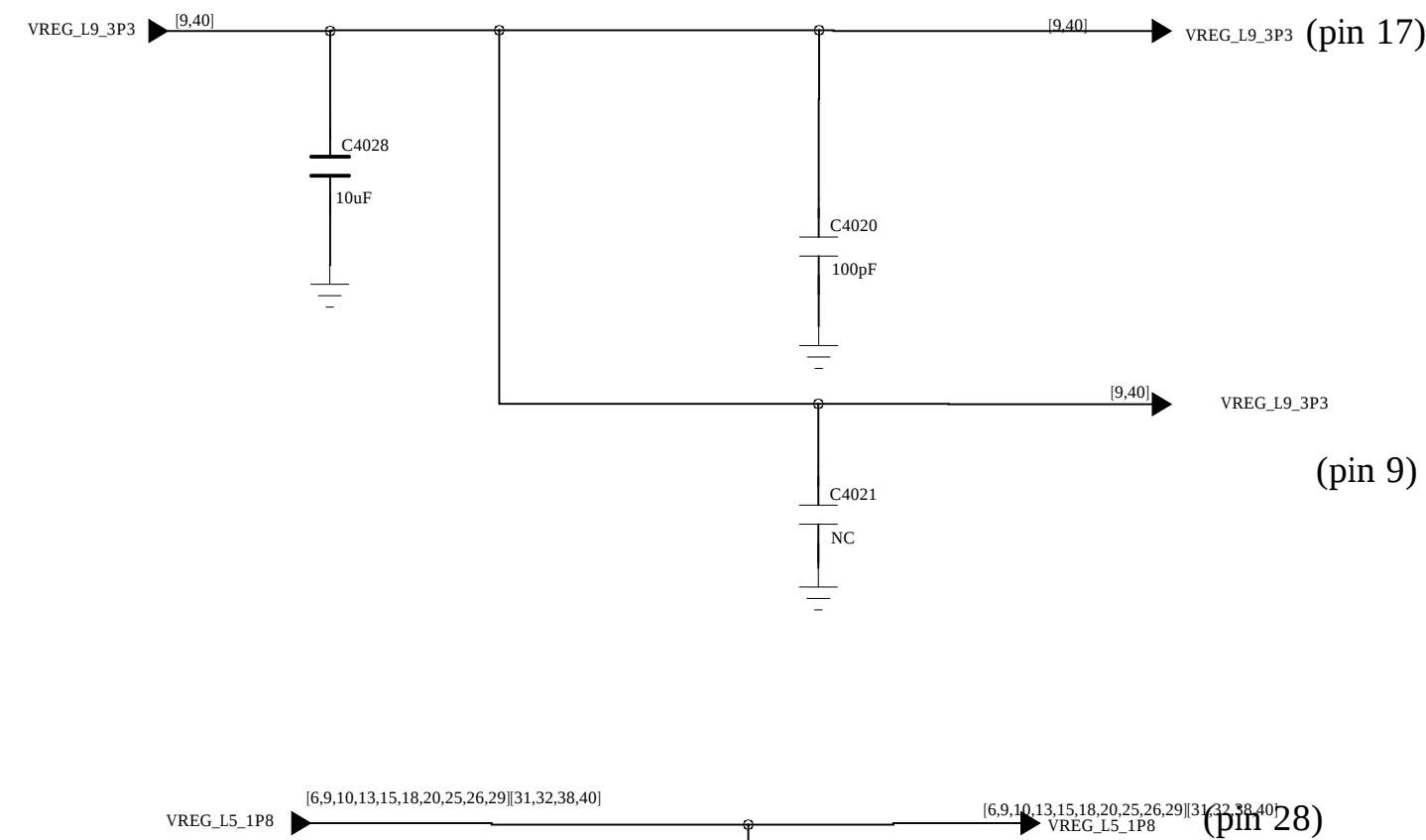
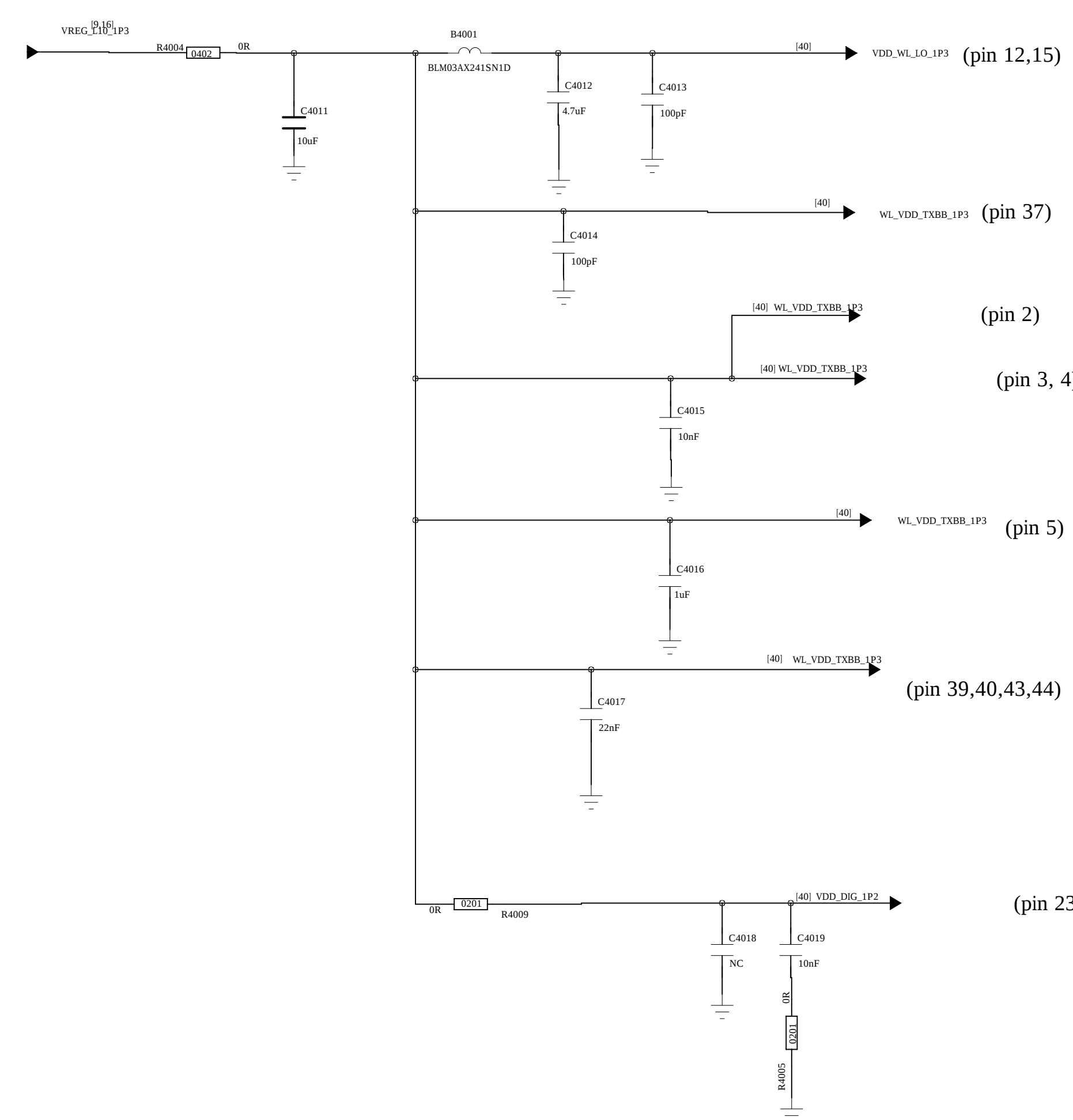
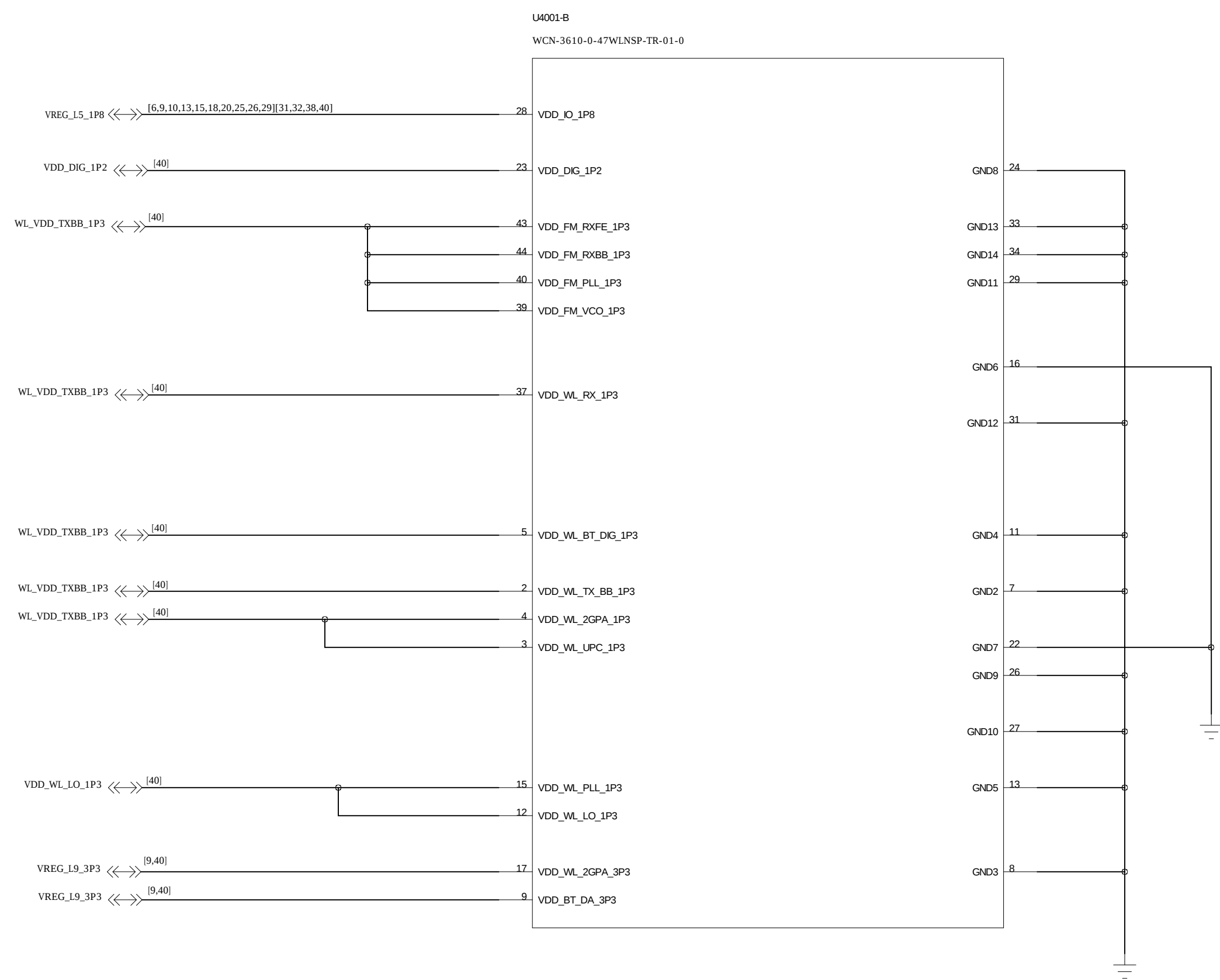
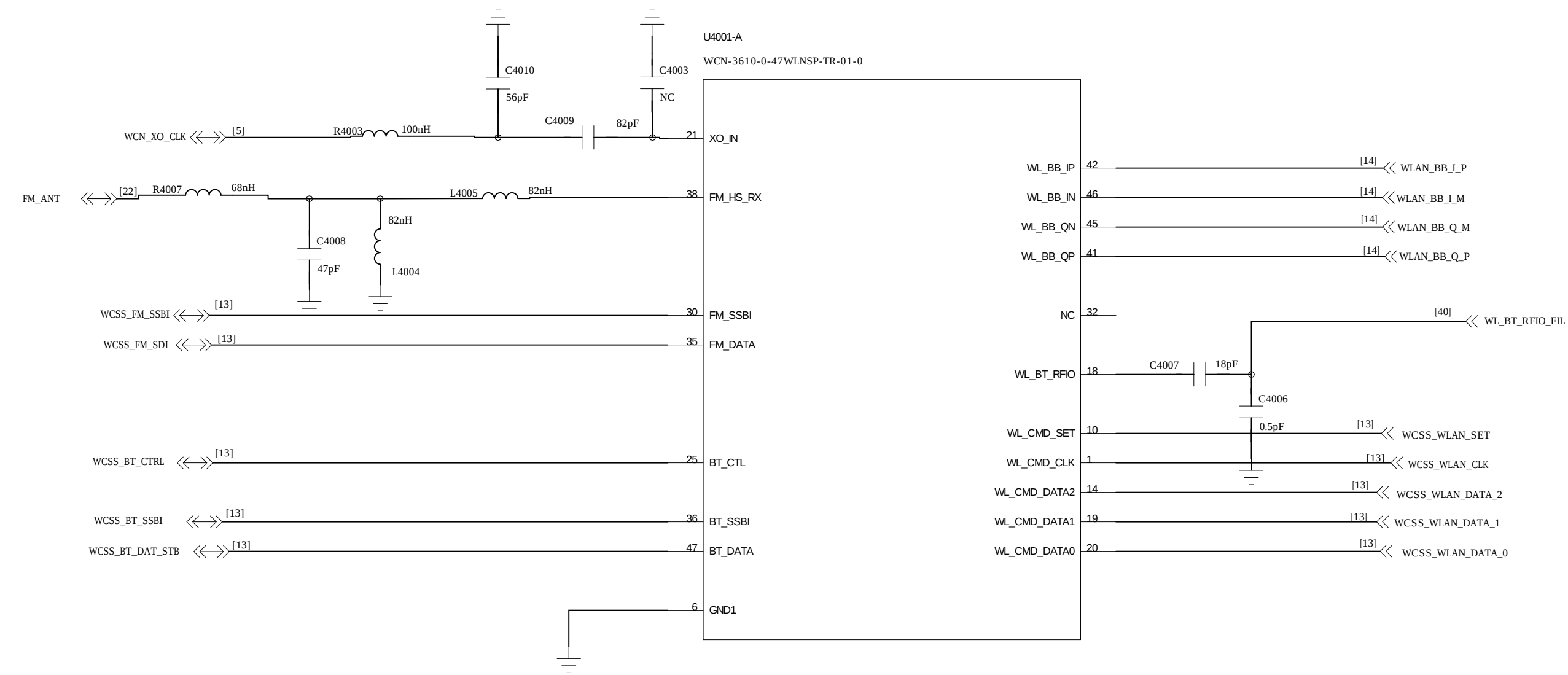
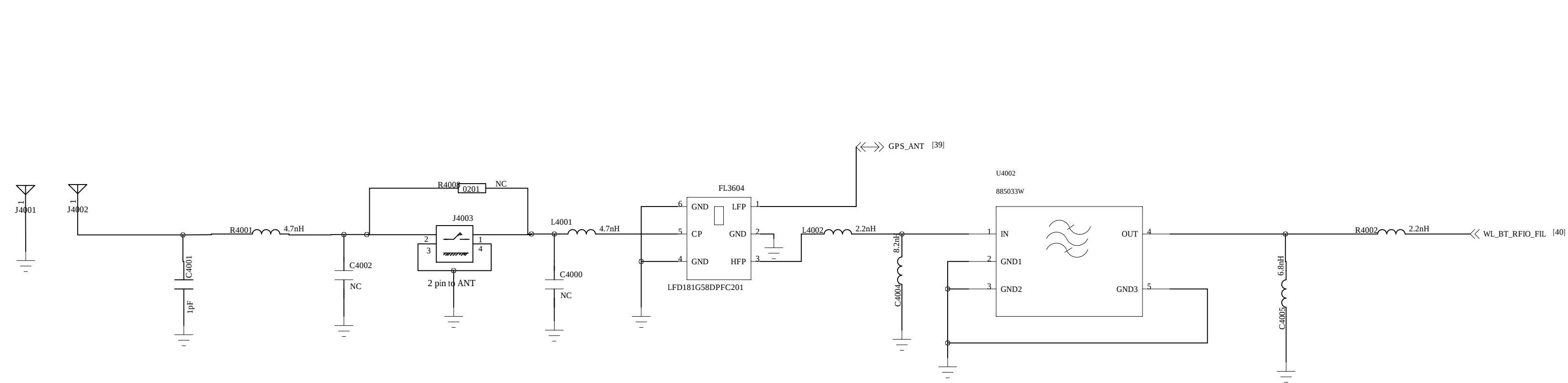
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Item	Description	Reason										
C									C			
B									B			
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