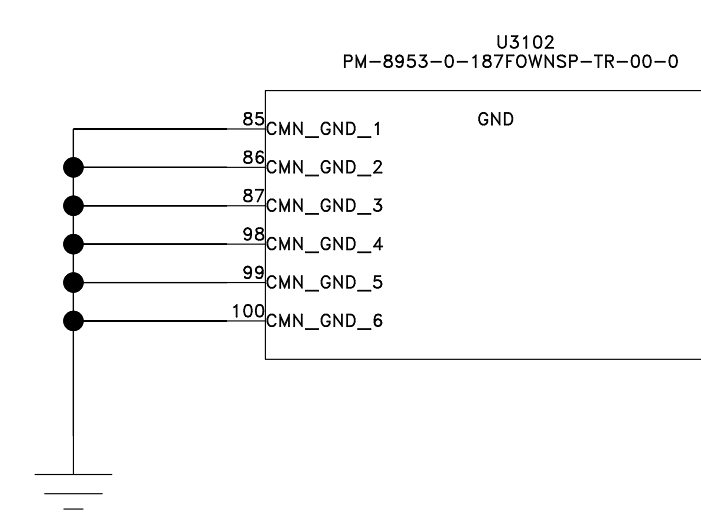
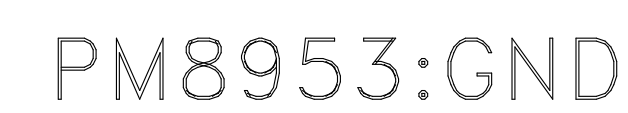
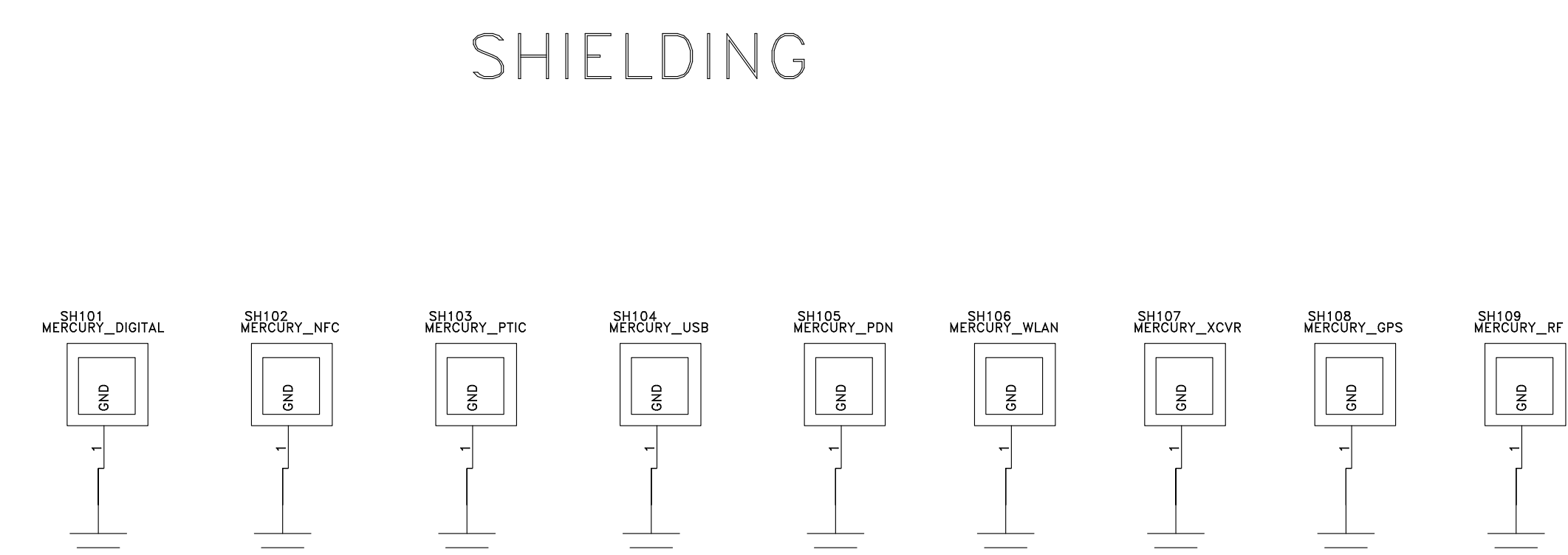
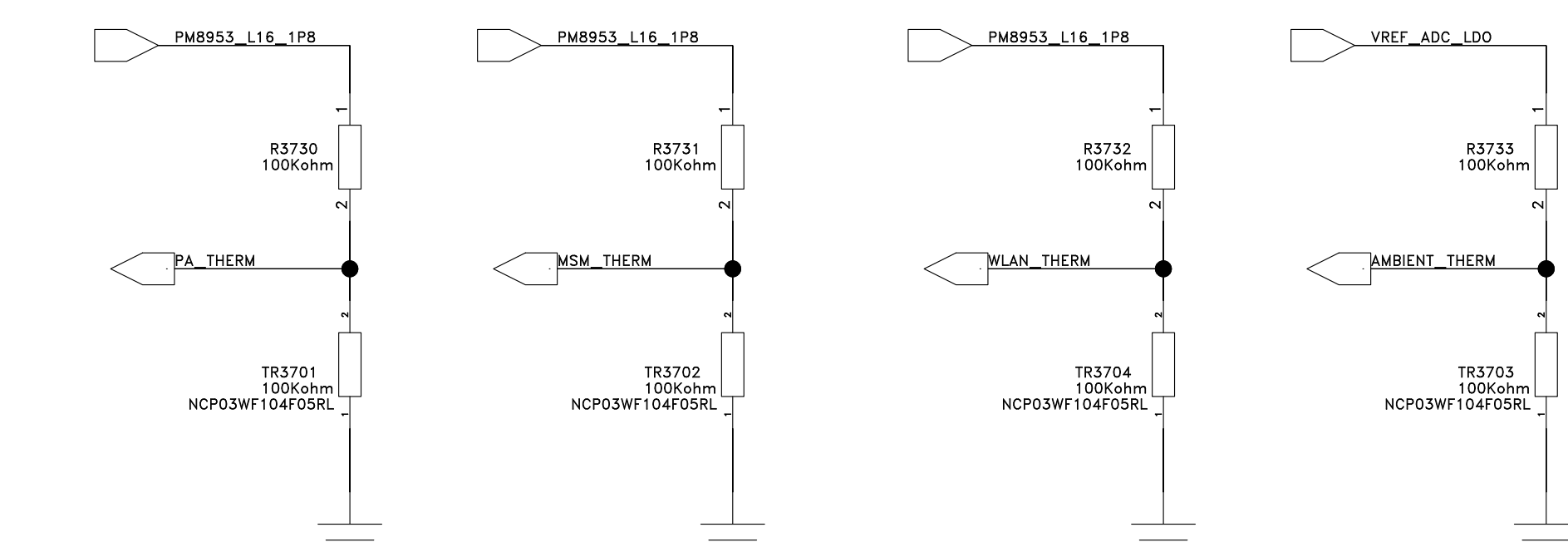
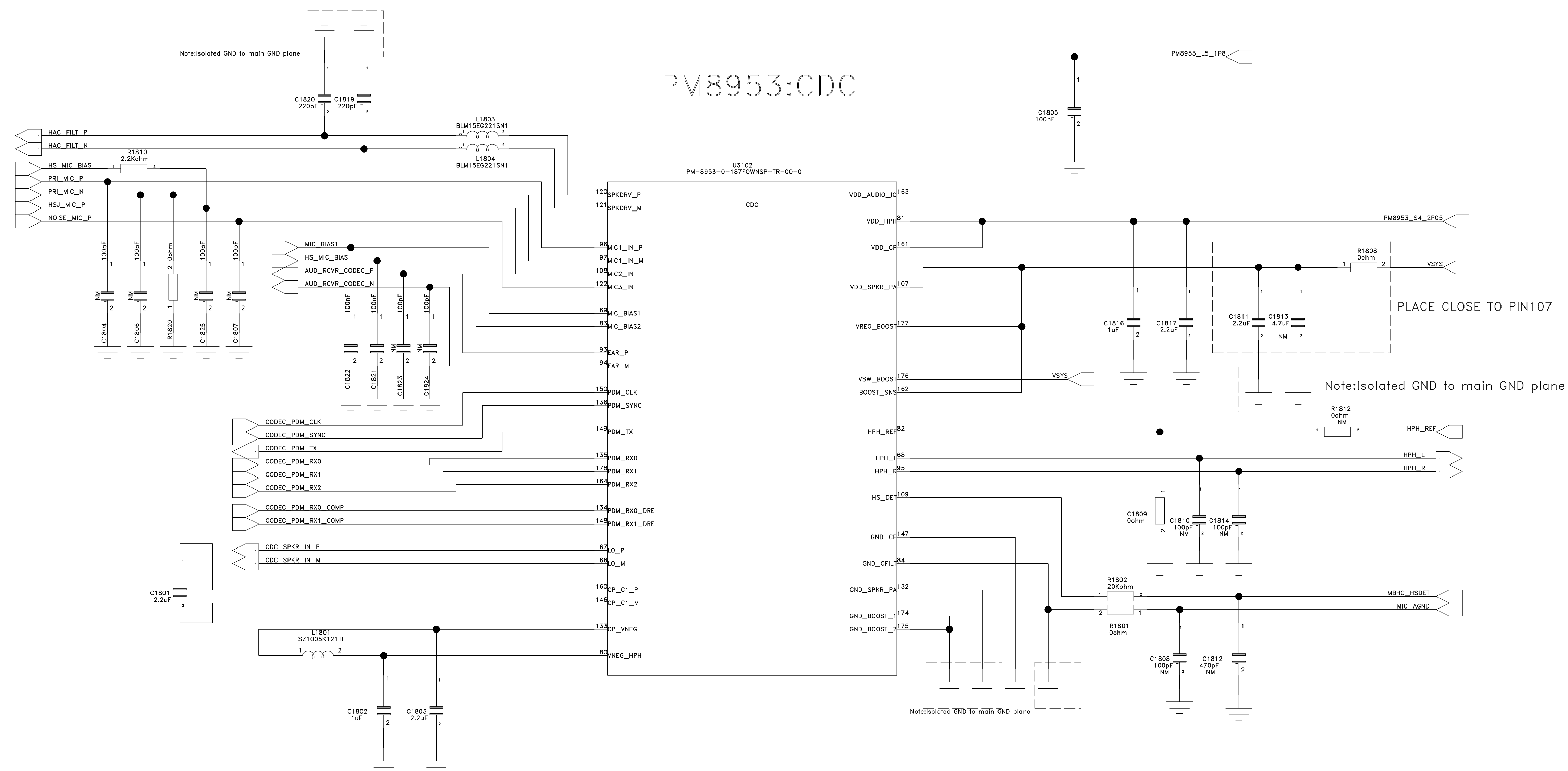
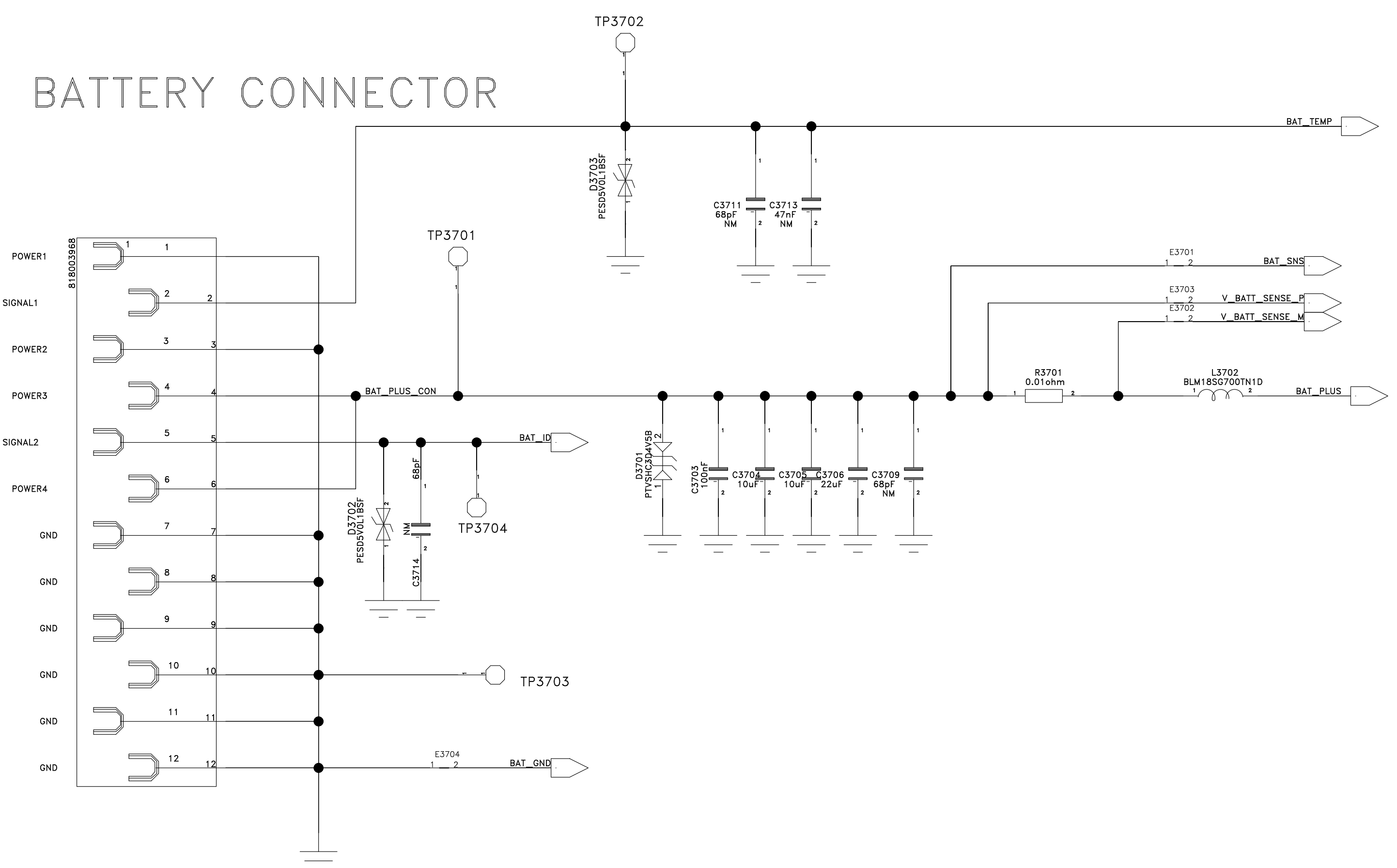
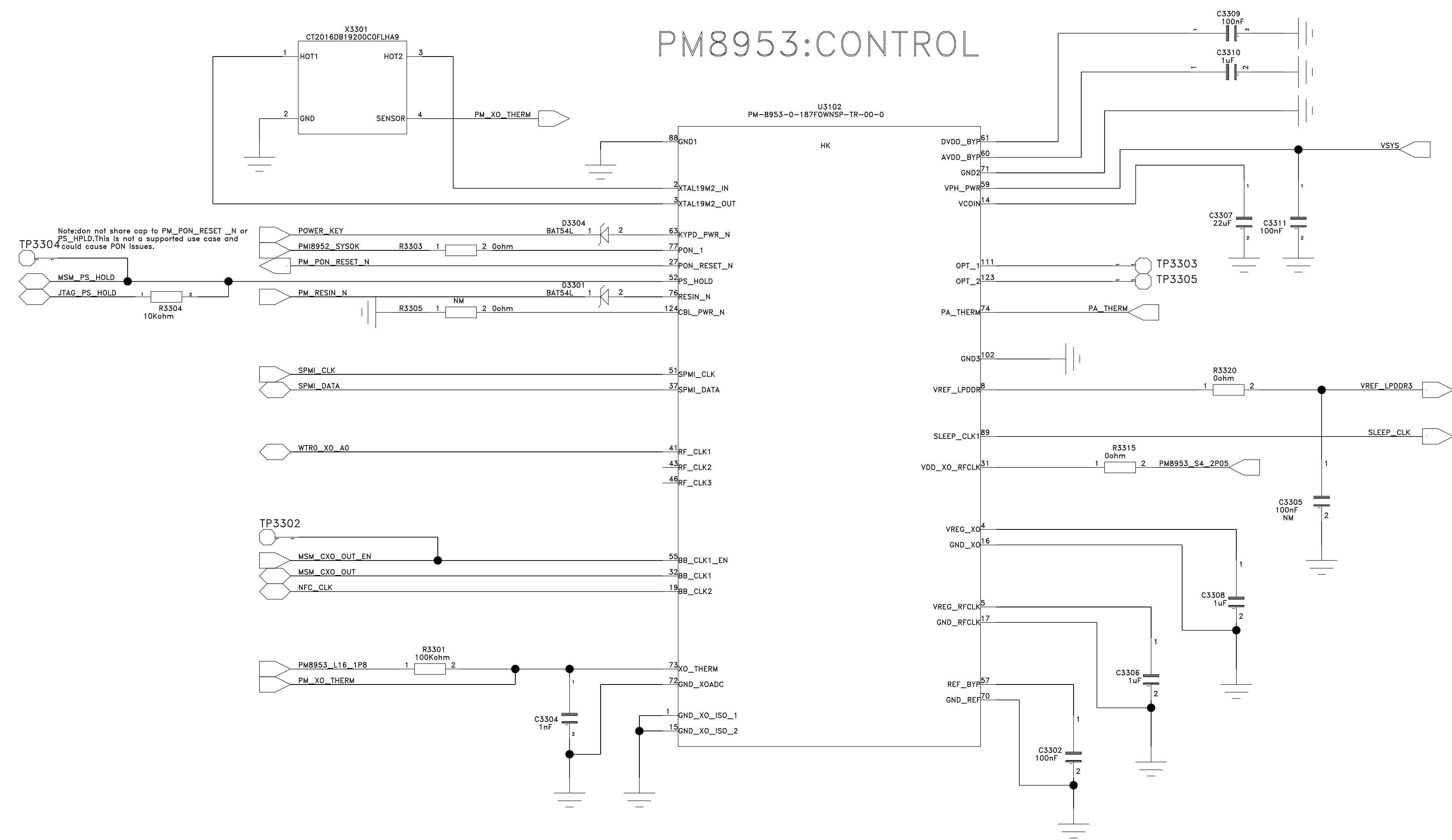


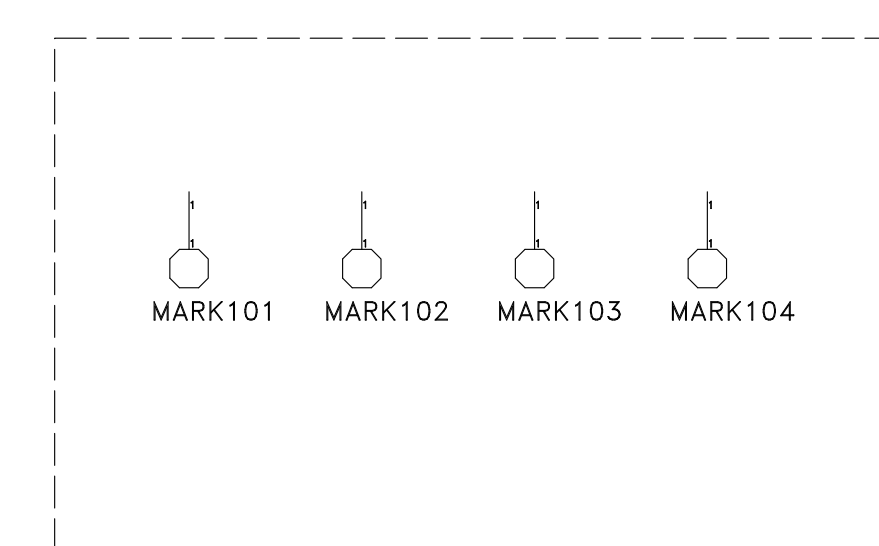
Table of contents

Contents	PAGE
PM8953	1–3
PMI8952/SMB1350	4–5
CPU:MSM8953	6–11
RF:WTR3925	12–13
RF:RFFE	13–18
WIFI/GPS	19–20
DISPLAY/CAM_CONNECT	21
SIM/T_FLASH/LED DRIVER	22
NPI TP	23
Main Memory	24
AUDIO	25
SENSOR/KEYPAD/SIDEKEY CON	26
NFC	27
HEADSET/MIC	28
TYPE C USB	29

Band configuration	GSM	WCDMA	TDS_CDMA	CDMA	TDD LTE	FDD LTE	CA
CN CA	QB	B1/2/5/8	B34/39	BC0	B38/39/40/41	B1/3/4/7/8/26	DL 2CA:1+3/3C/38C/39C/ 40C/41C/40+40/41+41/39+41 UL 2CA:39C/40C/41C
EU/APAC	QB	B1/2/5/8				B1/3/7/8/20/28	
LATAM	QB	B1/2/5/8				B1/2/3/4/5/7/12/17/28	

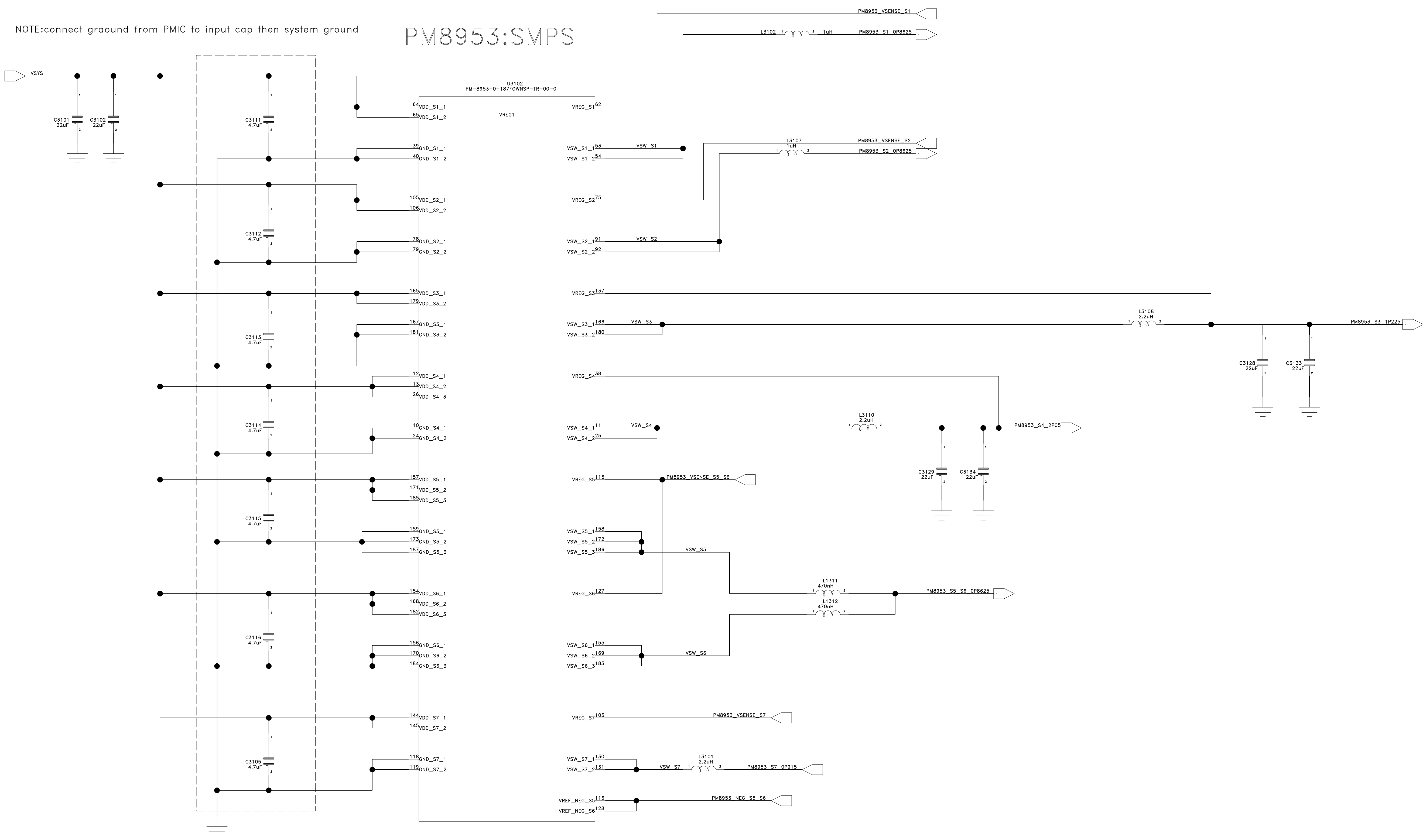


MARK

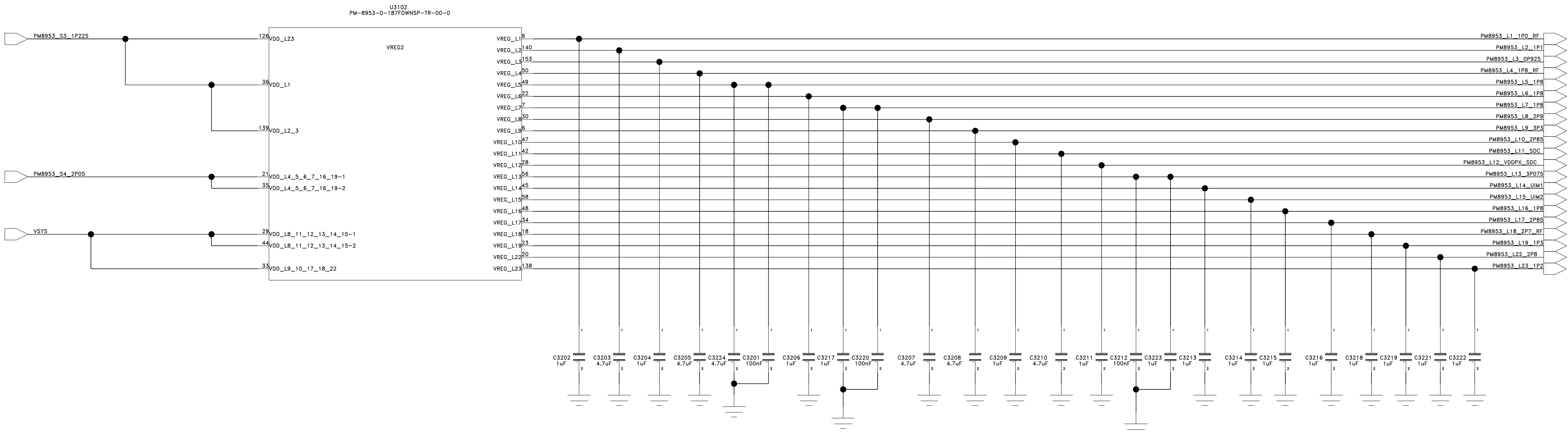


NOTE:connect graound from PMIC to input cap then system ground

PM8953:SMPS

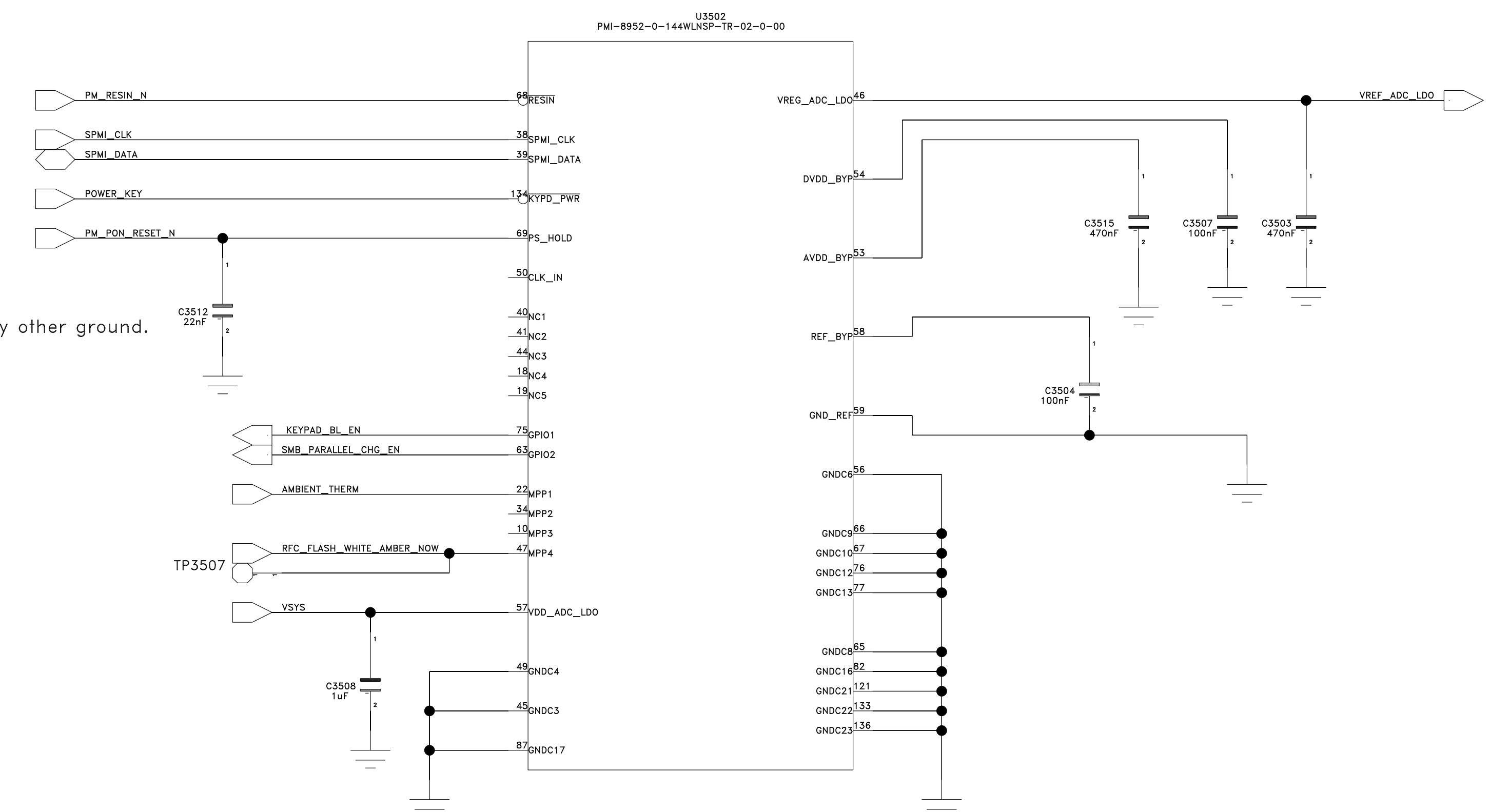


PM8953:LDO



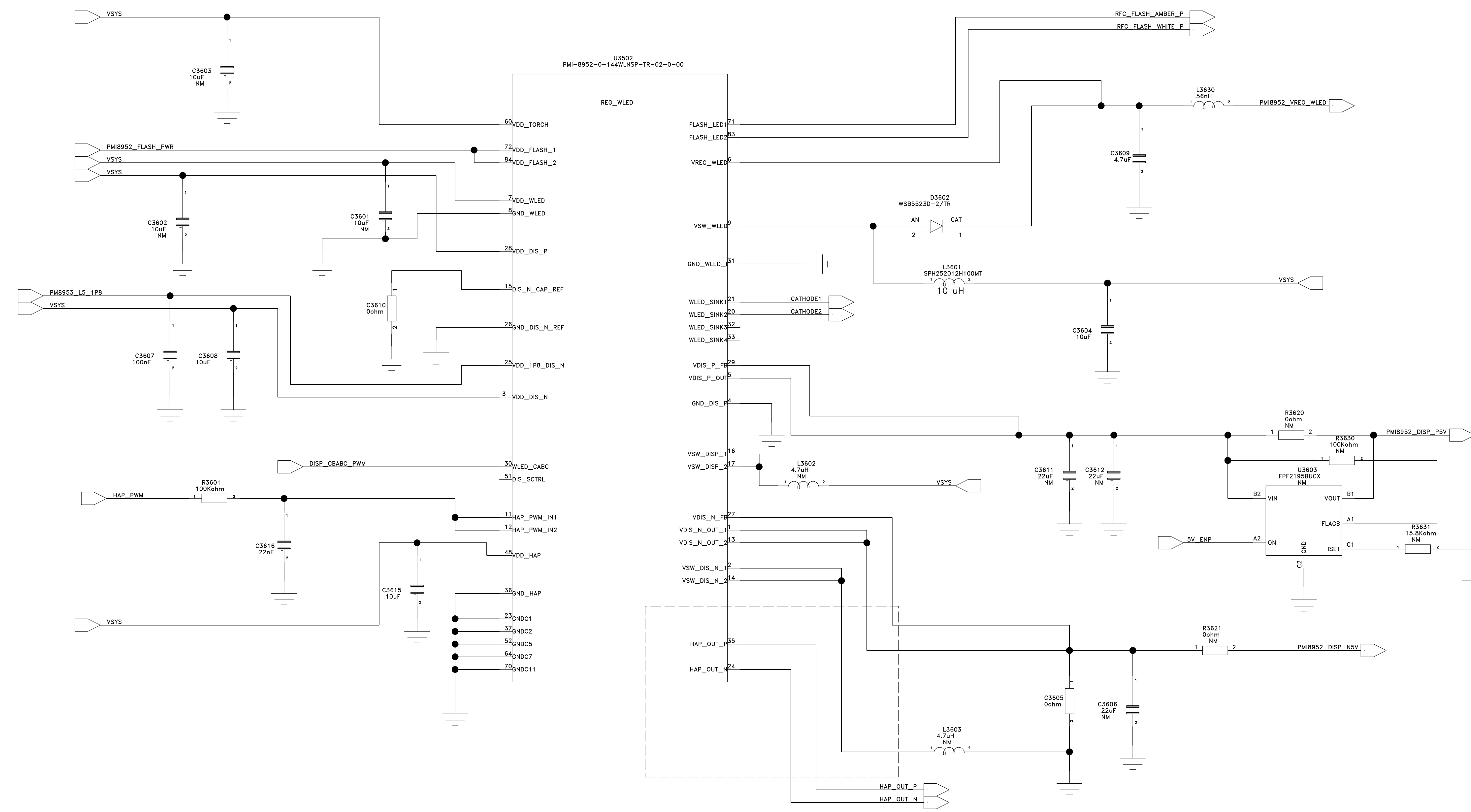
PSEUDO CAPLESS LDOs
L4,L6,L8,L9,L10,L11,L12,L14,L15,L17,L18,L22
L5,L7,L13,L16 HAVE INTERNAL pmic LOADS AND REQUIRE LOCAL caps
NOTE:If CAPLESS LODs decaps on the load side do not
add up to 1uf,then inastall the cap close to the PMIC

Page 4/28

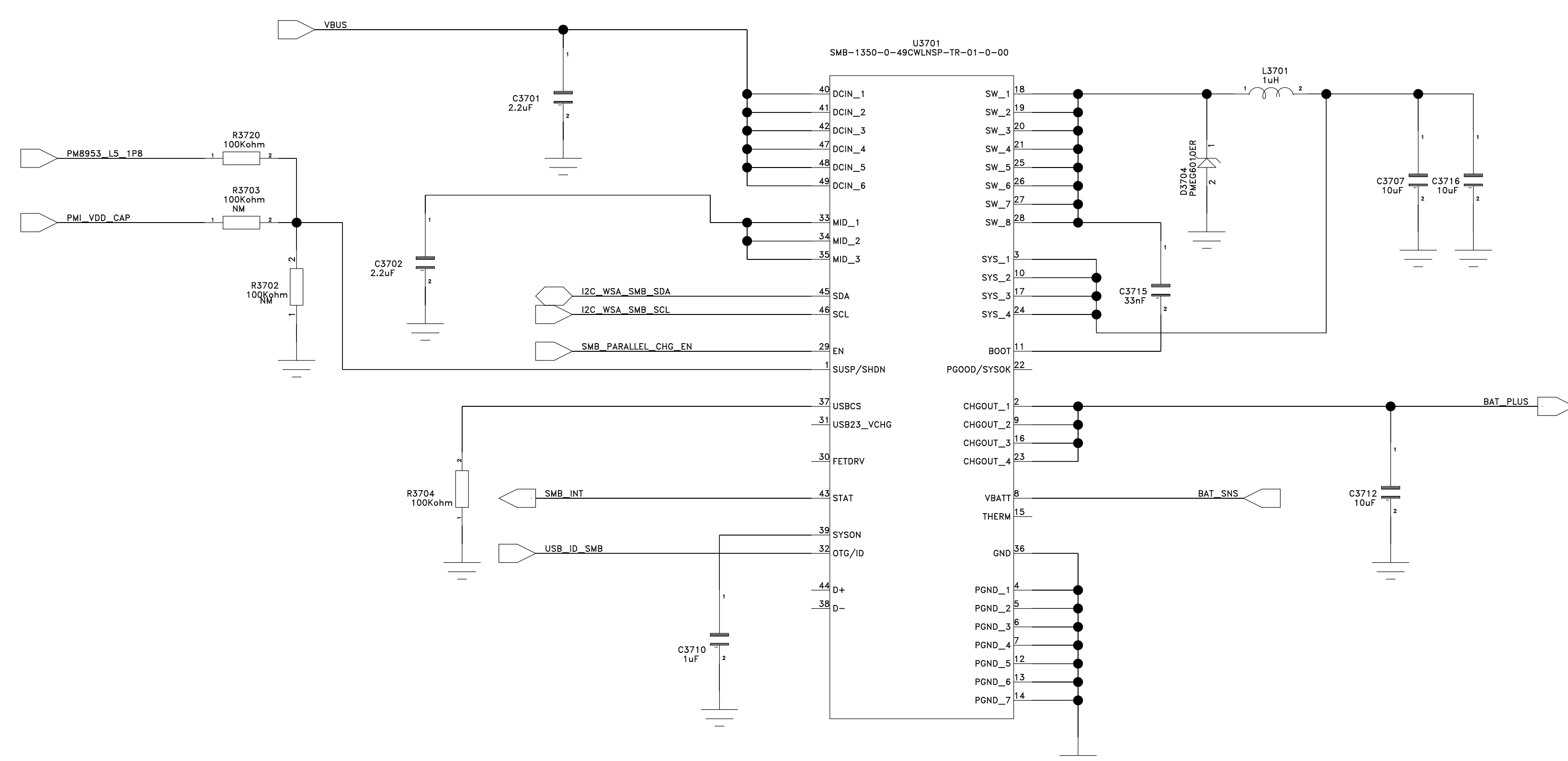


PM18952 PIN	WIPOWER	NO WIPOWER
DIV2_EN	CONNECT TO WIPOWER MODE PIN	0OHM PD TO GND
CHG_OK	CONNECT TO WIPOWER CHOKE PIN	51K PU TO PM1_VDD_CAP
PM1_GPIO_02	10K PD TO GND	Hi _Z

PMI8952 USER INTERFACE



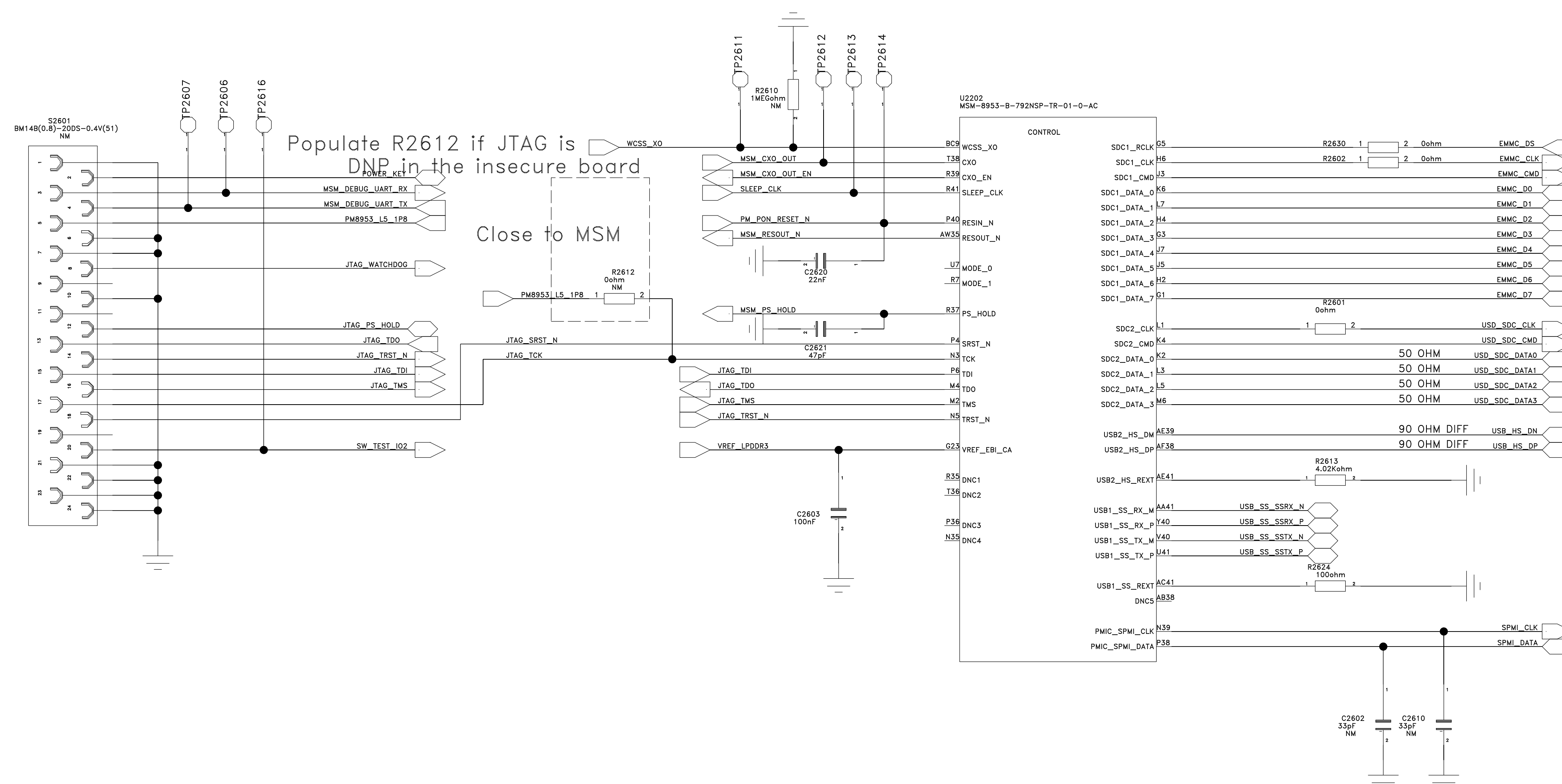
PARALLEL CHARGER:SMB 1350



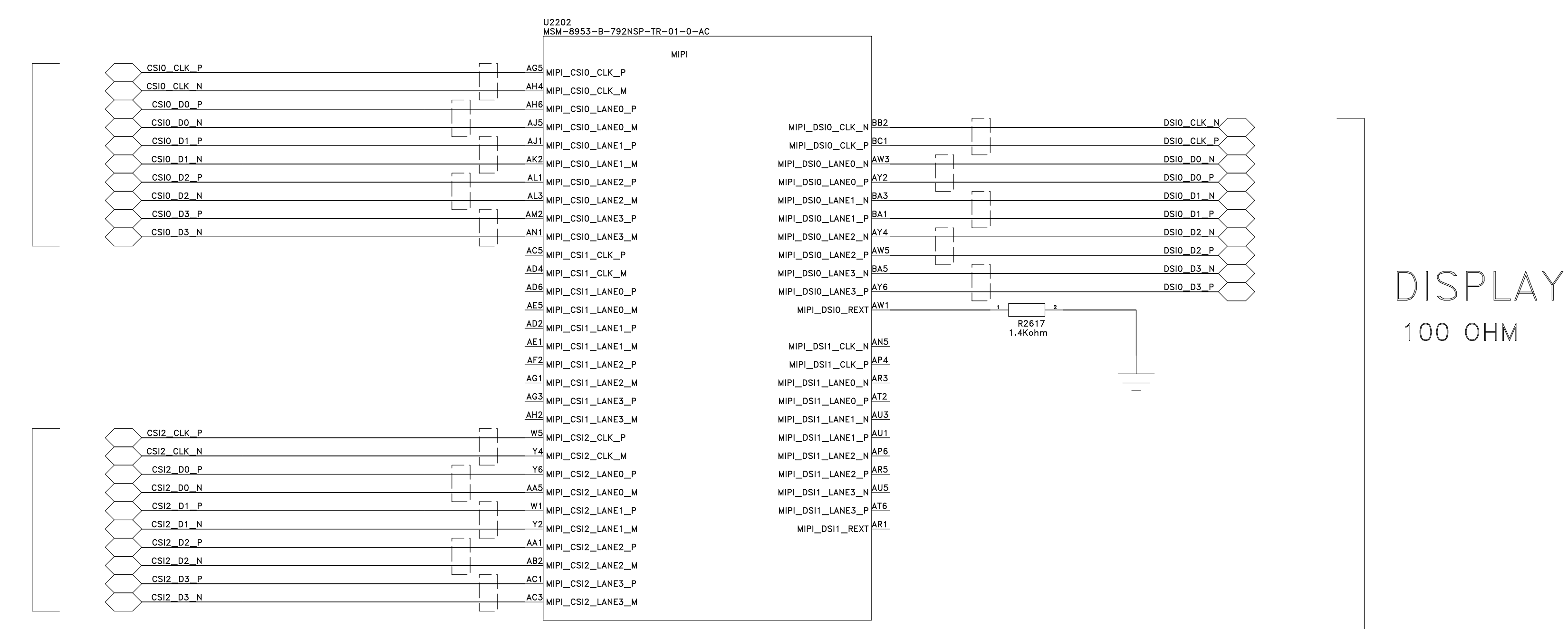
MSM8953 CONTROL

Note:wcss_xo signal required only for 5ghz.

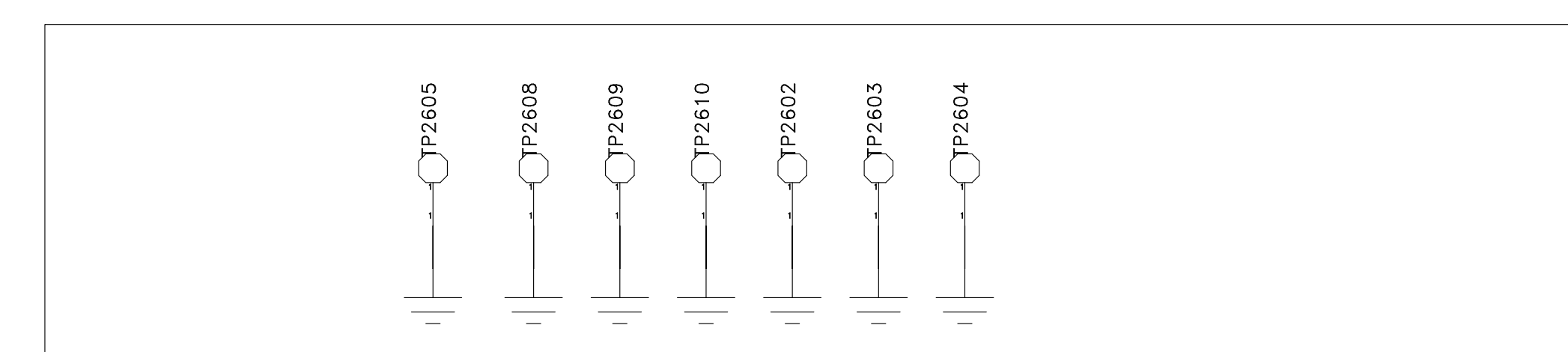
When using WCN3615 connect pin BD10 to GND.

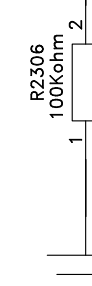


MSM8953 MIPI

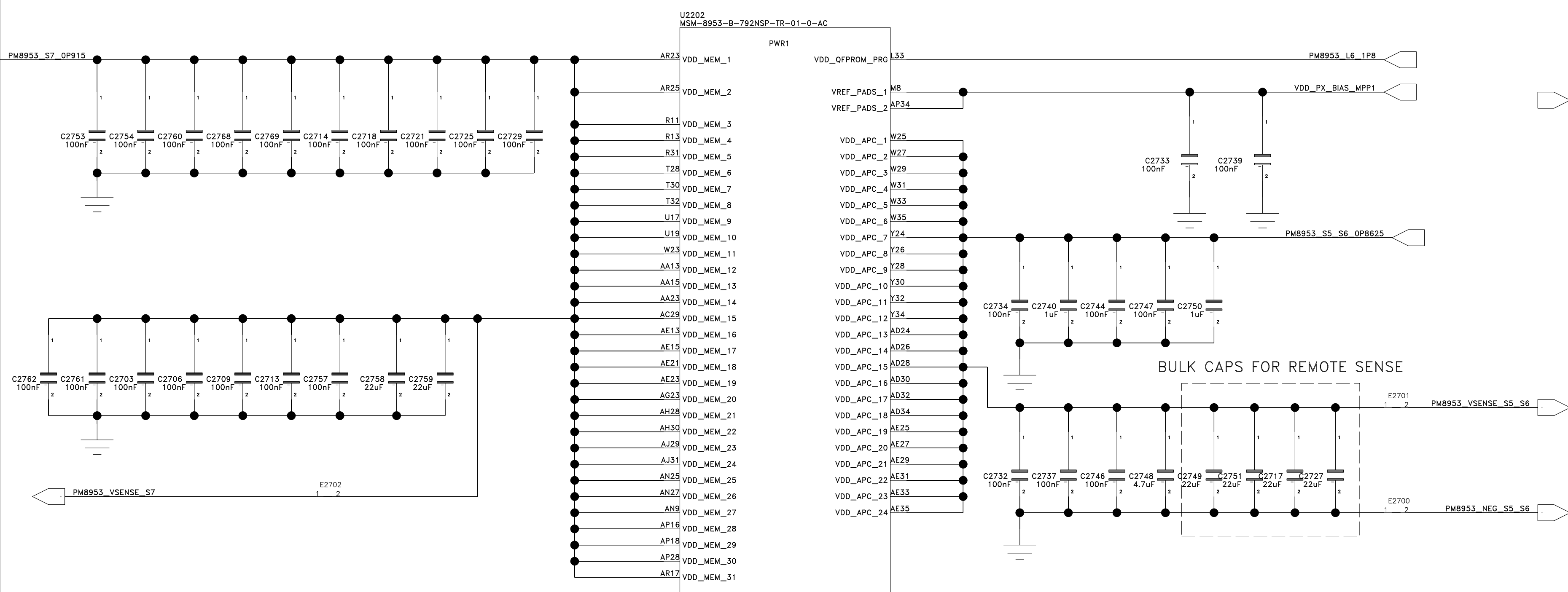


THERM RECEIVED

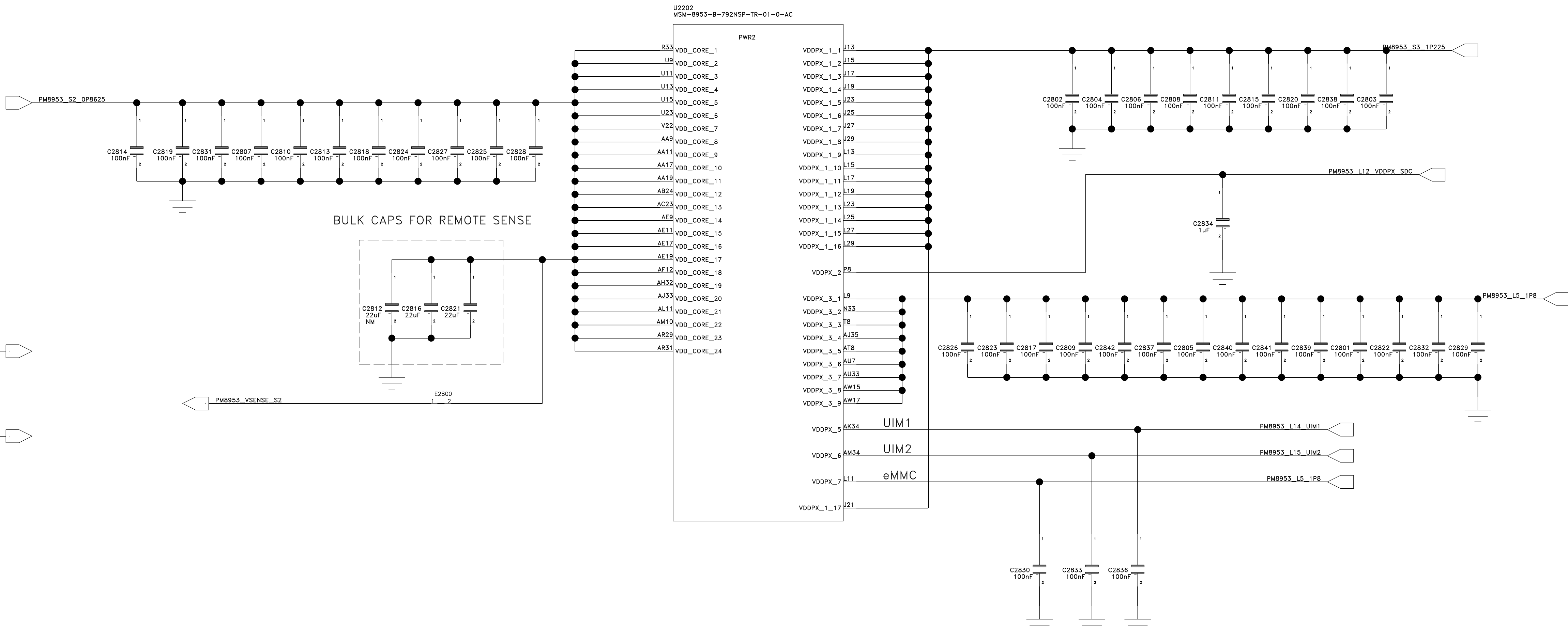




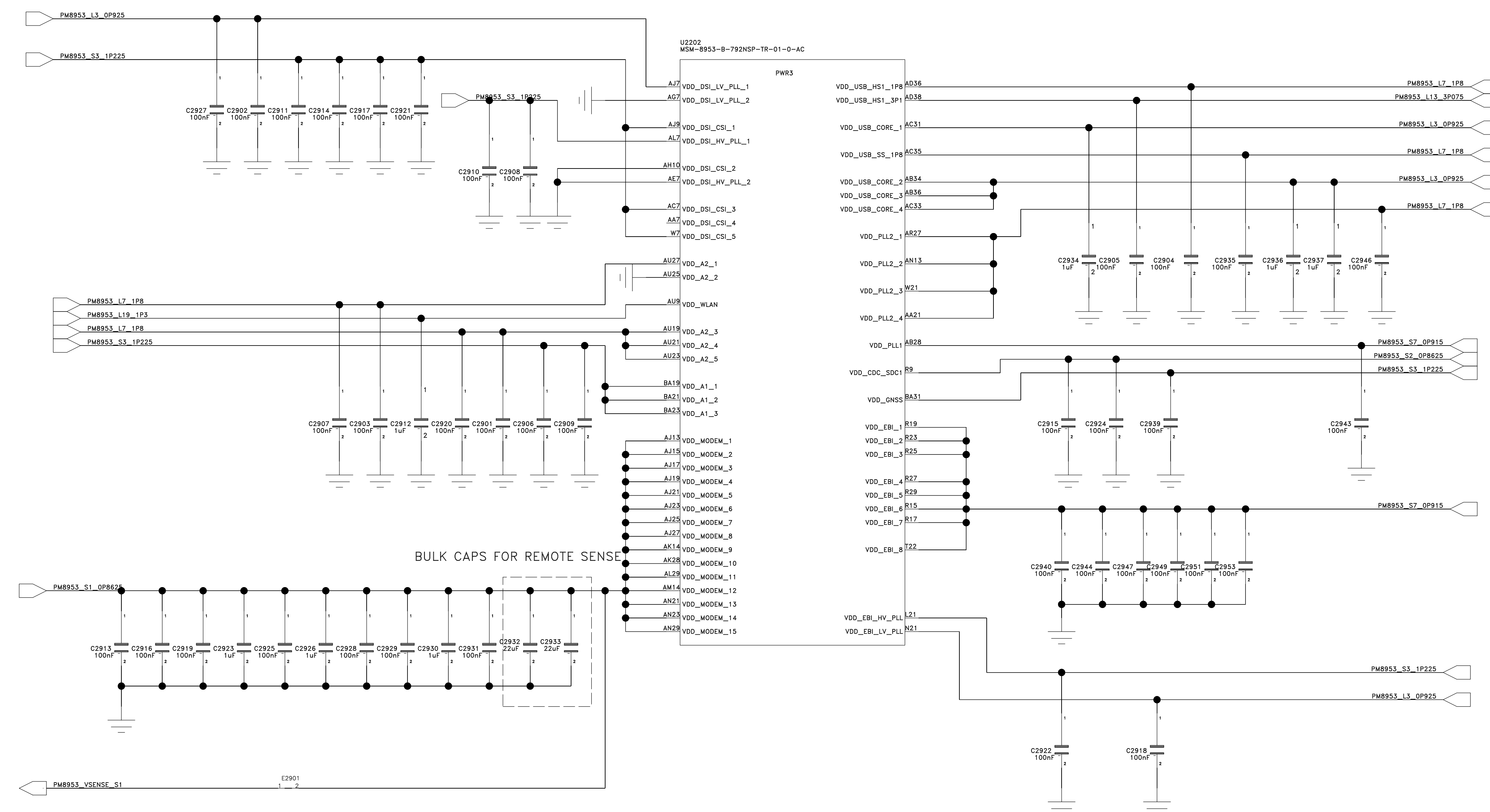
MSM8953 PWR1



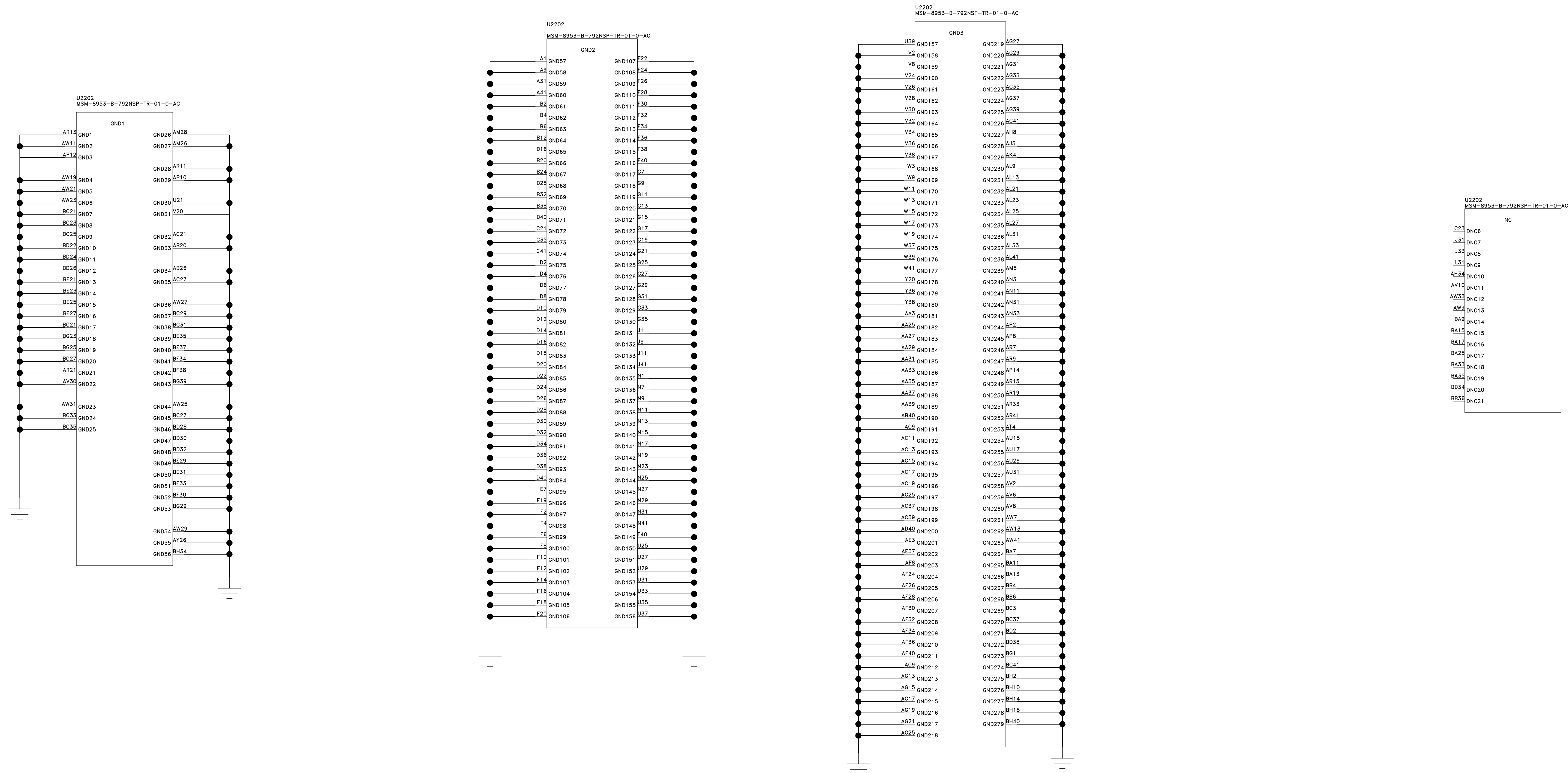
MSM8953 PWR2



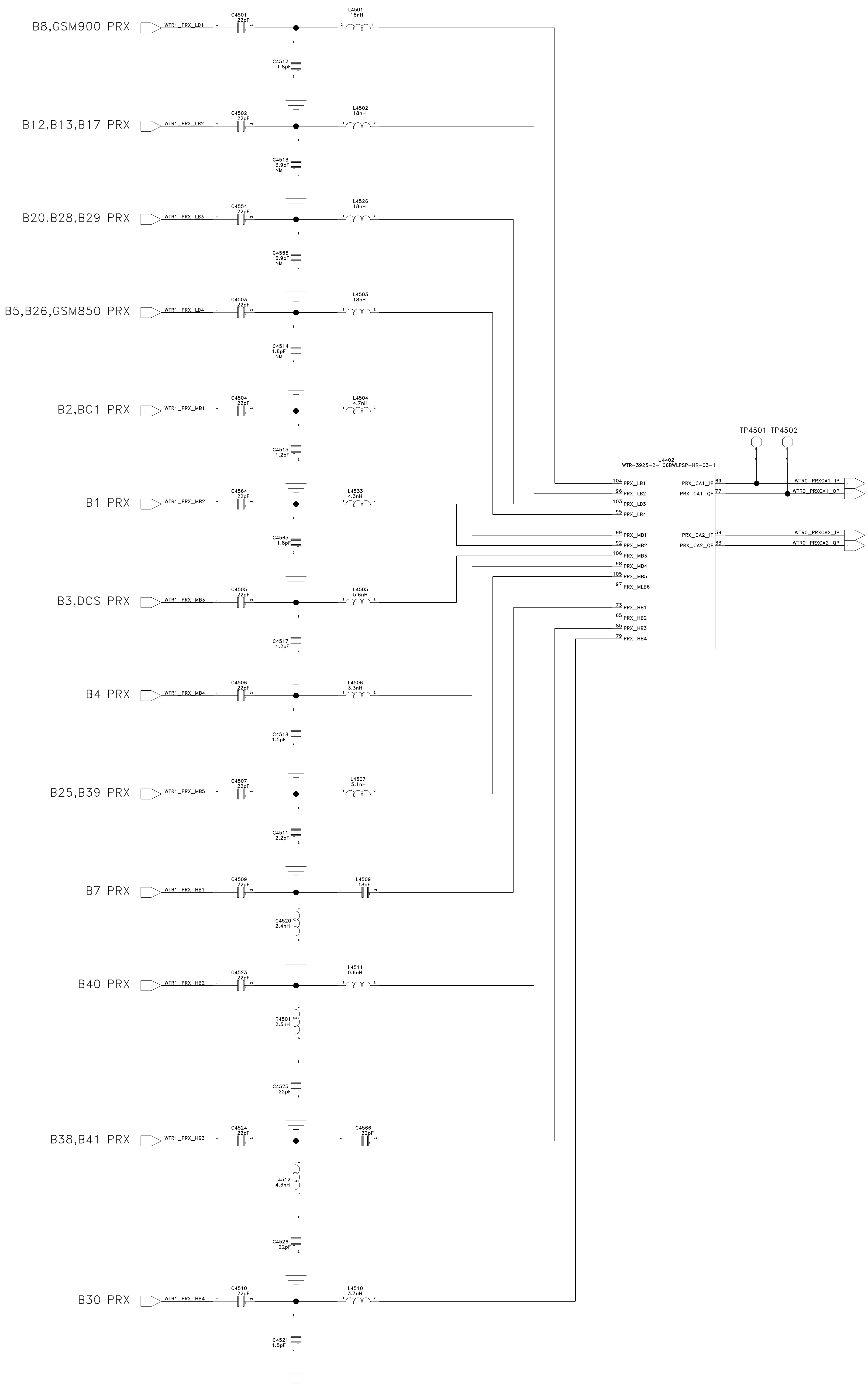
MSM8953 PWR3



MSM8953 GROUND & NC



WTR3925 PRX



WTR3925 DRX

