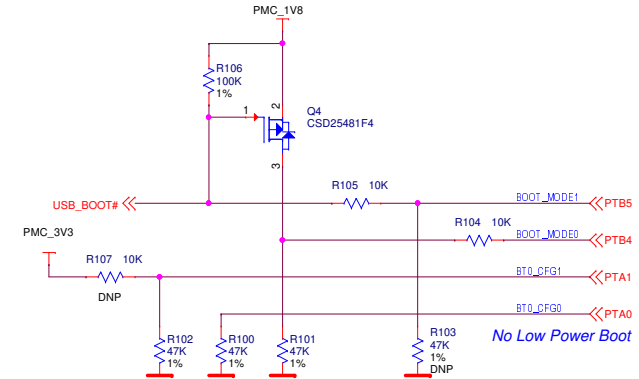
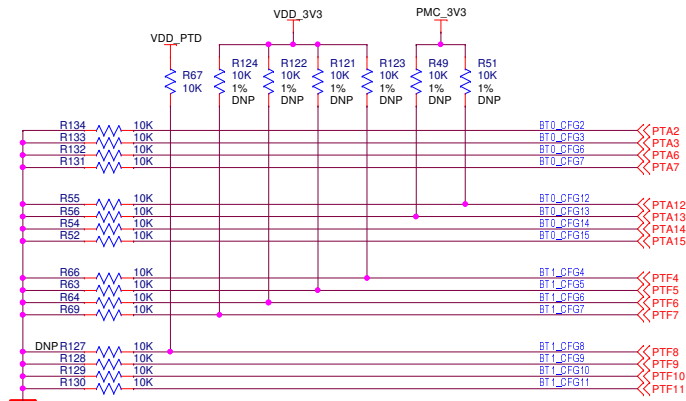


BOOT CONFIGURATION



SW4-1

ON Serial Downloader
OFF Internal Boot

SW4-2

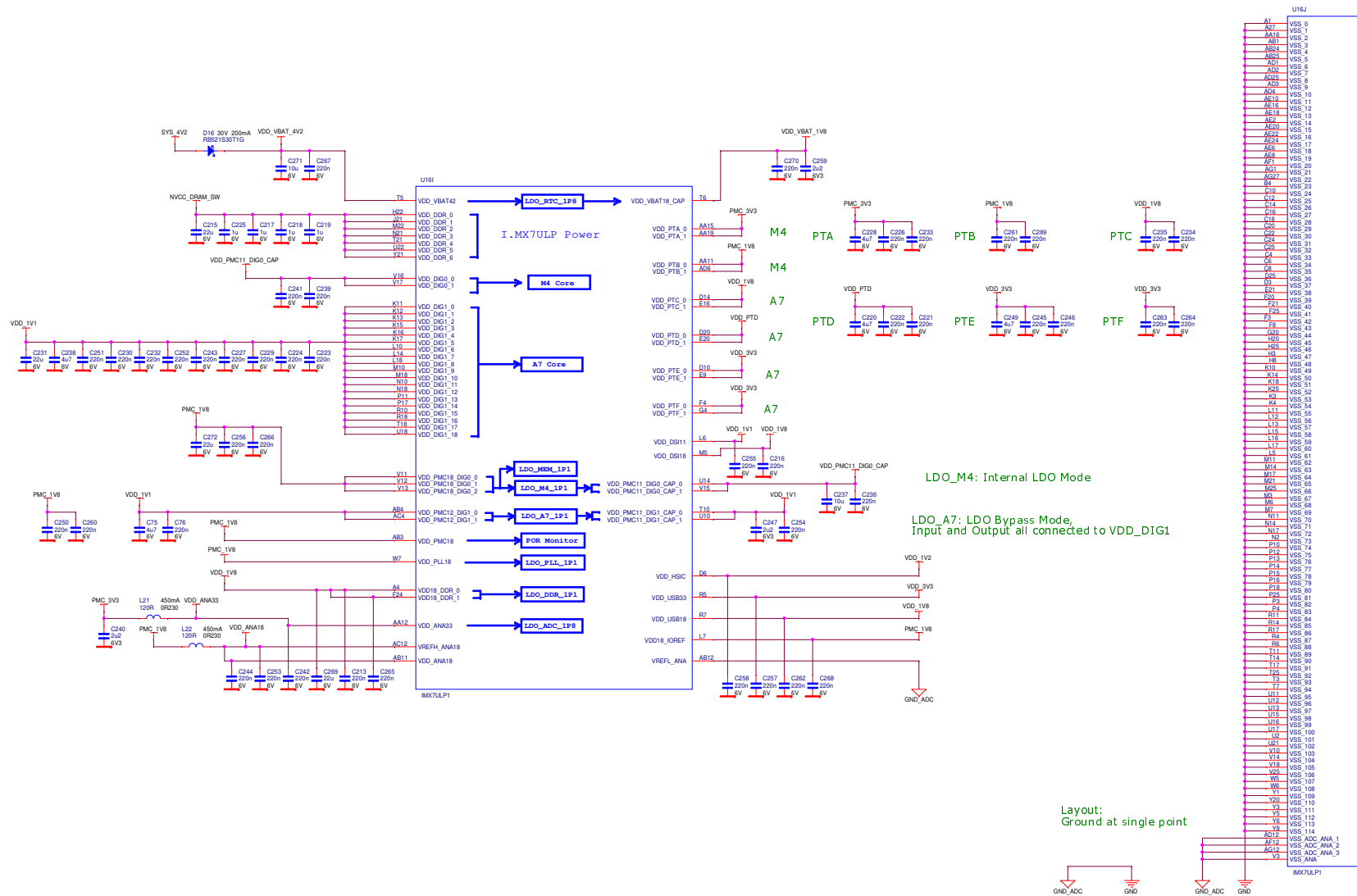
ON Dual A7/eMMC/SD and M4/QSPI
OFF Single A7 eMMC/SD

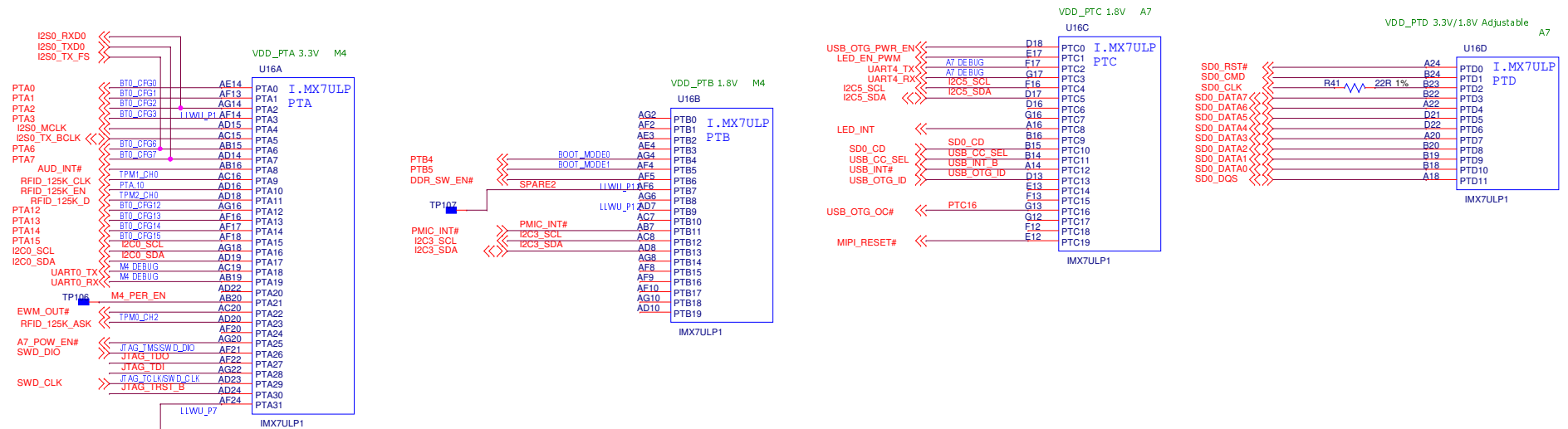
FUSE MAP

	0	0	0	0	0	0	0	0	1	0
TYPE	BT0_CFG15	BT0_CFG14	BT0_CFG13	BT0_CFG12	BT0_CFG7	BT0_CFG6	BT0_CFG3	BT0_CFG2	BT0_CFG1	BT0_CFG0
QSPI	QSPI instance 00 - QSPI0 Others - reserved		QSPI device type 00 - 3B read supported 01 - Hyperflash 1.8V 10 - Hyperflash 3.0 11 - 4B read supported		External OSC Freq Selection 00 - 24Mhz 01 - 30Mhz 10 - 19.2 Mhz 11 - 26Mhz (Not supported by ROM in ULP1 TO1.0)		M4 boot interface 0 - QSPI Others - reserved		Infini-Loop (Debug USE only) 0 - Disable 1 - Enable	
SD/eSD										
MMC/eMMC										

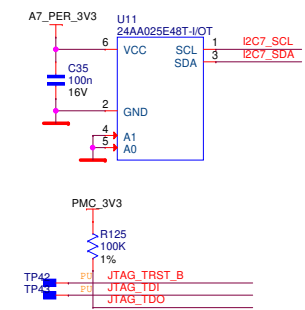
	0	0	0	1	0	0	0	0
TYPE	BT1_CFG11	BT1_CFG10	BT1_CFG9	BT1_CFG8	BT1_CFG7	BT1_CFG6	BT1_CFG5	BT1_CFG4
QSPI	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
SD/eSD	A7 boot interface 000 - USDHC0 001 - USDHC1 others - reserved			USDHC device type 0 - eMMC 1 - SD	SD Speed 000 - Normal 001 - High Others - Reserved		SD Loopback Clock Source Sel (for SDR50 and SDR104 only) 0 - through SD pad 1 - direct	
MMC/eMMC								

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EEPROM WITH UNIQUE ID



I2C5 (A7, 1V8)

RGB LED GLOBAL 0b1000000
 RGB LED 1 0b0110010
 RGB LED 2 0b0110011
 RGB LED 3 0b0110100

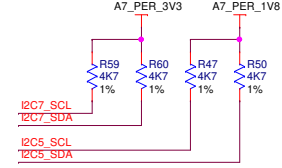
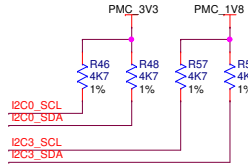
I2C7 (A7, 3V3)

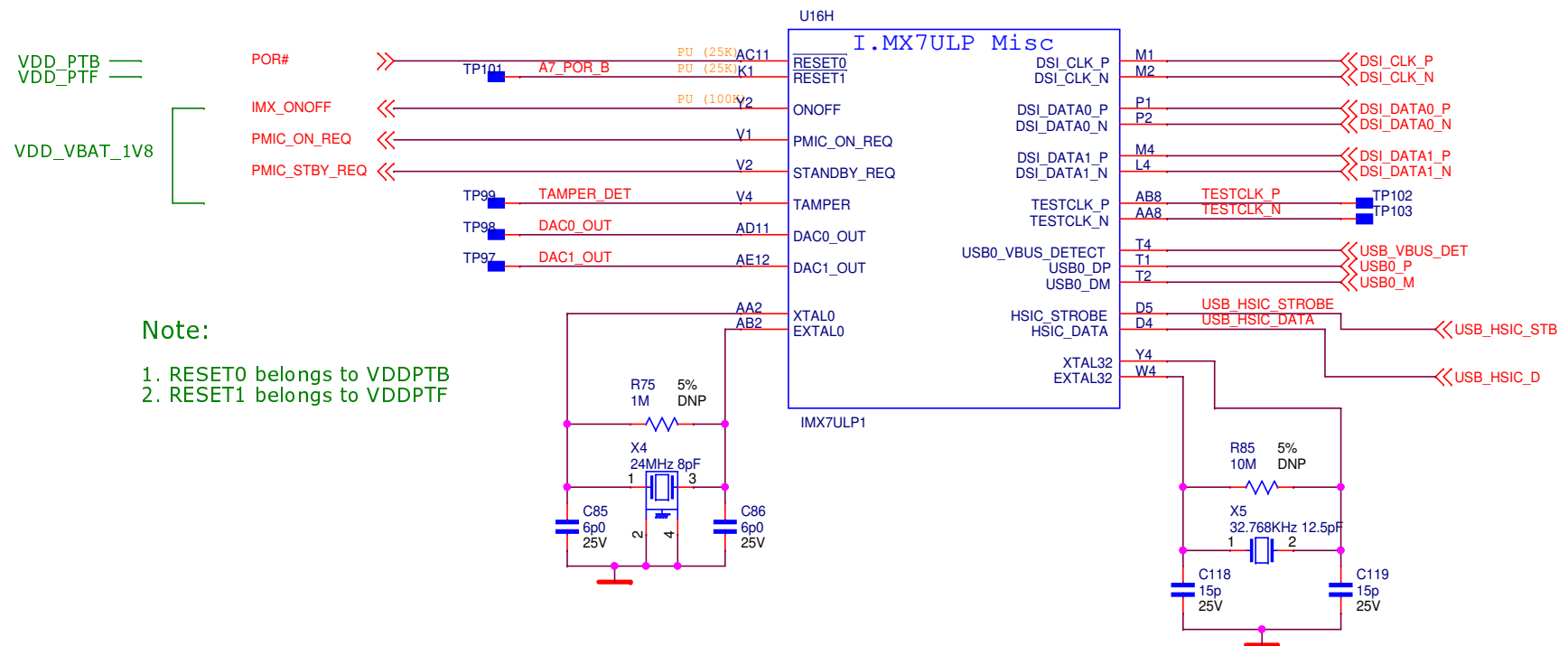
USB3503 0b0001000
 24AA025 0b1010000
 CTP ????????

I2C0 (M4, 3V3)

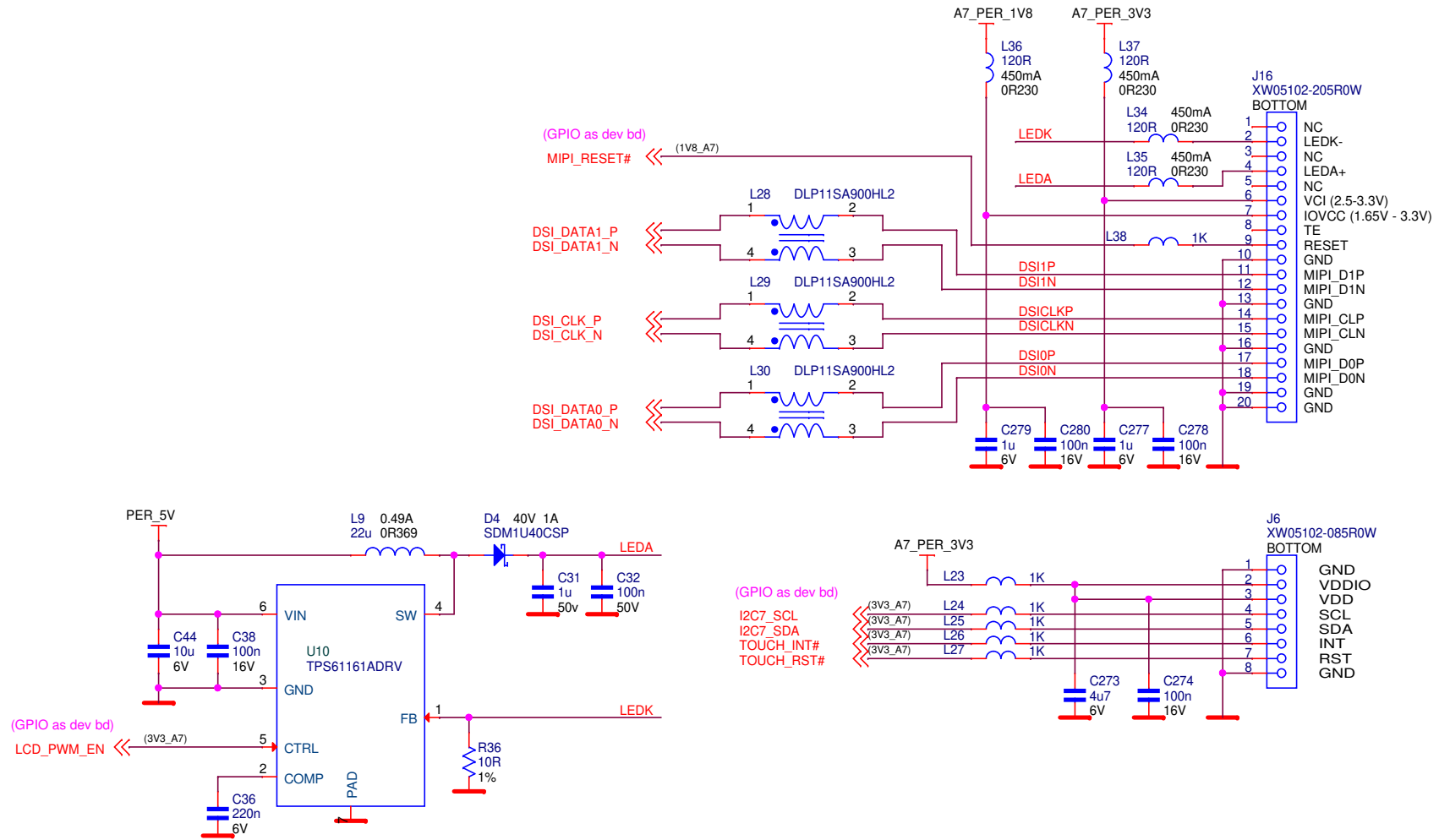
I2C3 (M4, 1V8)

PMIC 0b0001000



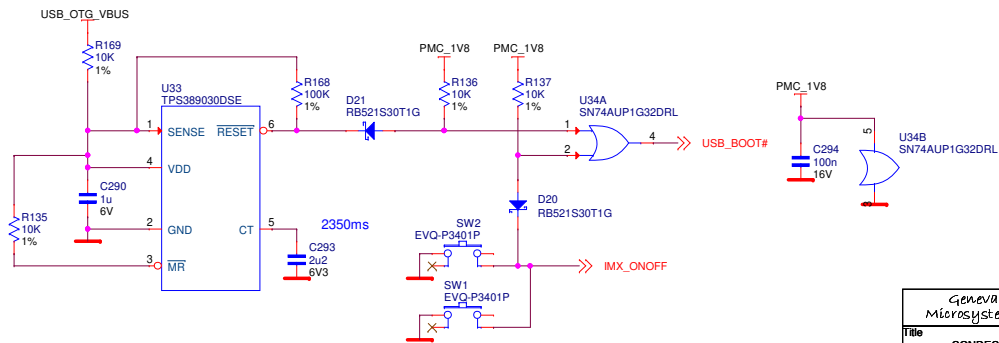
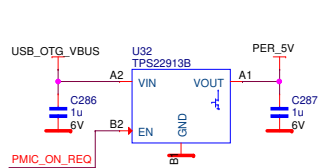
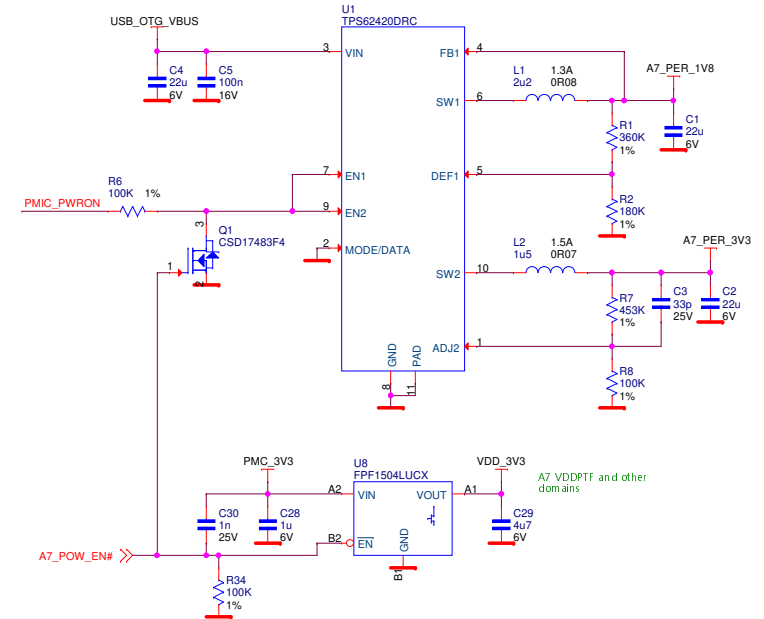
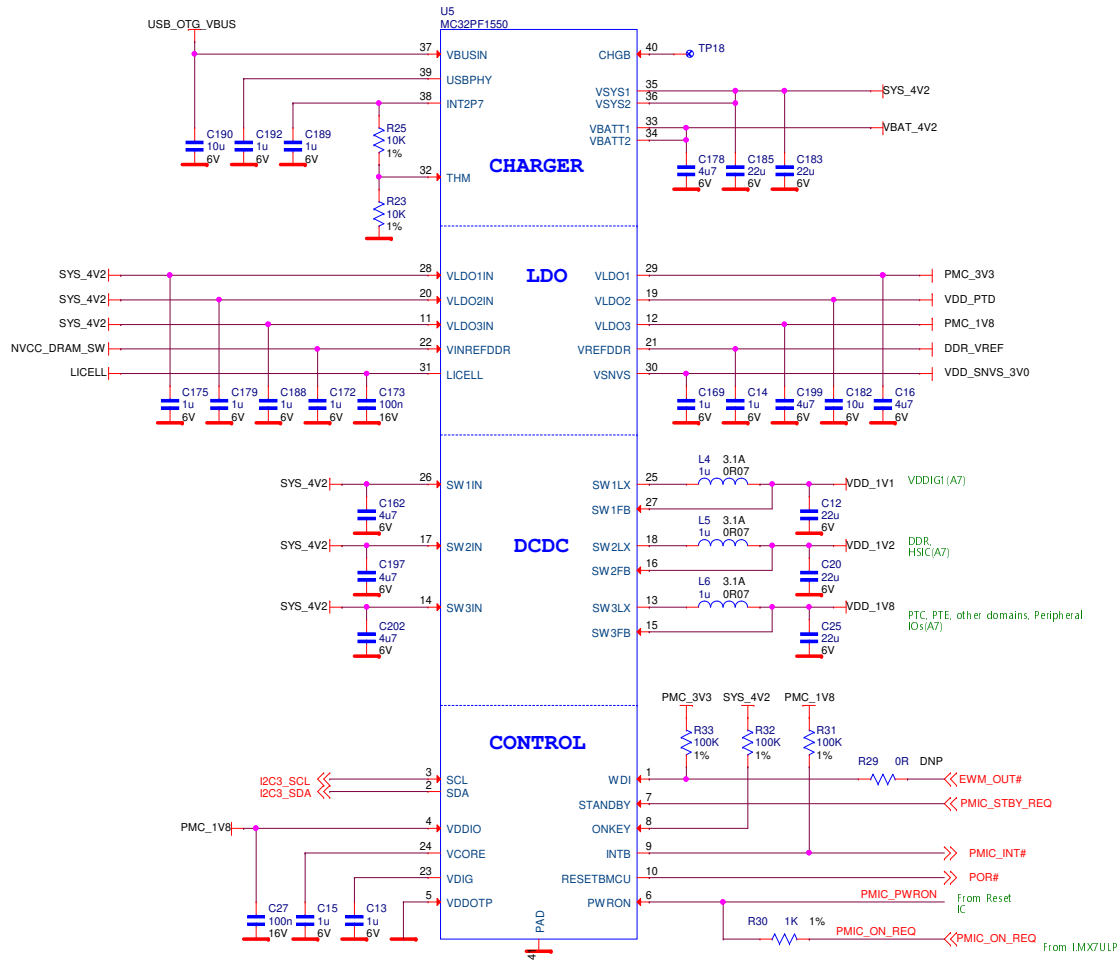


3" RGB LCD PANEL



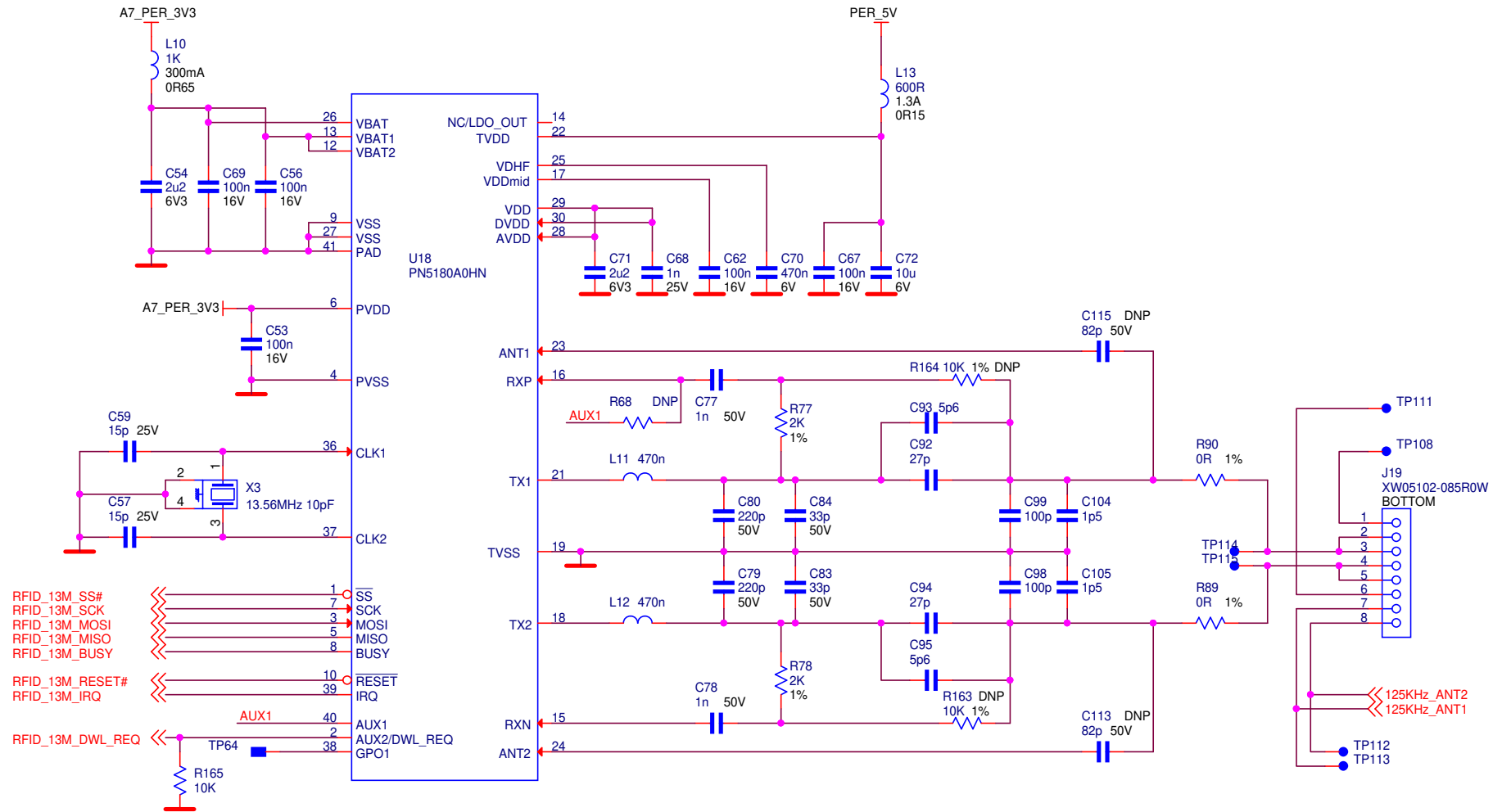
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Low enable;
Default low to output power for ULP and LPDDR3 VDDQ;
Set high to disable power, when LPDDR3 is in self-refresh mode.



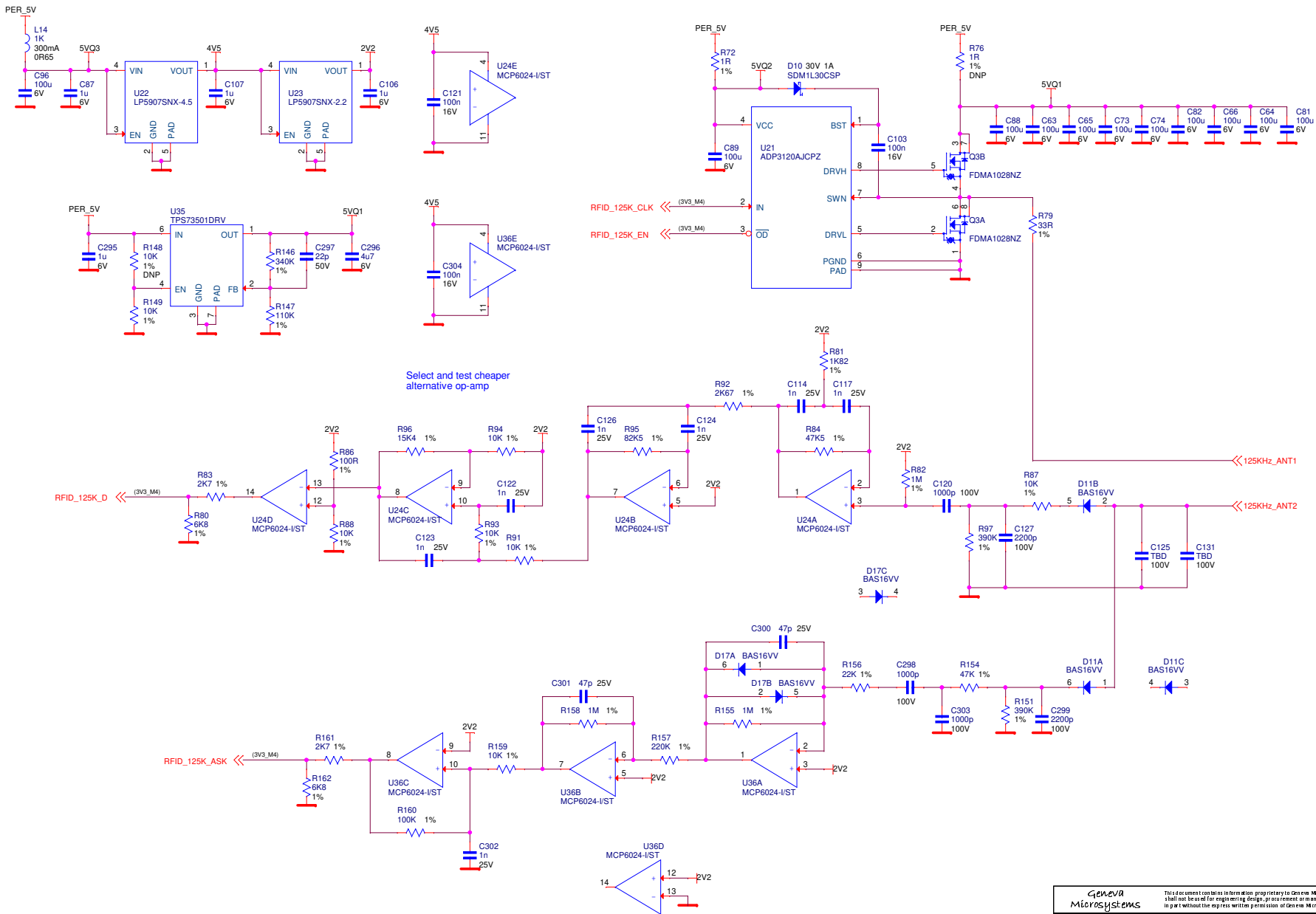
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13.56MHz NFC/RFID



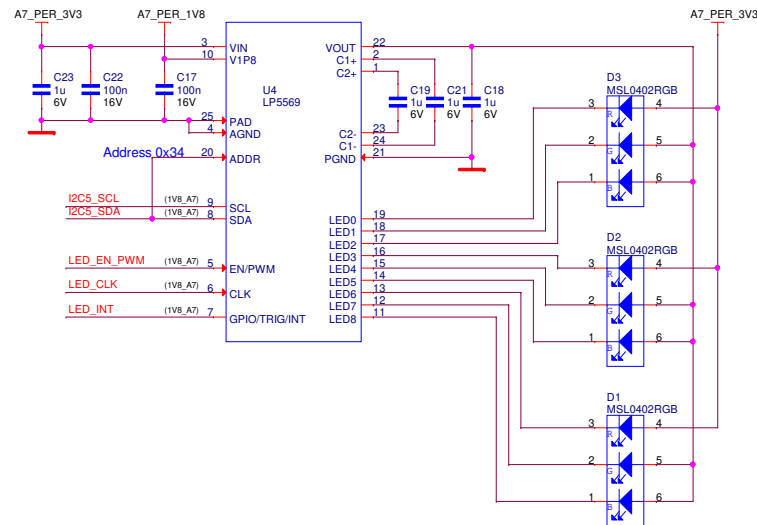
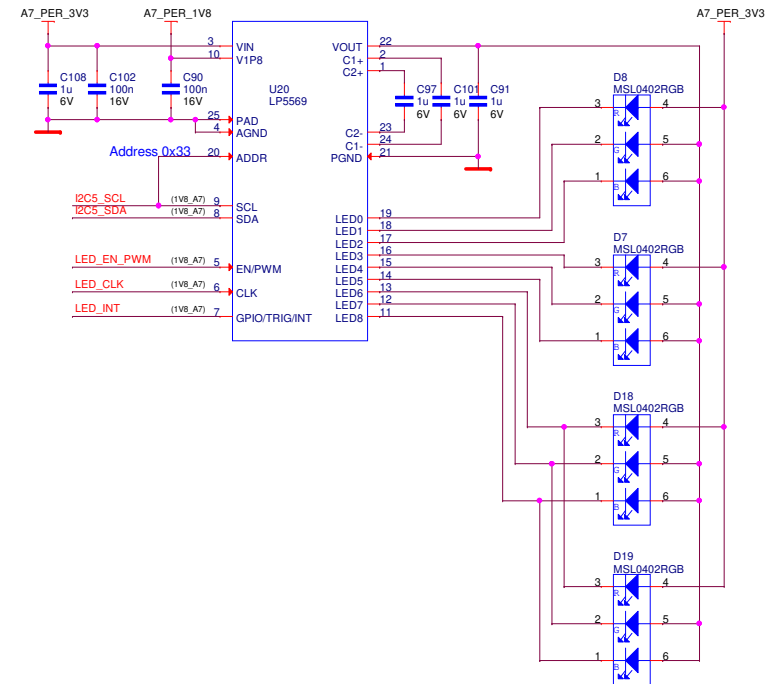
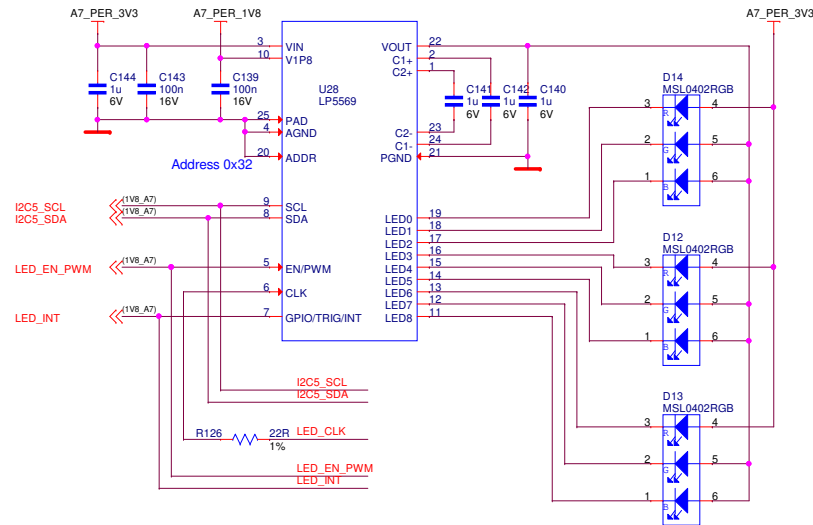
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125KHz RFID

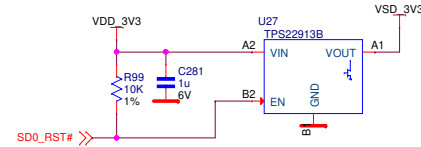
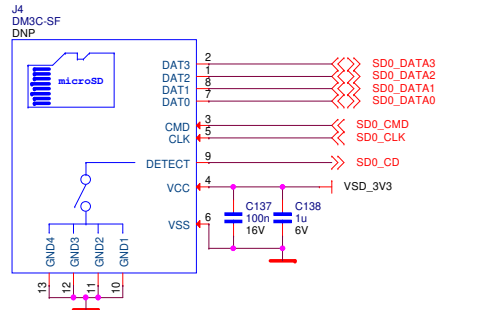


RGB LED

This first device must have its CLK set to output so that all are synchronised.

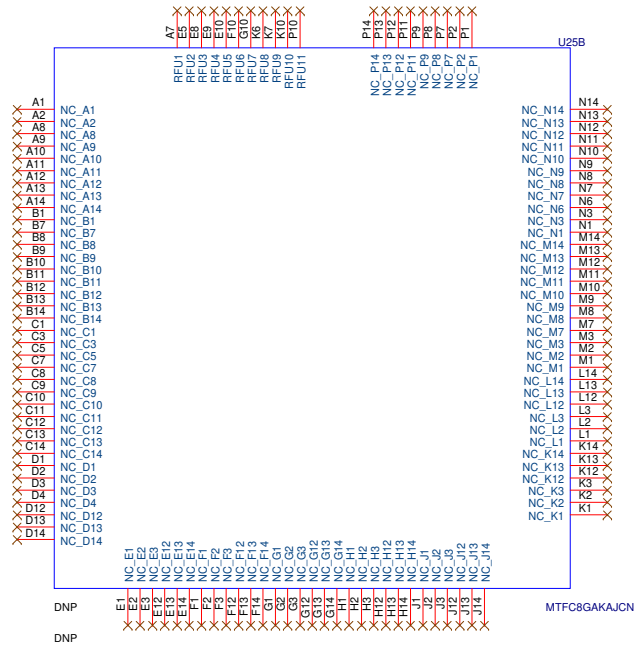


SD AND eMMC



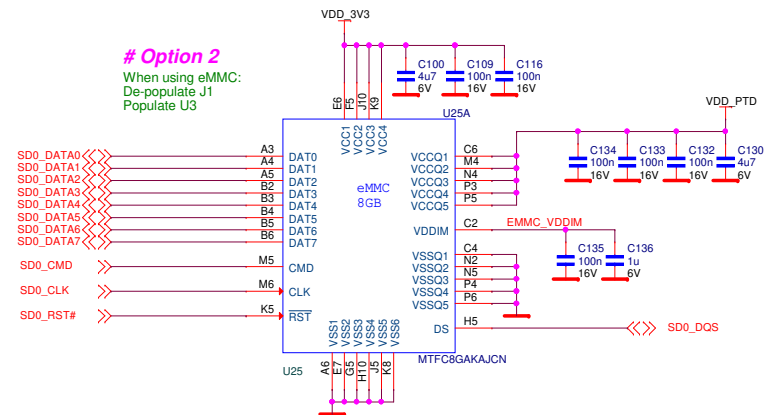
Note:

1. ROM Code will reset the SD power during boot up through SD_RST#;

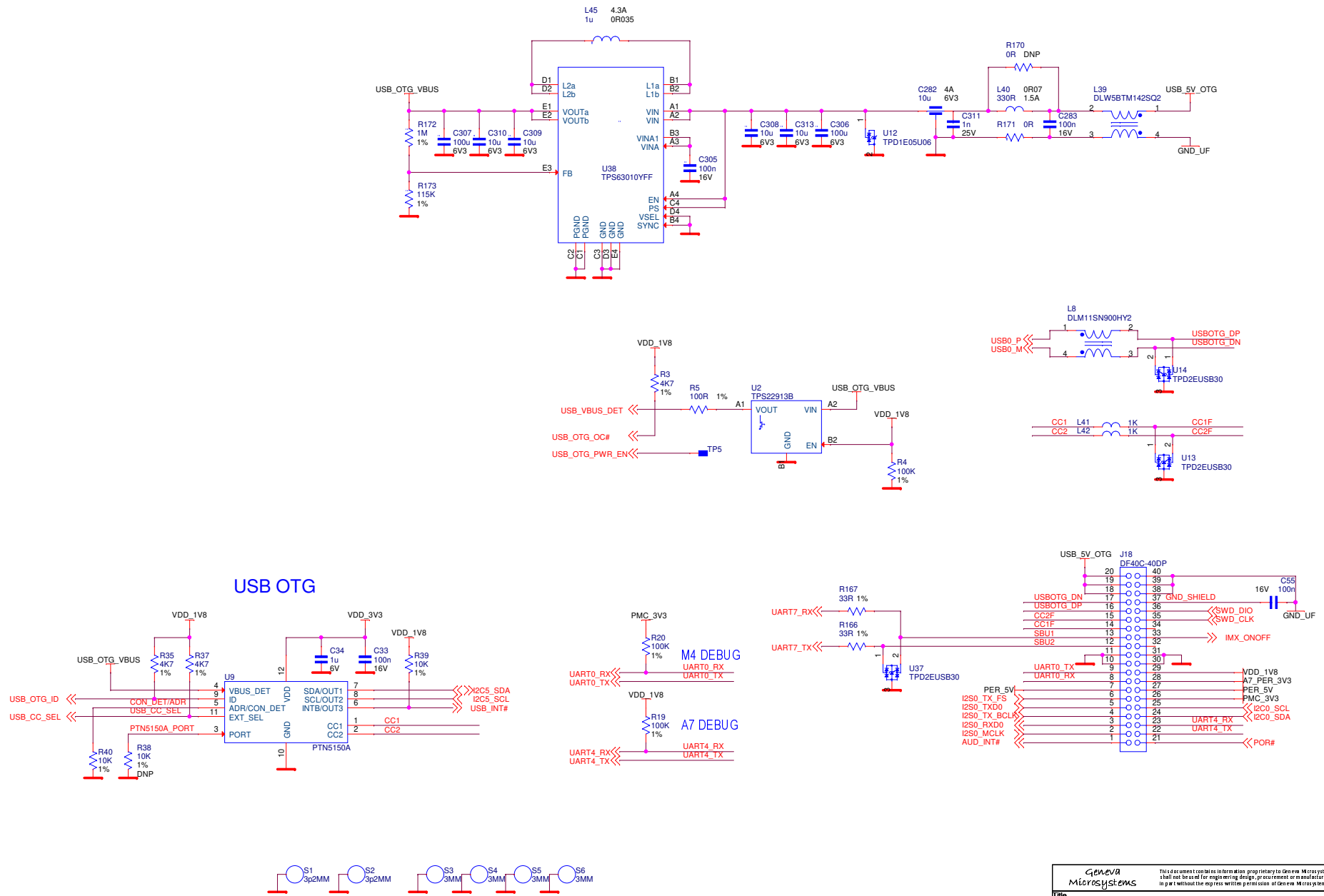


Option 2

When using eMMC:
De-populate J1
Populate U3



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WIFI AND BLE

