



Keyway Testing Technology (Guangdong) Co., Ltd
 21/F., Building 6, Dongyi Intelligent Equipment New Energy Vehicle Park, No.30,
 Tangxia District, Dongshen Road, Tangxia, Dongguan, Guangdong, China

E.U.T:	POWERED LOUDSPEAKER SERIES
Model Number:	THRASH12v2, THRASH15v2
Trade Name:	
Date of Receipt:	Apr. 14, 2026
Date of Test:	Mar. 10 ~ 30, 2026
FCC ID:	2AD4XTHRASHV2
IC:	12714A-THRASHV2

Note: The test data have all been compensated for cable loss, and when measuring the entire 2400-2500MHz, the maximum loss value 7.89 dB. and when measuring the entire 30MHz-25GHz, Input Compensation Coefficient (dB) = Max Cable Loss (dB)



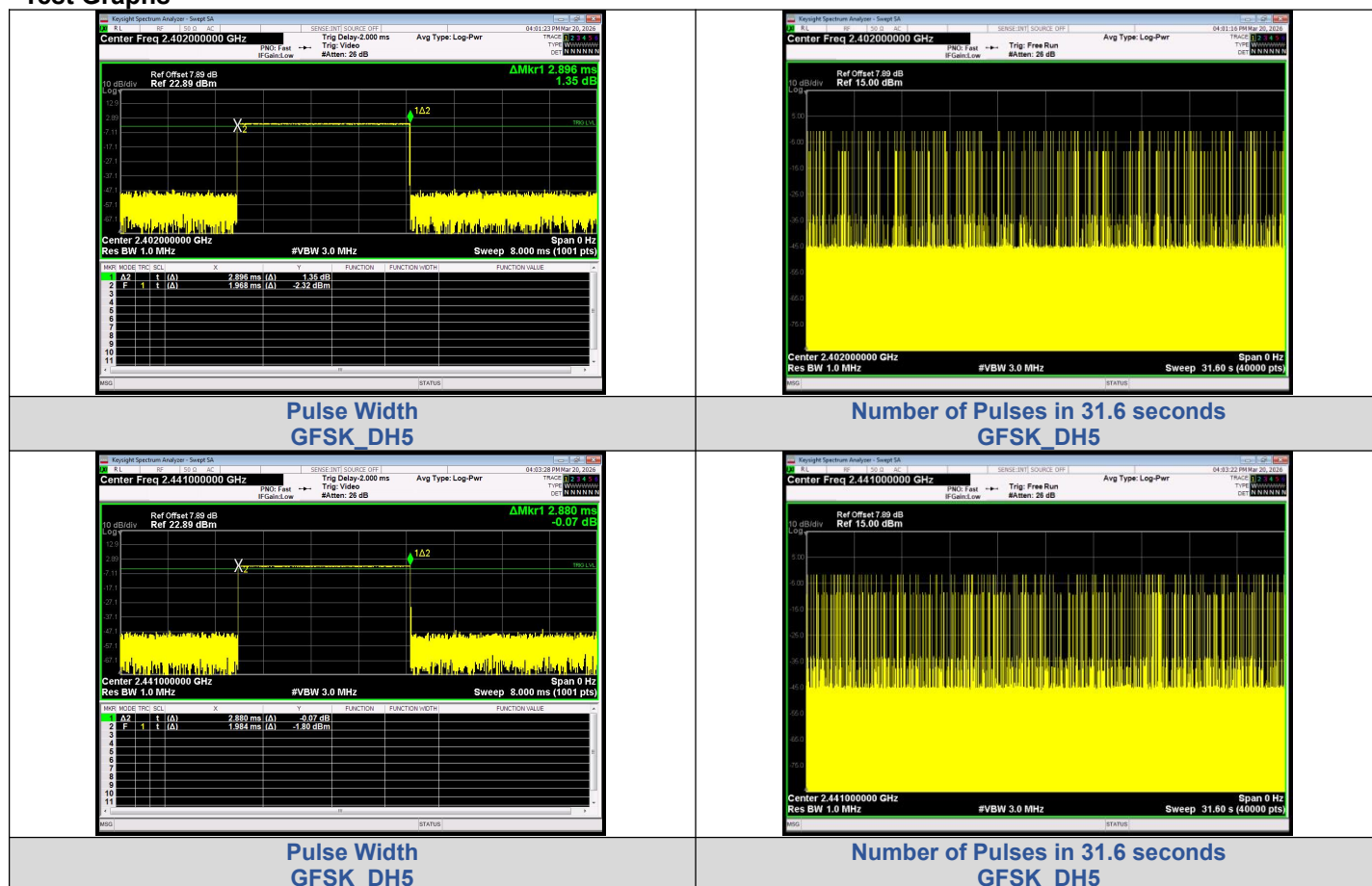
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A.1 DWELL TIME

Test Result

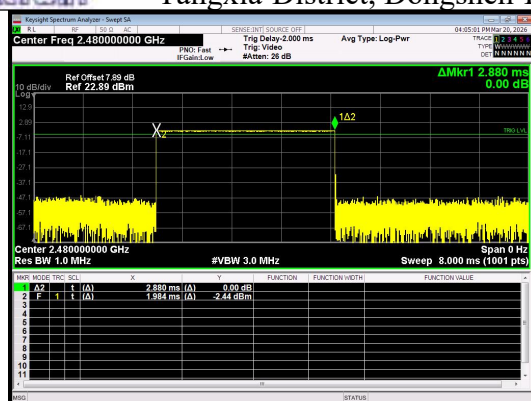
Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH5	CH0 (2402MHz)	2.896	102	321.46	< 400	PASS
	DH5	CH39 (2441MHz)	2.880	102	362.88		PASS
	DH5	CH78 (2480MHz)	2.880	94	288.0		PASS
$\pi/4$ DQPSK	2-DH5	CH0 (2402MHz)	2.896	109	289.6		PASS
	2-DH5	CH39 (2441MHz)	2.880	106	290.88		PASS
	2-DH5	CH78 (2480MHz)	2.880	107	313.92		PASS
8DPSK	3-DH5	CH0 (2402MHz)	2.896	114	327.25		PASS
	3-DH5	CH39 (2441MHz)	2.896	105	278.02		PASS
	3-DH5	CH78 (2480MHz)	2.896	113	275.12		PASS

Test Graphs

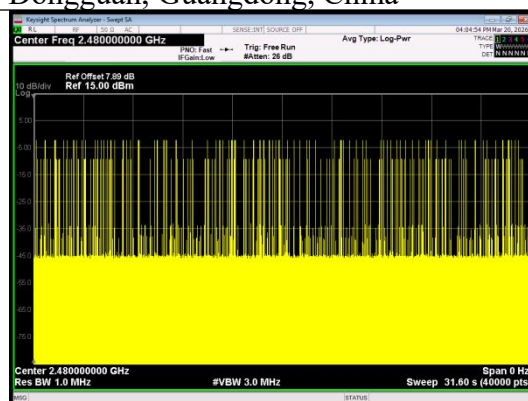




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Pulse Width
GFSK_DH5



Number of Pulses in 31.6 seconds
GFSK_DH5



Pulse Width
 $\pi/4$ DQPSK_2-DH5



Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH5



Pulse Width
 $\pi/4$ DQPSK_2-DH5



Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH5



Pulse Width
 $\pi/4$ DQPSK_2-DH5



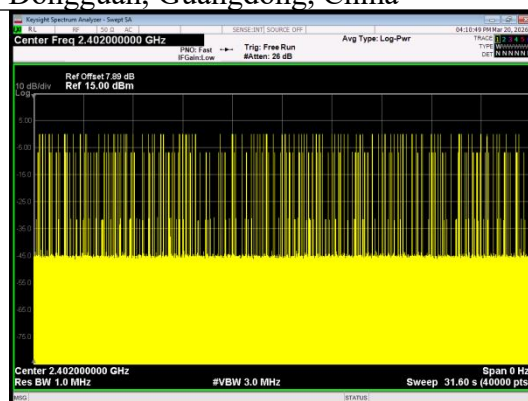
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH5



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Pulse Width
8DPSK_3-DH5



Number of Pulses in 31.6 seconds
8DPSK_3-DH5



Pulse Width
8DPSK_3-DH5



Number of Pulses in 31.6 seconds
8DPSK_3-DH5



Pulse Width
8DPSK_3-DH5



Number of Pulses in 31.6 seconds
8DPSK_3-DH5



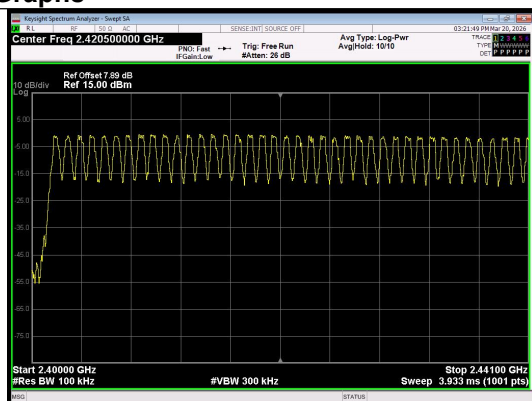
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A.2 NUMBER OF HOPPING CHANNEL

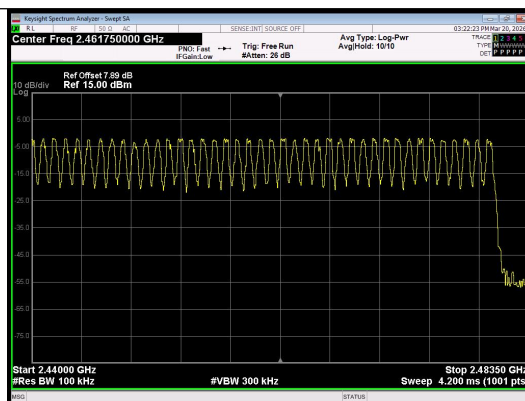
Test Result

Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH1	79	15	PASS
$\pi/4$ DQPSK	2-DH1	79	15	PASS
8DPSK	3-DH1	79	15	PASS

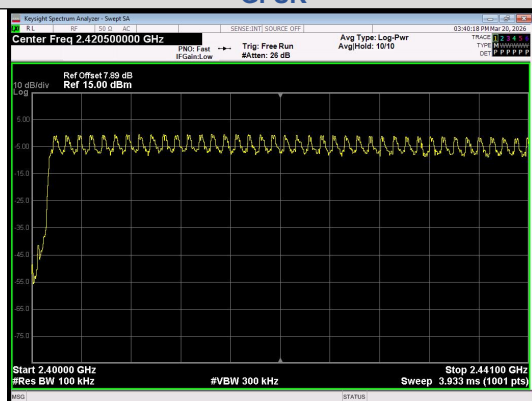
Test Graphs



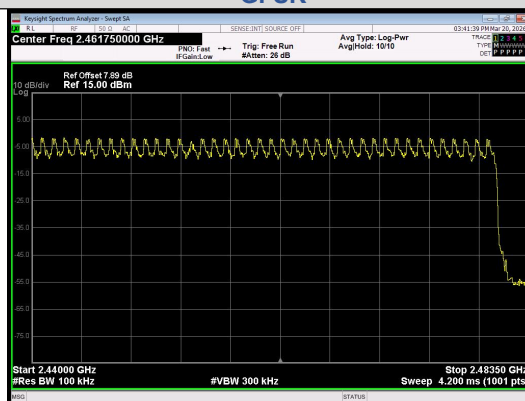
Low End Spectrum Channel Hopping Plot
GFSK



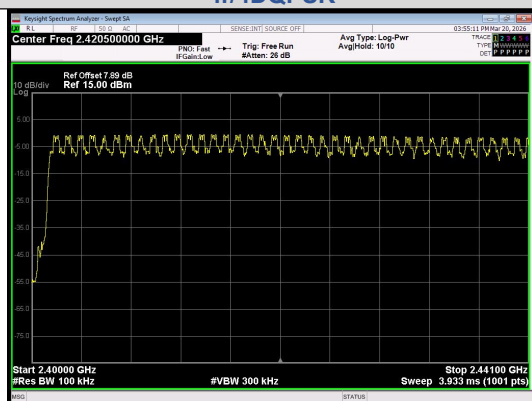
High End Spectrum Channel Hopping Plot
GFSK



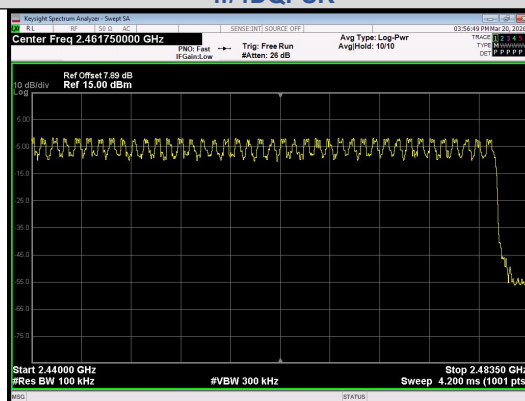
Low End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



High End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



Low End Spectrum Channel Hopping Plot
8DPSK



High End Spectrum Channel Hopping Plot
8DPSK



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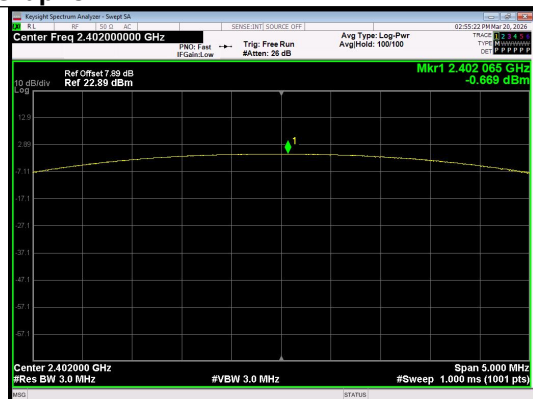
A.3 MAXIMUM PEAK OUTPUT POWER TEST (EIRP)

Test Result

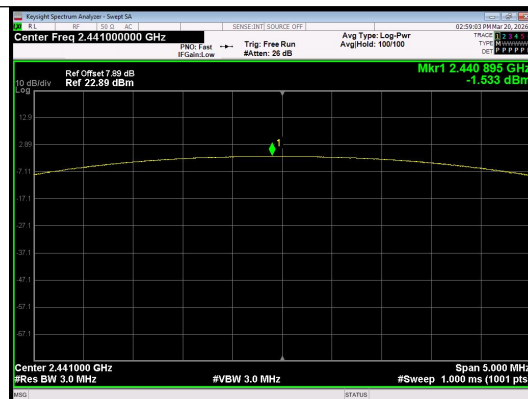
Modulation	Packet Type	Channel	Peak Output Power (dBm)	Peak Power Limit (dBm)	Gain (dBm)	ISED EIRP (dBm)	ISED EIRP Limit (dBm)	Result
GFSK	DH1	0	-0.67	≤21	-0.58	-1.25	≤36.02	PASS
		39	-1.53	≤21	-0.58	2.11	≤36.02	PASS
		78	-2.16	≤21	-0.58	-2.74	≤36.02	PASS
π/4DQPSK	2-DH1	0	0.08	≤21	-0.58	-0.50	≤36.02	PASS
		39	-0.94	≤21	-0.58	-1.52	≤36.02	PASS
		78	-1.47	≤21	-0.58	-2.05	≤36.02	PASS
8DPSK	3-DH1	0	0.61	≤21	-0.58	0.03	≤36.02	PASS
		39	-0.28	≤21	-0.58	-0.86	≤36.02	PASS
		78	-0.83	≤21	-0.58	-1.41	≤36.02	PASS

Note: ISED EIRP (dBm) = Peak Output Power (dBm) + Gain (dBi)

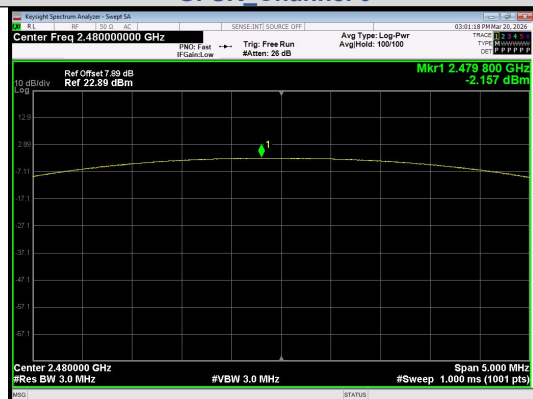
Test Graphs



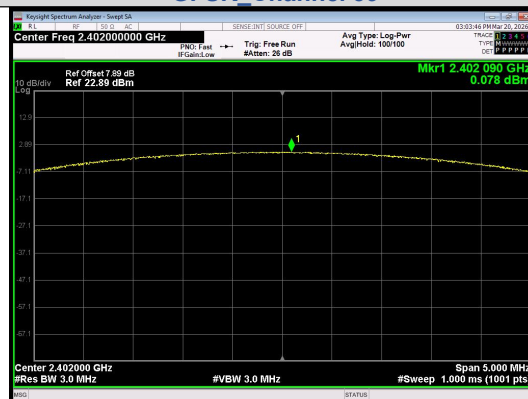
Peak Output Power
GFSK Channel 0



Peak Output Power
GFSK Channel 39



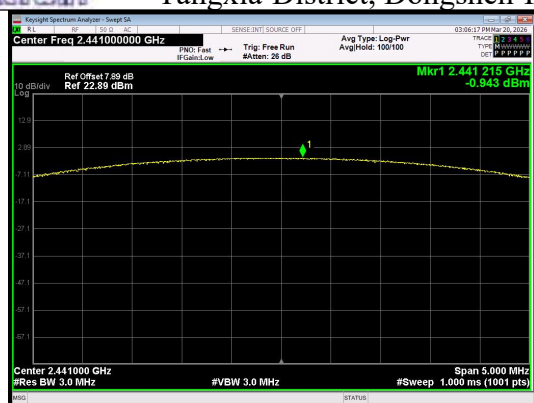
Peak Output Power
GFSK Channel 78



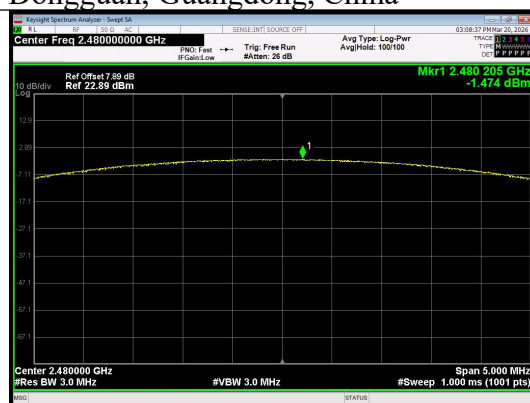
Peak Output Power
π/4DQPSK Channel 0



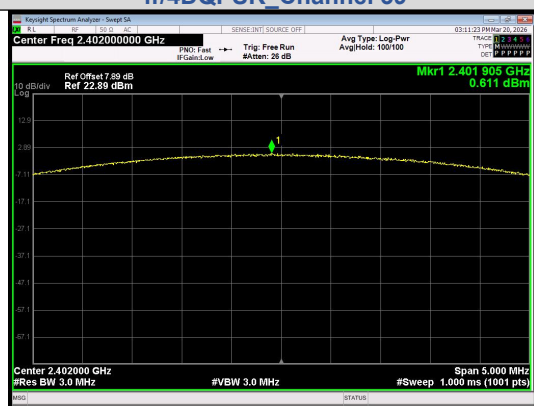
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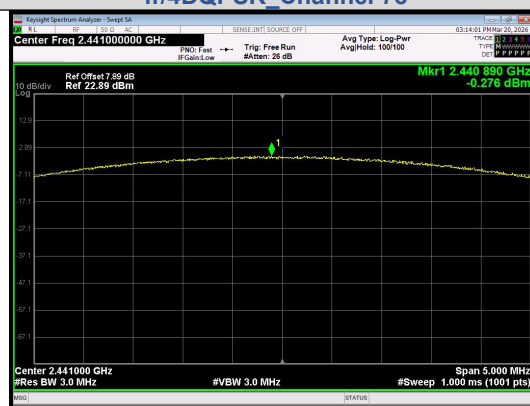
Peak Output Power
 $\pi/4$ DQPSK_Channel 39



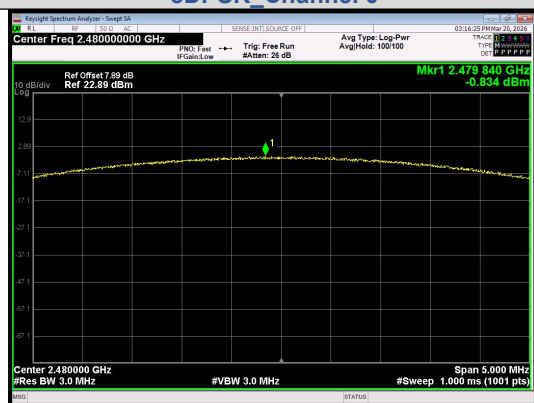
Peak Output Power
 $\pi/4$ DQPSK_Channel 78



Peak Output Power
8DPSK_Channel 0



Peak Output Power
8DPSK_Channel 39



Peak Output Power
8DPSK_Channel 78



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A.4 99% BANDWIDTH

Test Result

Modulation	Channel	Center Frequency (MHz)	99% BW (MHz)
GFSK	0	2402	0.84984
	39	2441	0.87242
	78	2480	0.86960
π /4DQPSK	0	2402	1.1626
	39	2441	1.1677
	78	2480	1.1696
8DPSK	0	2402	1.1534
	39	2441	1.1492
	78	2480	1.1485

Test Graphs

