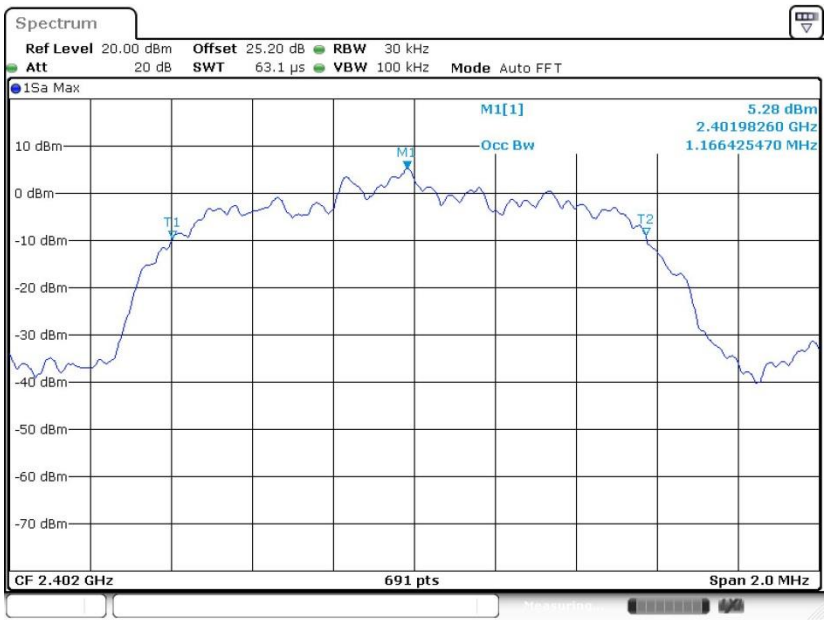
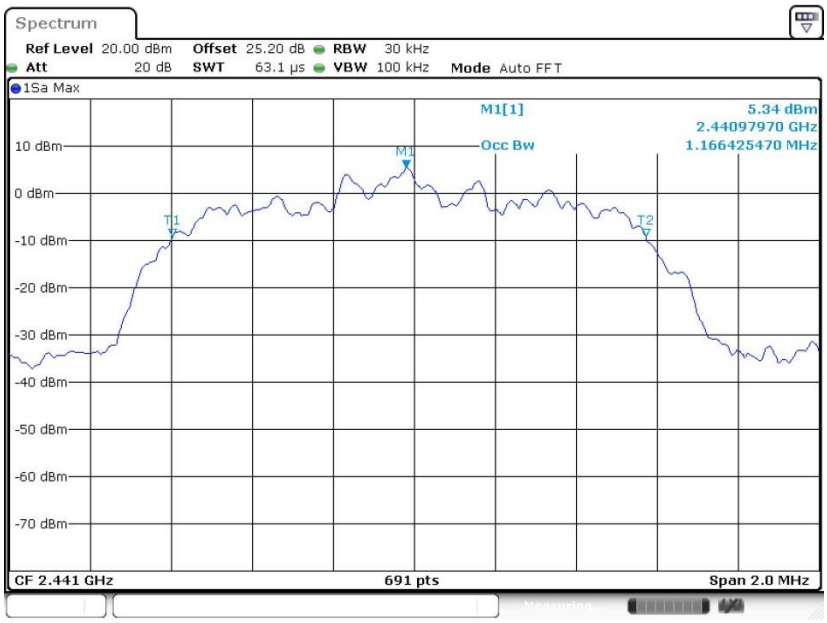
<2Mbps>

99% Occupied Bandwidth Plot on Channel 00



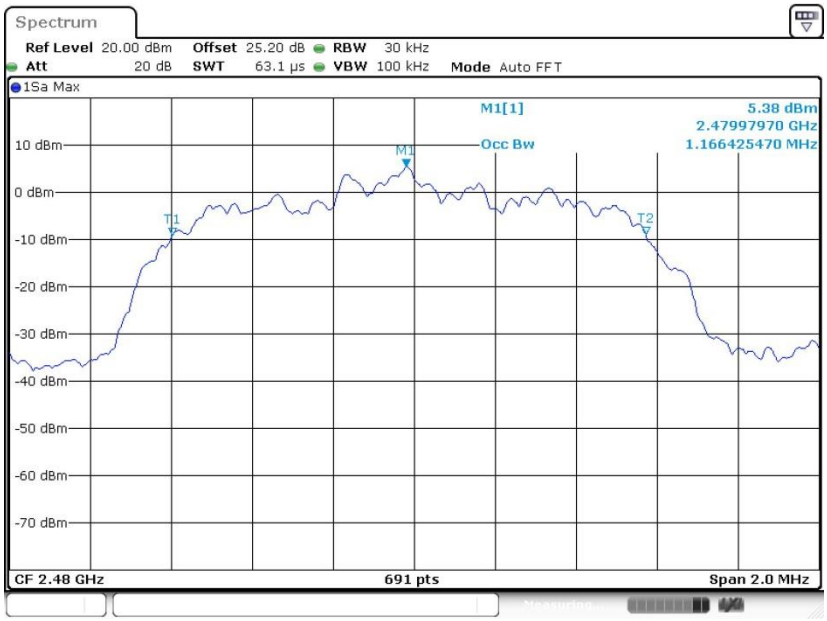
Date: 19.JUN.2019 12:20:27

99% Occupied Bandwidth Plot on Channel 39



Date: 19.JUN.2019 12:21:08

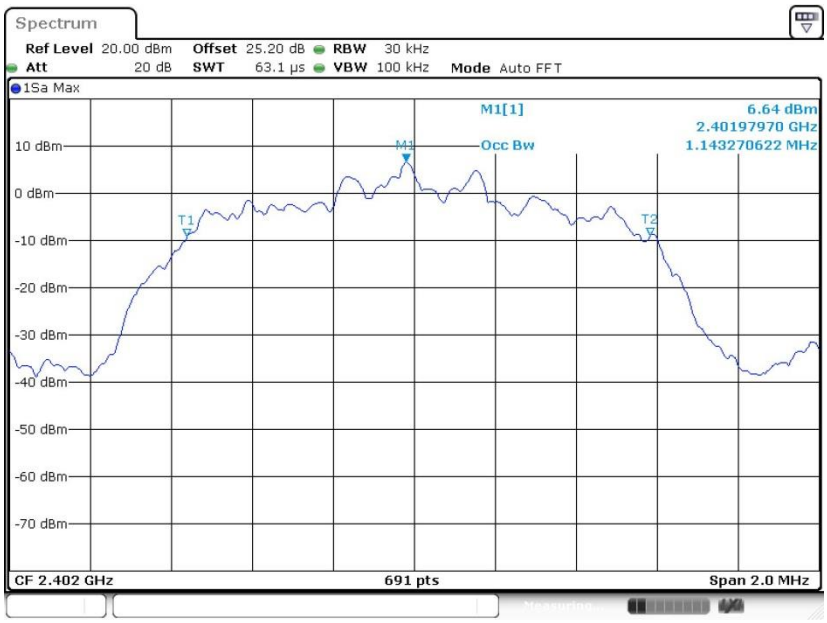
99% Occupied Bandwidth Plot on Channel 78



Date: 19.JUN.2019 12:21:46

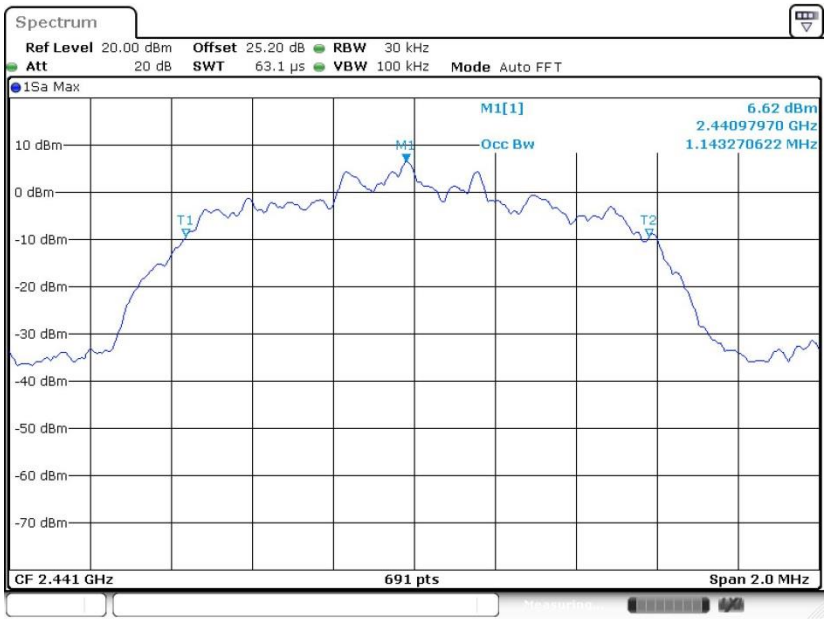
<3Mbps>

99% Occupied Bandwidth Plot on Channel 00



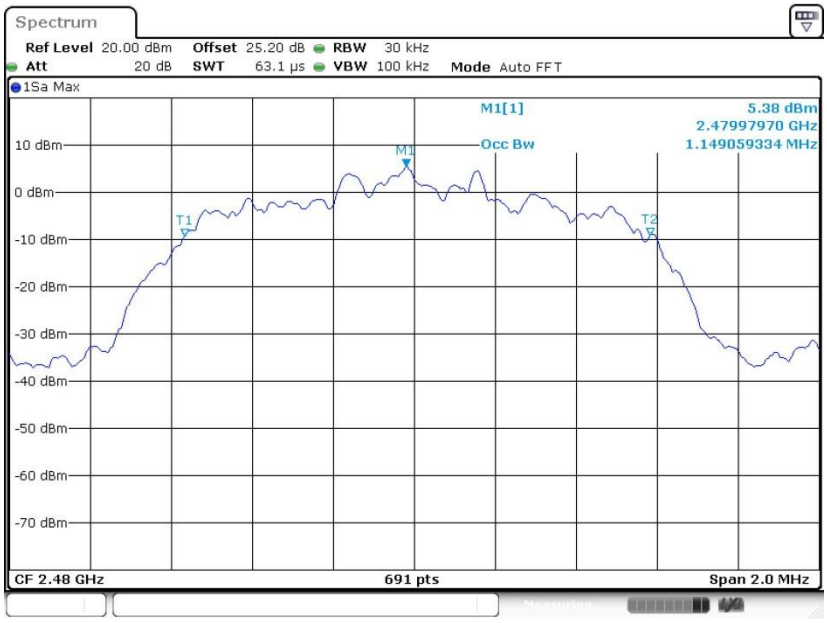
Date: 19.JUN.2019 12:22:32

99% Occupied Bandwidth Plot on Channel 39



Date: 19.JUN.2019 12:23:10

99% Occupied Bandwidth Plot on Channel 78



Date: 19.JUN.2019 12:23:48

Note : The occupied channel bandwidth is maintained within the band of operation for all of the modulations.

3.5 Output Power Measurement

3.5.1 Limit of Output Power

The maximum peak conducted output power of the intentional radiator shall not exceed the following: (1) For frequency hopping systems operating in the 2400-2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725-5850 MHz band: 1 watt. For all other frequency hopping systems in the 2400-2483.5 MHz band 0.125 watts. The power limit for 1Mbps, 2Mbps, 3Mbps and AFH modes are 0.125 watts.

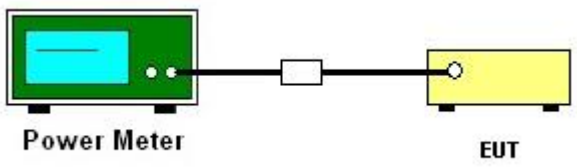
3.5.2 Measuring Instruments

The measuring equipment is listed in the section 4 of this test report.

3.5.3 Test Procedures

1. The testing follows ANSI C63.10-2013 clause 7.8.5.
2. The RF output of EUT was connected to the power meter by RF cable and attenuator. The path loss was compensated to the results for each measurement.
3. Set to the maximum power setting and enable the EUT transmit continuously.
4. Measure the conducted output power with cable loss and record the results in the test report.
5. Measure and record the results in the test report.

3.5.4 Test Setup



3.5.5 Test Result of Peak Output Power

Please refer to Appendix A.

3.5.6 Test Result of Average Output Power (Reporting Only)

Please refer to Appendix A.

3.6 Conducted Band Edges Measurement

3.6.1 Limit of Band Edges

In any 100 kHz bandwidth outside the intentional radiation frequency band, the radio frequency power shall be at least 20 dB below the highest level of the radiated power. In addition, radiated emissions which fall in the restricted bands must also comply with the radiated emission limits.

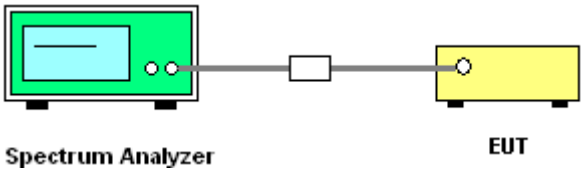
3.6.2 Measuring Instruments

The measuring equipment is listed in the section 4 of this test report.

3.6.3 Test Procedures

1. The testing follows ANSI C63.10-2013 clause 7.8.6.
2. Set to the maximum power setting and enable the EUT transmit continuously.
3. Set RBW = 100kHz, VBW = 300kHz. Band edge emissions must be at least 20 dB down from the highest emission level within the authorized band as measured with a 100kHz RBW. The attenuation shall be 30 dB instead of 20 dB when RMS conducted output power procedure is used.
4. Enable hopping function of the EUT and then repeat step 2. and 3.
5. Measure and record the results in the test report.

3.6.4 Test Setup

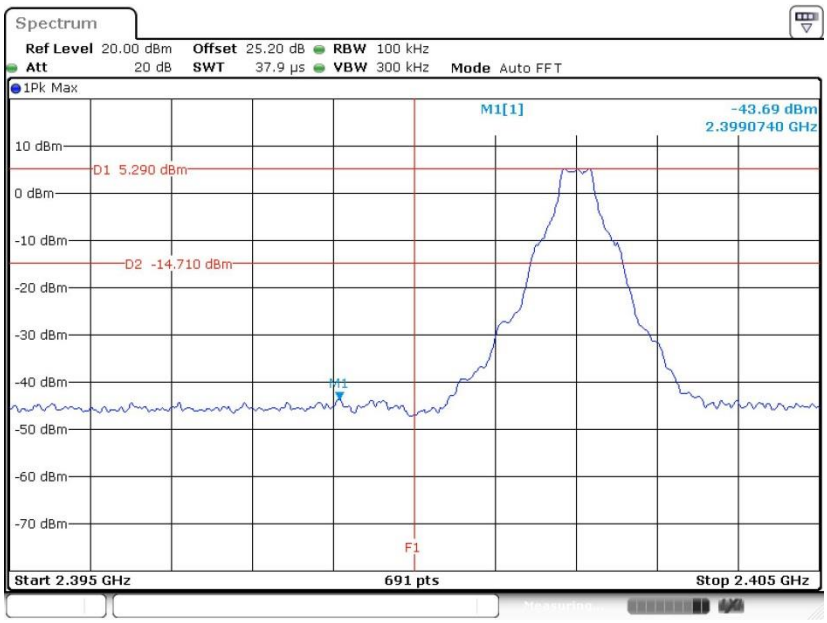


3.6.5 Test Result of Conducted Band Edges

BT EDR chip of CYW2070：

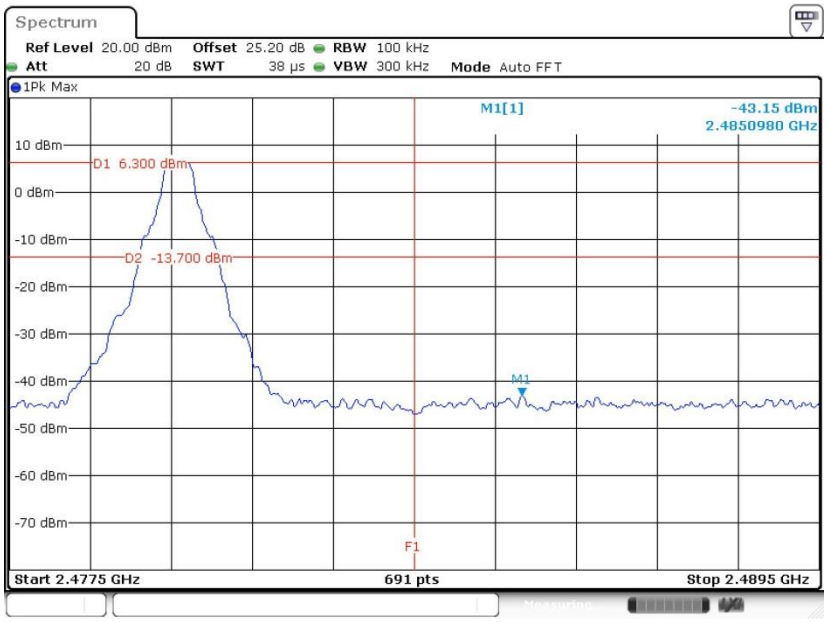
<1Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 10:30:00

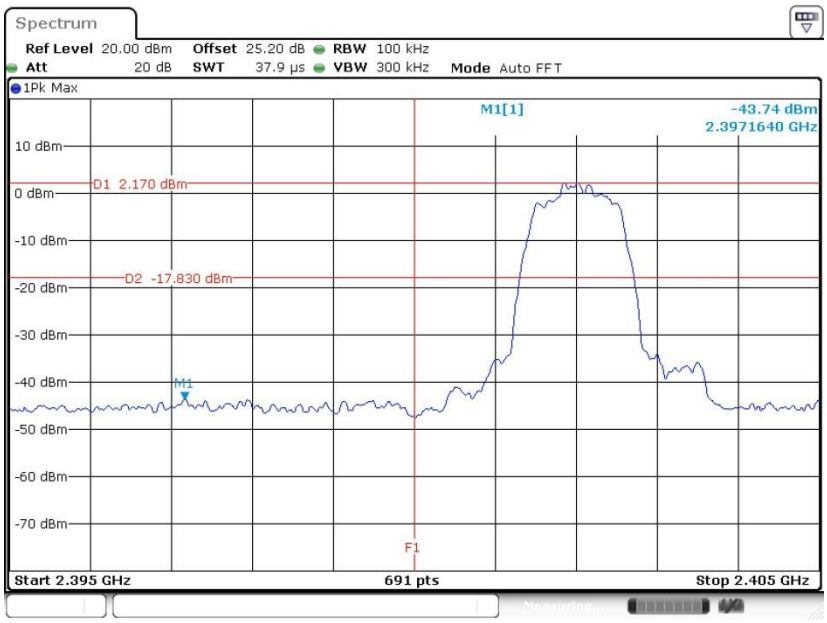
High Band Edge Plot on Channel 78



Date: 19.JUN.2019 10:30:27

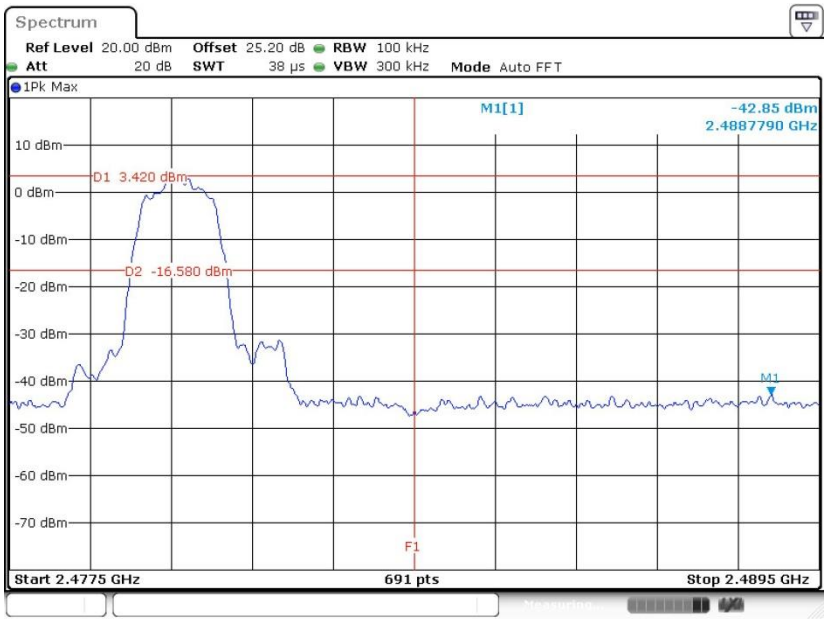
<2Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 10:46:12

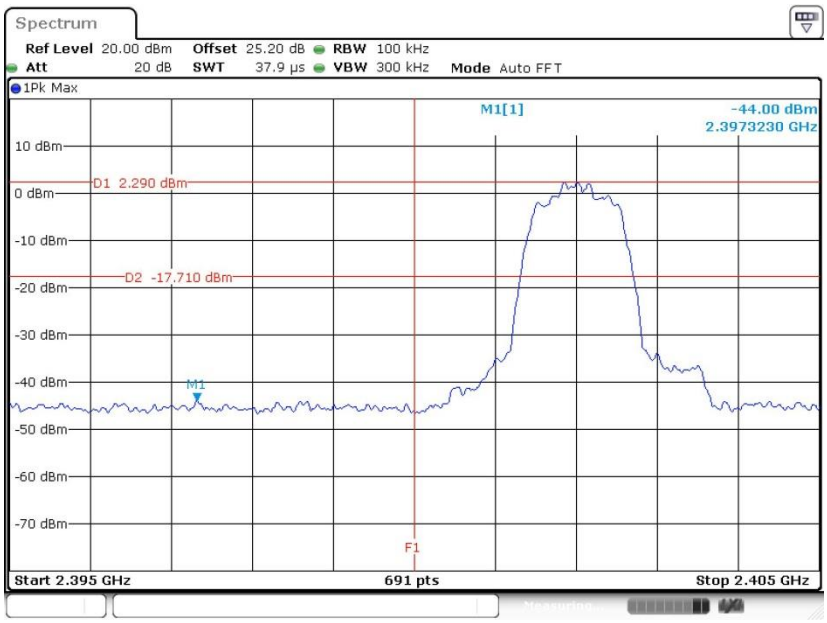
High Band Edge Plot on Channel 78



Date: 19.JUN.2019 10:46:39

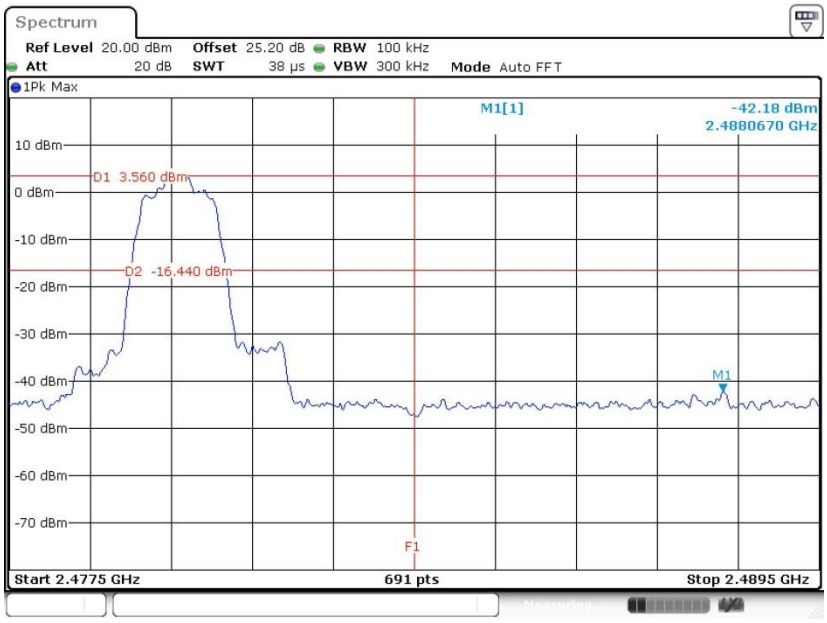
<3Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 10:58:43

High Band Edge Plot on Channel 78

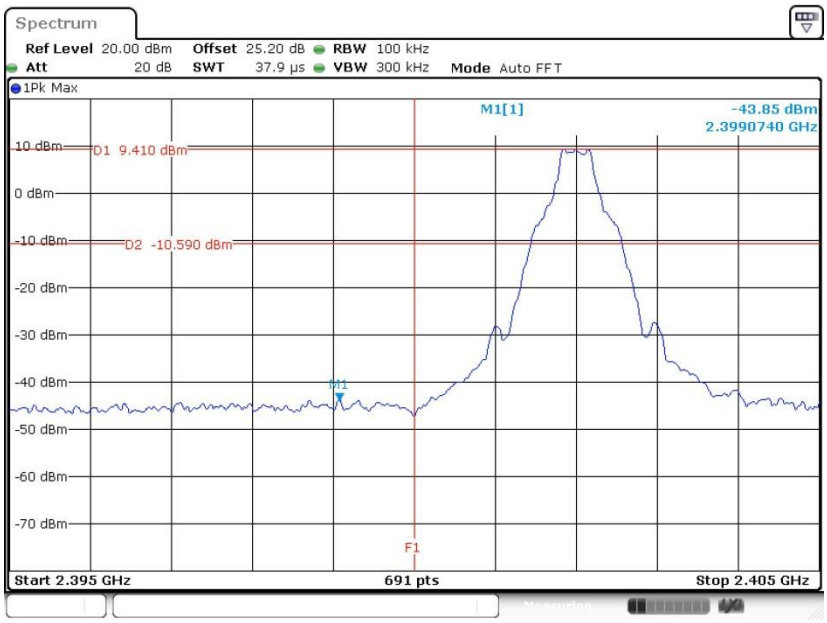


Date: 19.JUN.2019 10:59:12

BT EDR chip of WCN3660B：

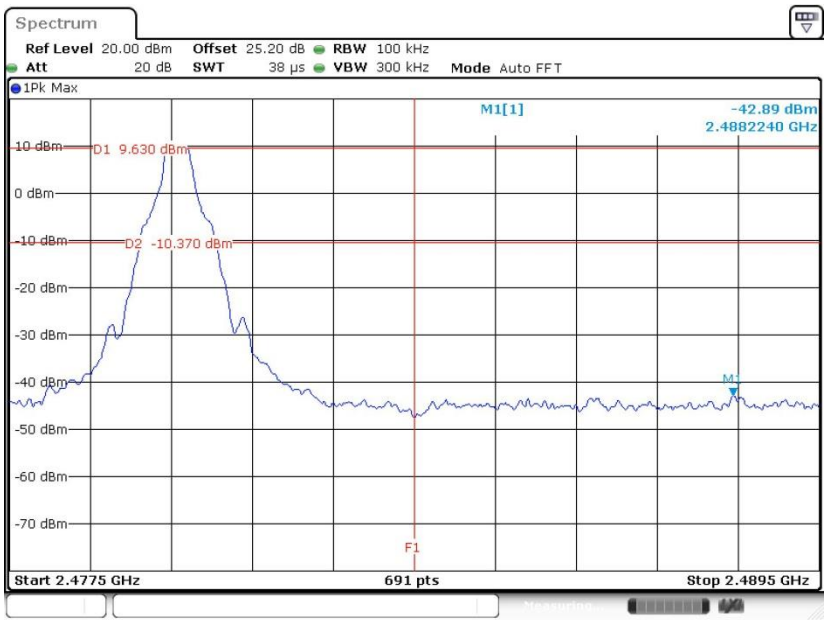
<1Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 12:12:19

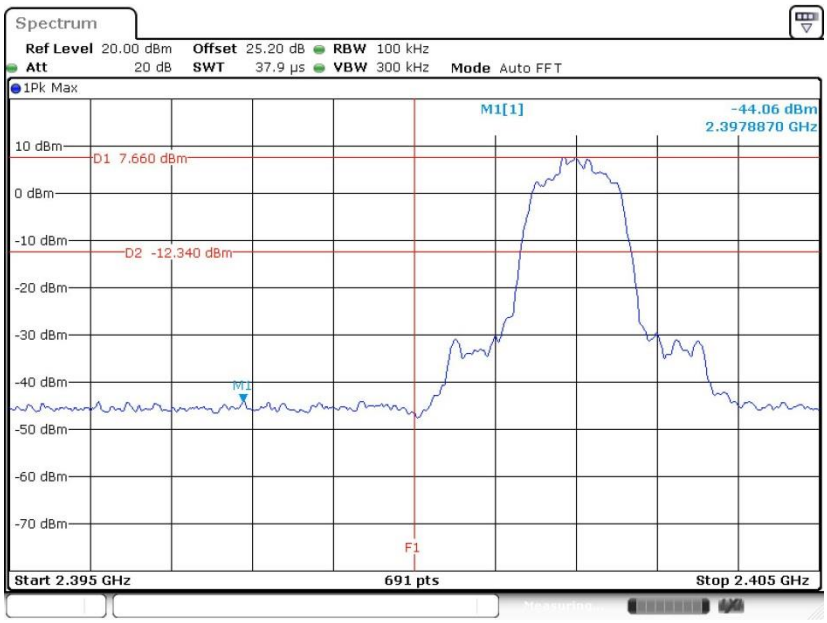
High Band Edge Plot on Channel 78



Date: 19.JUN.2019 12:12:57

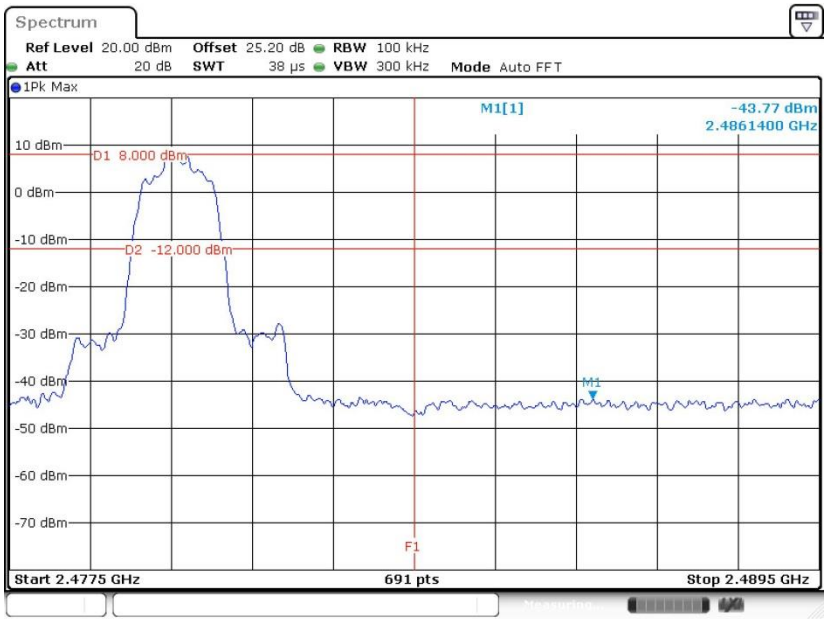
<2Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 12:13:31

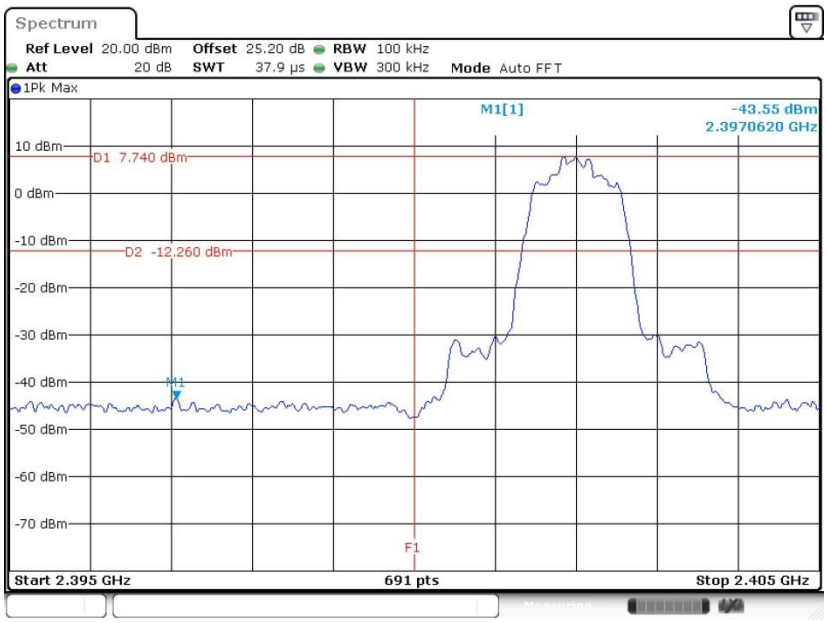
High Band Edge Plot on Channel 78



Date: 19.JUN.2019 12:14:05

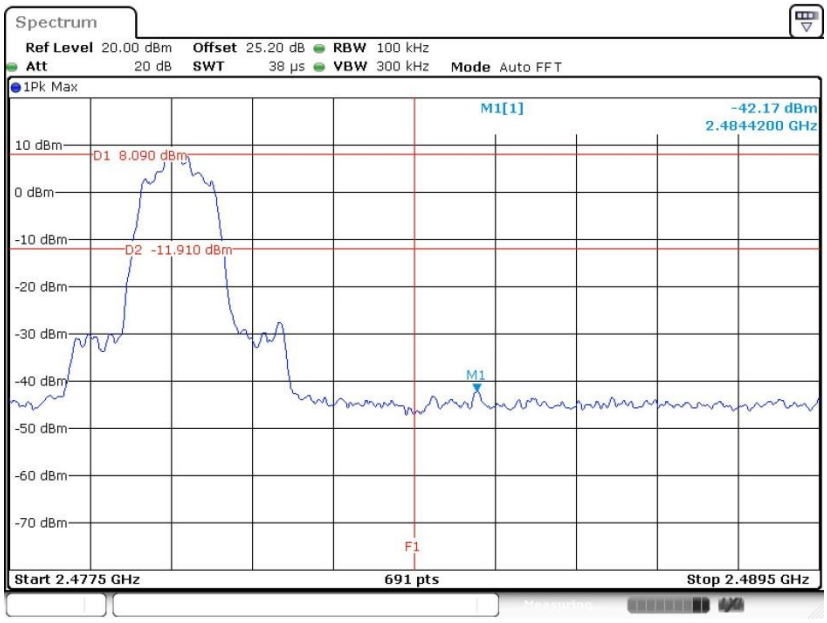
<3Mbps>

Low Band Edge Plot on Channel 00



Date: 19.JUN.2019 12:14:39

High Band Edge Plot on Channel 78



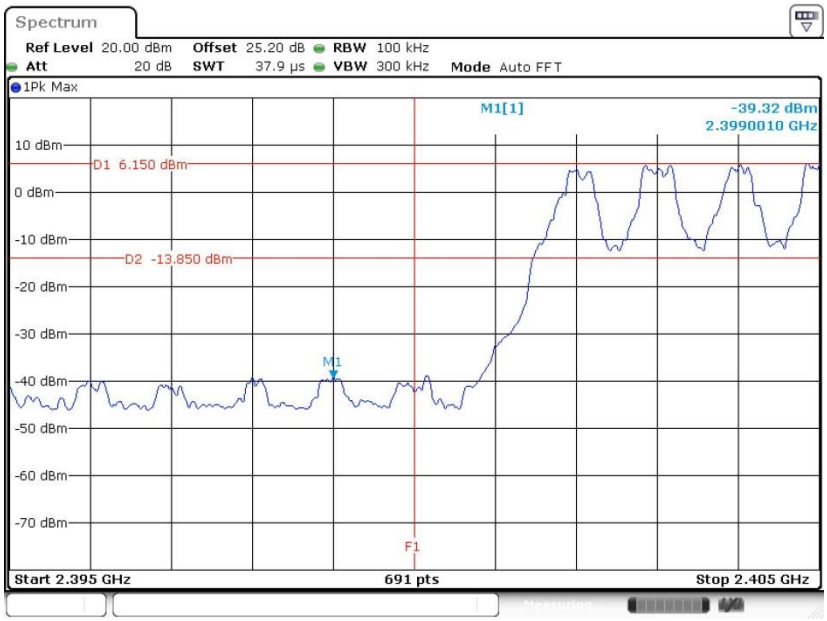
Date: 19.JUN.2019 12:15:04

3.6.6 Test Result of Conducted Hopping Mode Band Edges

BT EDR chip of CYW2070：

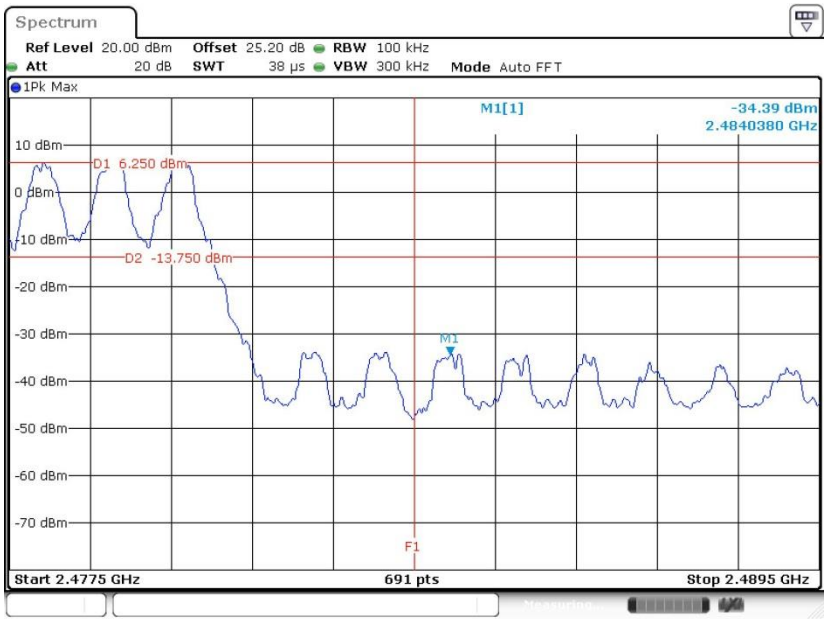
<1Mbps>

Hopping Mode Low Band Edge Plot



Date: 19.JUN.2019 10:31:17

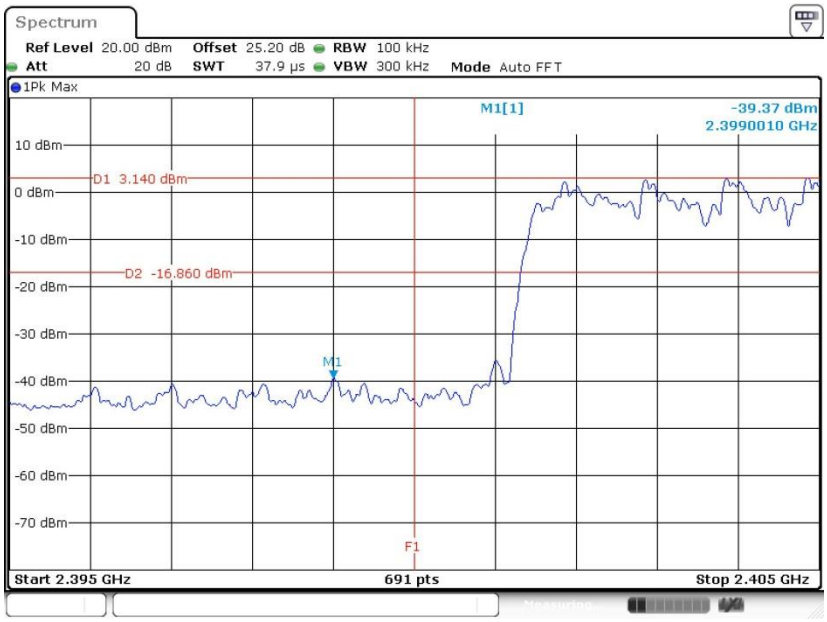
Hopping Mode High Band Edge Plot



Date: 19.JUN.2019 10:31:44

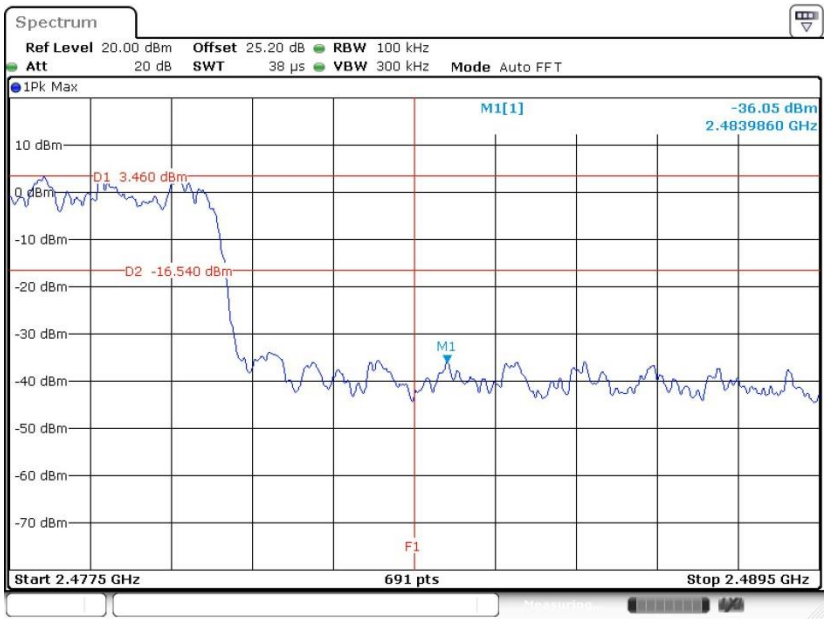
<2Mbps>

Hopping Mode Low Band Edge Plot



Date: 19.JUN.2019 10:47:30

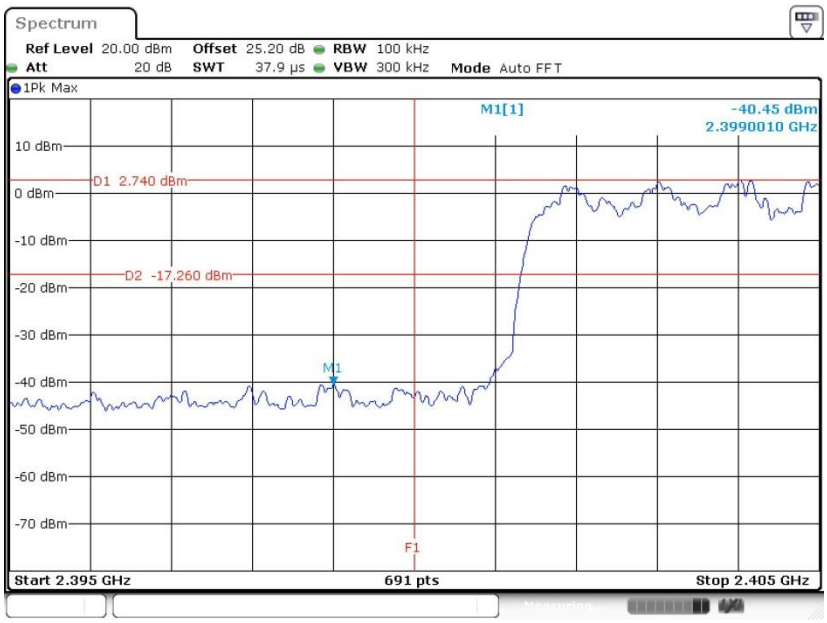
Hopping Mode High Band Edge Plot



Date: 19.JUN.2019 10:48:12

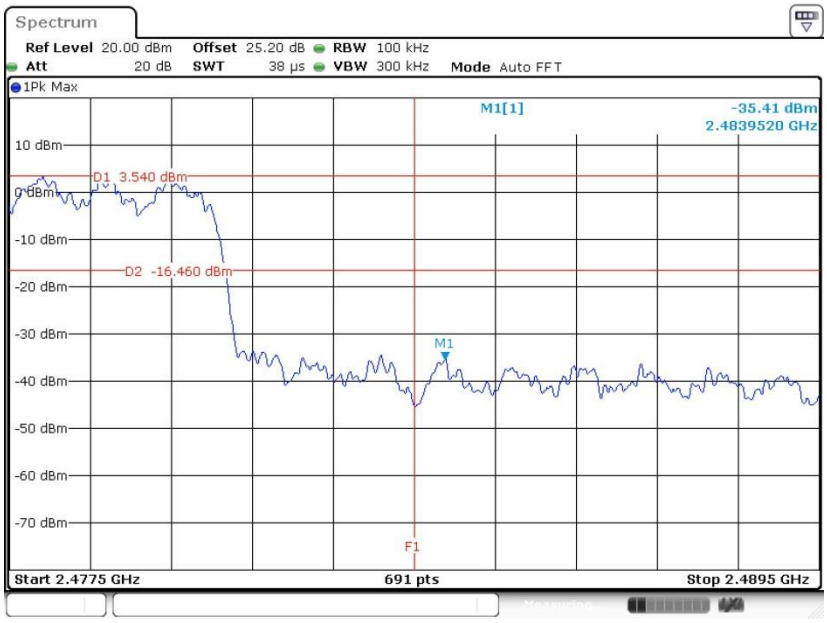
<3Mbps>

Hopping Mode Low Band Edge Plot



Date: 19.JUN.2019 11:00:00

Hopping Mode High Band Edge Plot

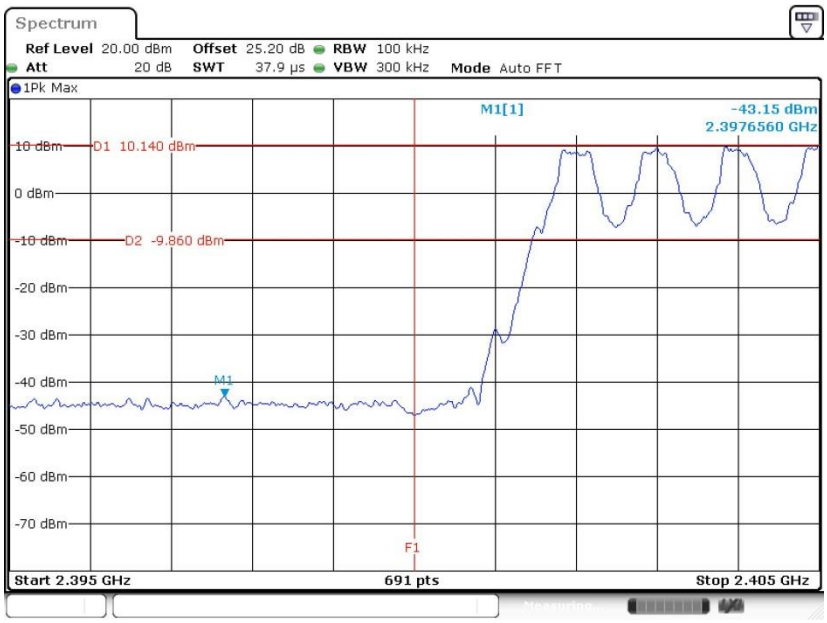


Date: 19.JUN.2019 11:00:35

BT EDR chip of WCN3660B：

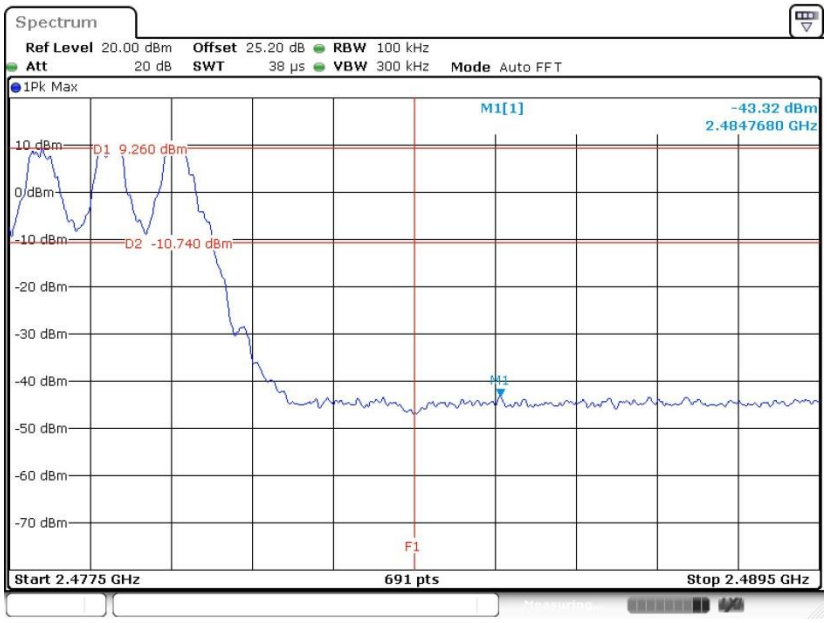
<1Mbps>

Hopping Mode Low Band Edge Plot



Date: 19.JUN.2019 12:16:42

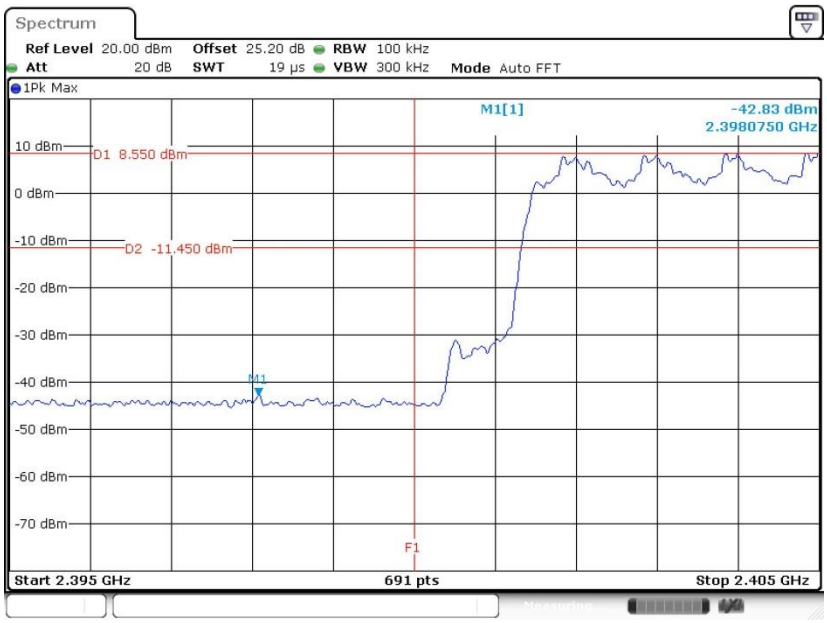
Hopping Mode High Band Edge Plot



Date: 19.JUN.2019 12:17:22

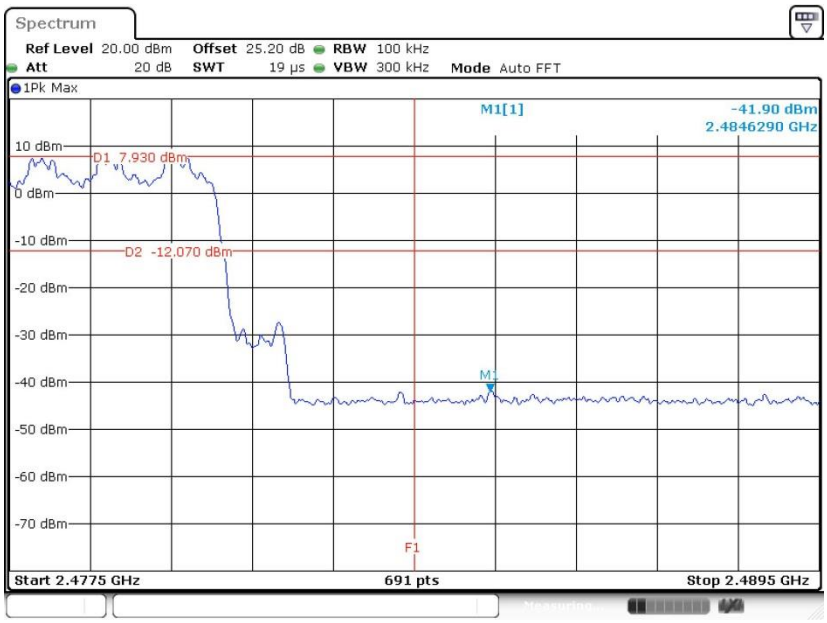
<2Mbps>

Hopping Mode Low Band Edge Plot



Date: 21.JUN.2019 21:09:18

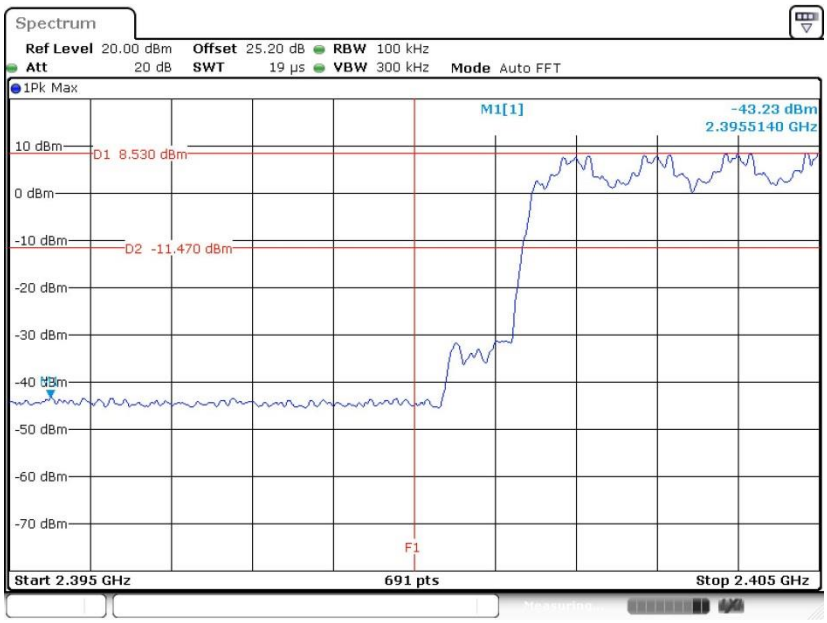
Hopping Mode High Band Edge Plot



Date: 21.JUN.2019 21:09:53

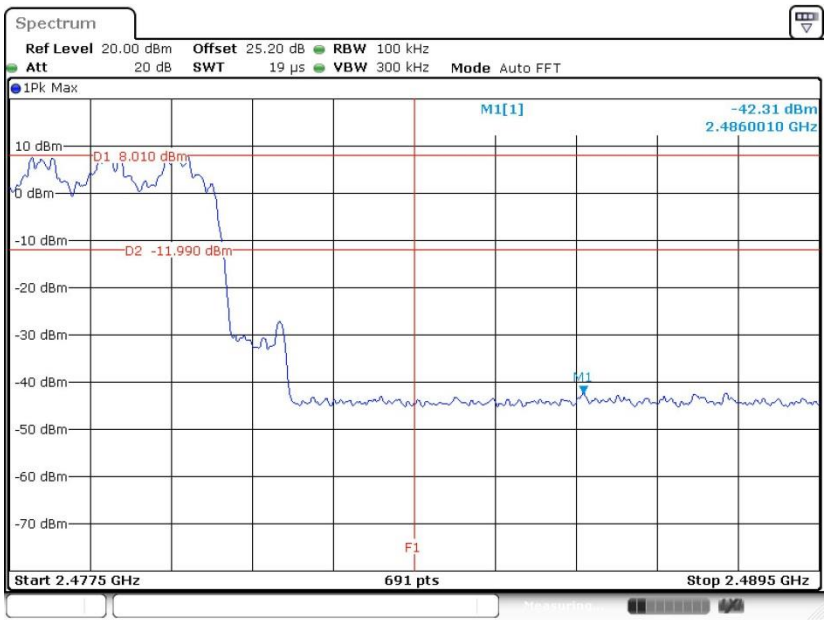
<3Mbps>

Hopping Mode Low Band Edge Plot



Date: 21.JUN.2019 21:10:31

Hopping Mode High Band Edge Plot



Date: 21.JUN.2019 21:10:50

3.7 Conducted Spurious Emission Measurement

3.7.1 Limit of Spurious Emission Measurement

In any 100 kHz bandwidth outside the intentional radiation frequency band, the radio frequency power shall be at least 20 dB below the highest level of the radiated power. In addition, radiated emissions which fall in the restricted bands must also comply with the radiated emission limits.

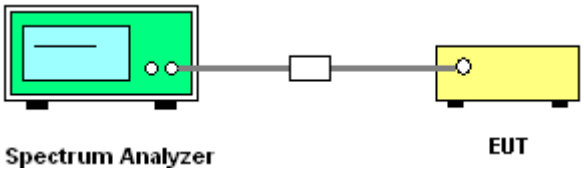
3.7.2 Measuring Instruments

The measuring equipment is listed in the section 4 of this test report.

3.7.3 Test Procedure

1. The testing follows ANSI C63.10-2013 clause 7.8.8.
2. The RF output of EUT was connected to the spectrum analyzer by RF cable and attenuator. The path loss was compensated to the results for each measurement.
3. Set to the maximum power setting and enable the EUT transmit continuously.
4. Set RBW = 100 kHz, VBW = 300kHz, scan up through 10th harmonic. All harmonics / spurs must be at least 20 dB down from the highest emission level within the authorized band as measured with a 100 kHz RBW.
5. Measure and record the results in the test report.
6. The RF fundamental frequency should be excluded against the limit line in the operating frequency band.

3.7.4 Test Setup

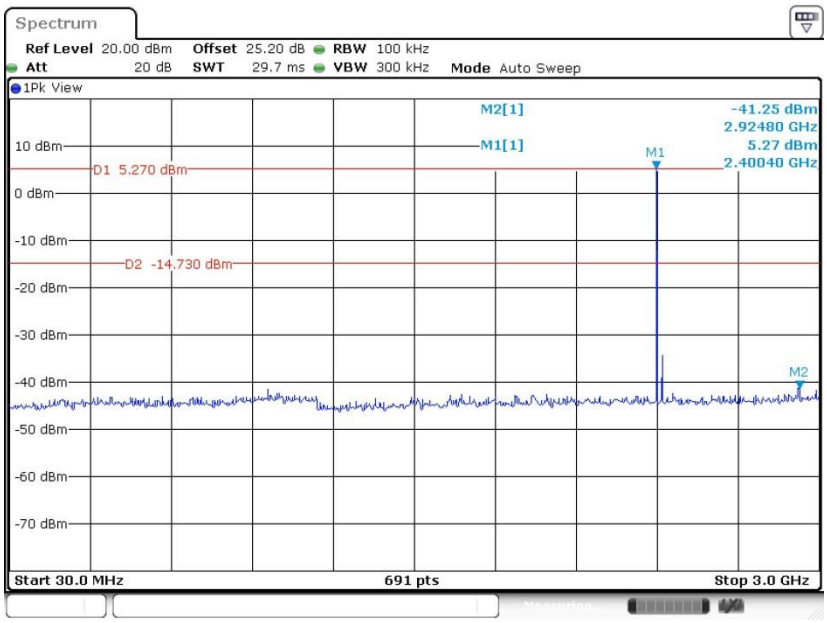


3.7.5 Test Result of Conducted Spurious Emission

BT EDR chip of CYW2070：

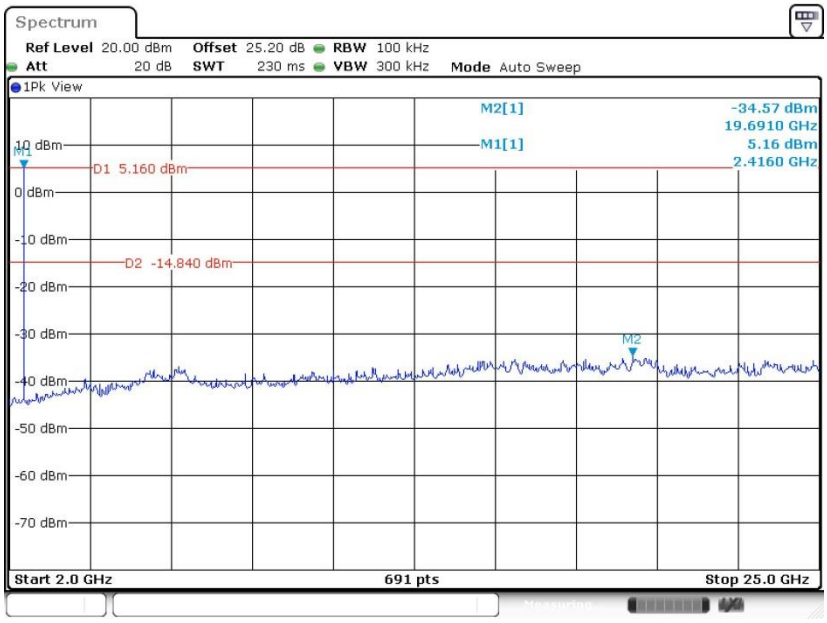
<1Mbps>

CSE Plot on Ch 00 between 30MHz ~ 3 GHz



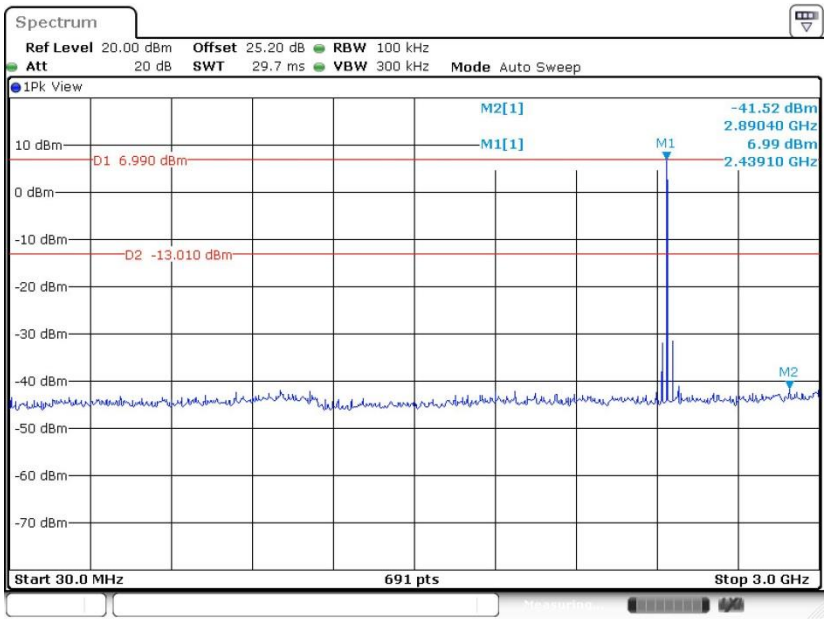
Date: 19.JUN.2019 10:33:34

CSE Plot on Ch 00 between 2 GHz ~ 25 GHz



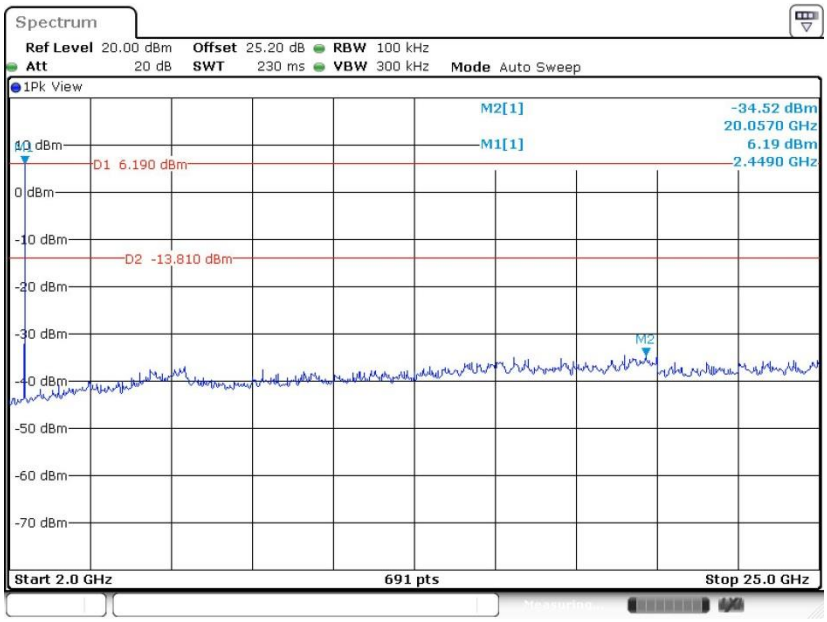
Date: 19.JUN.2019 10:34:07

CSE Plot on Ch 39 between 30MHz ~ 3 GHz



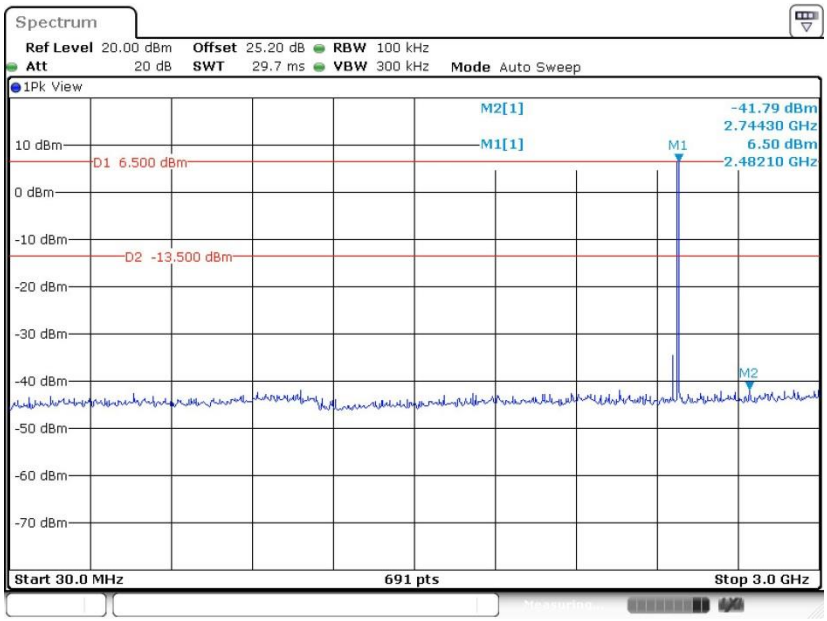
Date: 19.JUN.2019 10:35:27

CSE Plot on Ch 39 between 2 GHz ~ 25 GHz



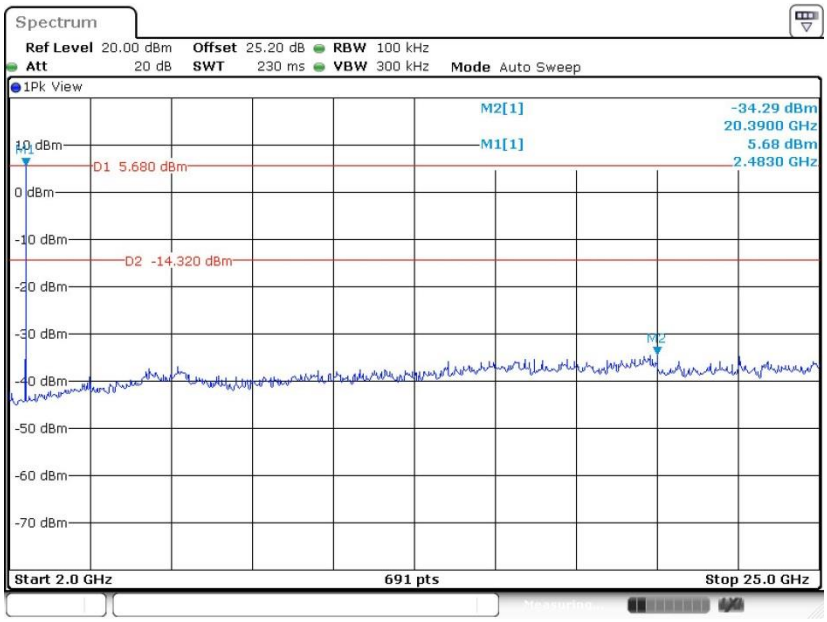
Date: 19.JUN.2019 10:35:56

CSE Plot on Ch 78 between 30MHz ~ 3 GHz



Date: 19.JUN.2019 10:36:52

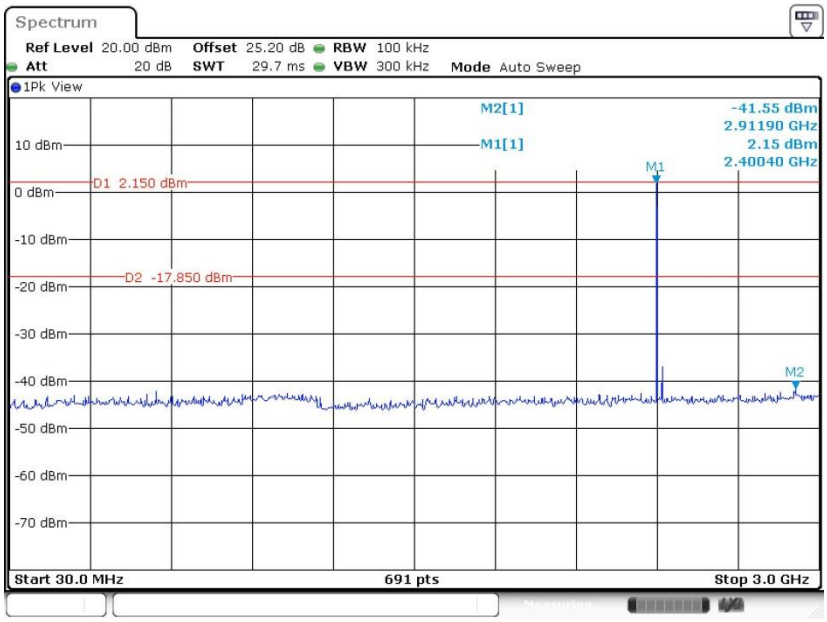
CSE Plot on Ch 78 between 2 GHz ~ 25 GHz



Date: 19.JUN.2019 10:37:21

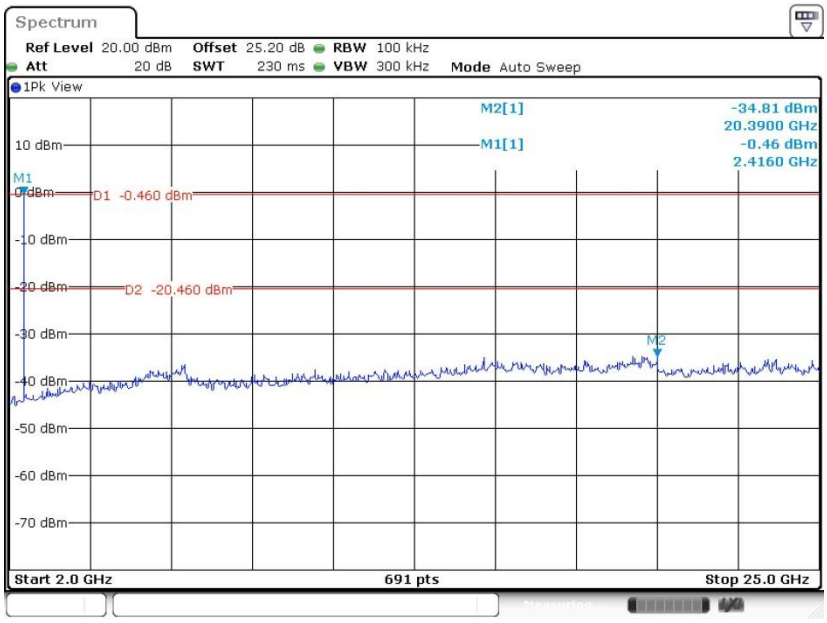
<2Mbps>

CSE Plot on Ch 00 between 30MHz ~ 3 GHz



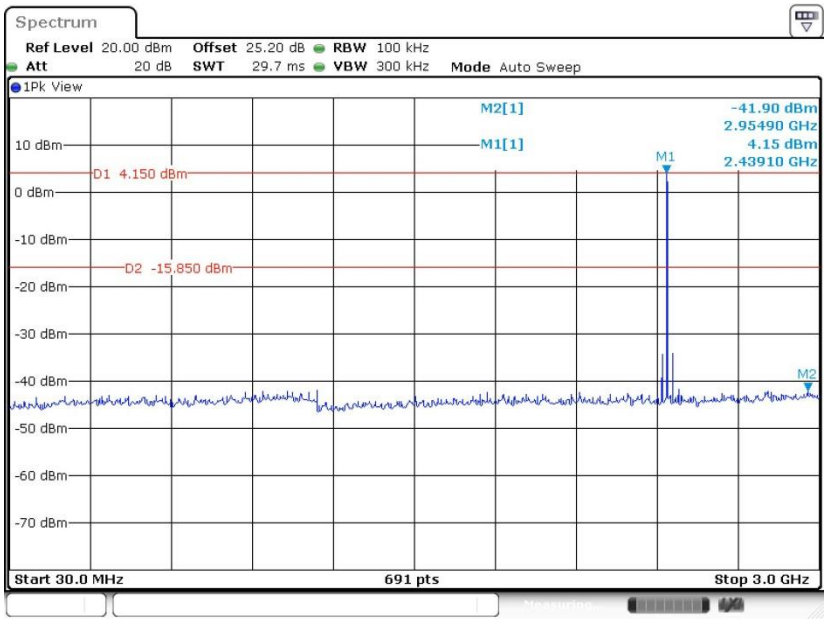
Date: 19.JUN.2019 10:51:24

CSE Plot on Ch 00 between 2 GHz ~ 25 GHz



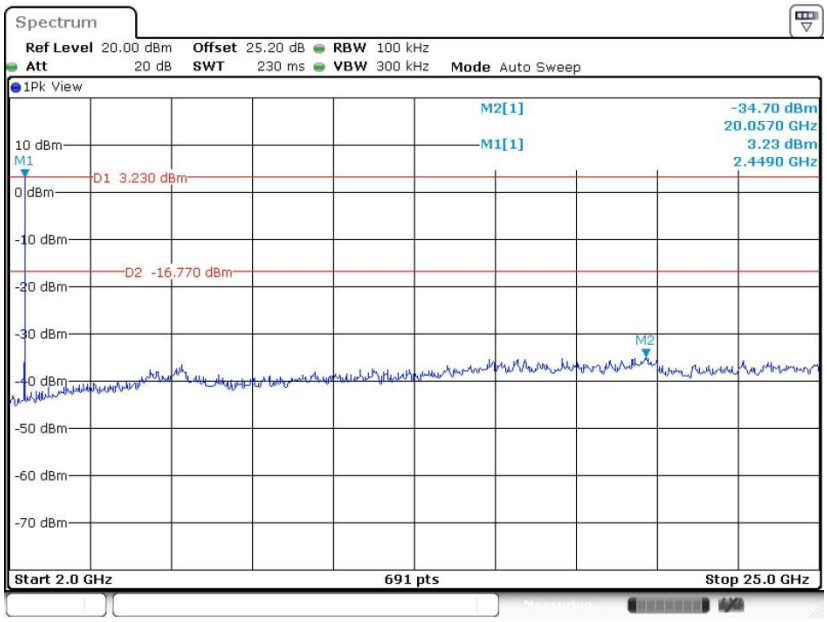
Date: 19.JUN.2019 10:51:52

CSE Plot on Ch 39 between 30MHz ~ 3 GHz



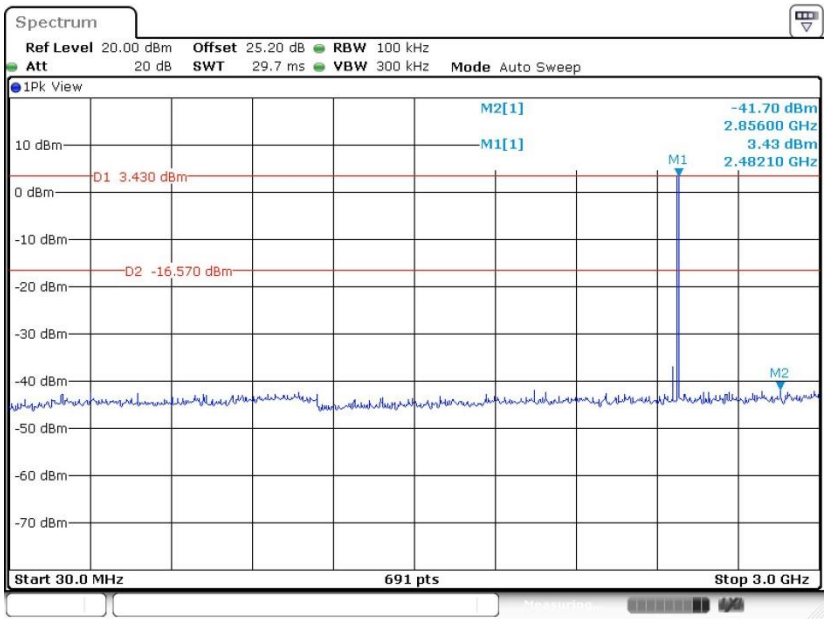
Date: 19.JUN.2019 10:54:05

CSE Plot on Ch 39 between 2 GHz ~ 25 GHz



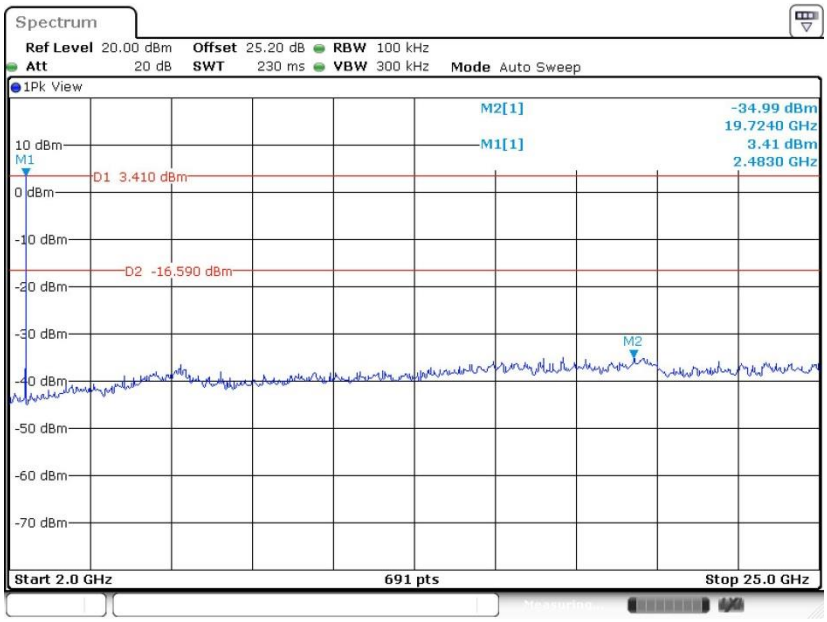
Date: 19.JUN.2019 10:54:37

CSE Plot on Ch 78 between 30MHz ~ 3 GHz



Date: 19.JUN.2019 10:52:37

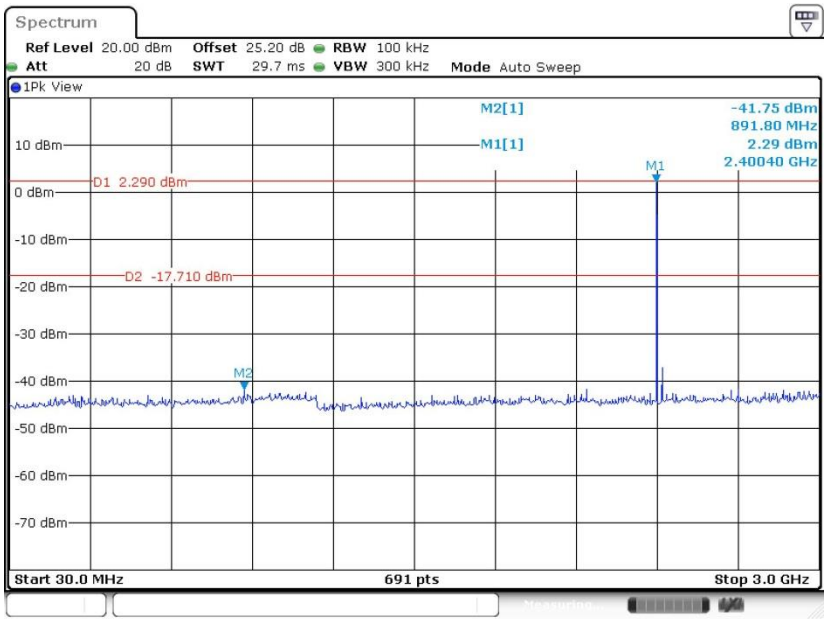
CSE Plot on Ch 78 between 2 GHz ~ 25 GHz



Date: 19.JUN.2019 10:53:04

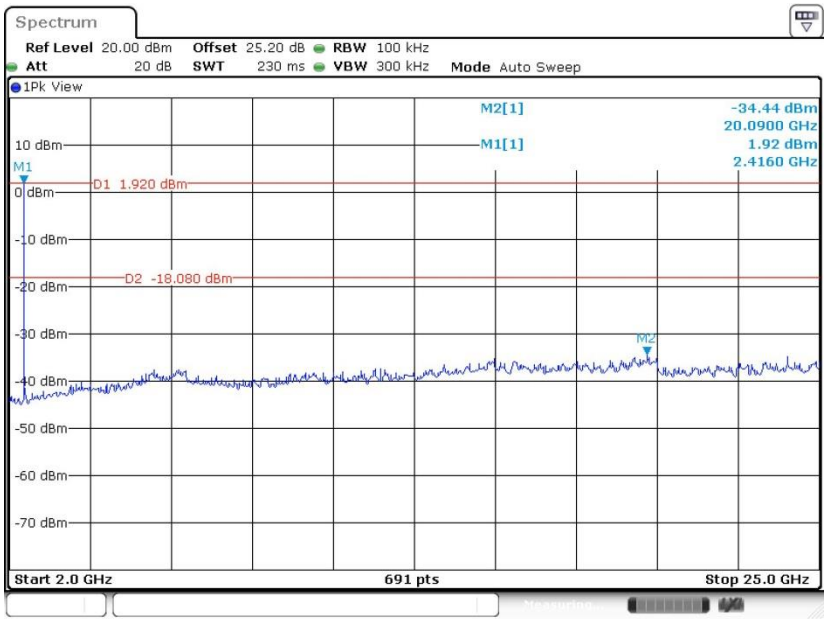
<3Mbps>

CSE Plot on Ch 00 between 30MHz ~ 3 GHz



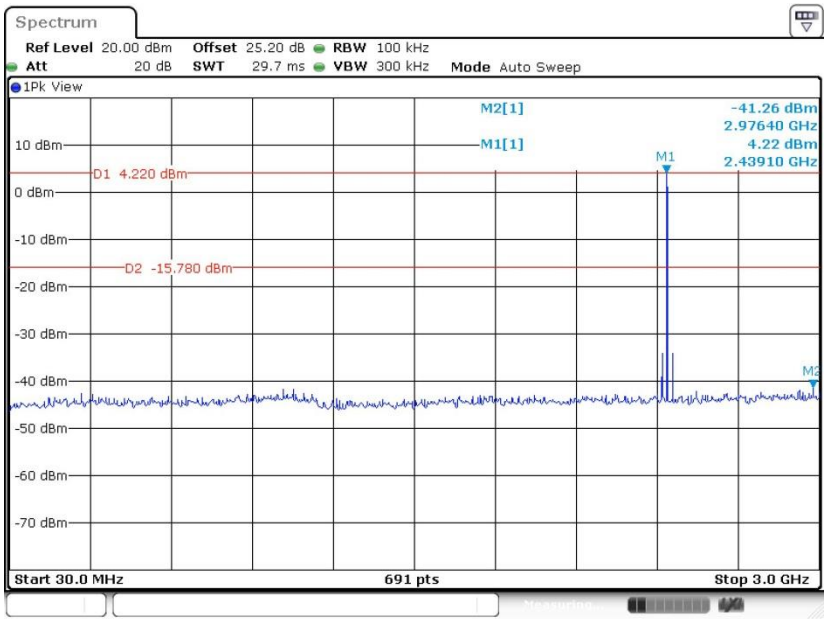
Date: 19.JUN.2019 11:06:33

CSE Plot on Ch 00 between 2 GHz ~ 25 GHz



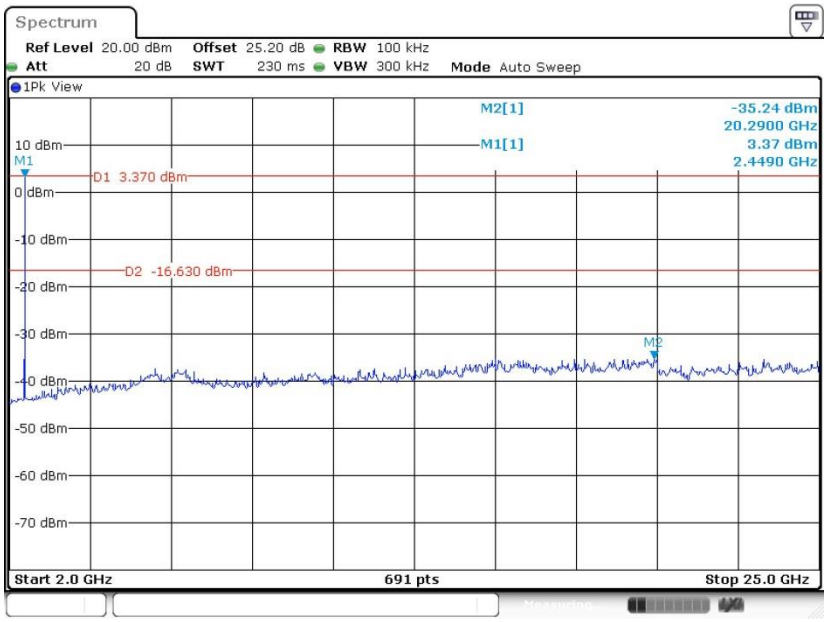
Date: 19.JUN.2019 11:07:01

CSE Plot on Ch 39 between 30MHz ~ 3 GHz



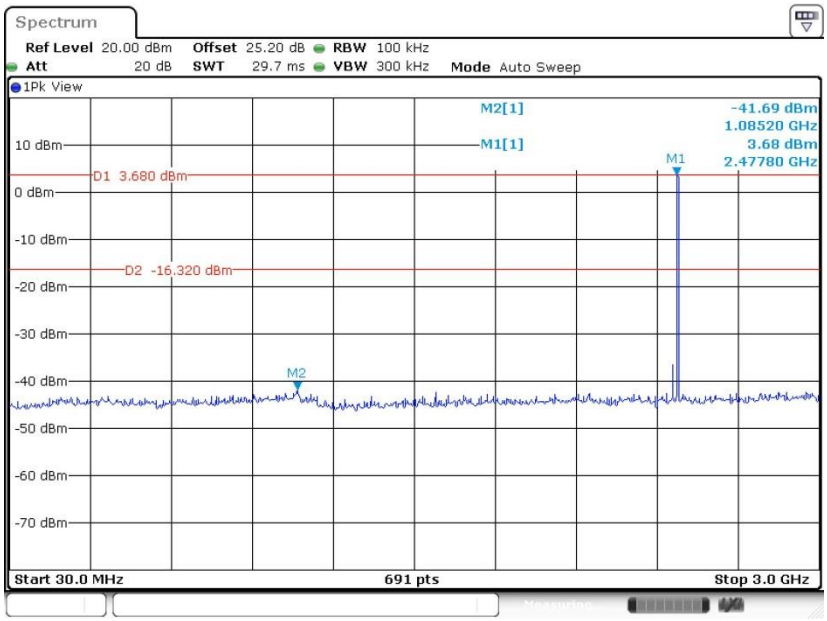
Date: 19.JUN.2019 11:07:46

CSE Plot on Ch 39 between 2 GHz ~ 25 GHz



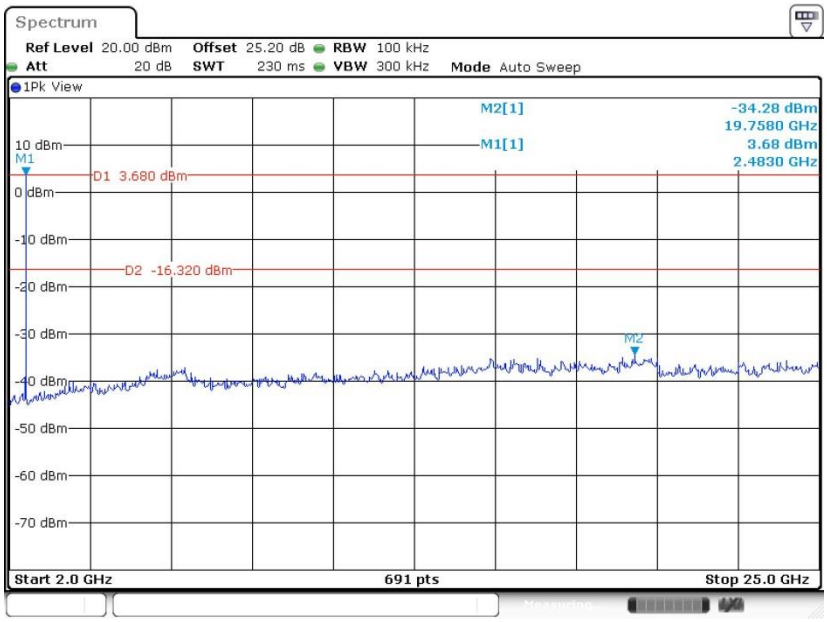
Date: 19.JUN.2019 11:08:13

CSE Plot on Ch 78 between 30MHz ~ 3 GHz



Date: 19.JUN.2019 11:08:57

CSE Plot on Ch 78 between 2 GHz ~ 25 GHz

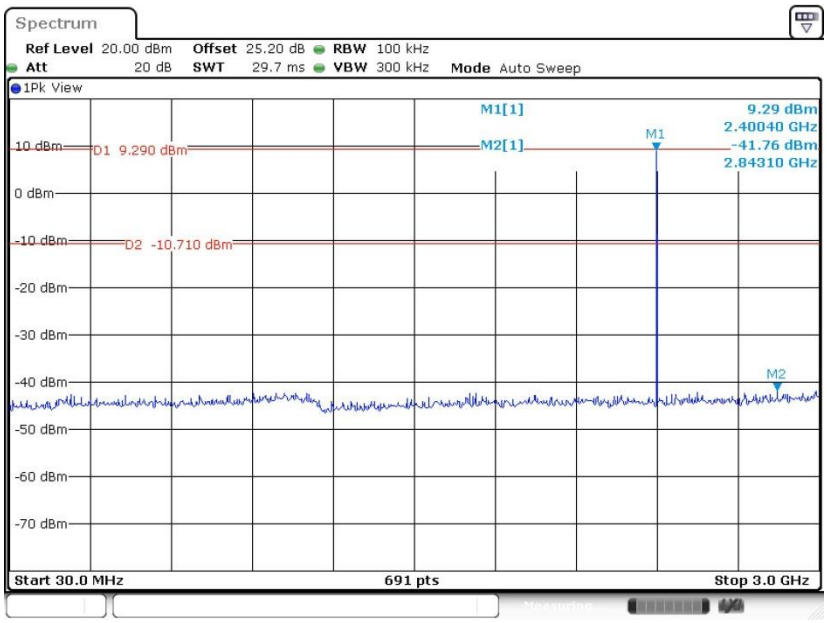


Date: 19.JUN.2019 11:09:24

BT EDR chip of WCN3660B：

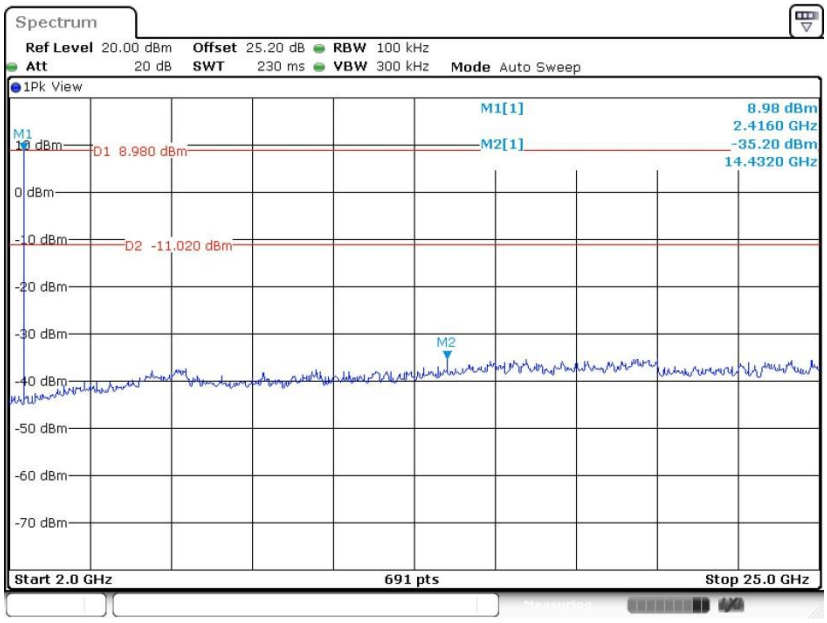
<1Mbps>

CSE Plot on Ch 00 between 30MHz ~ 3 GHz



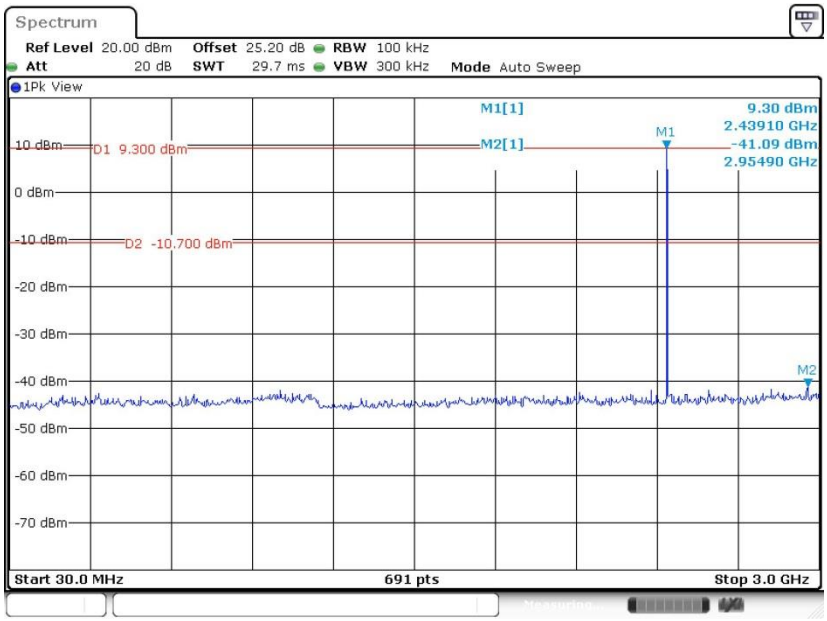
Date: 19.JUN.2019 12:24:27

CSE Plot on Ch 00 between 2 GHz ~ 25 GHz



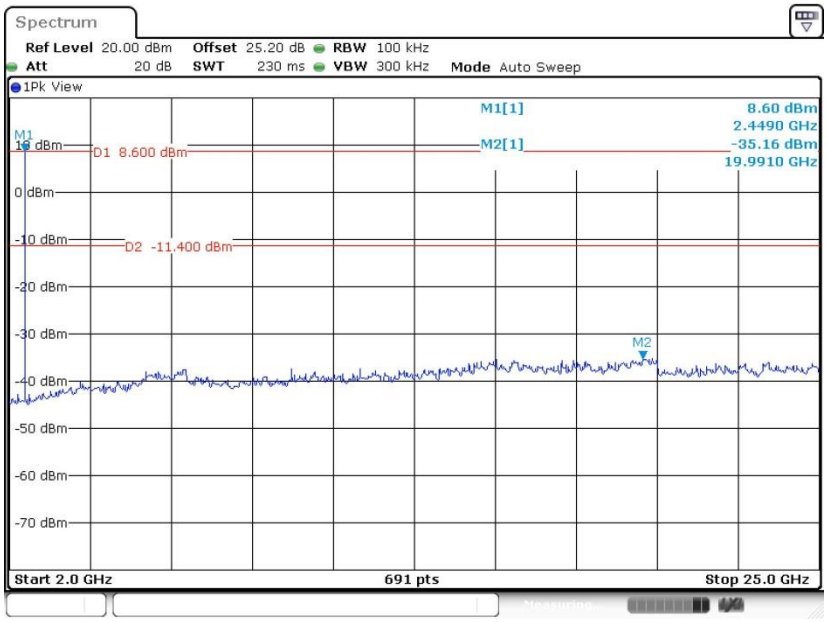
Date: 19.JUN.2019 12:24:55

CSE Plot on Ch 39 between 30MHz ~ 3 GHz



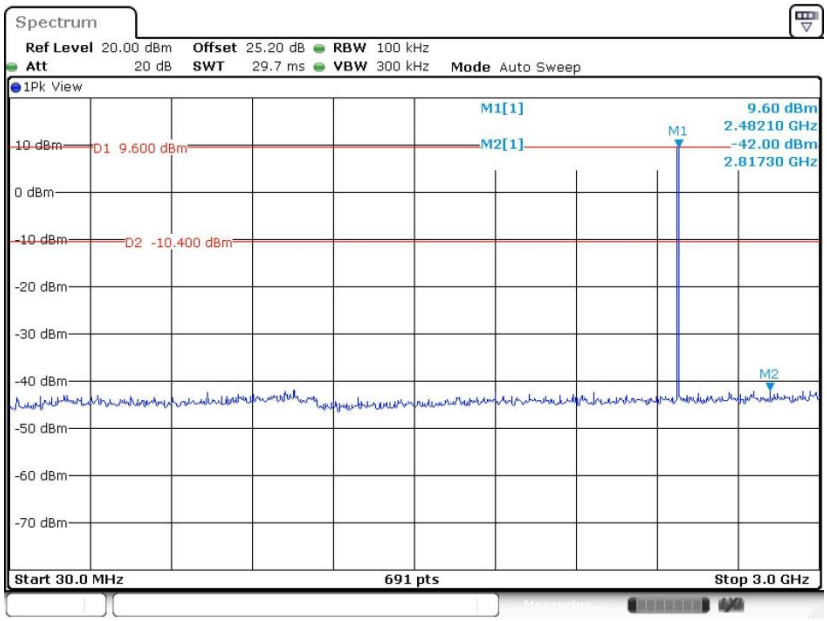
Date: 19.JUN.2019 12:25:34

CSE Plot on Ch 39 between 2 GHz ~ 25 GHz



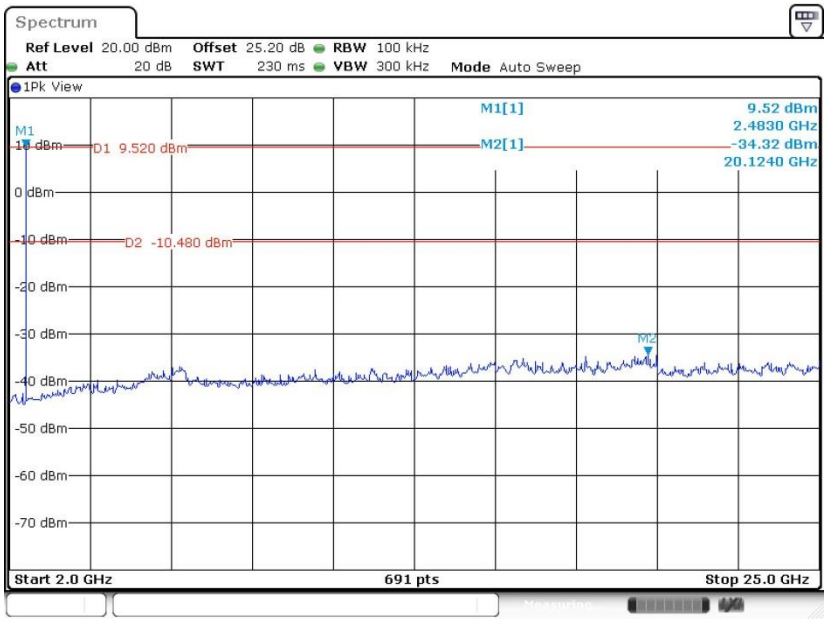
Date: 19.JUN.2019 12:26:02

CSE Plot on Ch 78 between 30MHz ~ 3 GHz



Date: 19.JUN.2019 12:26:36

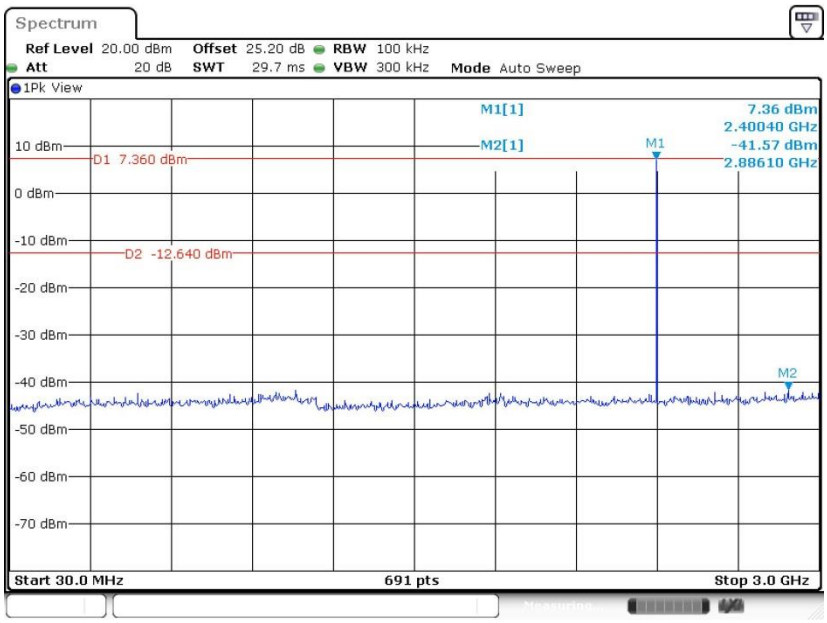
CSE Plot on Ch 78 between 2 GHz ~ 25 GHz



Date: 19.JUN.2019 12:27:03

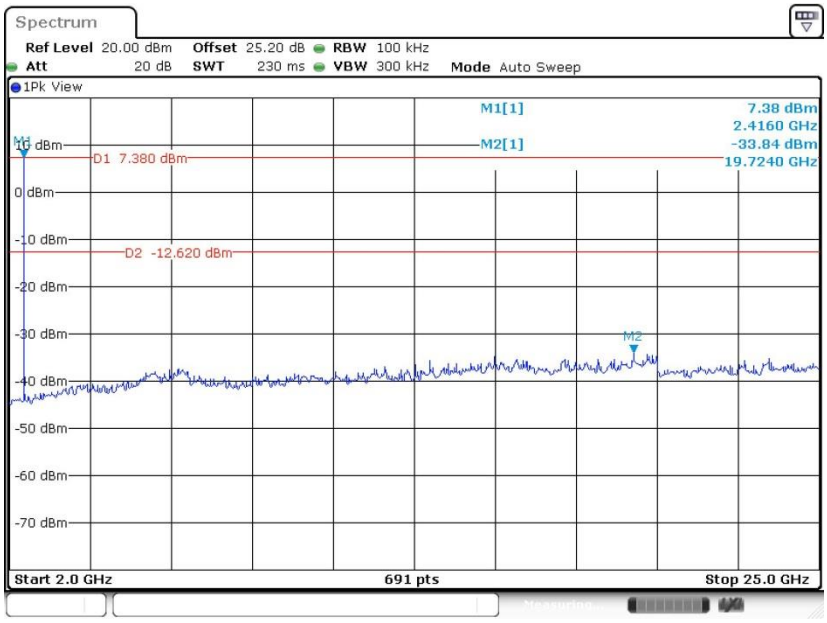
<2Mbps>

CSE Plot on Ch 00 between 30MHz ~ 3 GHz



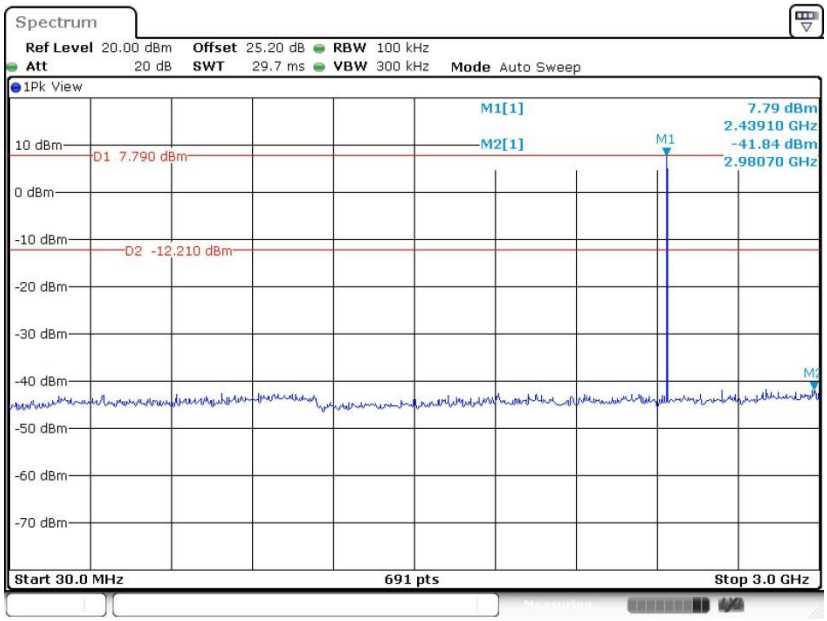
Date: 19.JUN.2019 12:27:47

CSE Plot on Ch 00 between 2 GHz ~ 25 GHz



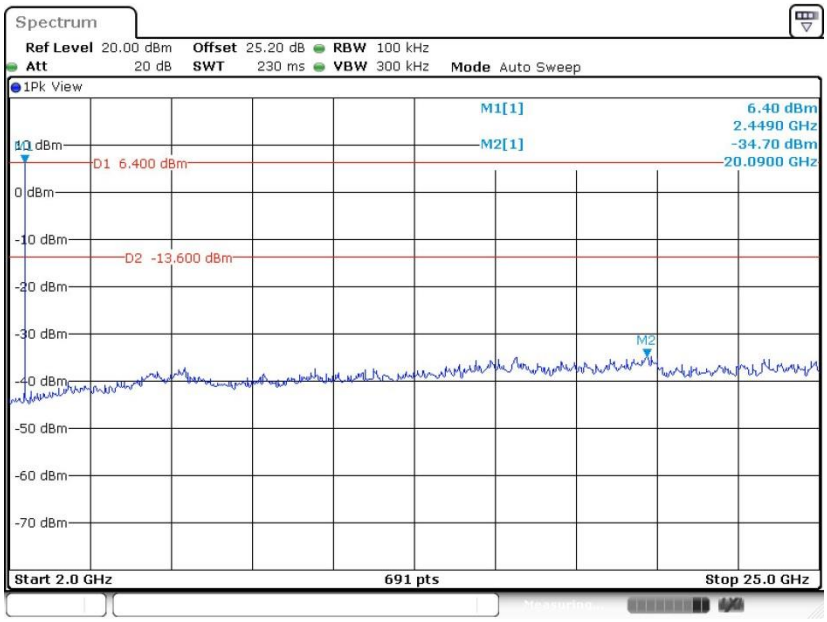
Date: 19.JUN.2019 12:28:14

CSE Plot on Ch 39 between 30MHz ~ 3 GHz



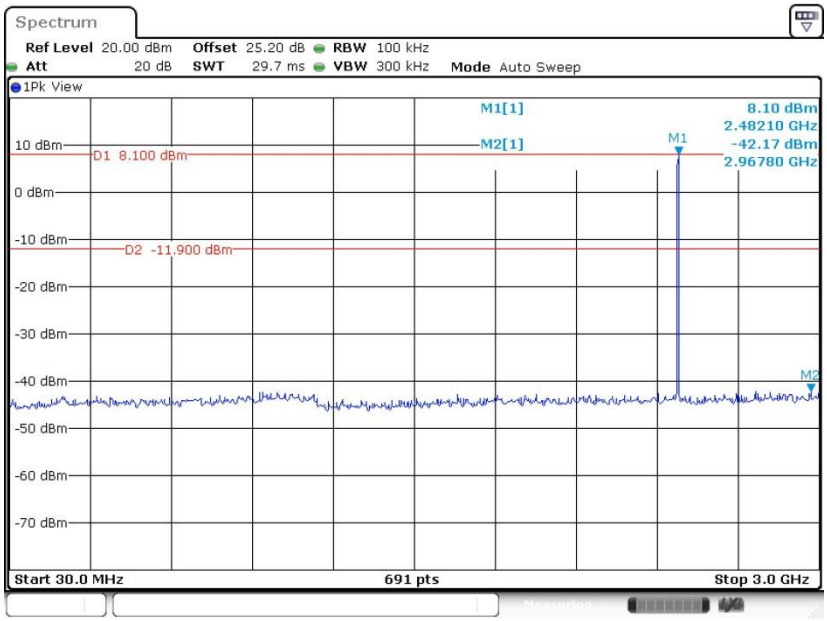
Date: 19.JUN.2019 12:29:07

CSE Plot on Ch 39 between 2 GHz ~ 25 GHz



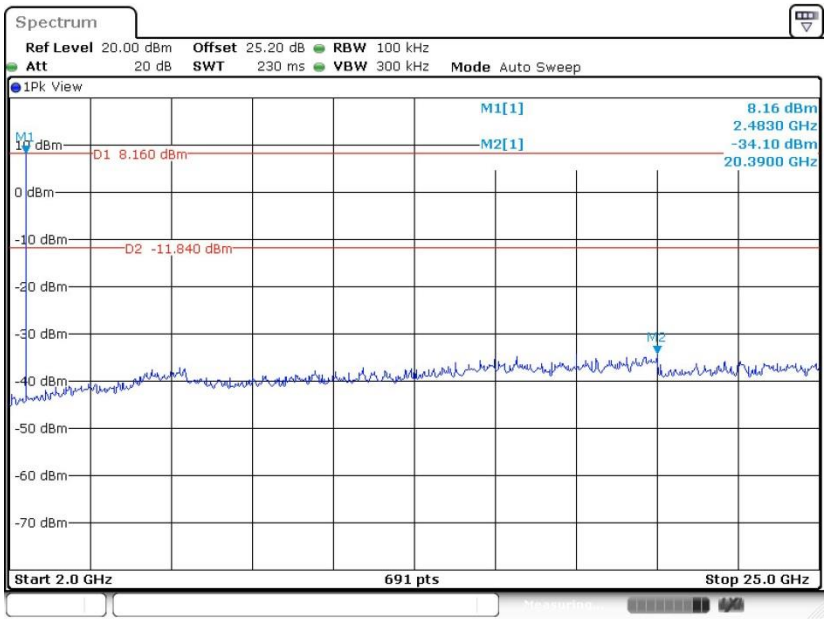
Date: 19.JUN.2019 12:29:34

CSE Plot on Ch 78 between 30MHz ~ 3 GHz



Date: 19.JUN.2019 12:30:25

CSE Plot on Ch 78 between 2 GHz ~ 25 GHz



Date: 19.JUN.2019 12:30:51