

11N40SISO_Ant1_5310



Date: 25 APR 2021 15:09:54

11N40SISO_Ant2_5310



Date: 27 APR 2021 11:41:55

11N40SISO_Ant1_5510



Date: 25 APR 2021 15:14:32

11N40SISO_Ant2_5510



Date: 27 APR 2021 11:47:44

11N40SISO_Ant1_5550



Date: 25 APR 2021 15:19:39

11N40SISO_Ant2_5550



Date: 27 APR 2021 12:12:04

11N40SISO_Ant1_5670



Date: 25 APR 2021 15:28:31

11N40SISO_Ant2_5670



Date: 27 APR 2021 12:17:32

11N40SISO_Ant1_5755



Date: 25 APR 2021 15:31:32

11N40SISO_Ant2_5755



Date: 27 APR 2021 12:25:18

11N40SISO_Ant1_5795



Date: 25 APR 2021 15:37:08

11N40SISO_Ant2_5795



Date: 27 APR 2021 12:45:02

11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



Date: 27 APR 2021 13:23:51

11AC20SISO_Ant1_5200



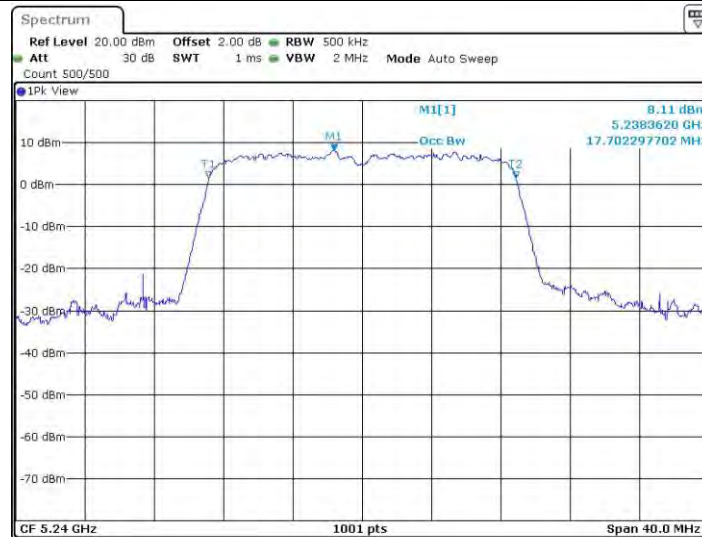
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11AC20SISO_Ant2_5200



Date: 27 APR 2021 13:46:53

11AC20SISO_Ant1_5240



Date: 25 APR 2021 15:56:55

11AC20SISO_Ant2_5240



Date: 27 APR 2021 13:54:29

11AC20SISO_Ant1_5260



Date: 25 APR 2021 16:01:45

11AC20SISO_Ant2_5260



Date: 27 APR 2021 13:59:15

11AC20SISO_Ant1_5280



Date: 25 APR 2021 16:06:30

11AC20SISO_Ant2_5280



Date: 27 APR 2021 14:05:58

11AC20SISO_Ant1_5320



Date: 25 APR 2021 16:13:19

11AC20SISO_Ant2_5320



Date: 27 APR 2021 14:11:12

11AC20SISO_Ant1_5500



Date: 25 APR 2021 16:23:16

11AC20SISO_Ant2_5500



Date: 27 APR 2021 14:24:58

11AC20SISO_Ant1_5580



Date: 25 APR 2021 16:29:50

11AC20SISO_Ant2_5580



Date: 27 APR 2021 14:32:11

11AC20SISO_Ant1_5700



Date: 25 APR 2021 16:34:22

11AC20SISO_Ant2_5700



Date: 27 APR 2021 14:52:26

11AC20SISO_Ant1_5745



Date: 25 APR 2021 16:48:24

11AC20SISO_Ant2_5745



Date: 27 APR 2021 15:14:07

11AC20SISO_Ant1_5785



Date: 25 APR 2021 16:53:57

11AC20SISO_Ant2_5785



Date: 27 APR 2021 15:51:10

11AC20SISO_Ant1_5825



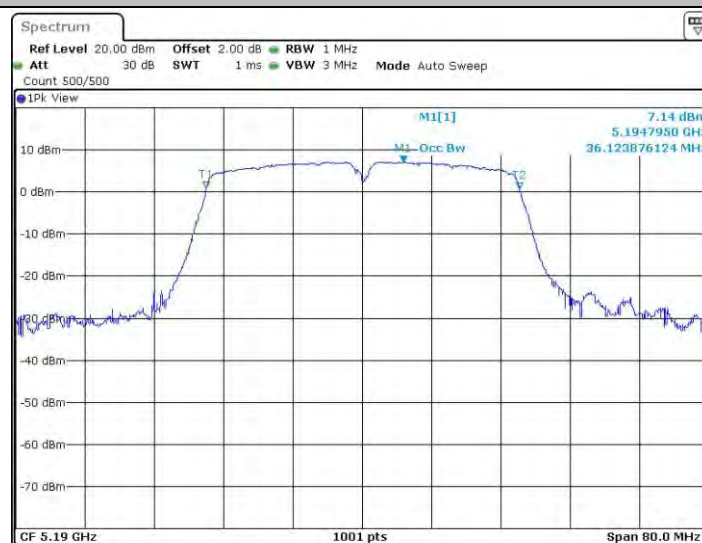
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11AC20SISO_Ant2_5825



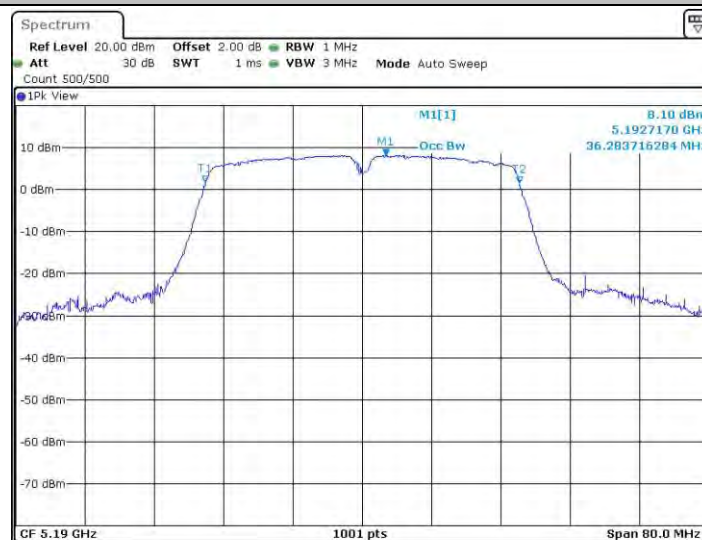
Date: 27 APR 2021 16:08:06

11AC40SISO_Ant1_5190



Date: 25 APR 2021 17:07:11

11AC40SISO_Ant2_5190



Date: 27 APR 2021 16:13:29

11AC40SISO_Ant1_5230



11AC40SISO_Ant2_5230



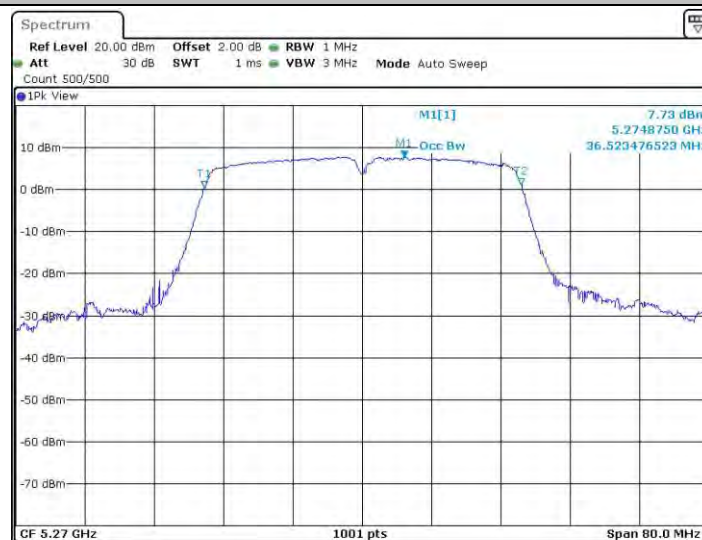
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11AC40SISO_Ant1_5270



Date: 26 APR 2021 15:35:01

11AC40SISO_Ant2_5270



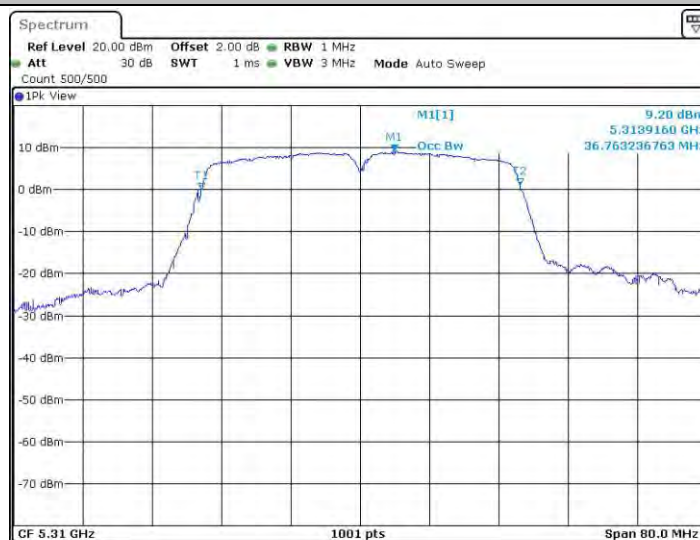
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11AC40SISO_Ant1_5310



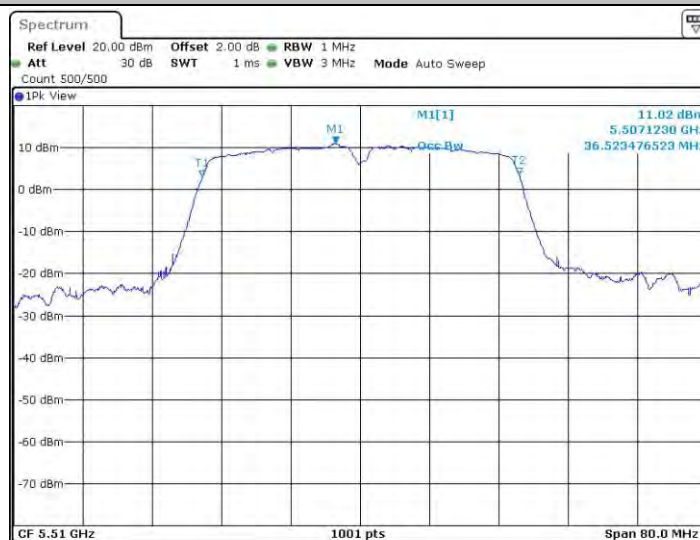
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11AC40SISO_Ant2_5310



Date: 27 APR 2021 16:34:40

11AC40SISO_Ant1_5510



Date: 26 APR 2021 15:47:54

11AC40SISO_Ant2_5510



Date: 27 APR 2021 16:51:30

11AC40SISO_Ant1_5550



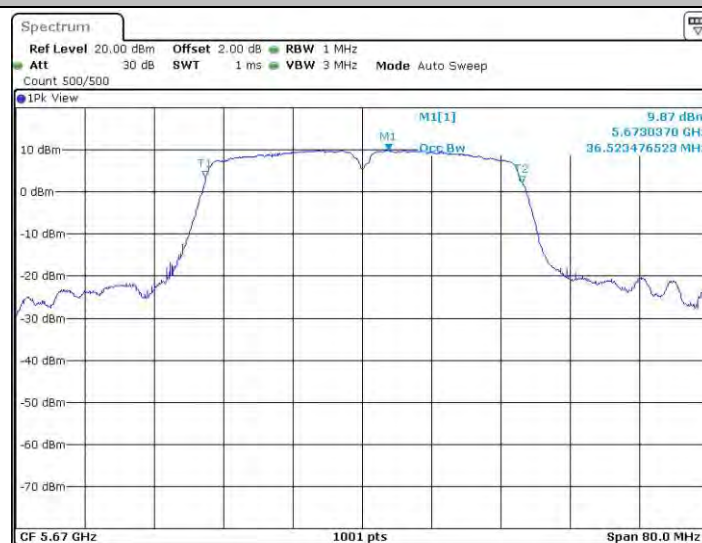
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11AC40SISO_Ant2_5550



Date: 27 APR 2021 16:56:10

11AC40SISO_Ant1_5670



Date: 26 APR 2021 16:02:54

11AC40SISO_Ant2_5670



Date: 27 APR 2021 17:12:24

11AC40SISO_Ant1_5755



Date: 26 APR 2021 16:14:18

11AC40SISO_Ant2_5755



Date: 27 APR 2021 17:17:11

11AC40SISO_Ant1_5795



Date: 26 APR 2021 16:24:11

11AC40SISO_Ant2_5795



Date: 27 APR 2021 17:23:28

11AC80SISO_Ant1_5210



Date: 26 APR 2021 16:50:04

11AC80SISO_Ant2_5210



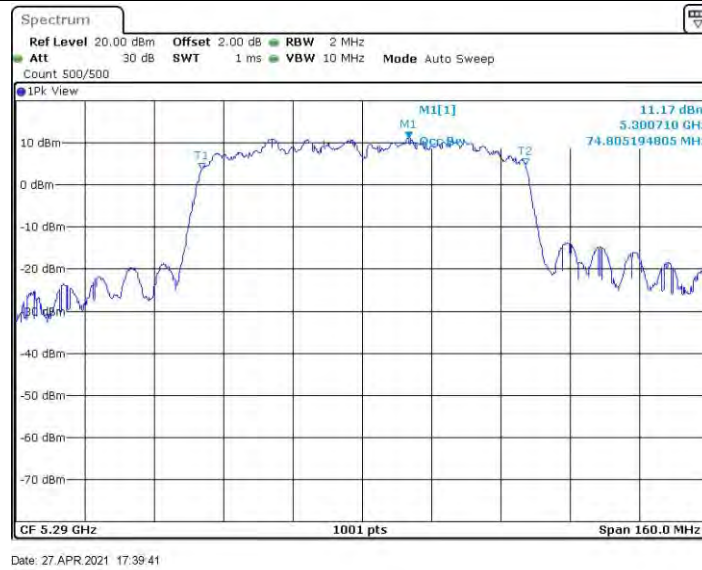
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11AC80SISO_Ant1_5290



Date: 26 APR 2021 16:54:44

11AC80SISO_Ant2_5290



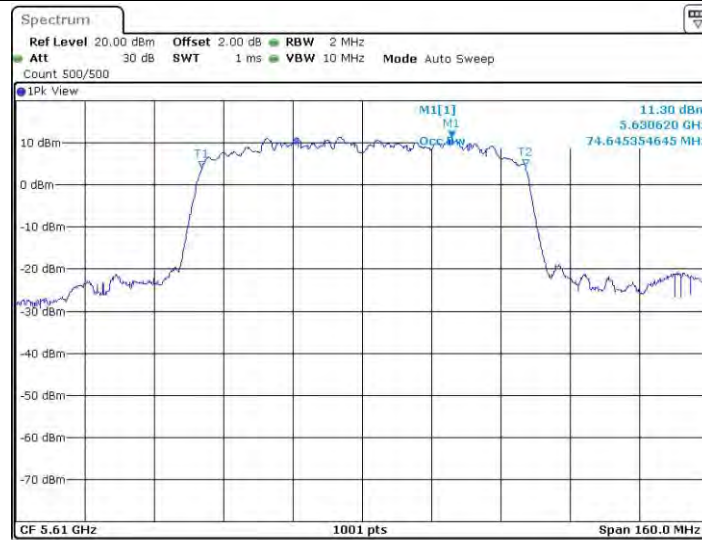
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11AC80SISO_Ant2_5530



11AC80SISO_Ant1_5610



Date: 26 APR 2021 17:20:35

11AC80SISO_Ant2_5610



Date: 27 APR 2021 17:50:47

11AC80SISO_Ant1_5775



Date: 26 APR 2021 17:25:59



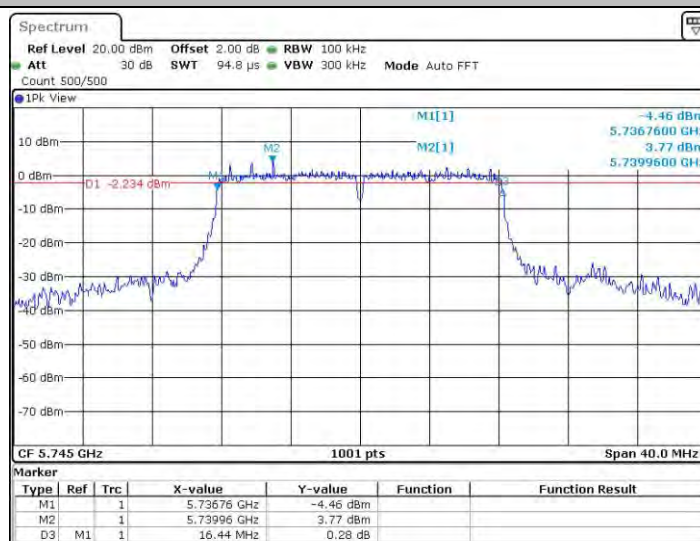
Appendix A3: Min emission bandwidth

Test Result

TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.440	5736.760	5753.200	0.5	PASS
	Ant2	5745	16.360	5736.800	5753.160	0.5	PASS
	Ant1	5785	16.400	5776.800	5793.200	0.5	PASS
	Ant2	5785	16.360	5776.800	5793.160	0.5	PASS
	Ant1	5825	16.360	5816.800	5833.160	0.5	PASS
	Ant2	5825	16.440	5816.760	5833.200	0.5	PASS
11N20SISO	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant2	5745	17.680	5736.160	5753.840	0.5	PASS
	Ant1	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant2	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
	Ant2	5825	17.640	5816.160	5833.800	0.5	PASS
11N40SISO	Ant1	5755	35.360	5737.320	5772.680	0.5	PASS
	Ant2	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant2	5795	35.280	5777.400	5812.680	0.5	PASS
11AC20SISO	Ant1	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant2	5745	17.640	5736.160	5753.800	0.5	PASS
	Ant1	5785	17.640	5776.160	5793.800	0.5	PASS
	Ant2	5785	17.600	5776.160	5793.760	0.5	PASS
	Ant1	5825	17.640	5816.160	5833.800	0.5	PASS
	Ant2	5825	17.640	5816.160	5833.800	0.5	PASS
11AC40SISO	Ant1	5755	35.360	5737.320	5772.680	0.5	PASS
	Ant2	5755	35.280	5737.320	5772.600	0.5	PASS
	Ant1	5795	35.280	5777.400	5812.680	0.5	PASS
	Ant2	5795	35.280	5777.400	5812.680	0.5	PASS
11AC80SISO	Ant1	5775	66.720	5737.240	5803.960	0.5	PASS
	Ant2	5775	75.360	5737.240	5812.600	0.5	PASS

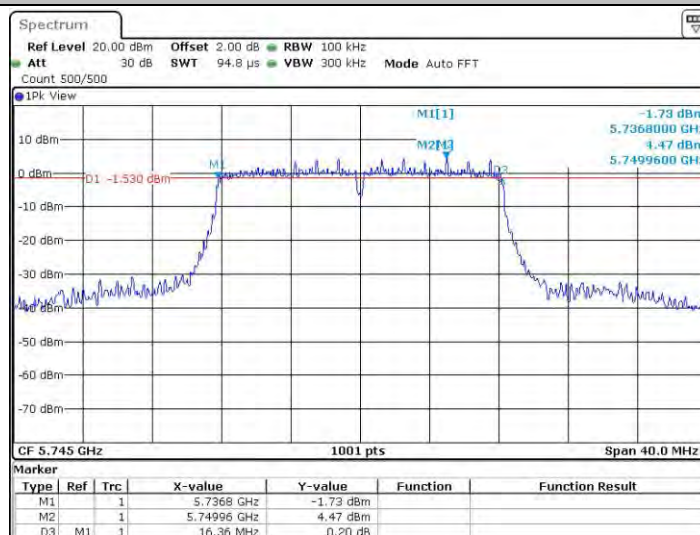
Test Graphs

11A_Ant1_5745



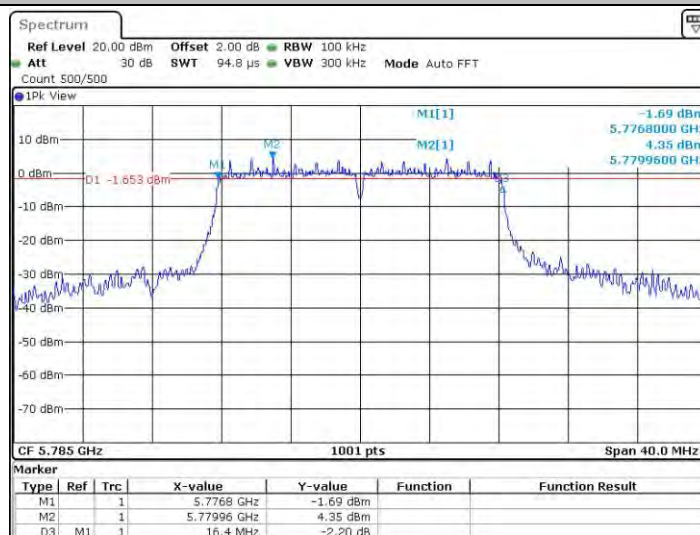
Date: 25 APR 2021 10:53:23

11A_Ant2_5745



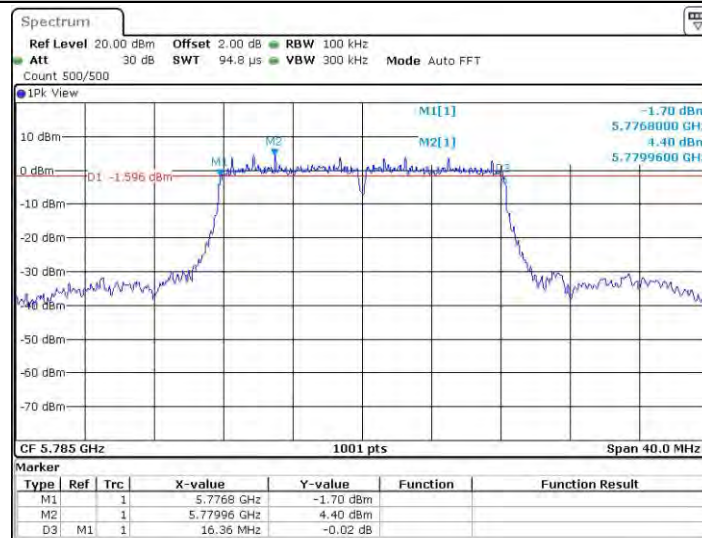
Date: 26 APR 2021 18:37:05

11A_Ant1_5785



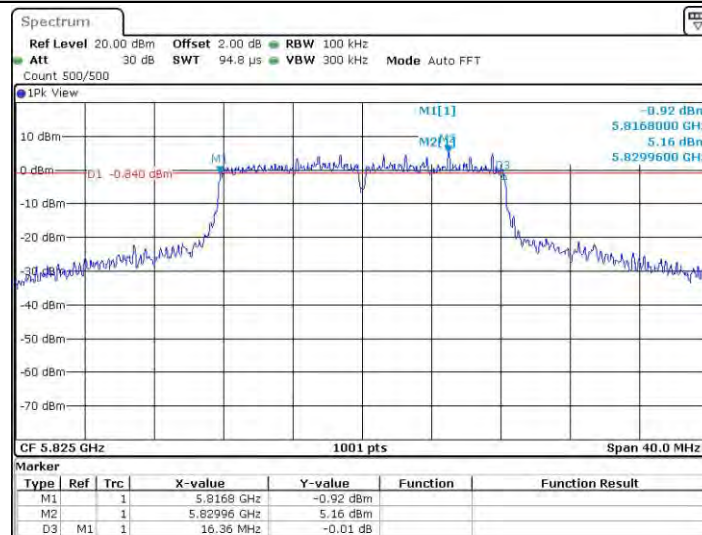
Date: 25 APR 2021 10:59:01

11A_Ant2_5785



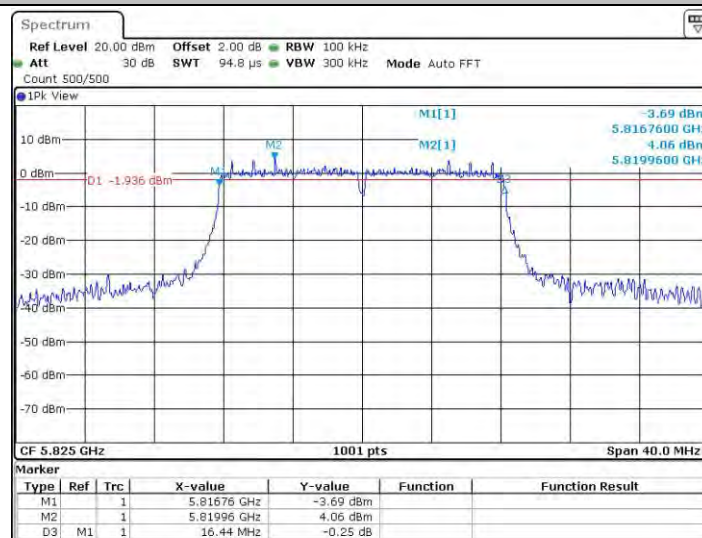
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11A_Ant1_5825



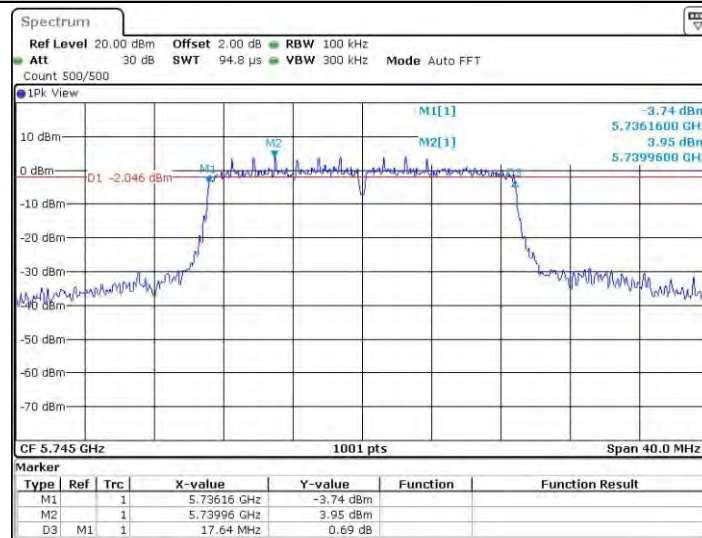
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11A_Ant2_5825



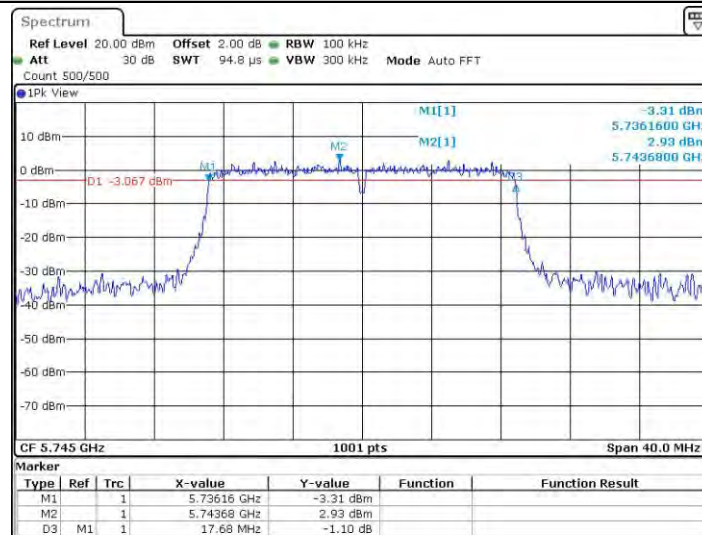
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11N20SISO_Ant1_5745



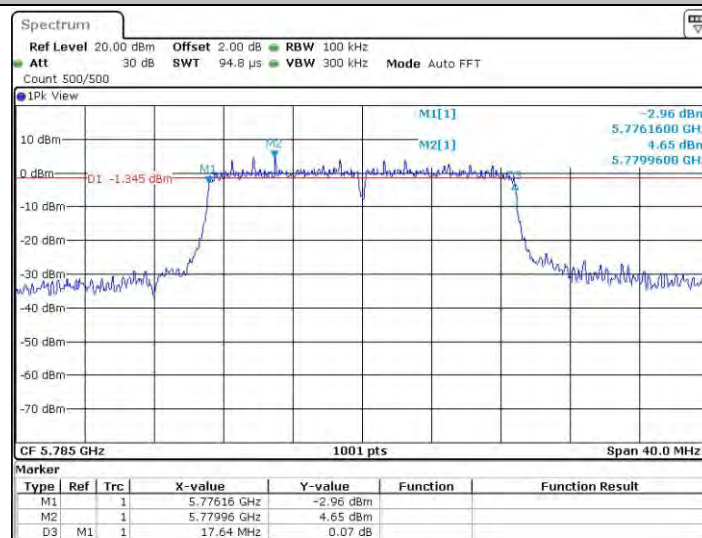
Date: 25 APR 2021 12:29:43

11N20SISO_Ant2_5745



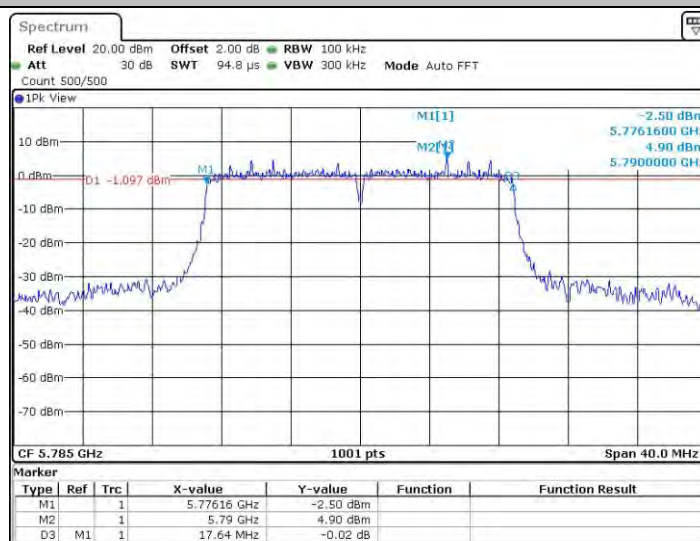
Date: 27 APR 2021 09:50:30

11N20SISO_Ant1_5785



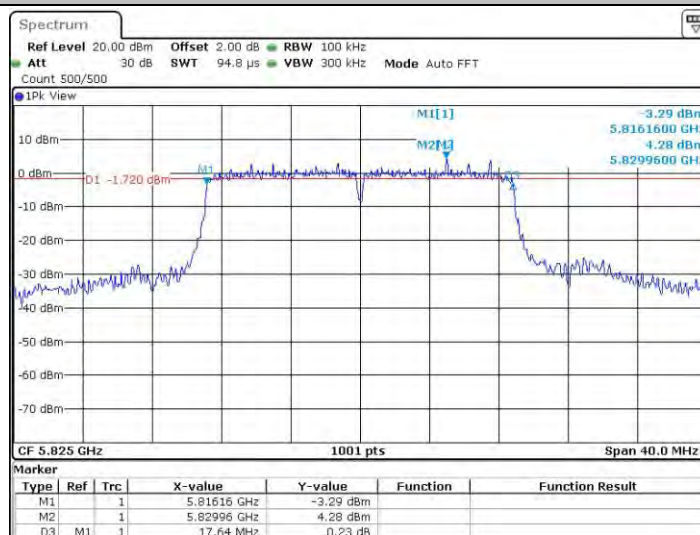
Date: 25 APR 2021 12:37:00

11N20SISO_Ant2_5785



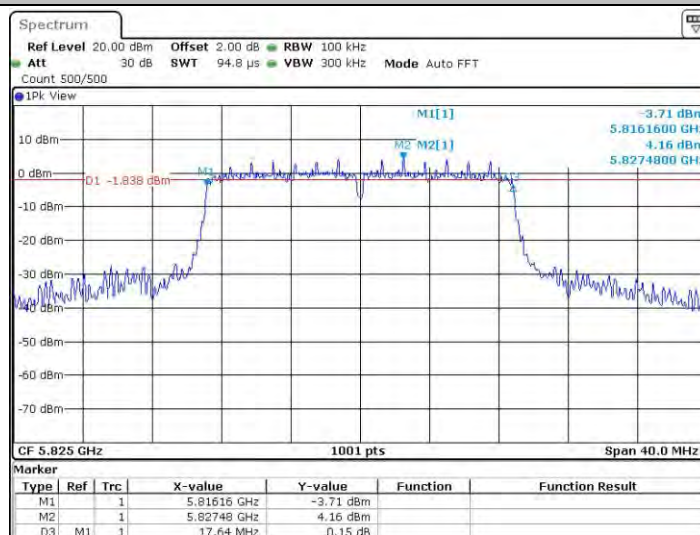
Date: 27 APR 2021 10:03:01

11N20SISO_Ant1_5825



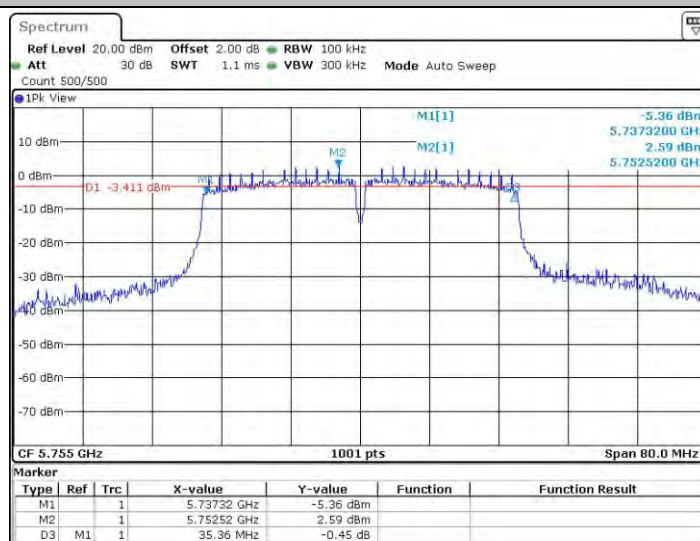
Date: 25 APR 2021 12:41:58

11N20SISO_Ant2_5825



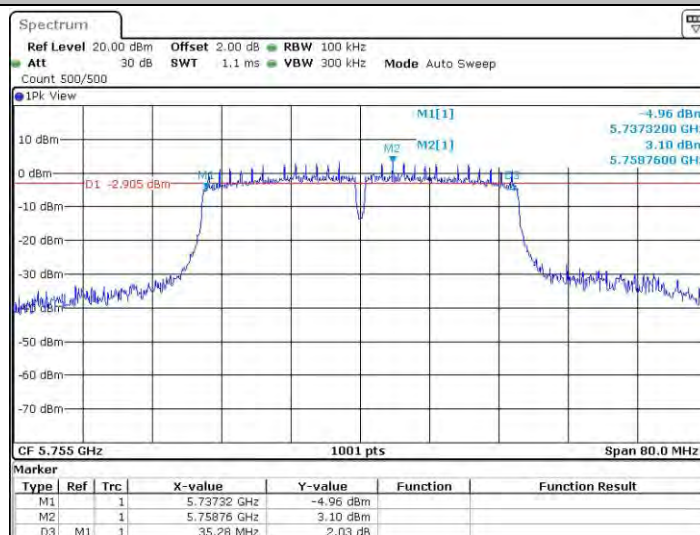
Date: 27 APR 2021 10:10:02

11N40SISO_Ant1_5755



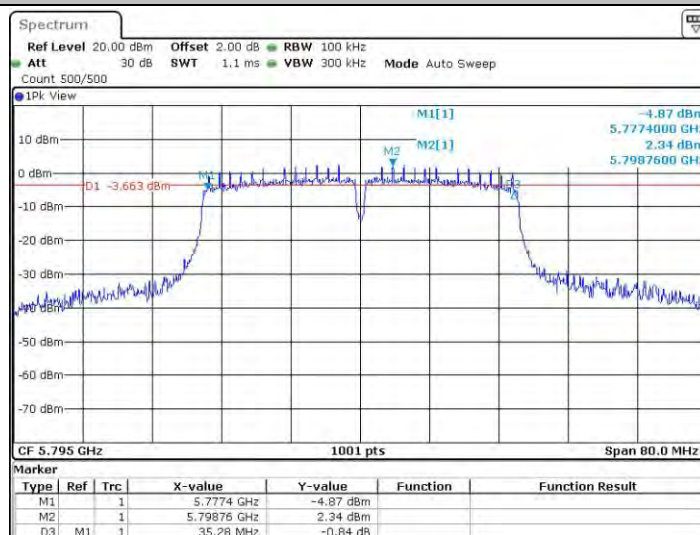
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11N40SISO_Ant2_5755



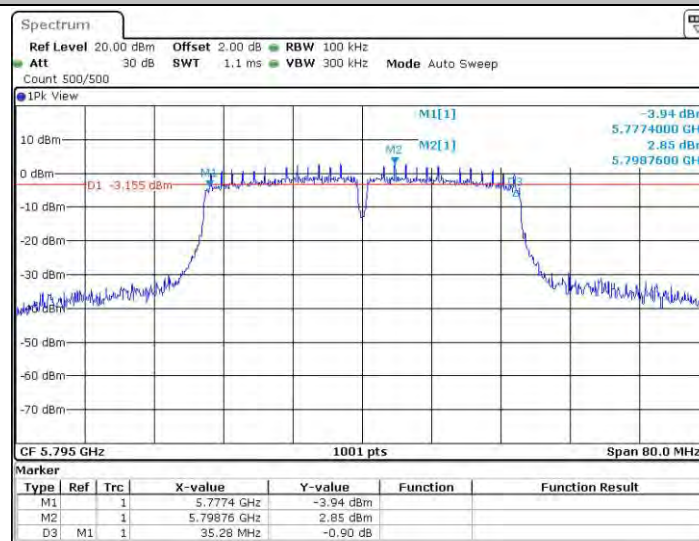
Date: 27 APR 2021 12:25:04

11N40SISO_Ant1_5795



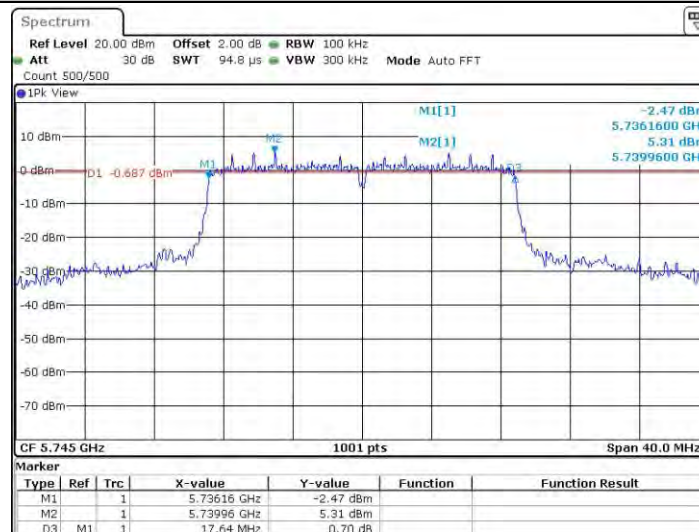
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11N40SISO_Ant2_5795



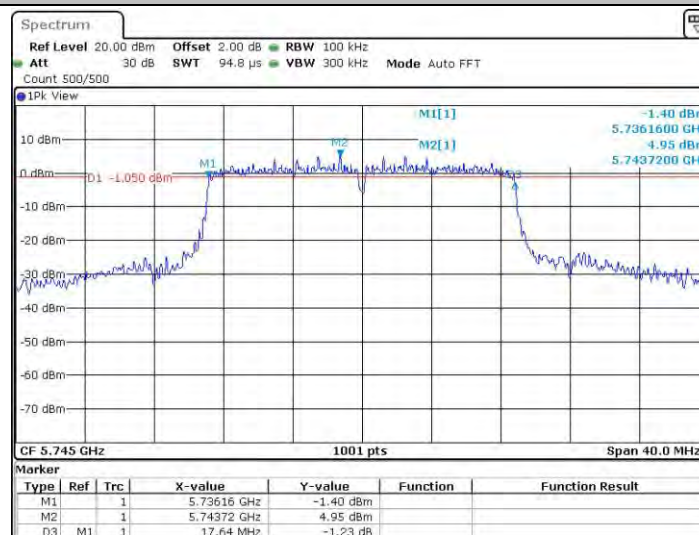
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11AC20SISO_Ant1_5745



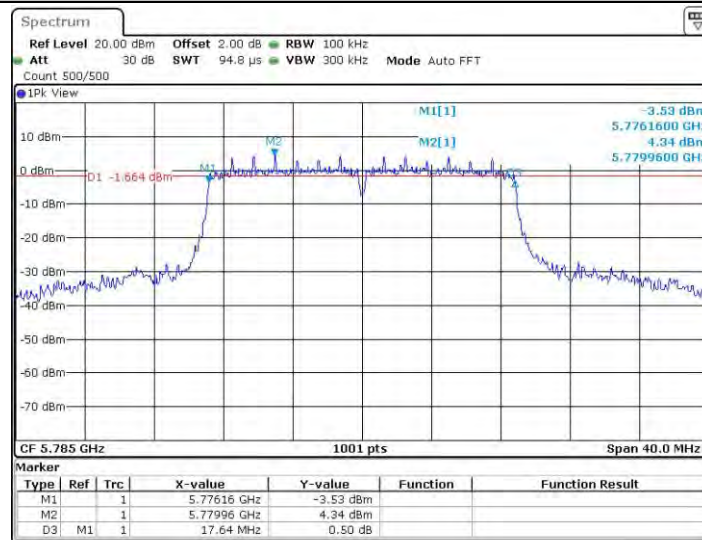
Date: 25 APR 2021 16:48:09

11AC20SISO_Ant2_5745



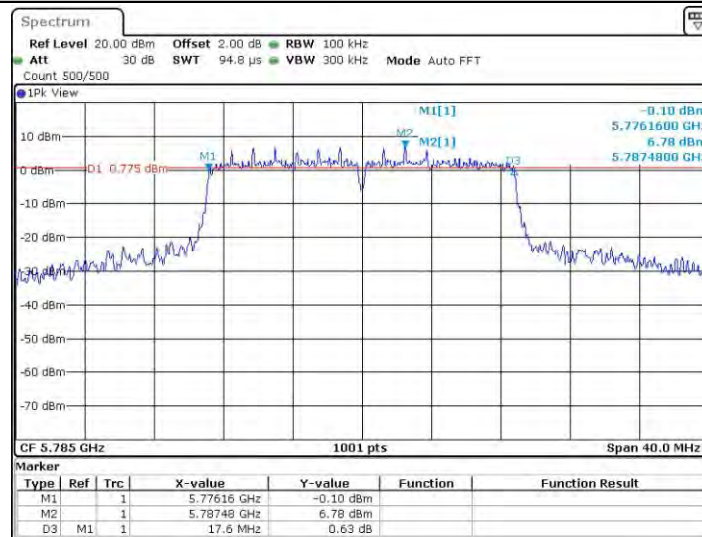
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11AC20SISO_Ant1_5785



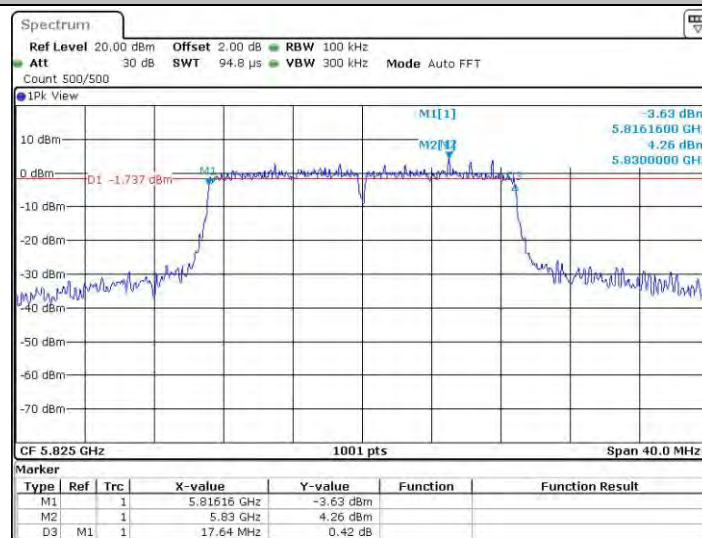
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11AC20SISO_Ant2_5785



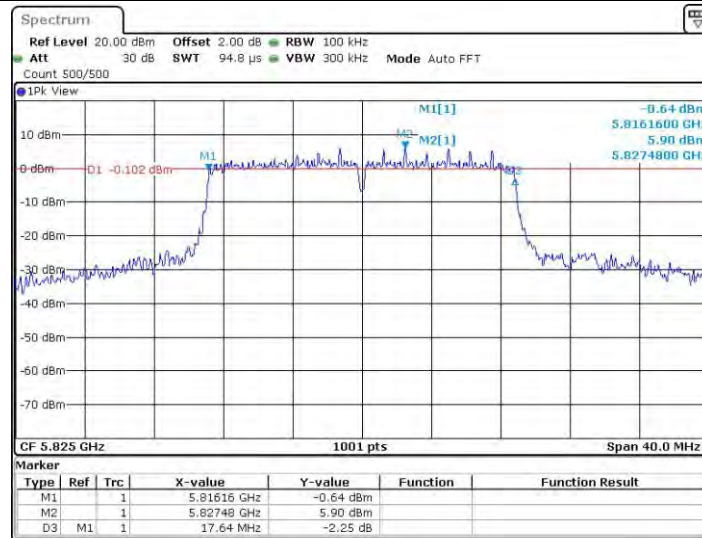
Date: 27 APR 2021 15:50:55

11AC20SISO_Ant1_5825



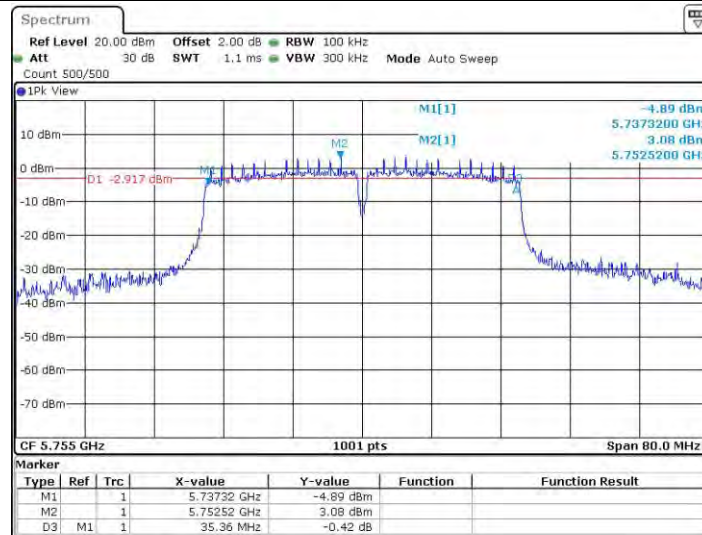
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11AC20SISO_Ant2_5825



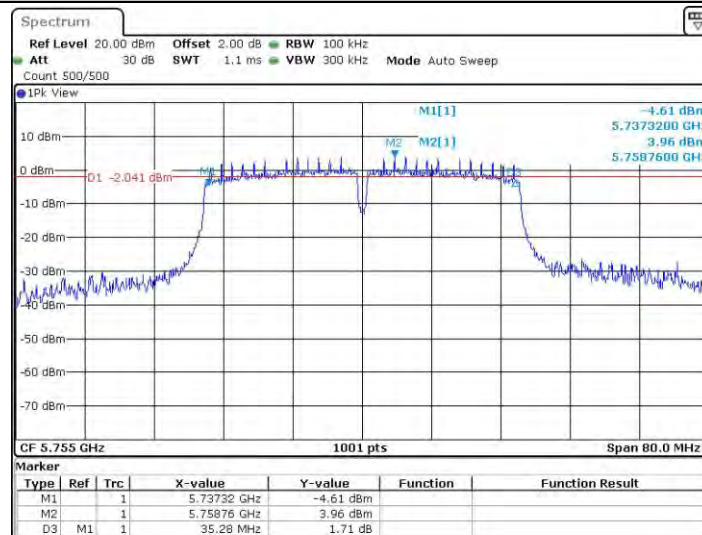
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11AC40SISO_Ant1_5755



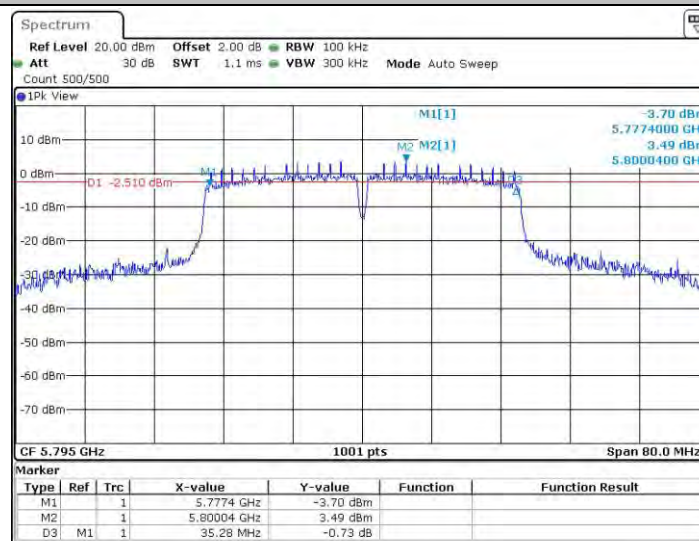
Date: 26 APR 2021 16:14:04

11AC40SISO_Ant2_5755



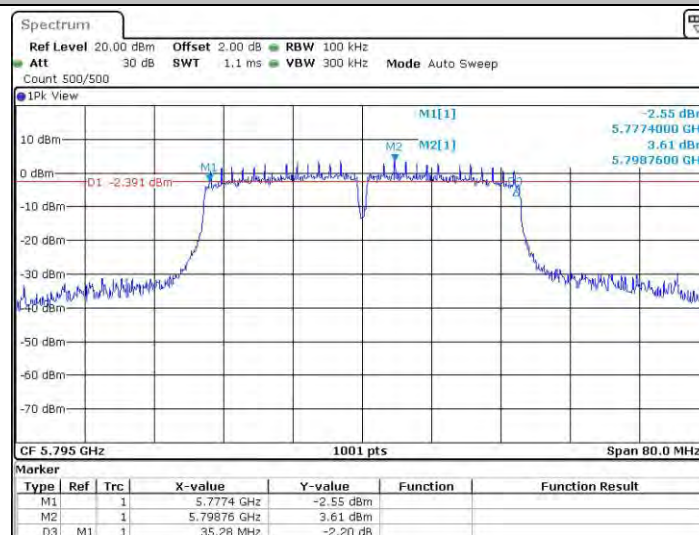
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11AC40SISO_Ant1_5795



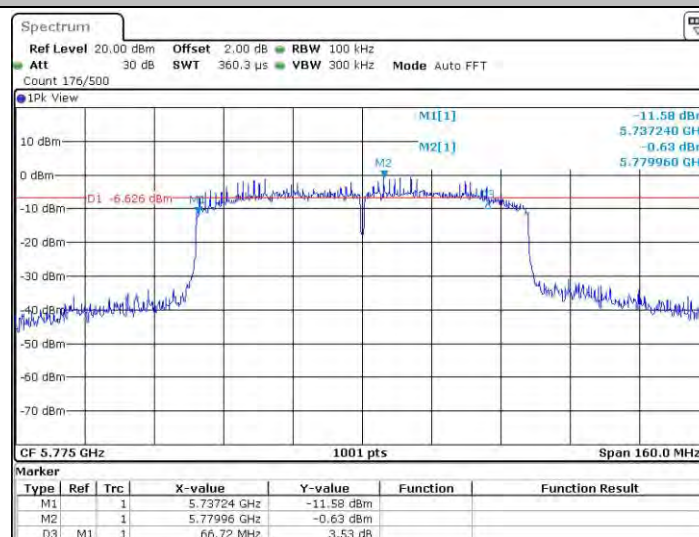
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11AC40SISO_Ant2_5795

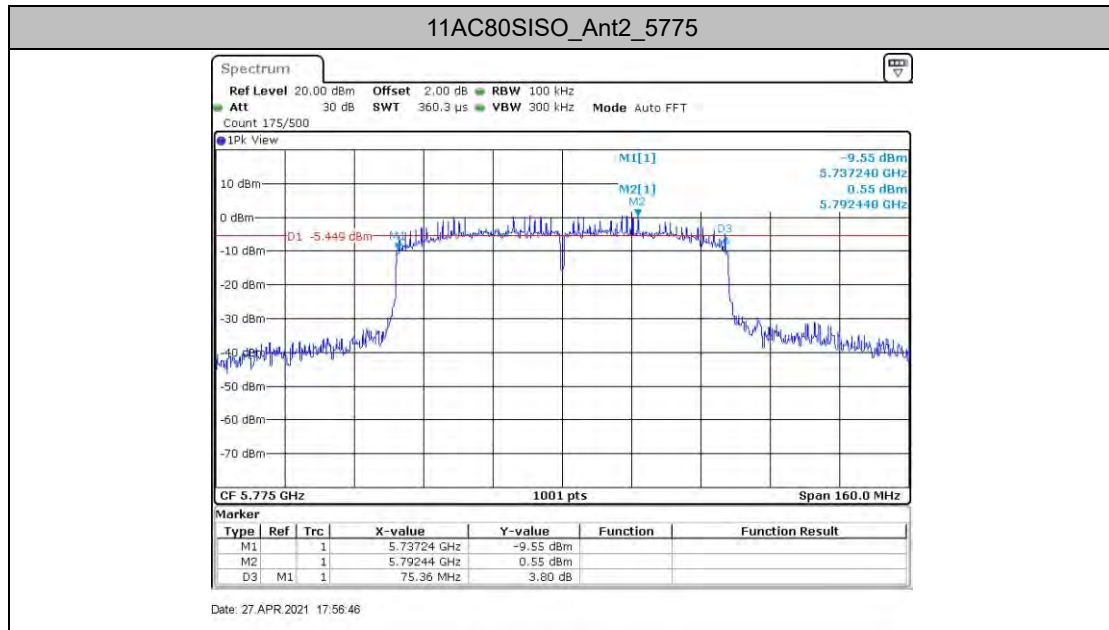


Date: 27 APR 2021 17:23:14

11AC80SISO_Ant1_5775



Date: 26 APR 2021 17:25:45



Appendix B1: Maximum conducted output power

Test Result

TestMode	Antenna	Frequency (MHz)	Result	Limit	Verdict
11A	Ant1	5180	13.35	<=23.98	PASS
	Ant2	5180	14.52	<=23.98	PASS
	Ant1	5200	13.47	<=23.98	PASS
	Ant2	5200	14.60	<=23.98	PASS
	Ant1	5240	14.19	<=23.98	PASS
	Ant2	5240	15.09	<=23.98	PASS
	Ant1	5260	14.03	<=23.69	PASS
	Ant2	5260	14.92	<=23.71	PASS
	Ant1	5280	14.41	<=23.69	PASS
	Ant2	5280	15.91	<=23.66	PASS
	Ant1	5320	14.39	<=23.67	PASS
	Ant2	5320	15.58	<=23.67	PASS
	Ant1	5500	15.36	<=23.63	PASS
	Ant2	5500	16.02	<=23.71	PASS
	Ant1	5580	15.69	<=23.64	PASS
	Ant2	5580	16.16	<=23.66	PASS
	Ant1	5700	15.22	<=23.64	PASS
	Ant2	5700	15.23	<=23.71	PASS
	Ant1	5745	15.59	<=30	PASS
	Ant2	5745	16.01	<=30	PASS
	Ant1	5785	15.64	<=30	PASS
	Ant2	5785	15.84	<=30	PASS
	Ant1	5825	16.42	<=30	PASS
	Ant2	5825	15.80	<=30	PASS
11N20SISO	Ant1	5180	13.43	<=23.98	PASS
	Ant2	5180	14.99	<=23.98	PASS
	Ant1	5200	13.68	<=23.98	PASS
	Ant2	5200	14.73	<=23.98	PASS
	Ant1	5240	14.19	<=23.98	PASS
	Ant2	5240	15.21	<=23.98	PASS
	Ant1	5260	17.44	<=23.98	PASS
	Ant2	5260	14.76	<=23.90	PASS
	Ant1	5280	15.56	<=23.89	PASS
	Ant2	5280	15.55	<=23.90	PASS
	Ant1	5320	14.63	<=23.90	PASS
	Ant2	5320	15.37	<=23.91	PASS
	Ant1	5500	15.58	<=23.91	PASS
	Ant2	5500	16.43	<=23.89	PASS
	Ant1	5580	15.66	<=23.87	PASS

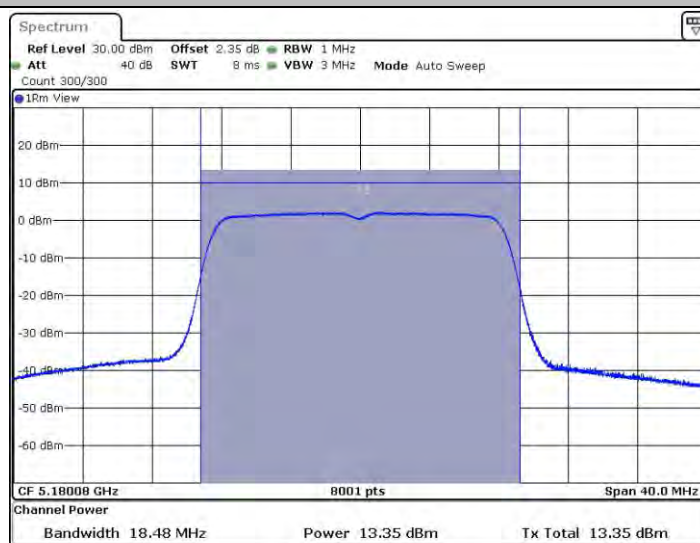
TestMode	Antenna	Frequency (MHz)	Result	Limit	Verdict
	Ant2	5580	15.86	<=23.90	PASS
	Ant1	5700	15.32	<=23.89	PASS
	Ant2	5700	17.07	<=23.89	PASS
	Ant1	5745	15.45	<=30	PASS
	Ant2	5745	16.09	<=30	PASS
	Ant1	5785	15.92	<=30	PASS
	Ant2	5785	16.06	<=30	PASS
	Ant1	5825	15.42	<=30	PASS
	Ant2	5825	15.40	<=30	PASS
11N40SISO	Ant1	5190	14.14	<=23.98	PASS
	Ant2	5190	15.27	<=23.98	PASS
	Ant1	5230	14.00	<=23.98	PASS
	Ant2	5230	14.74	<=23.98	PASS
	Ant1	5270	15.05	<=23.98	PASS
	Ant2	5270	14.51	<=23.98	PASS
	Ant1	5310	14.94	<=23.98	PASS
	Ant2	5310	15.00	<=23.98	PASS
	Ant1	5510	16.16	<=23.98	PASS
	Ant2	5510	17.31	<=23.98	PASS
	Ant1	5550	16.35	<=23.98	PASS
	Ant2	5550	15.89	<=23.98	PASS
	Ant1	5670	15.64	<=23.98	PASS
	Ant2	5670	16.01	<=23.98	PASS
	Ant1	5755	16.23	<=30	PASS
	Ant2	5755	16.38	<=30	PASS
	Ant1	5795	15.89	<=30	PASS
	Ant2	5795	16.10	<=30	PASS
11AC20SISO	Ant1	5180	12.52	<=23.98	PASS
	Ant2	5180	14.60	<=23.98	PASS
	Ant1	5200	14.57	<=23.98	PASS
	Ant2	5200	14.49	<=23.98	PASS
	Ant1	5240	14.33	<=23.98	PASS
	Ant2	5240	14.58	<=23.98	PASS
	Ant1	5260	14.05	<=23.87	PASS
	Ant2	5260	14.02	<=23.86	PASS
	Ant1	5280	14.23	<=23.82	PASS
	Ant2	5280	14.35	<=23.89	PASS
	Ant1	5320	15.53	<=23.90	PASS
	Ant2	5320	15.14	<=23.88	PASS
	Ant1	5500	15.45	<=23.88	PASS
	Ant2	5500	17.47	<=23.88	PASS
	Ant1	5580	15.55	<=23.90	PASS

TestMode	Antenna	Frequency (MHz)	Result	Limit	Verdict
	Ant2	5580	17.35	<=23.89	PASS
	Ant1	5700	15.01	<=23.87	PASS
	Ant2	5700	17.80	<=23.93	PASS
	Ant1	5745	16.25	<=30	PASS
	Ant2	5745	17.11	<=30	PASS
	Ant1	5785	15.55	<=30	PASS
	Ant2	5785	17.75	<=30	PASS
	Ant1	5825	15.62	<=30	PASS
	Ant2	5825	17.09	<=30	PASS
11AC40SISO	Ant1	5190	13.60	<=23.98	PASS
	Ant2	5190	13.17	<=23.98	PASS
	Ant1	5230	12.72	<=23.98	PASS
	Ant2	5230	14.05	<=23.98	PASS
	Ant1	5270	14.99	<=23.98	PASS
	Ant2	5270	13.71	<=23.98	PASS
	Ant1	5310	14.98	<=23.98	PASS
	Ant2	5310	14.90	<=23.98	PASS
	Ant1	5510	16.55	<=23.98	PASS
	Ant2	5510	16.76	<=23.98	PASS
	Ant1	5550	16.41	<=23.98	PASS
	Ant2	5550	16.97	<=23.98	PASS
	Ant1	5670	15.68	<=23.98	PASS
	Ant2	5670	17.01	<=23.98	PASS
	Ant1	5755	16.40	<=30	PASS
	Ant2	5755	17.62	<=30	PASS
	Ant1	5795	17.10	<=30	PASS
	Ant2	5795	17.32	<=30	PASS
11AC80SISO	Ant1	5210	14.82	<=23.98	PASS
	Ant2	5210	14.19	<=23.98	PASS
	Ant1	5290	15.06	<=23.98	PASS
	Ant2	5290	15.22	<=23.98	PASS
	Ant1	5530	16.48	<=23.98	PASS
	Ant2	5530	16.55	<=23.98	PASS
	Ant1	5610	14.84	<=23.98	PASS
	Ant2	5610	17.26	<=23.98	PASS
	Ant1	5775	15.71	<=30	PASS
	Ant2	5775	17.09	<=30	PASS

Note: The Duty Cycle Factor is compensated in the graph.

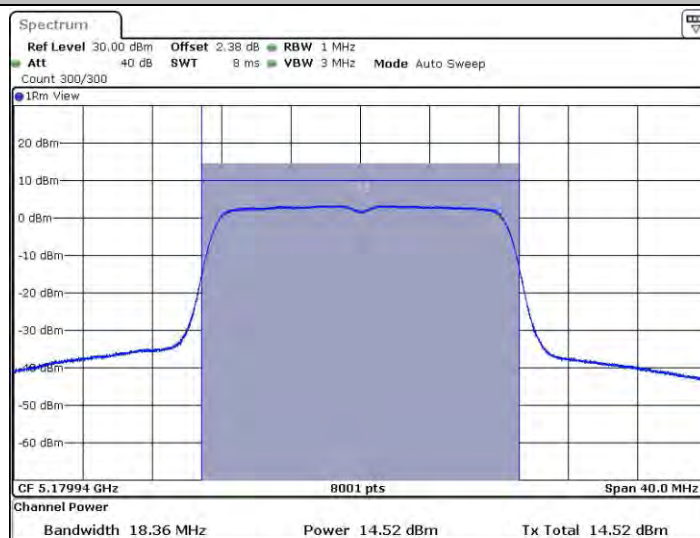
Test Graphs

11A_Ant1_5180



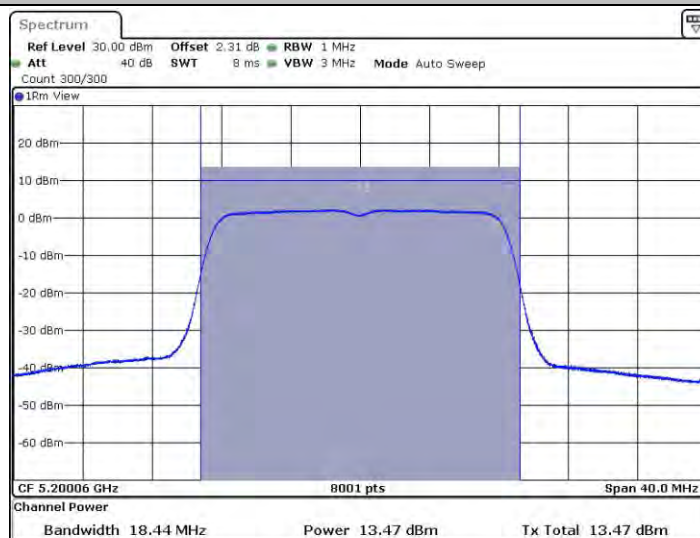
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11A_Ant2_5180



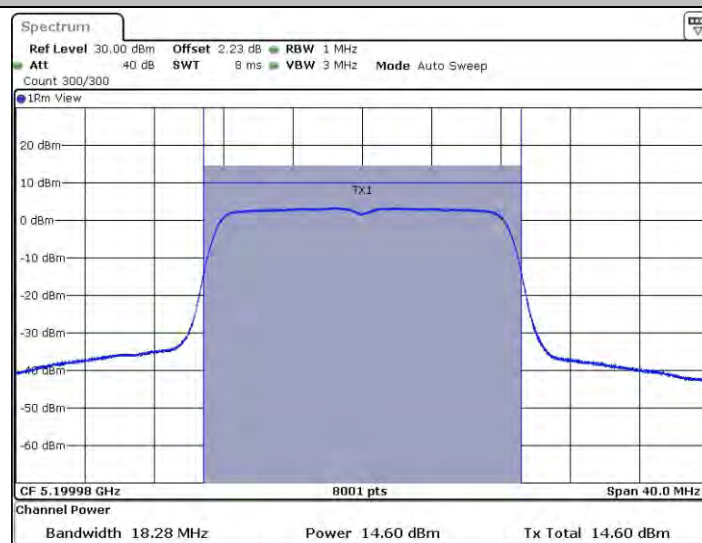
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11A_Ant1_5200



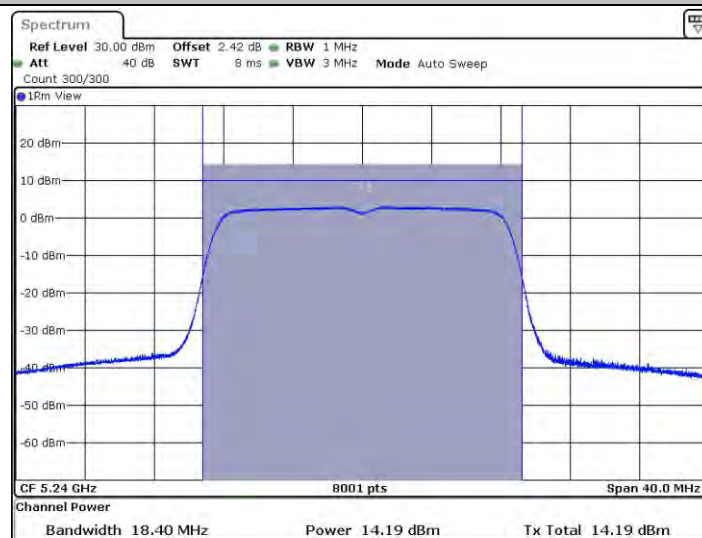
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11A_Ant2_5200



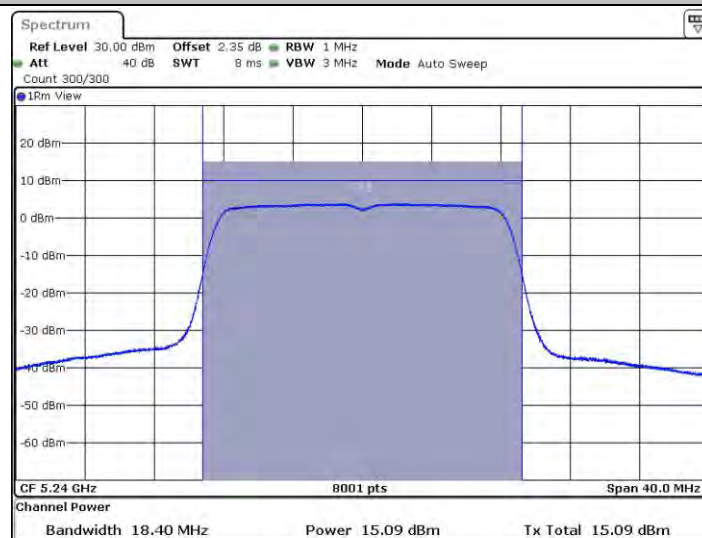
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11A_Ant1_5240



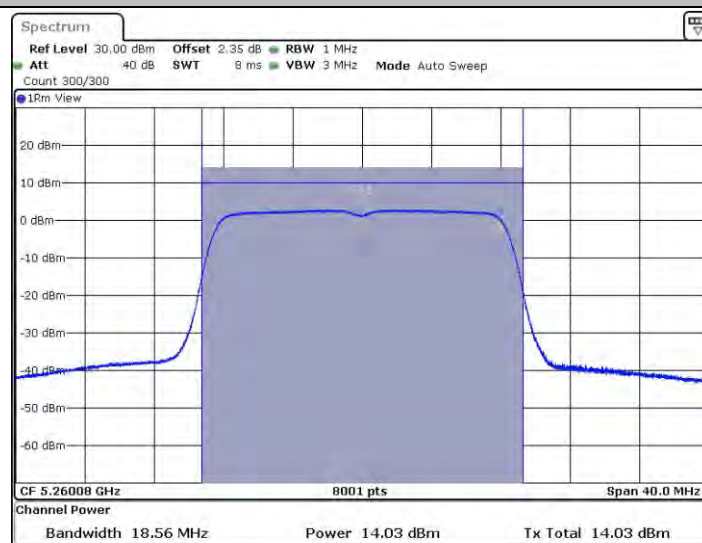
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11A_Ant2_5240



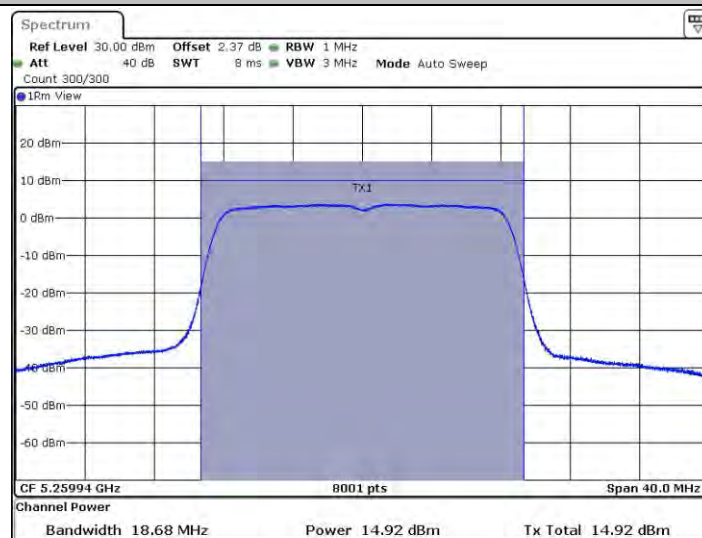
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11A_Ant1_5260



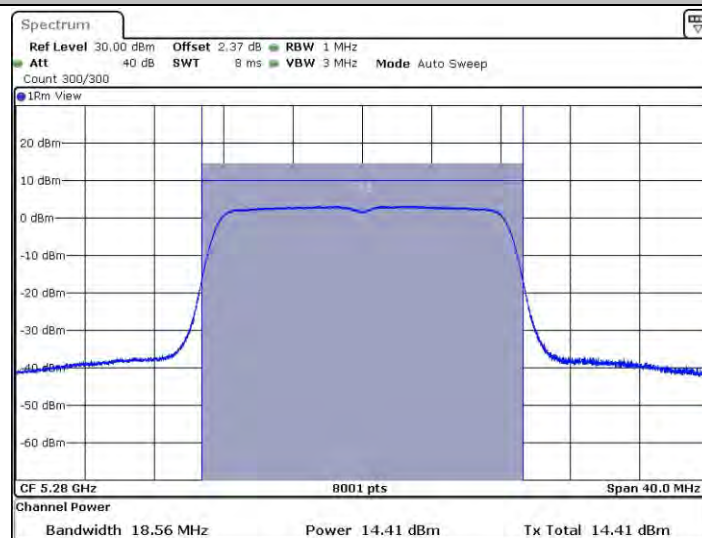
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11A_Ant2_5260



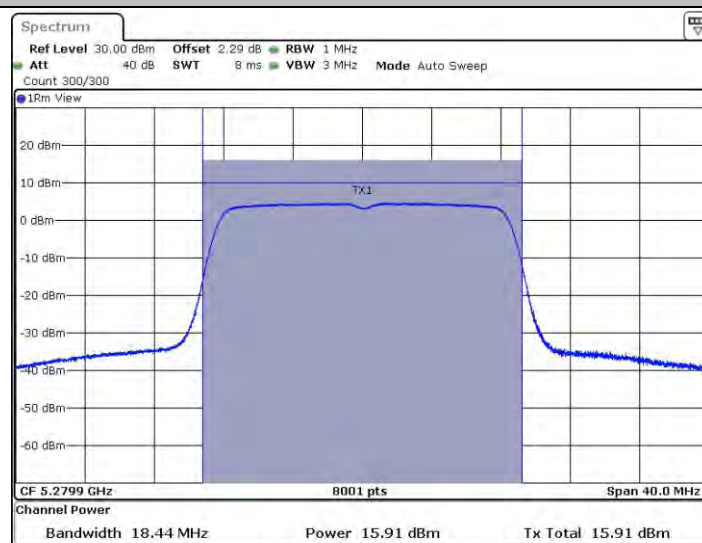
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11A_Ant1_5280



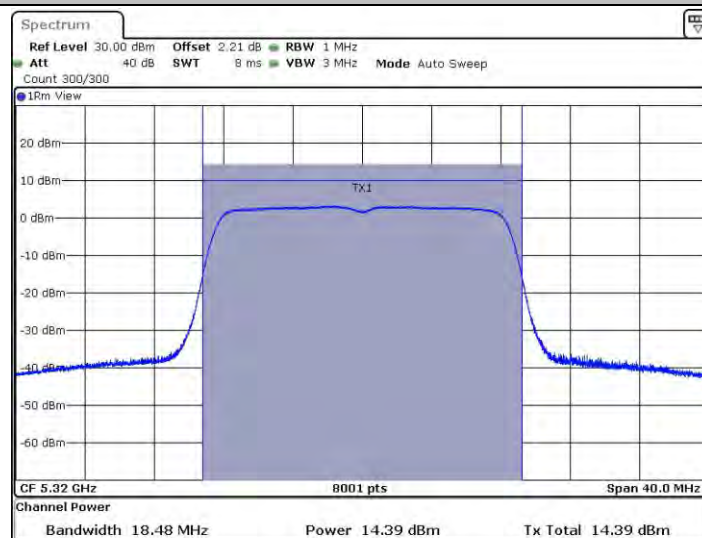
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11A_Ant2_5280



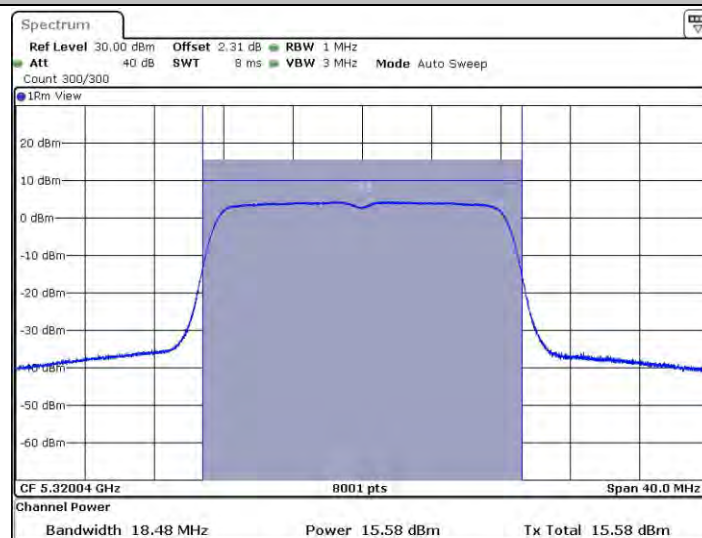
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11A_Ant1_5320



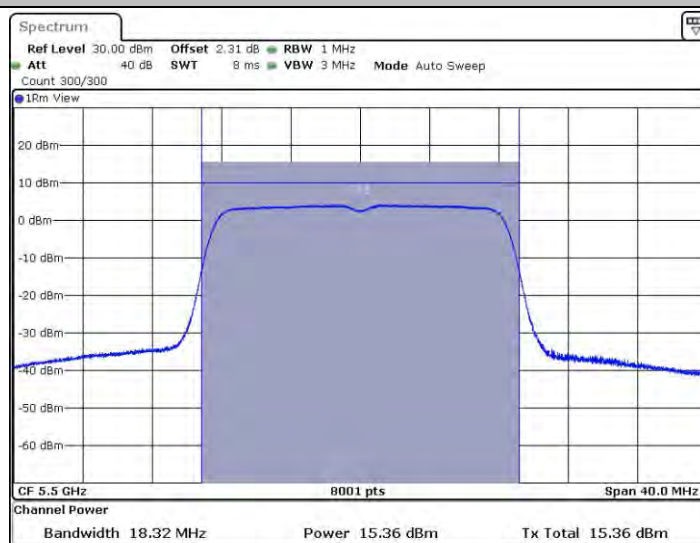
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11A_Ant2_5320



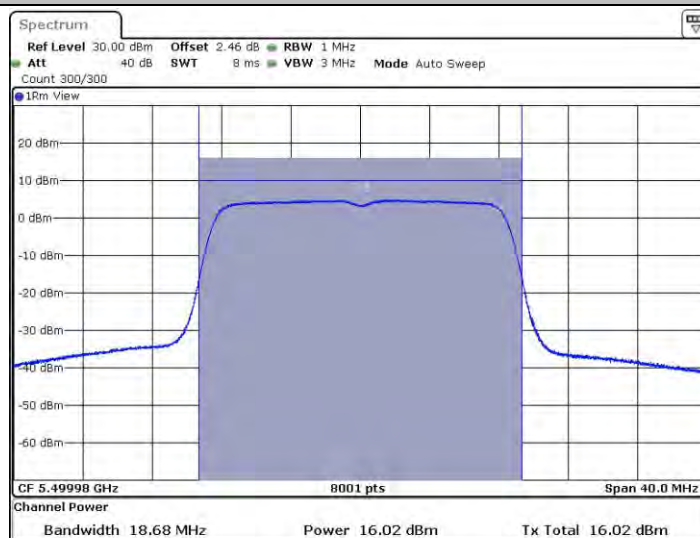
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11A_Ant1_5500



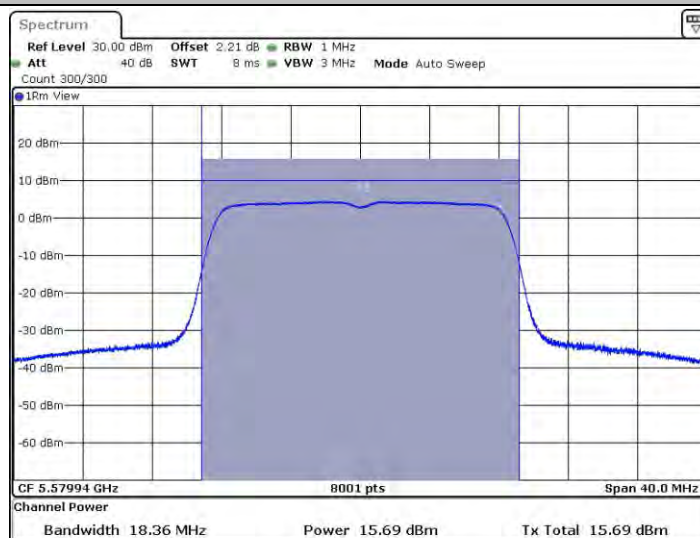
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11A_Ant2_5500



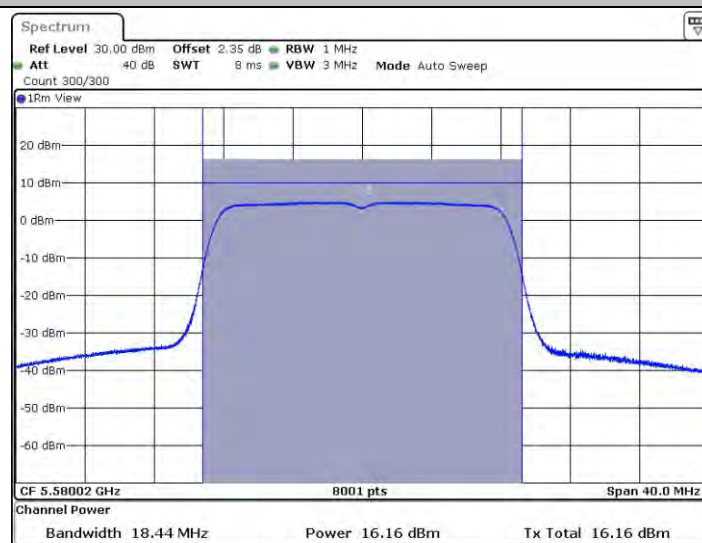
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11A_Ant1_5580

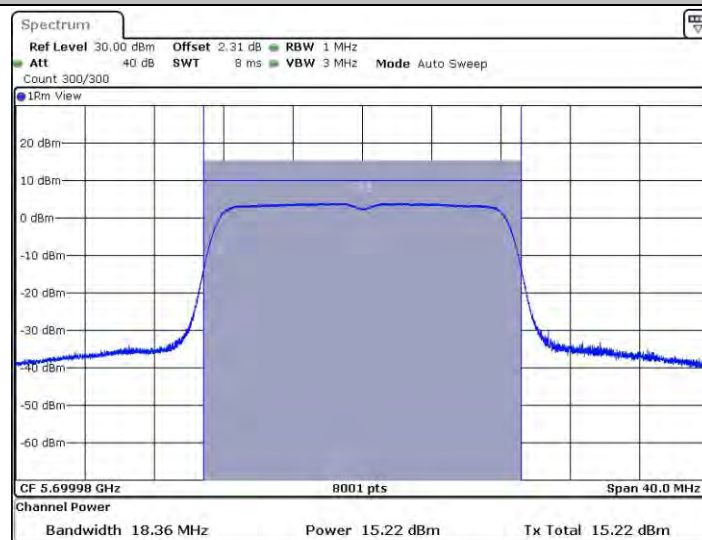


Date: 25 APR 2021 10:37:34

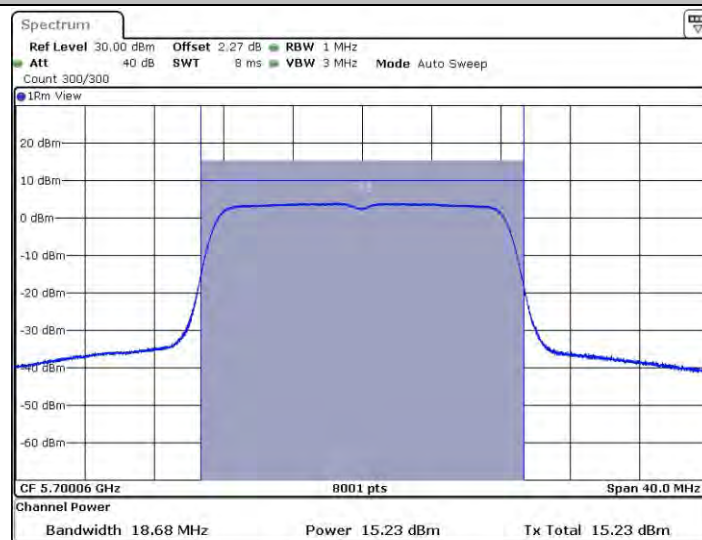
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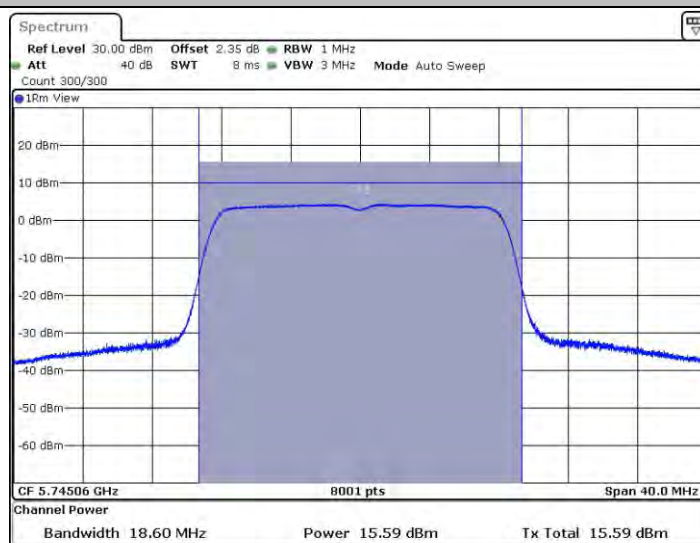
11A_Ant1_5700



11A_Ant2_5700

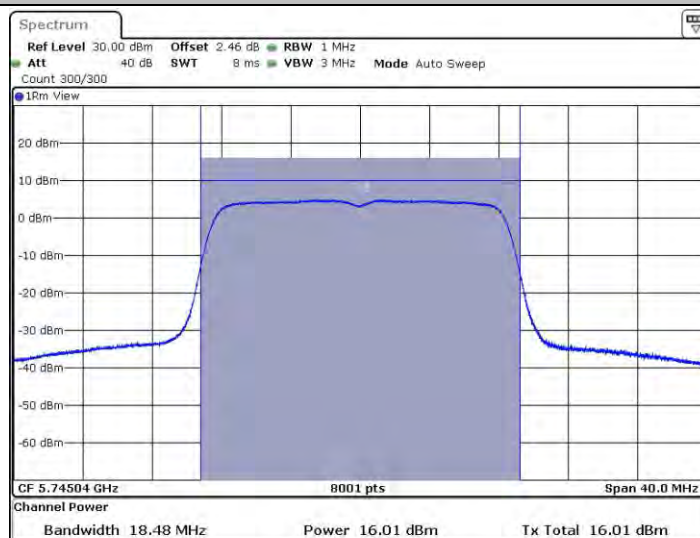


11A_Ant1_5745



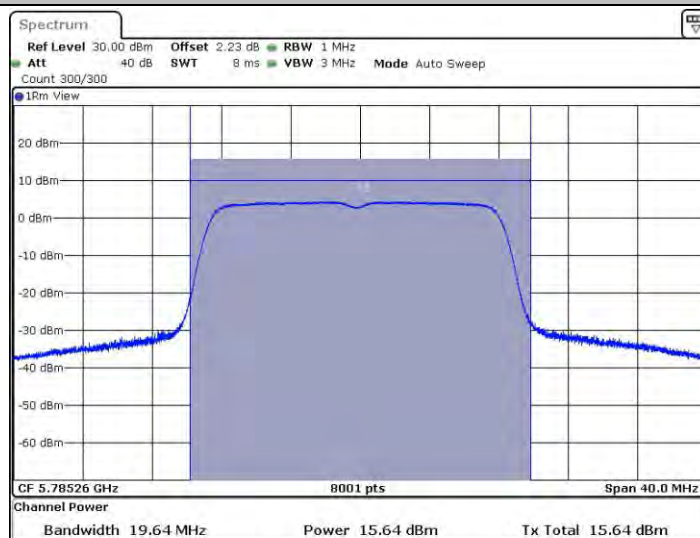
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11A_Ant2_5745



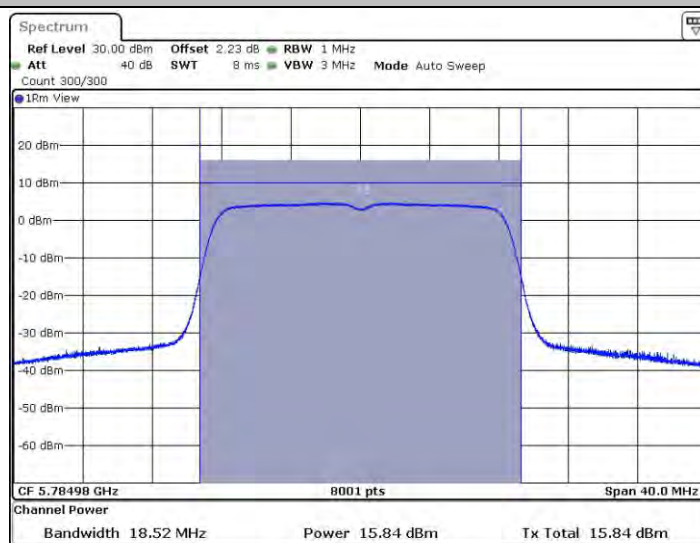
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11A_Ant1_5785



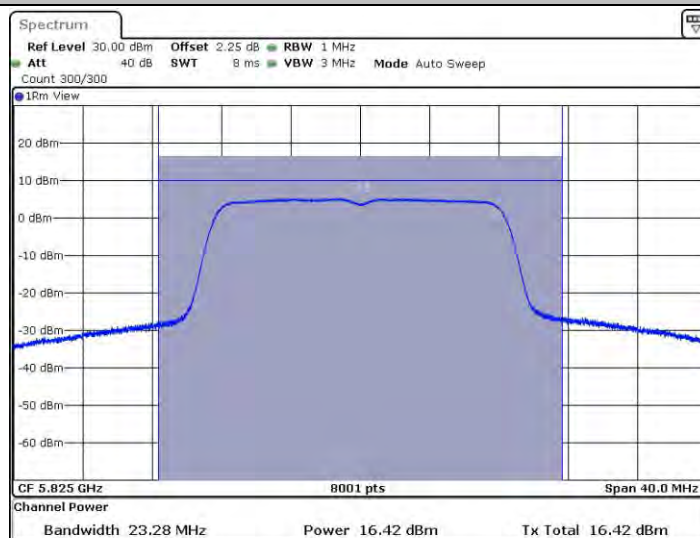
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11A_Ant2_5785



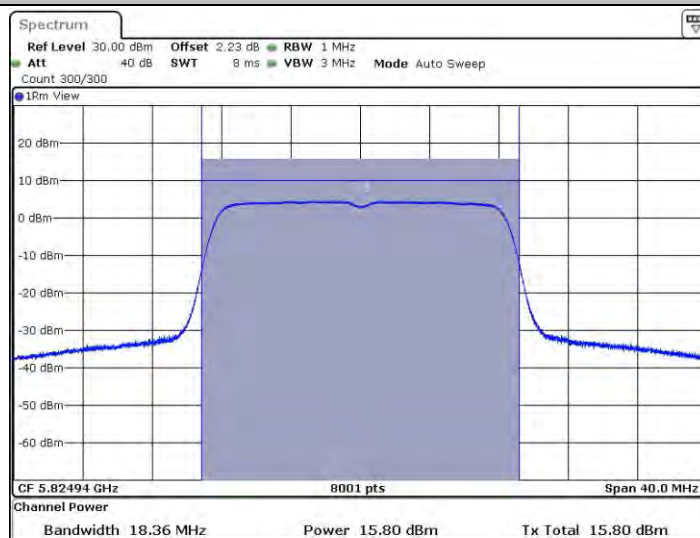
Date: 26 APR 2021 18:42:45

11A_Ant1_5825



Date: 25 APR 2021 11:12:37

11A_Ant2_5825



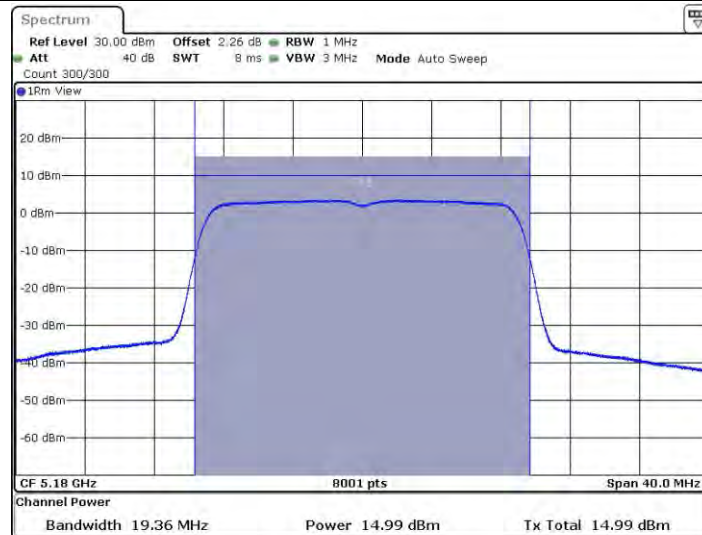
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11N20SISO_Ant1_5180



Date: 25 APR 2021 11:17:12

11N20SISO_Ant2_5180



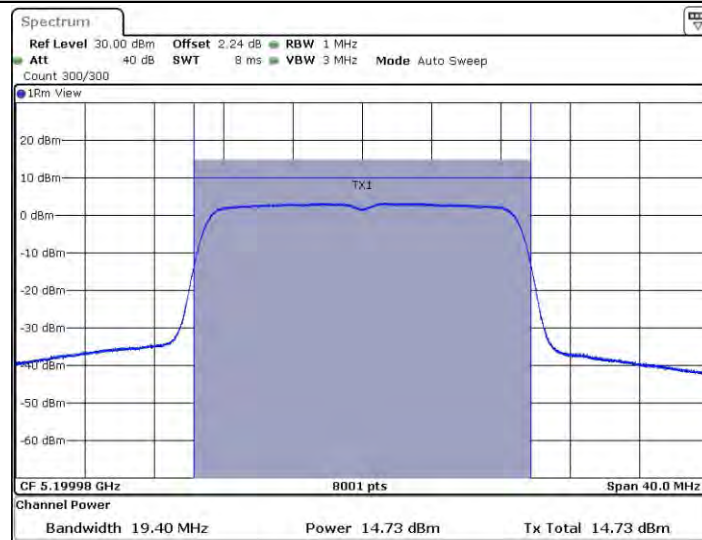
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11N20SISO_Ant1_5200



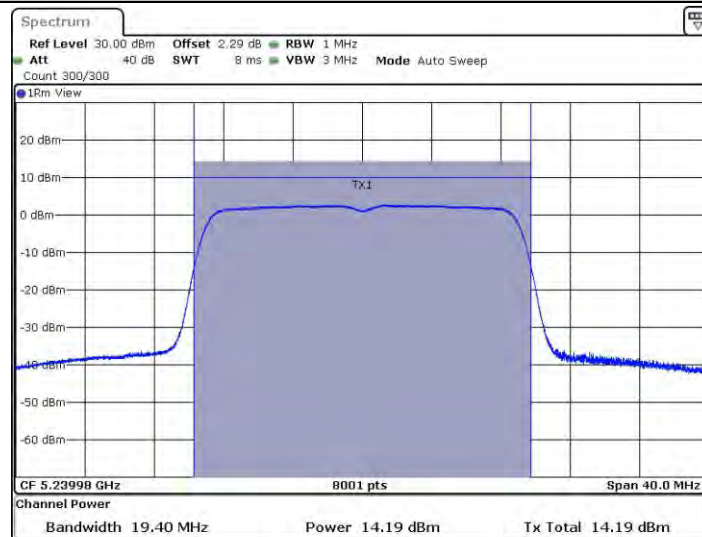
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11N20SISO_Ant2_5200



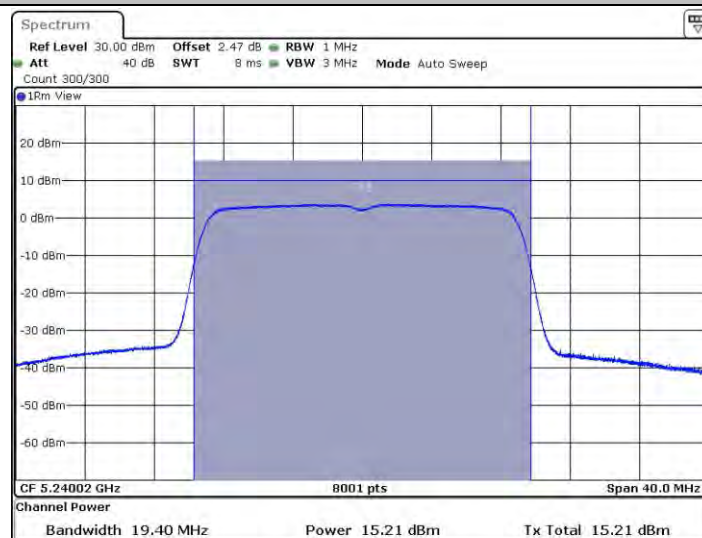
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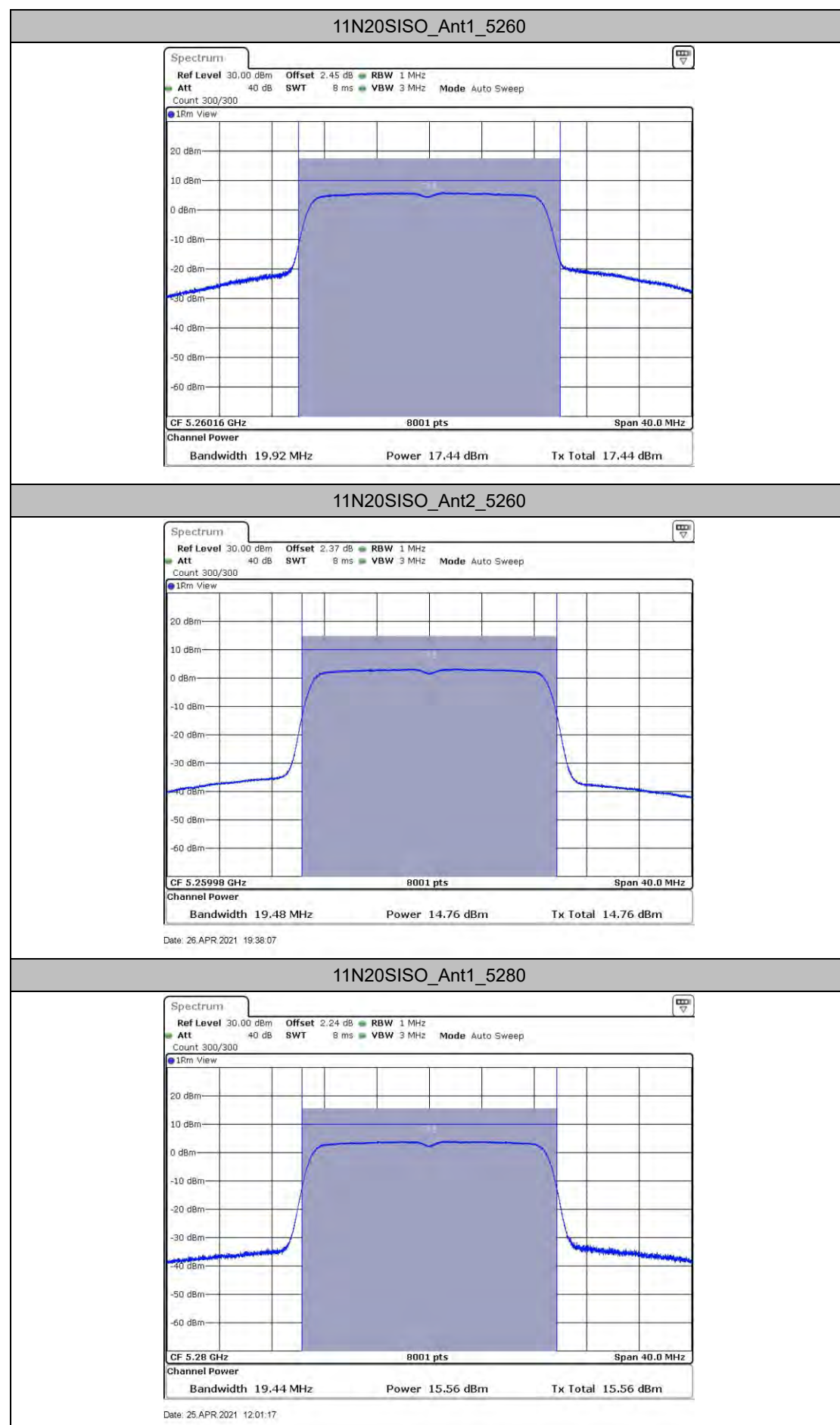


Date: 25 APR 2021 11:26:56

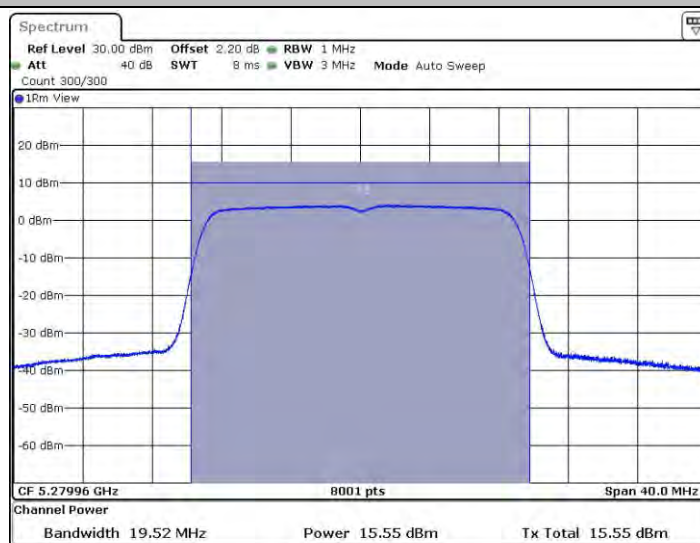
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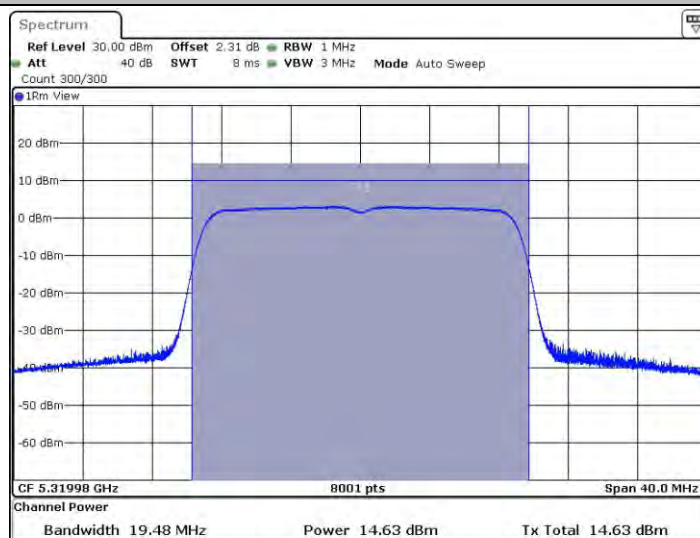


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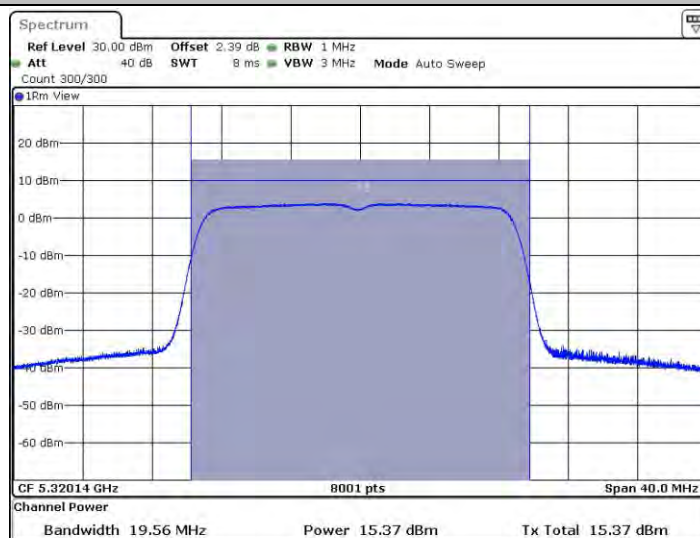
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11N20SISO_Ant1_5320



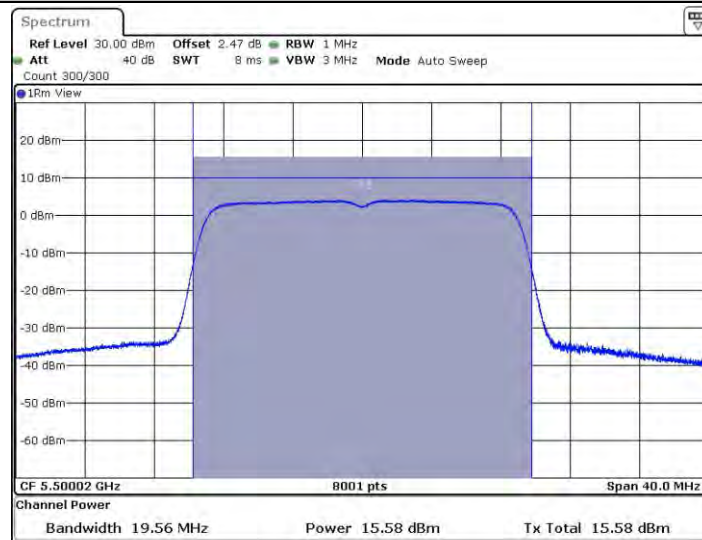
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11N20SISO_Ant2_5320



Date: 26 APR 2021 20:41:45

11N20SISO_Ant1_5500



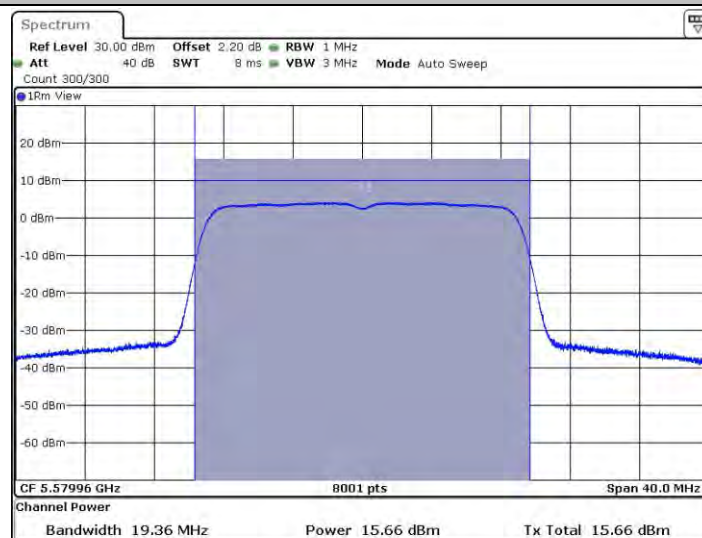
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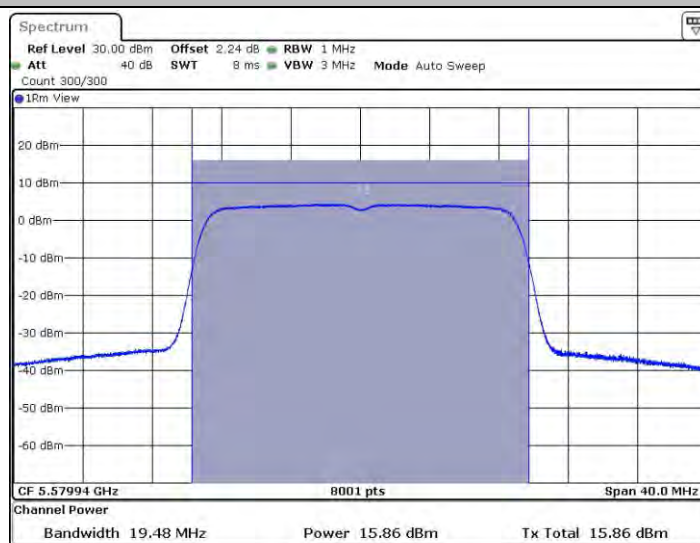
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11N20SISO_Ant1_5580



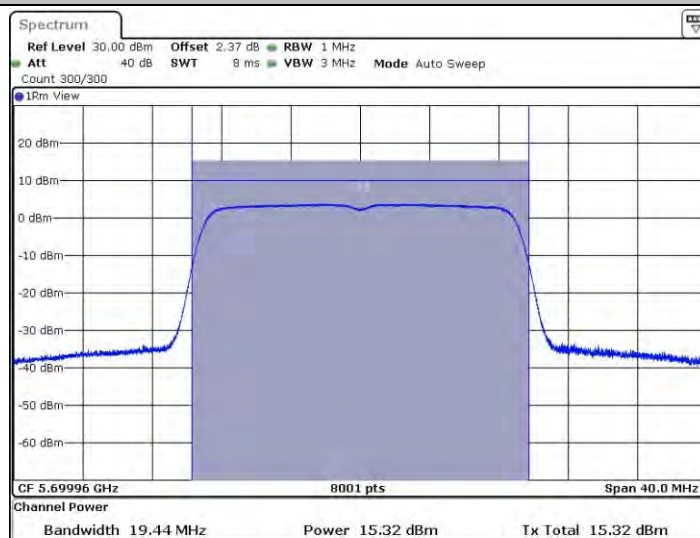
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11N20SISO_Ant2_5580



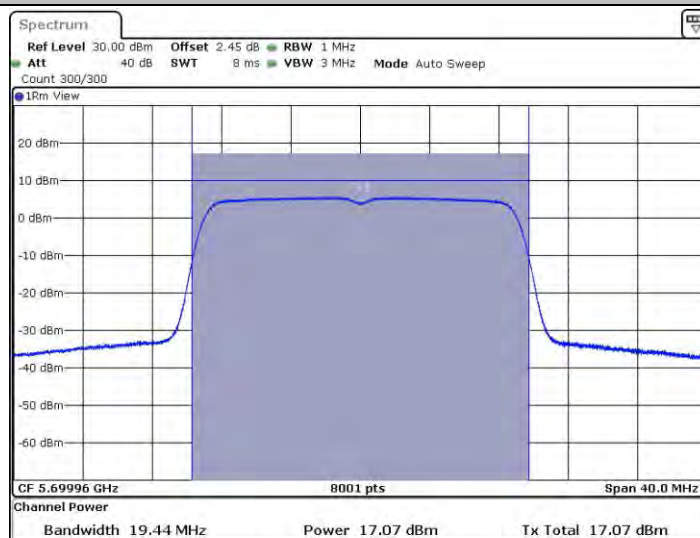
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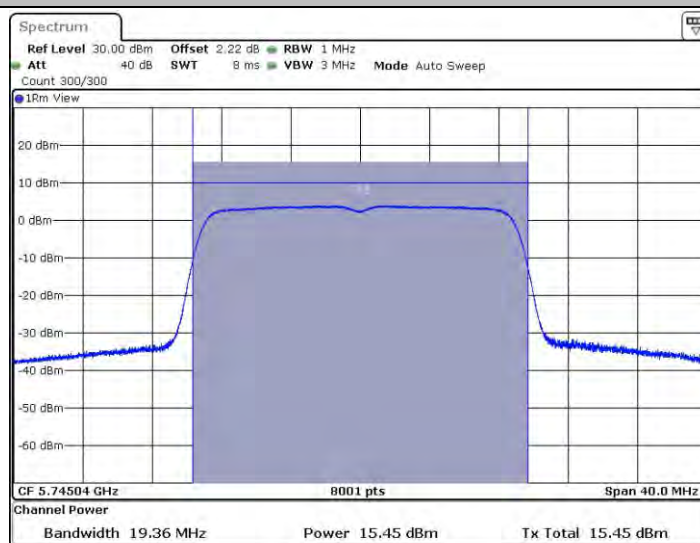
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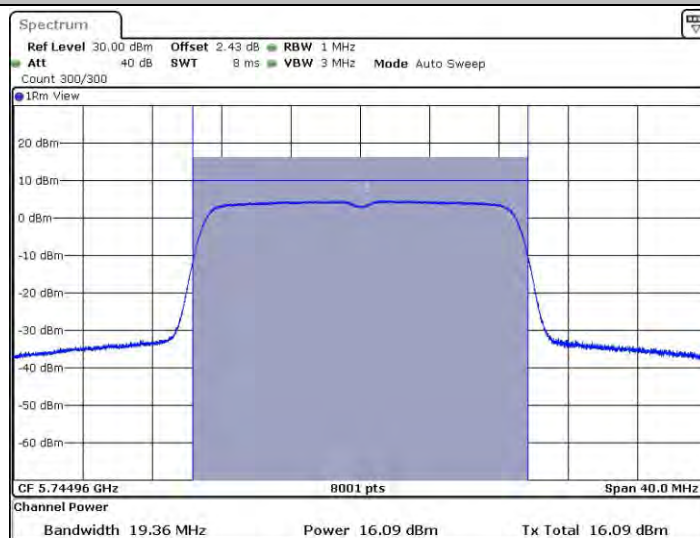
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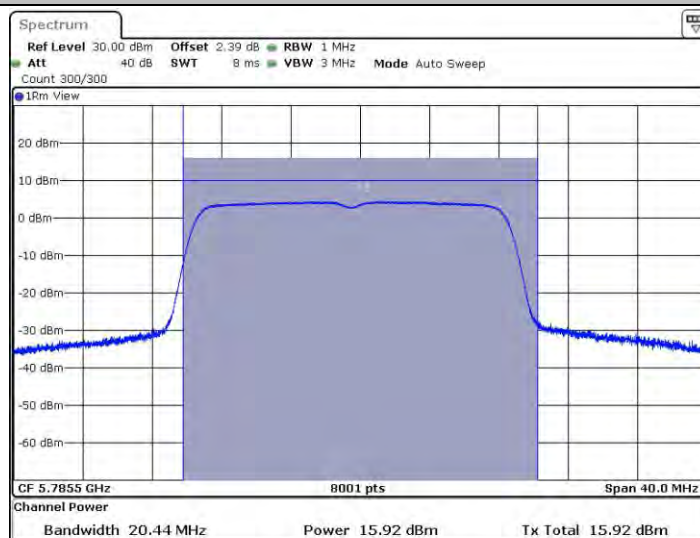
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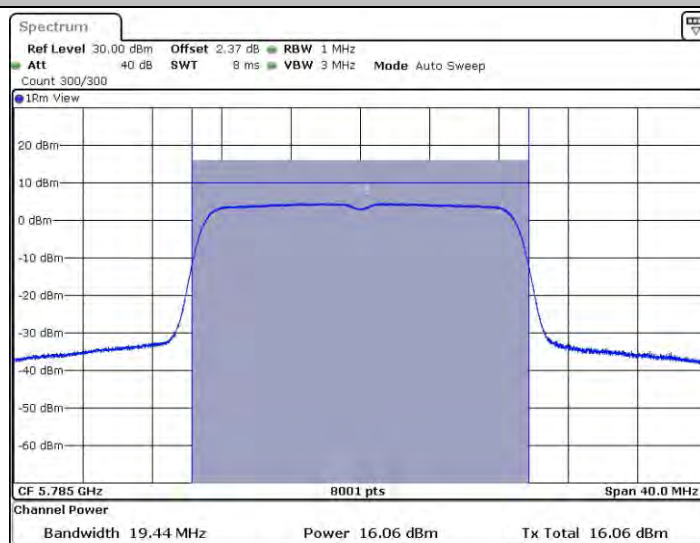
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11N20SISO_Ant1_5785



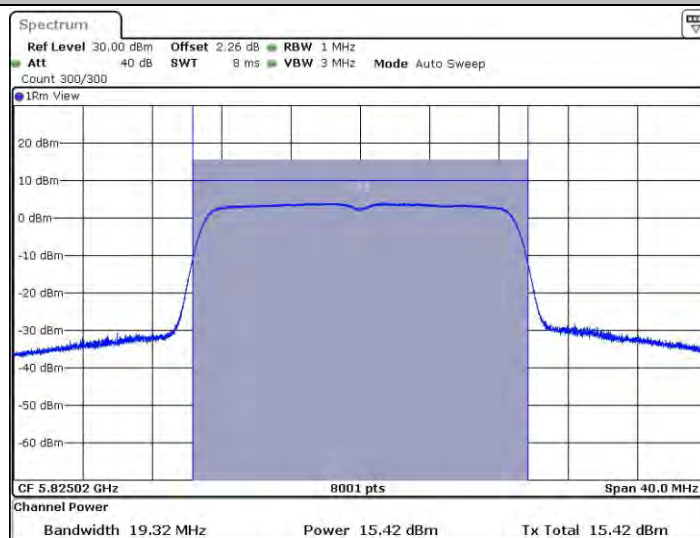
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11N20SISO_Ant2_5785



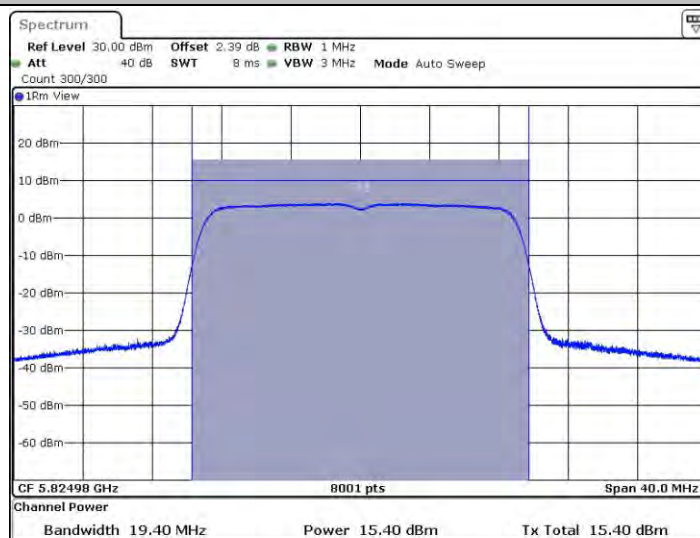
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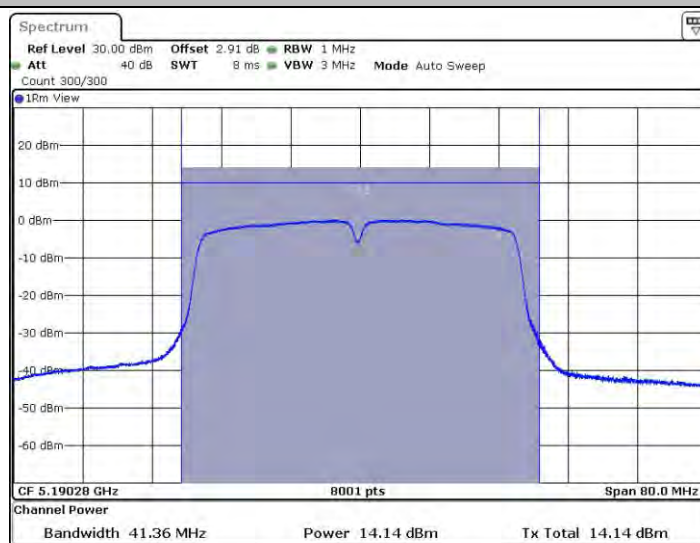
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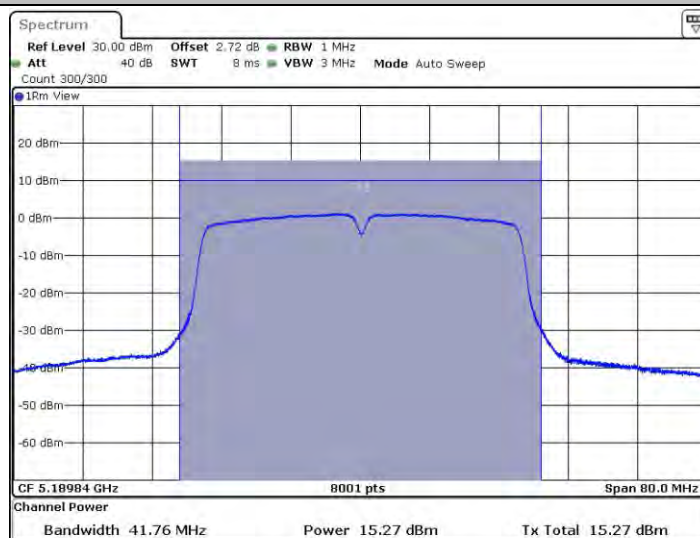
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11N40SISO_Ant1_5190



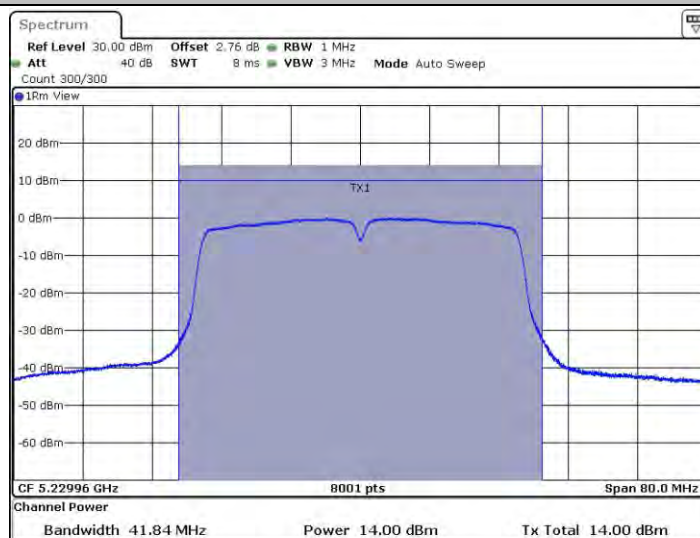
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11N40SISO_Ant2_5190



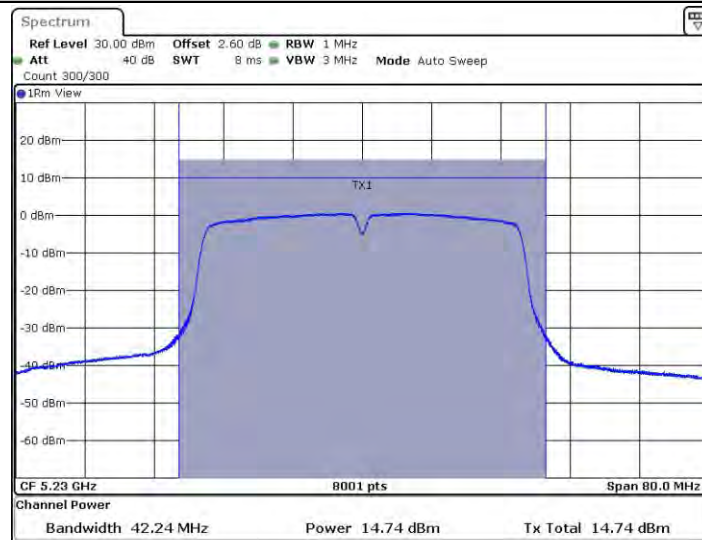
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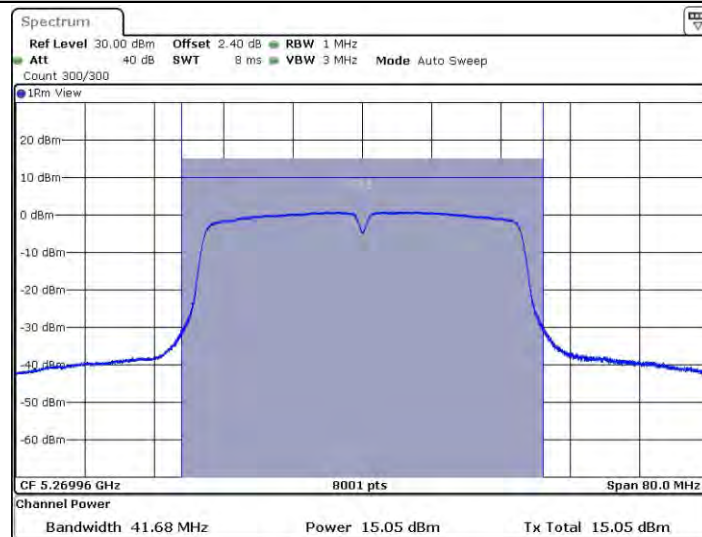
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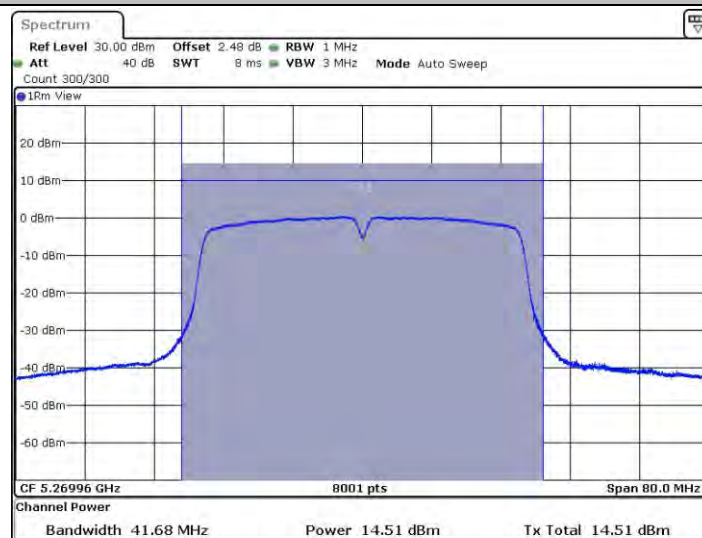
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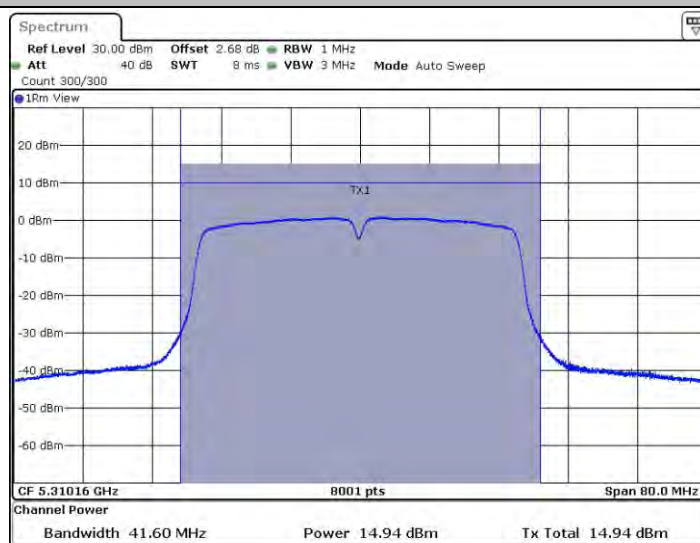
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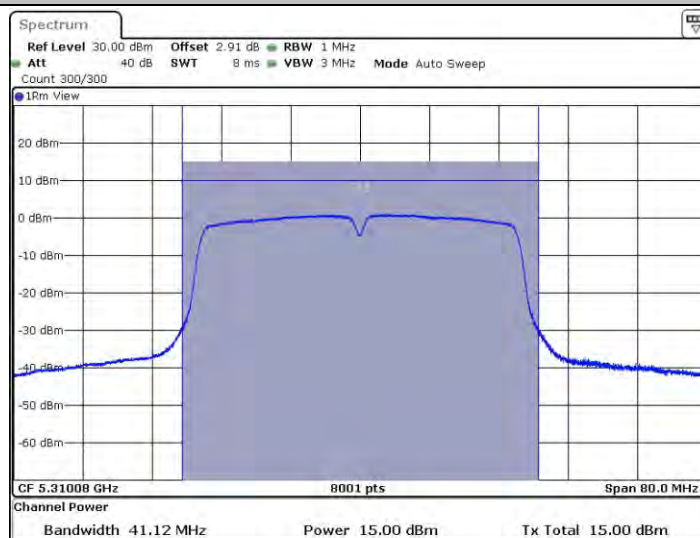
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11N40SISO_Ant1_5310



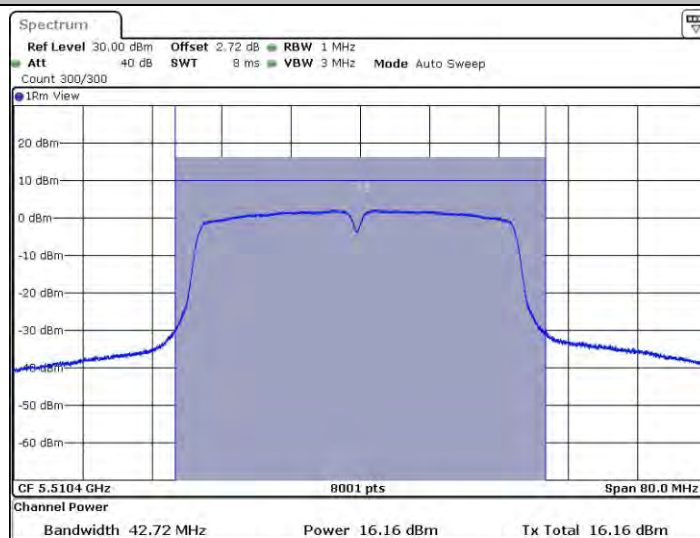
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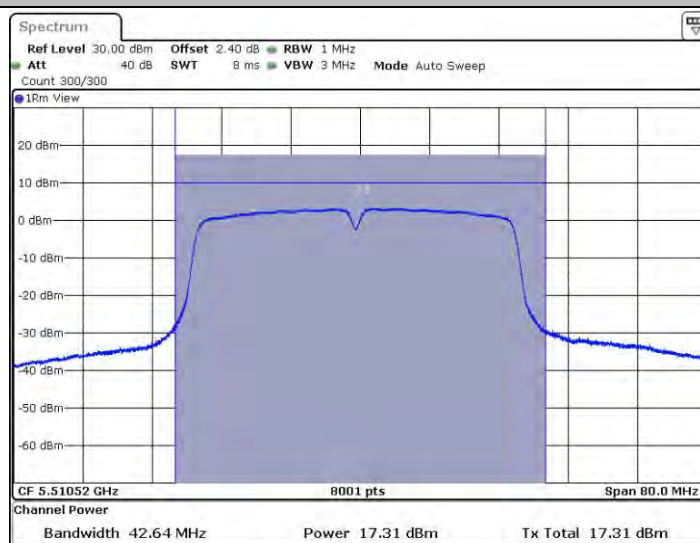
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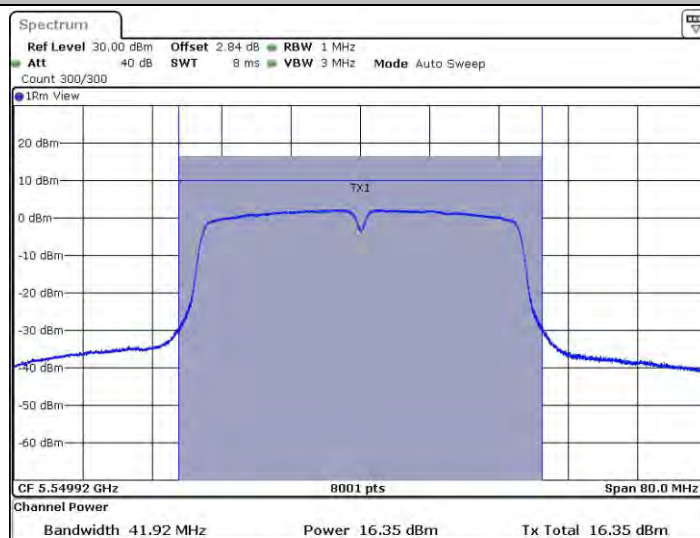
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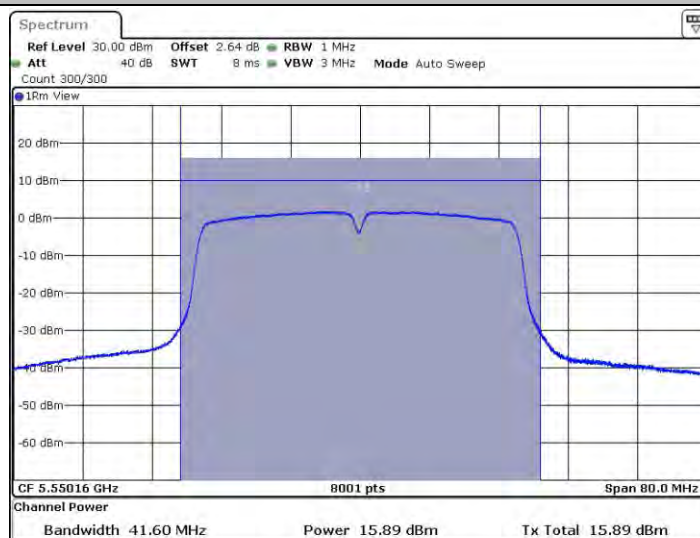
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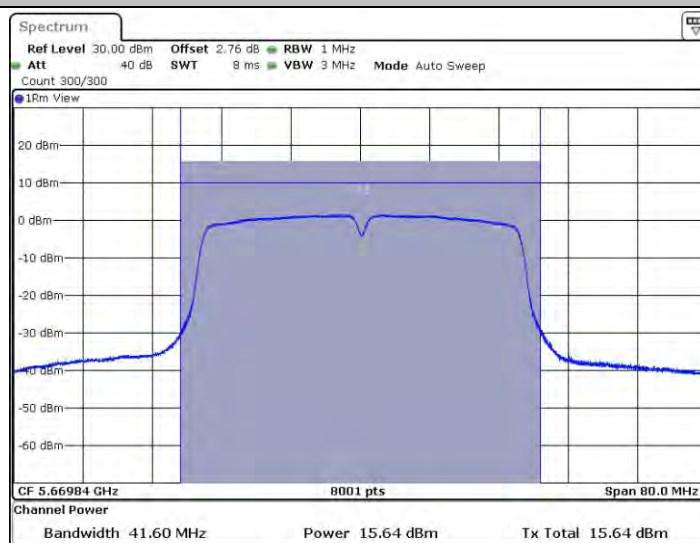
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11N40SISO_Ant2_5550



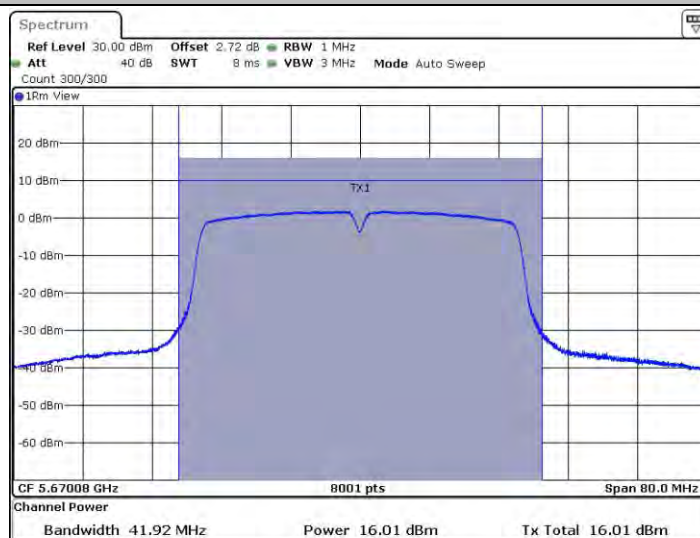
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11N40SISO_Ant1_5670



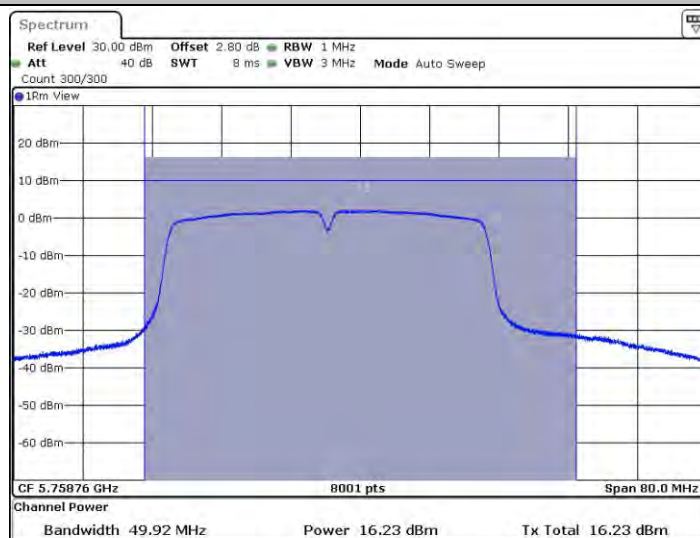
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11N40SISO_Ant2_5670



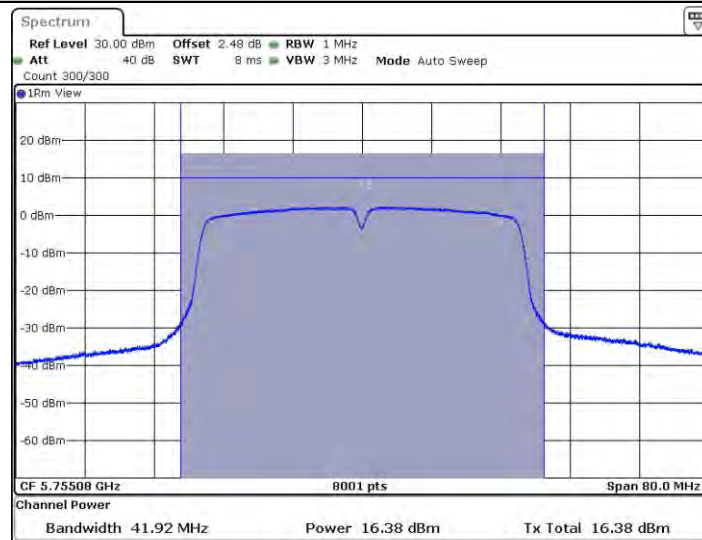
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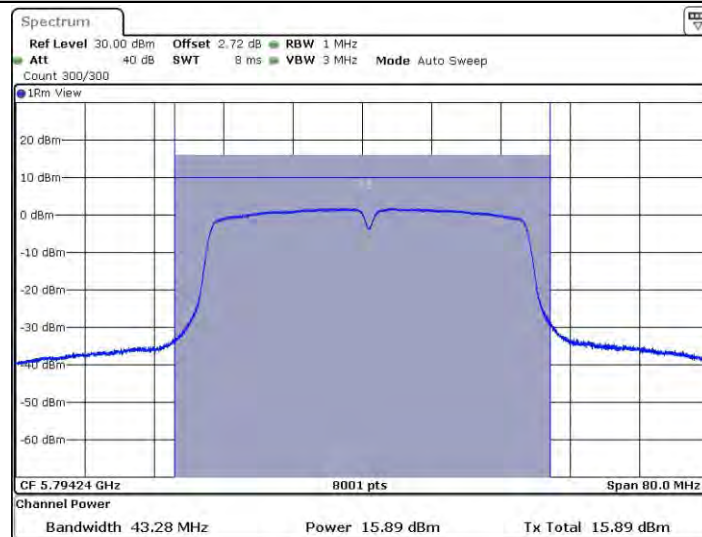
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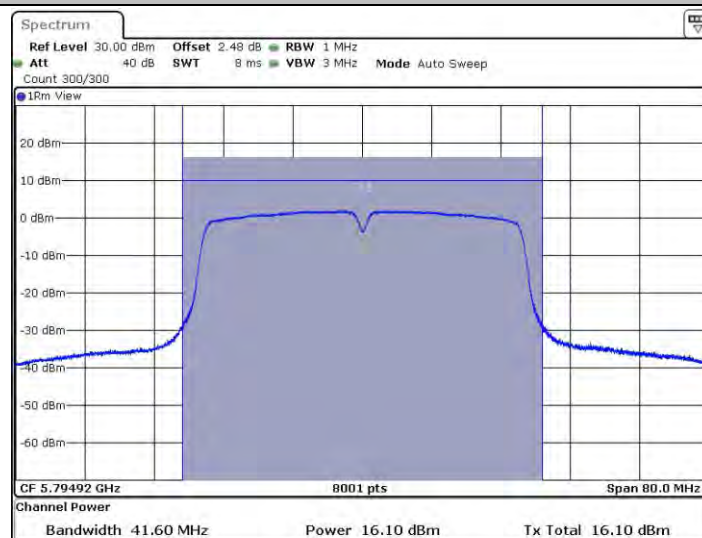
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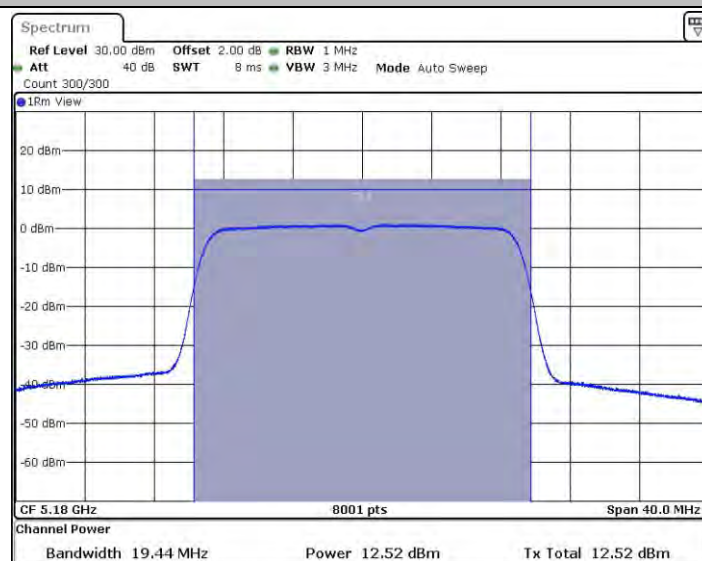
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11N40SISO_Ant2_5795



Date: 27 APR 2021 12:45:30

11AC20SISO_Ant1_5180

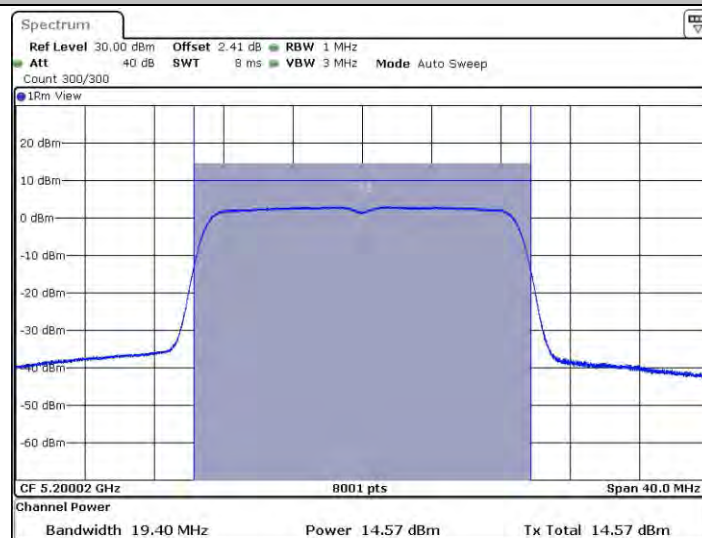


11AC20SISO_Ant2_5180

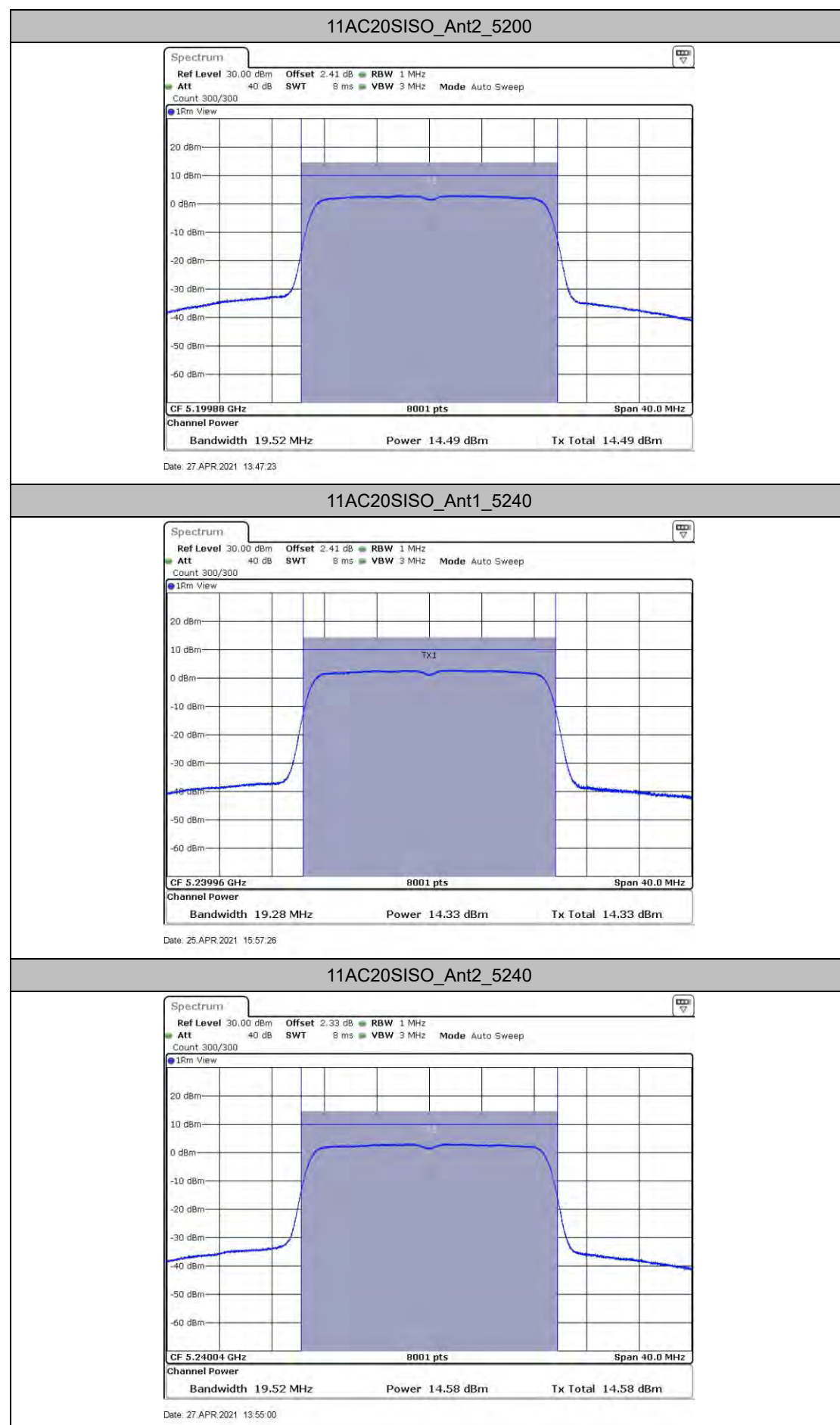


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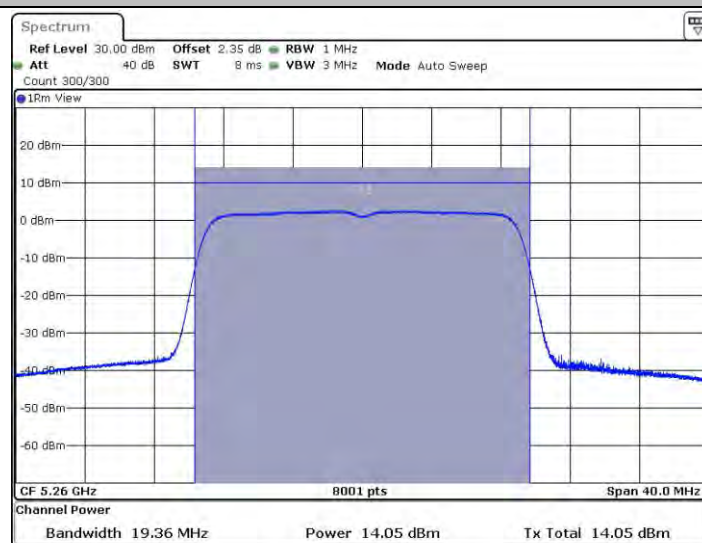
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Date: 25 APR 2021 15:52:44

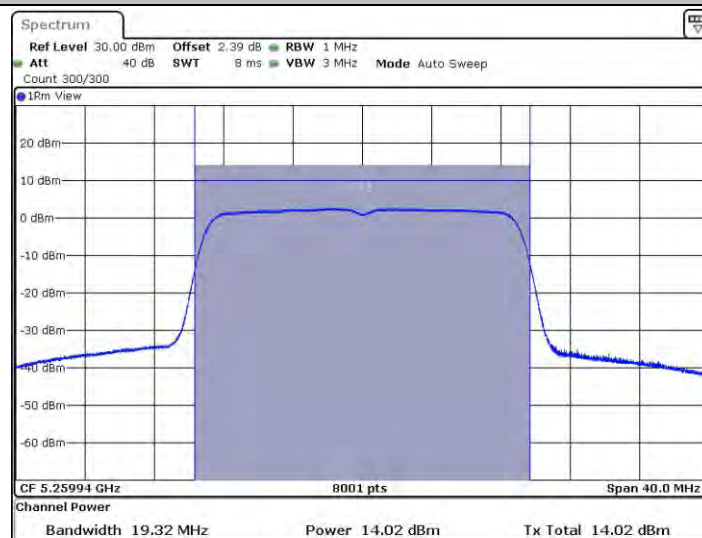


11AC20SISO_Ant1_5260



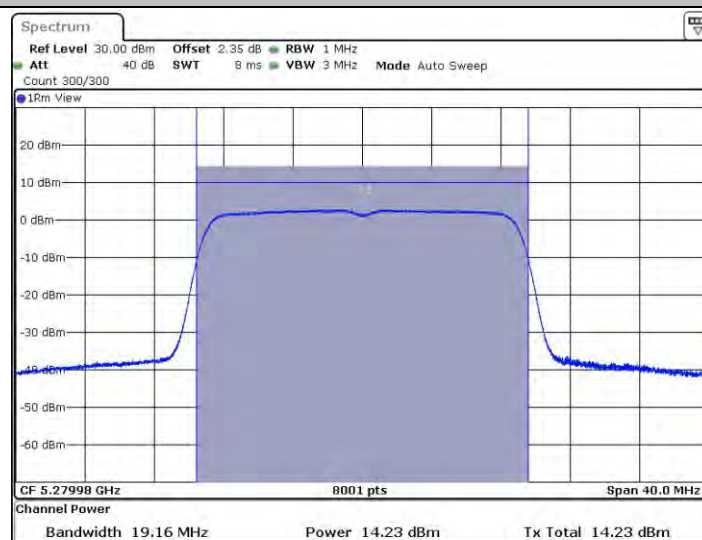
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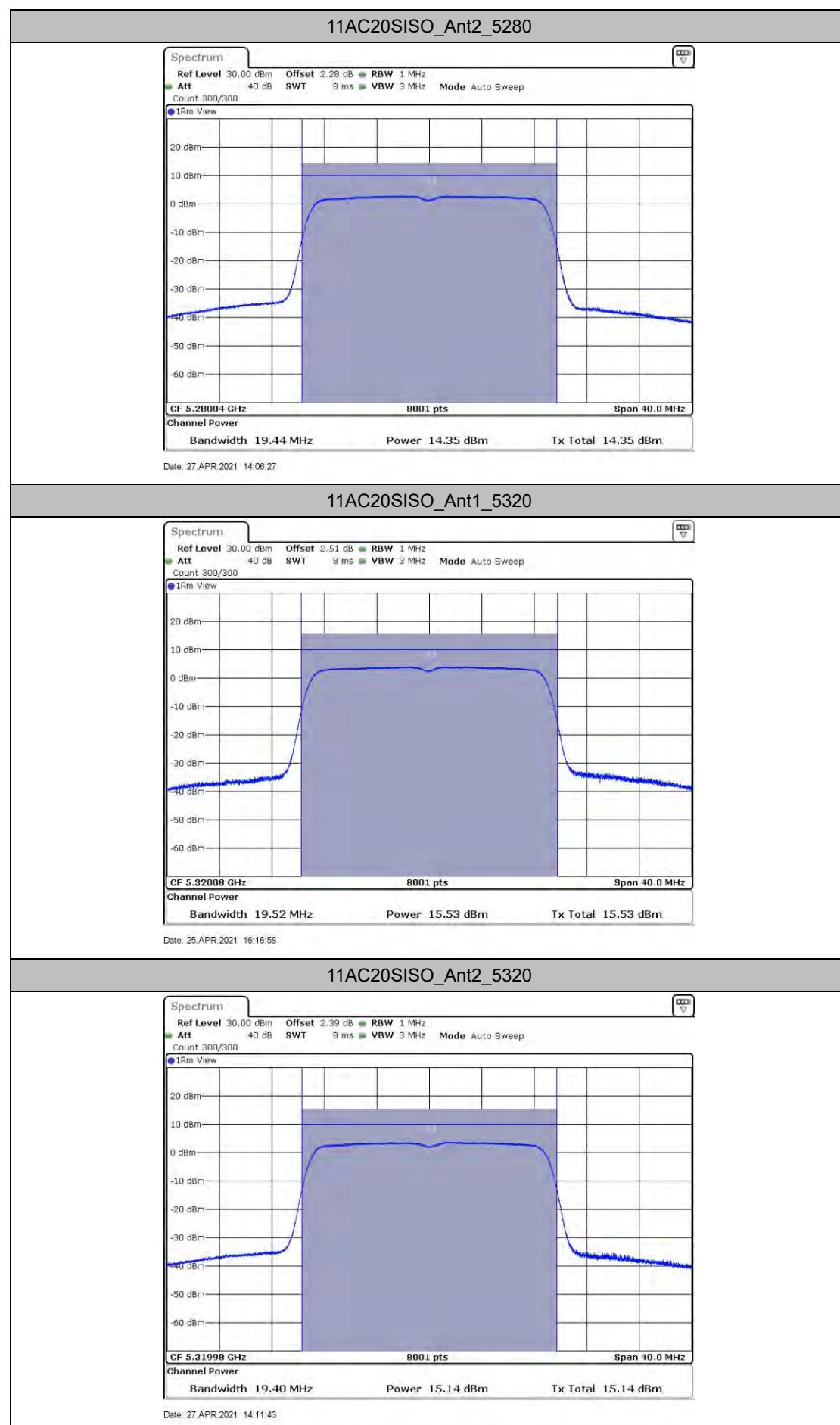


Date: 27 APR 2021 13:59:45

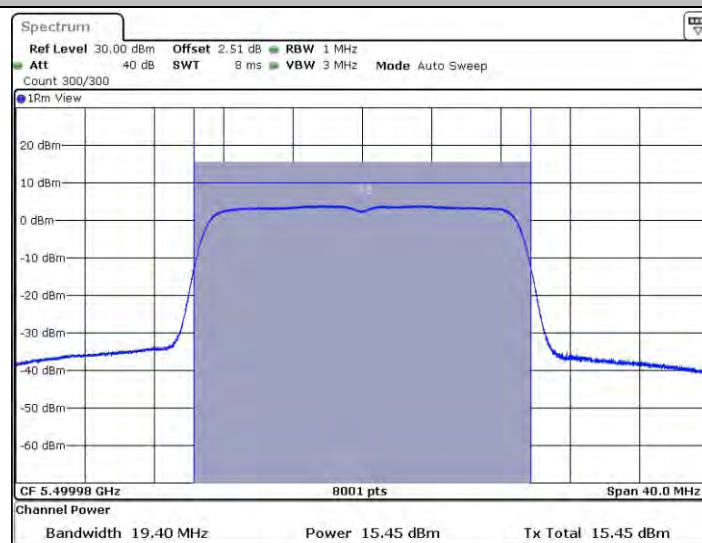
11AC20SISO_Ant1_5280



Date: 25 APR 2021 16:07:01

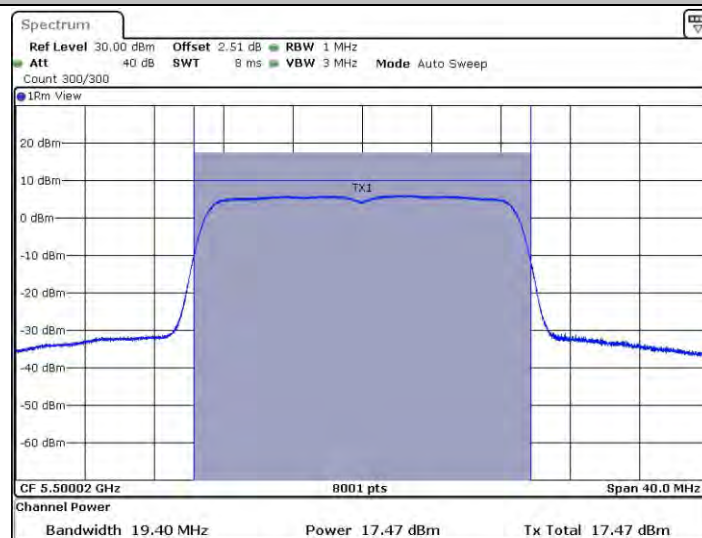


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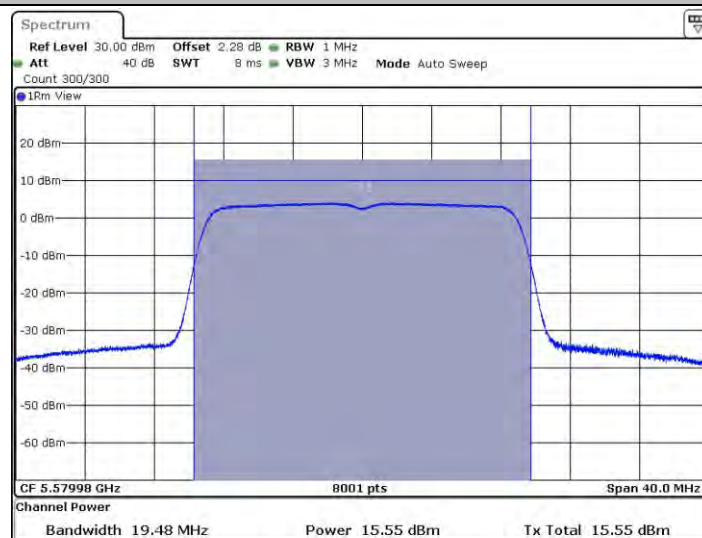
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11AC20SISO_Ant2_5500



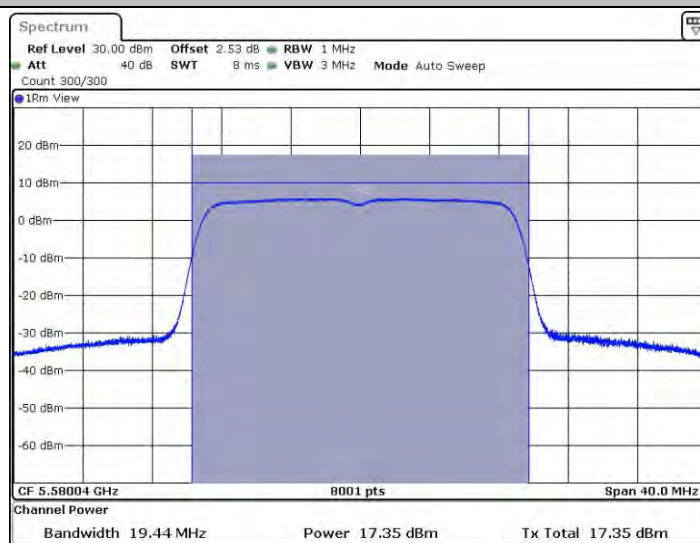
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11AC20SISO_Ant1_5580



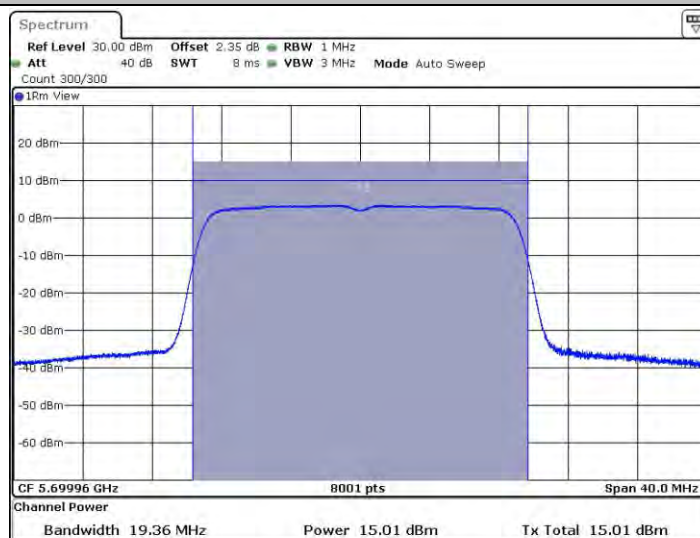
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11AC20SISO_Ant2_5580



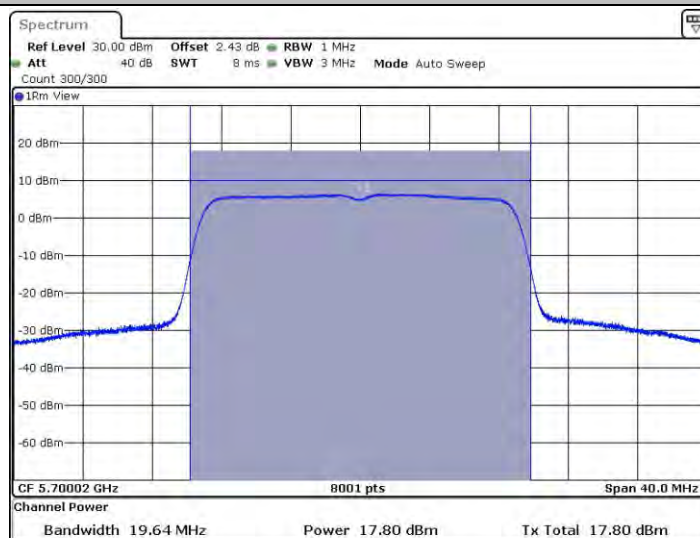
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11AC20SISO_Ant1_5700



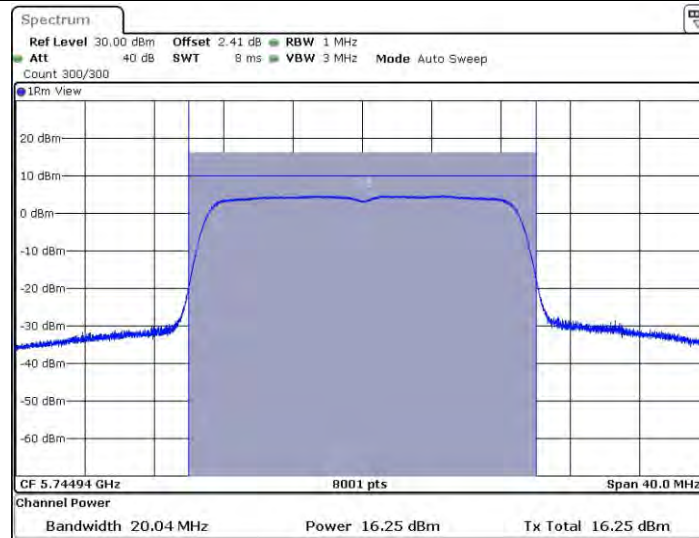
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11AC20SISO_Ant2_5700



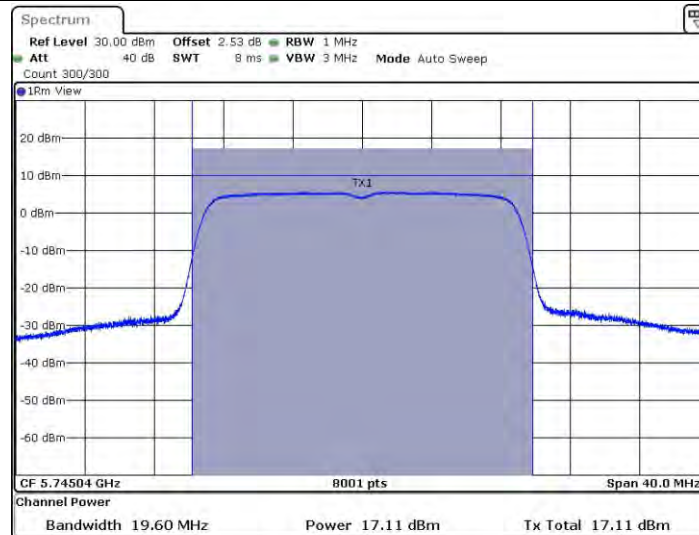
Date: 27 APR 2021 15:08:52

11AC20SISO_Ant1_5745



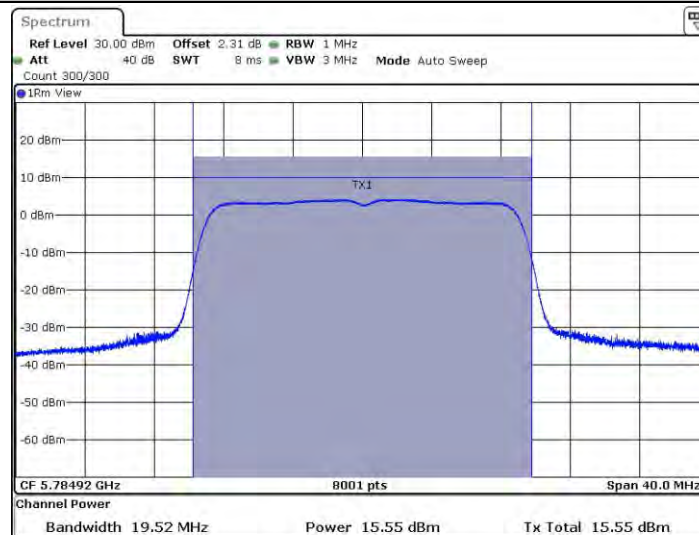
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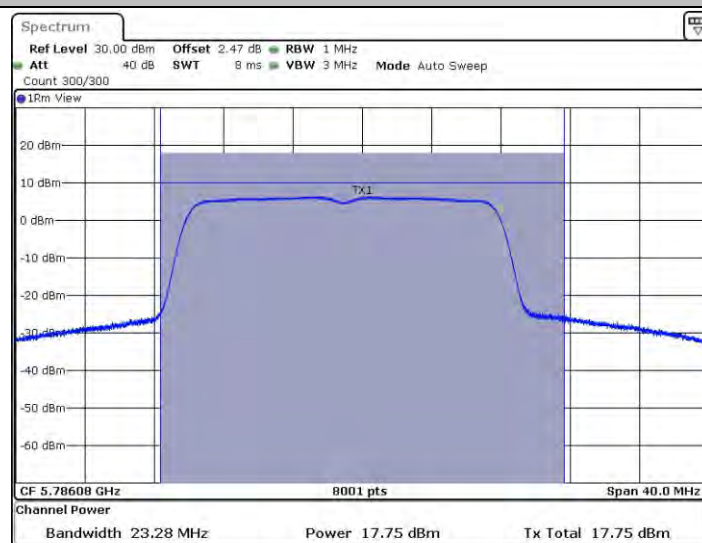
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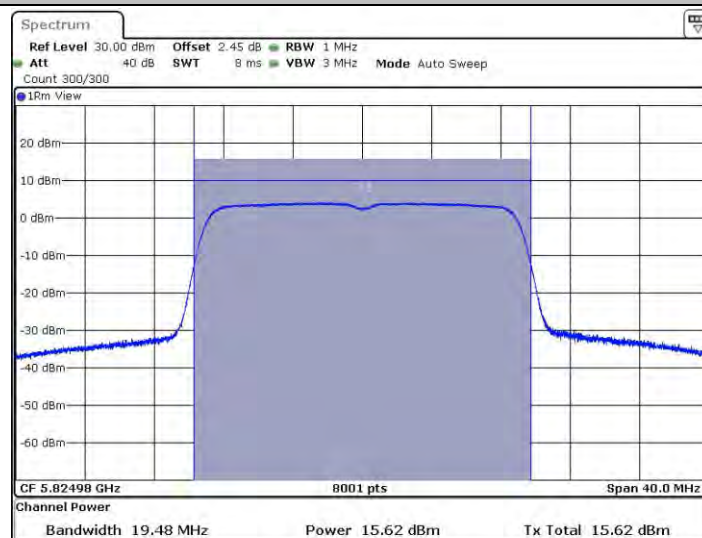
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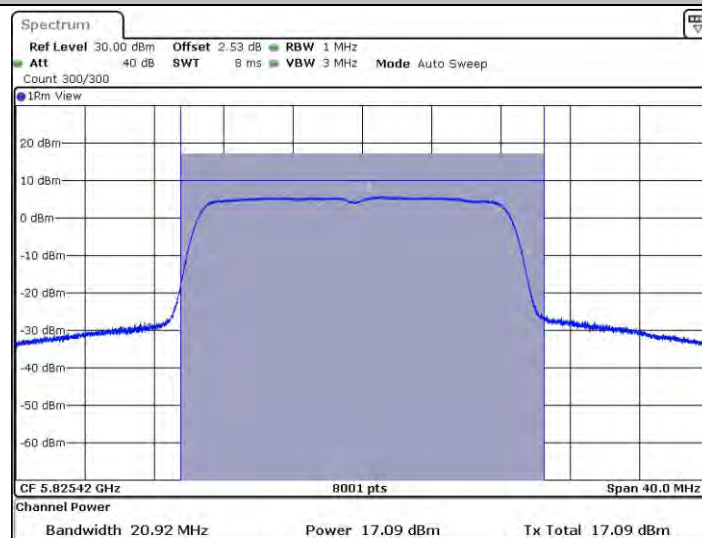
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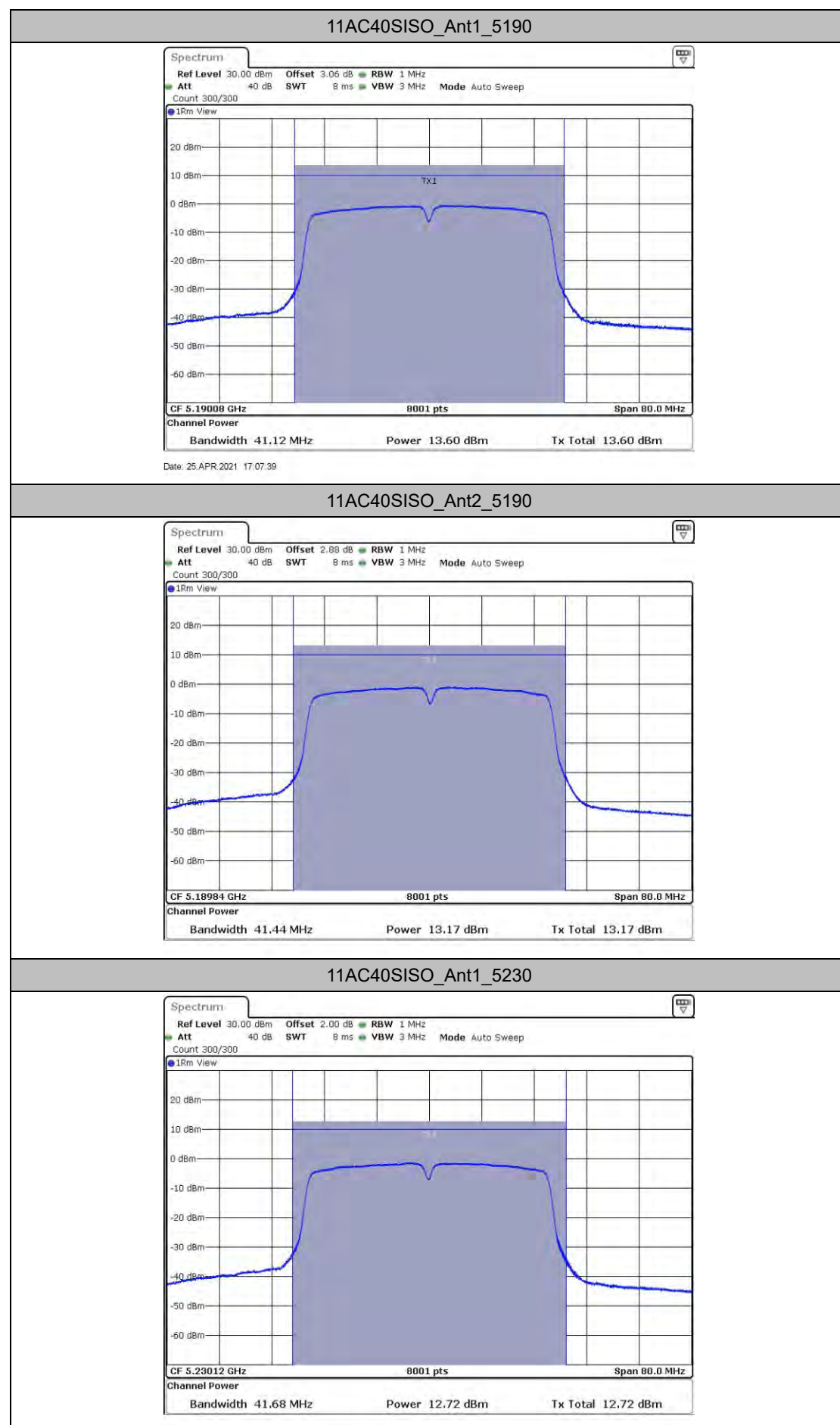


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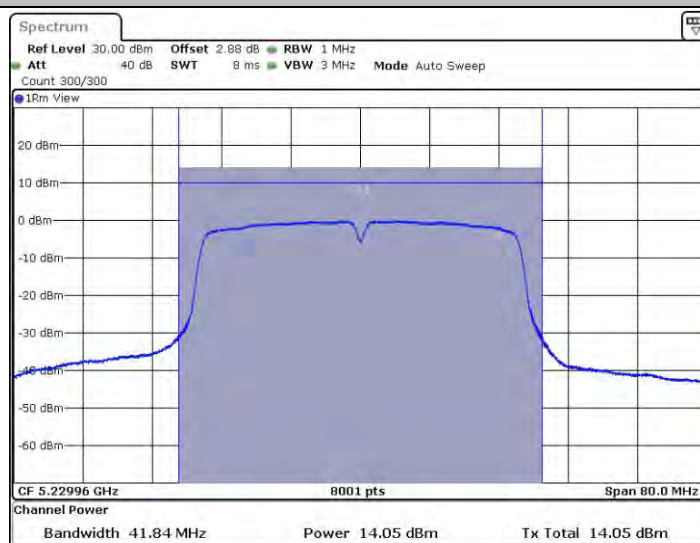
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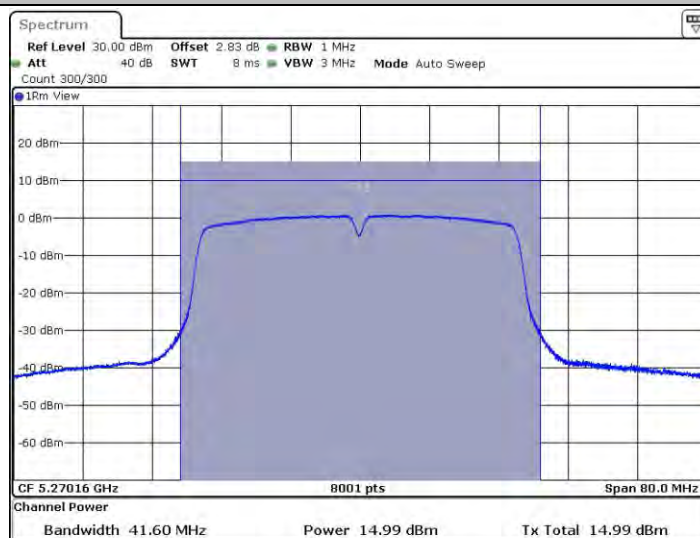


11AC40SISO_Ant2_5230



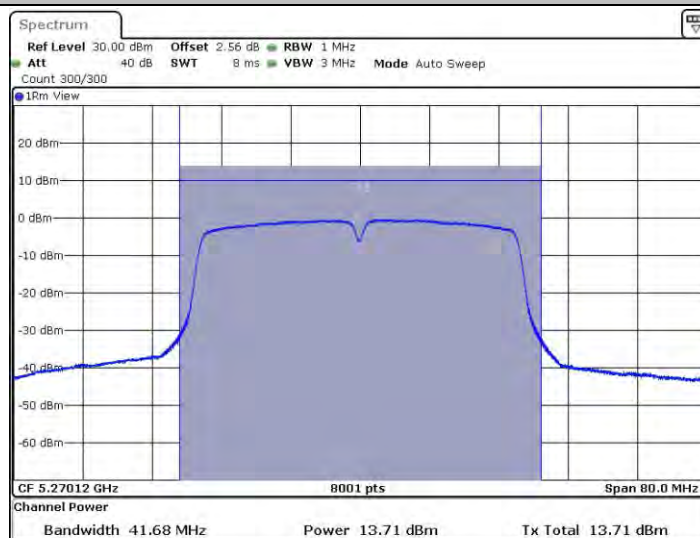
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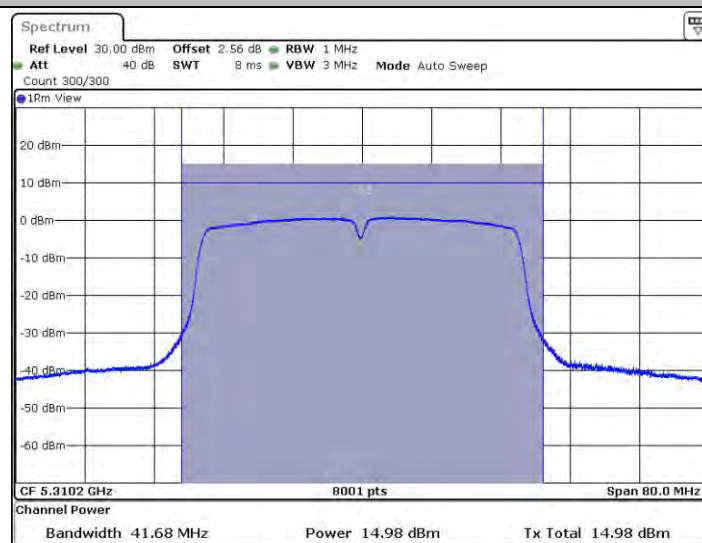
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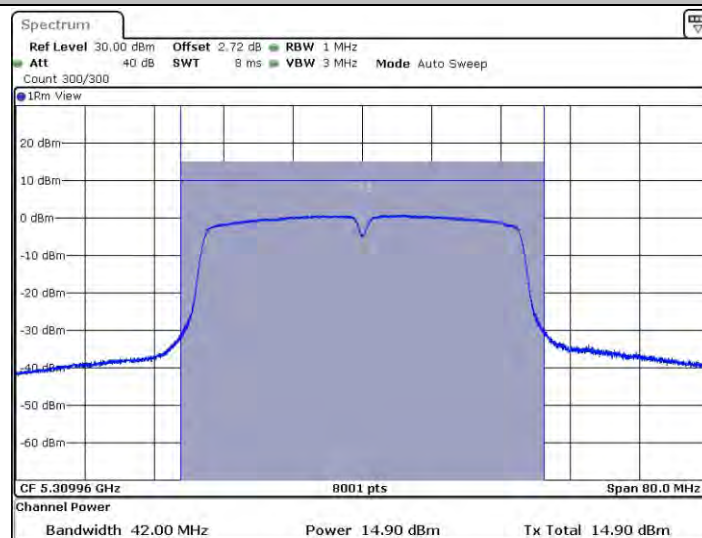
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11AC40SISO_Ant1_5310



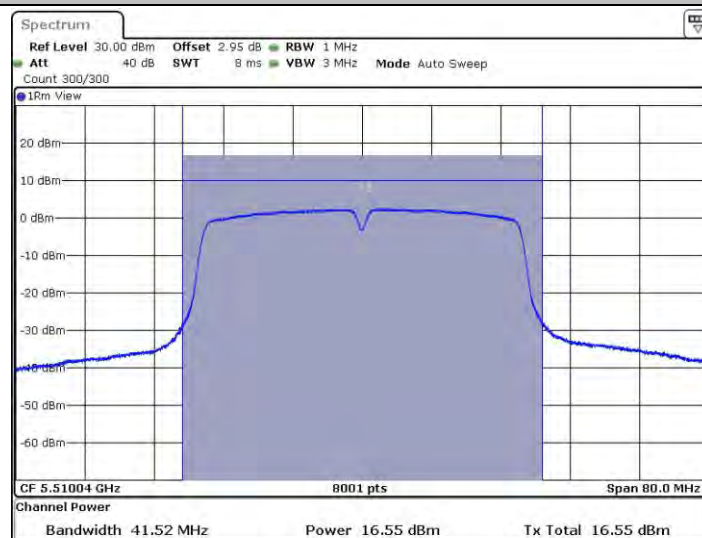
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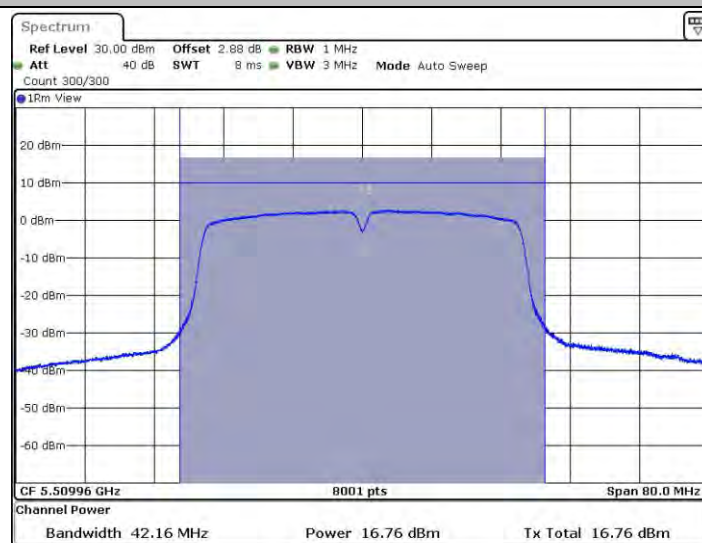
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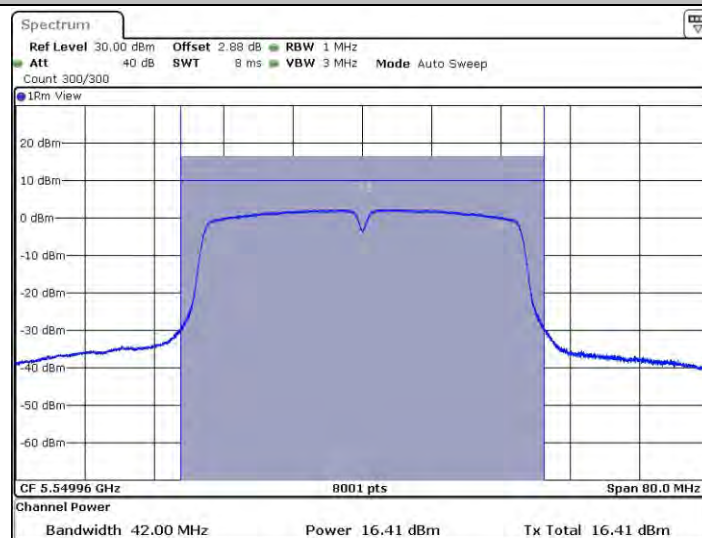
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11AC40SISO_Ant2_5510



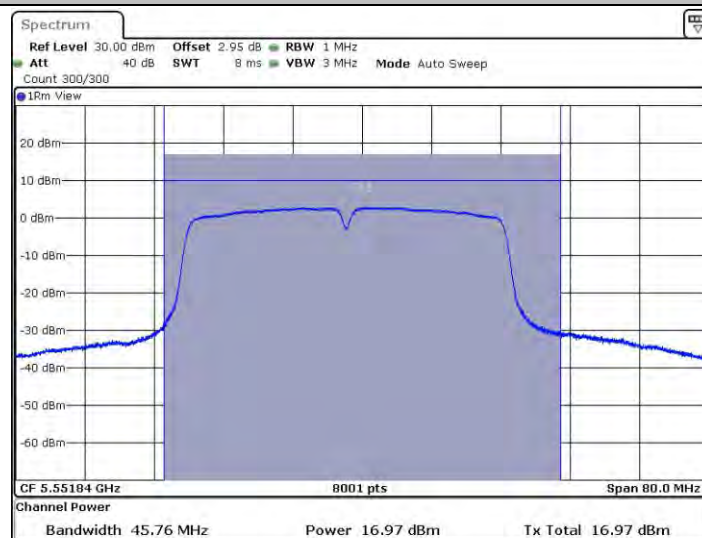
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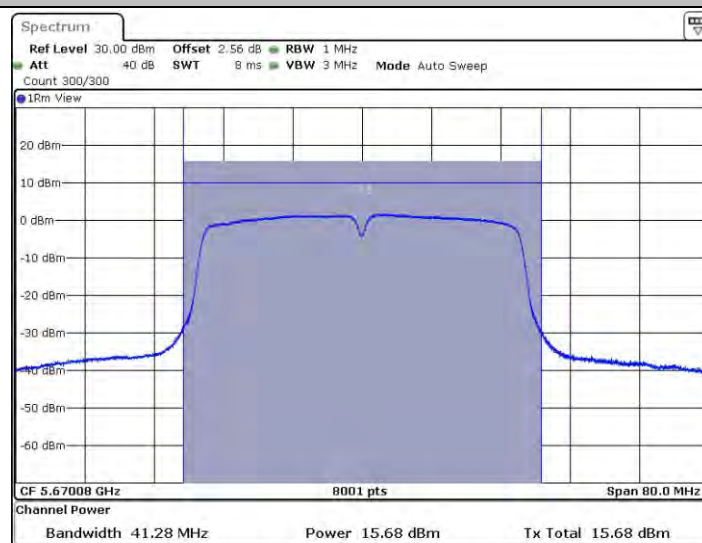
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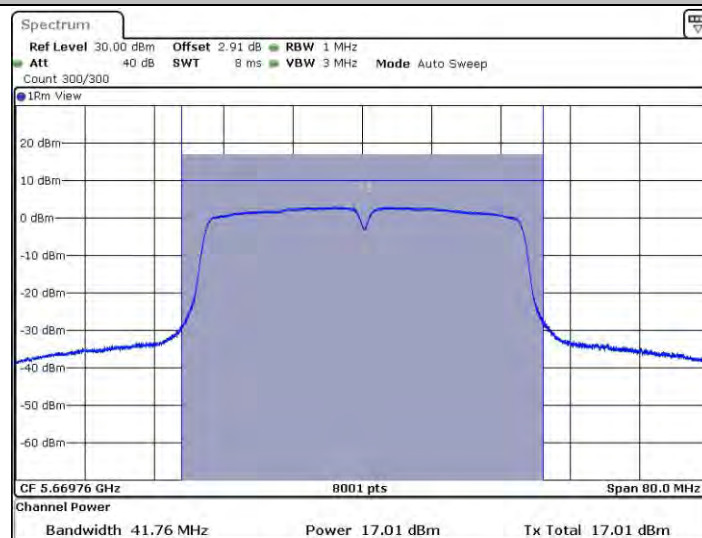
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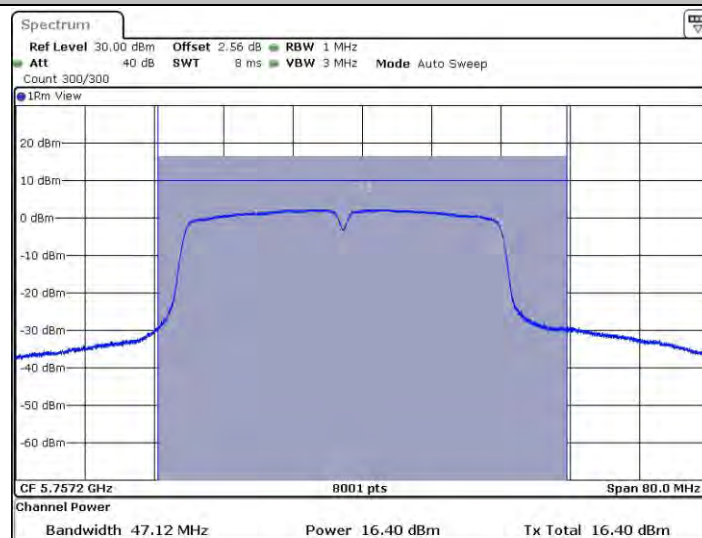
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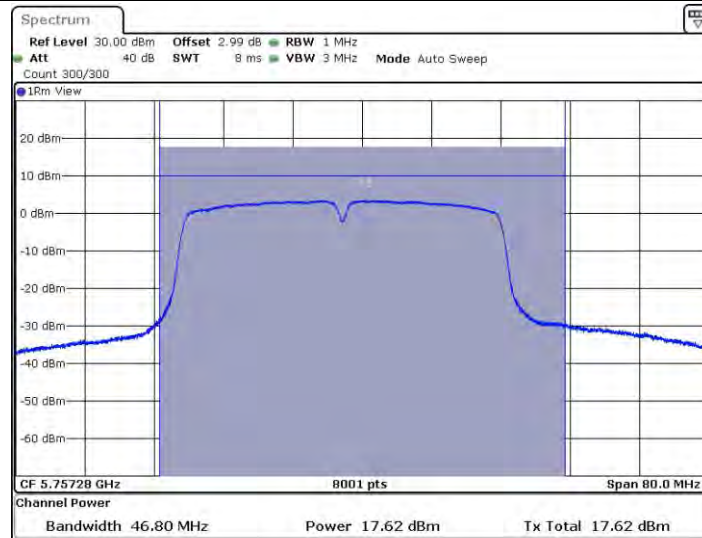
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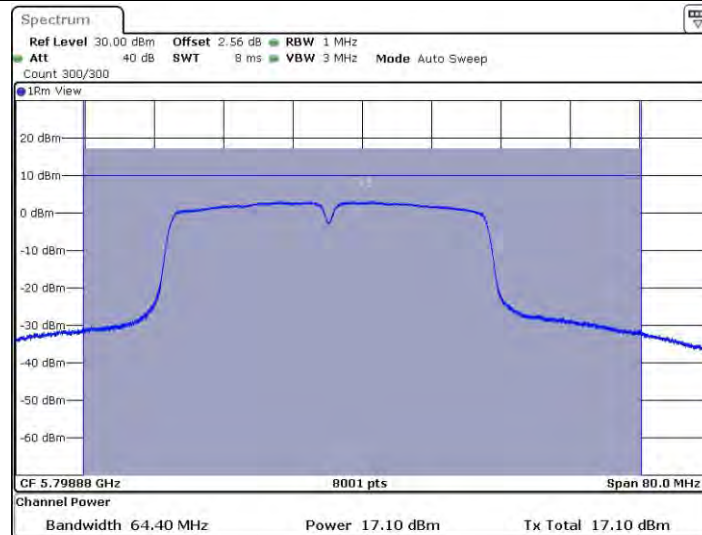
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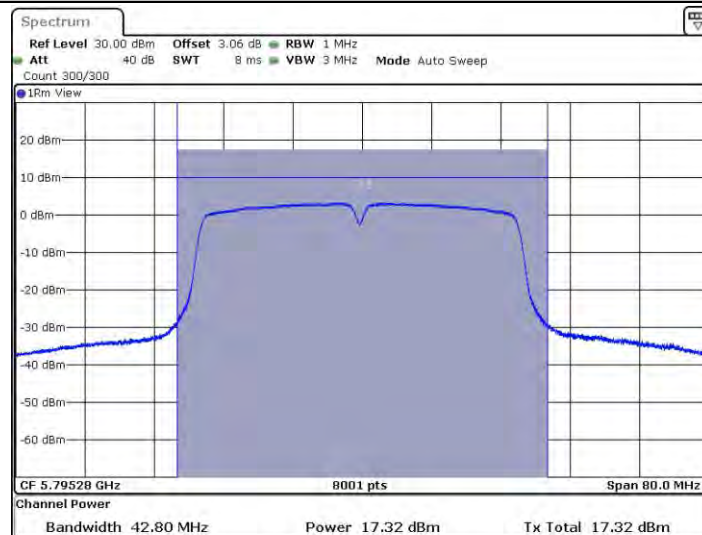
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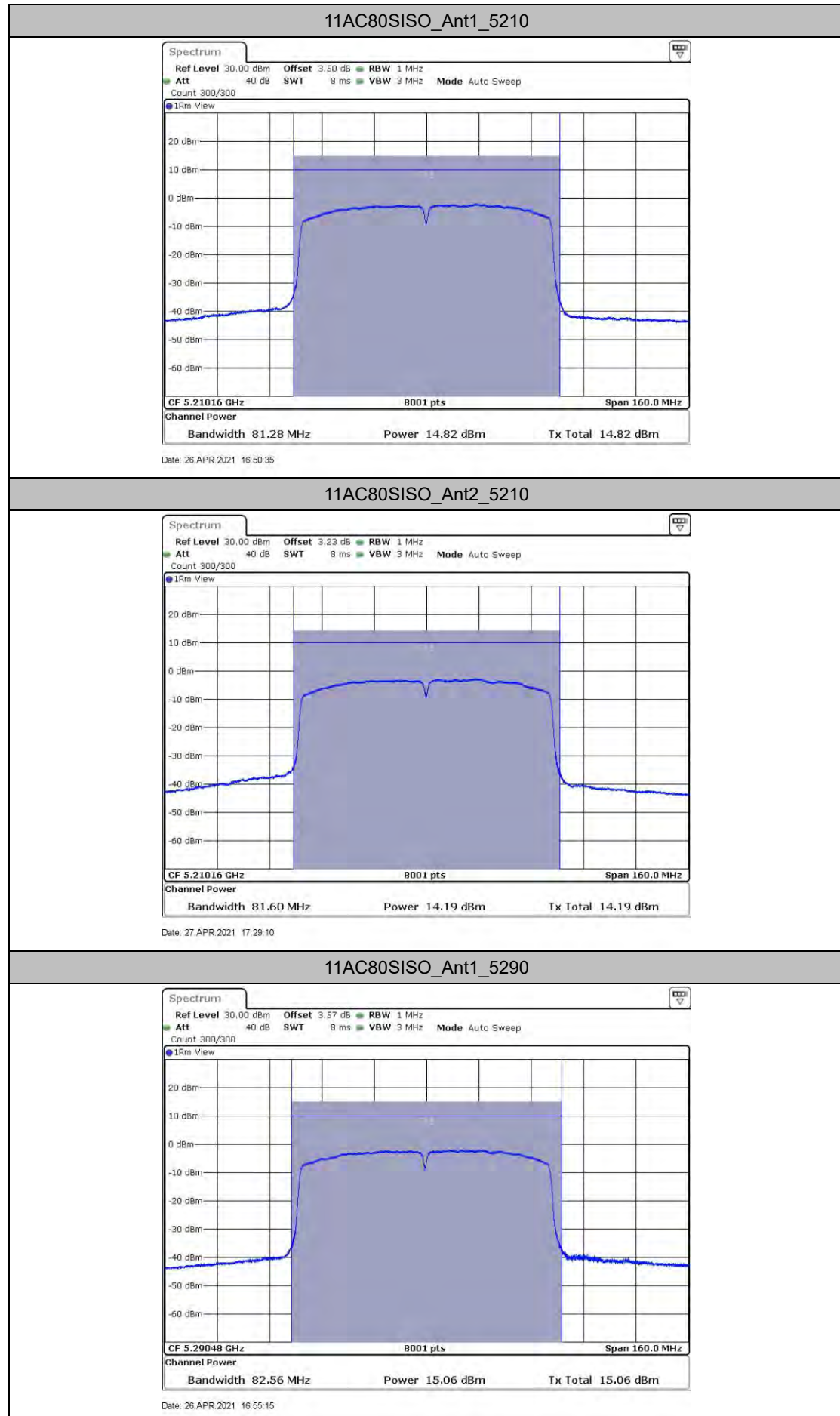


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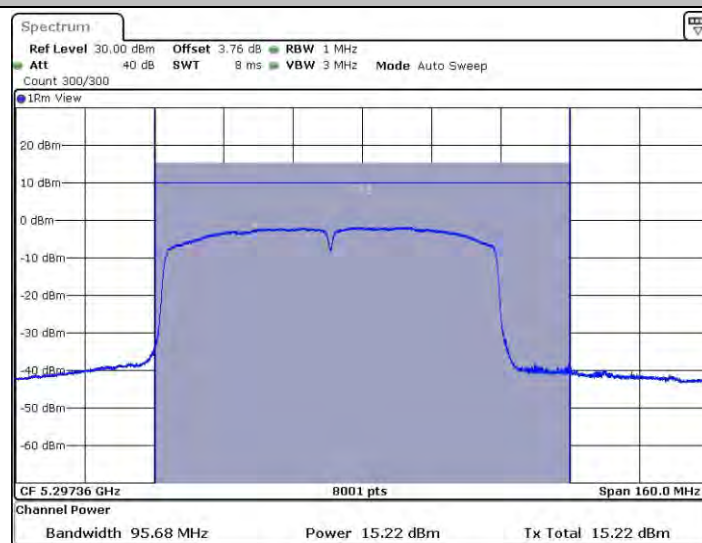
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Date: 27 APR 2021 17:23:59

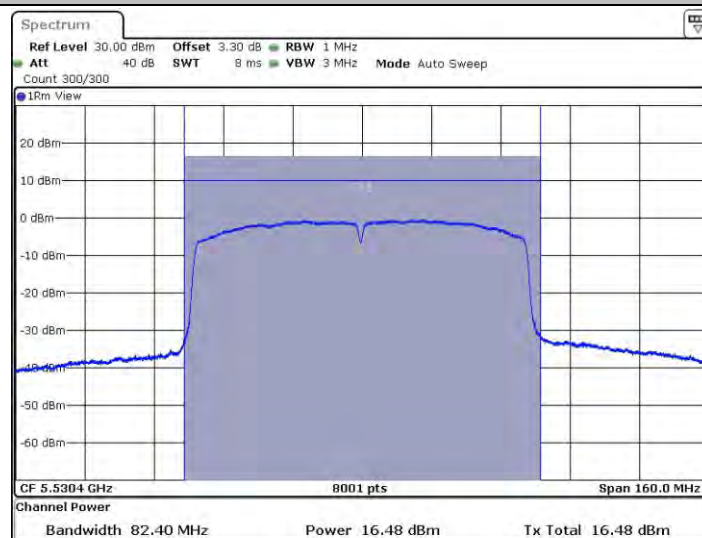


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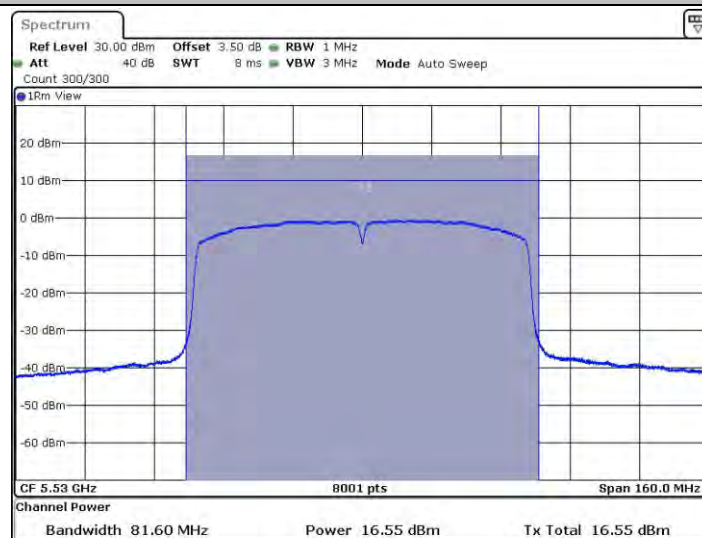
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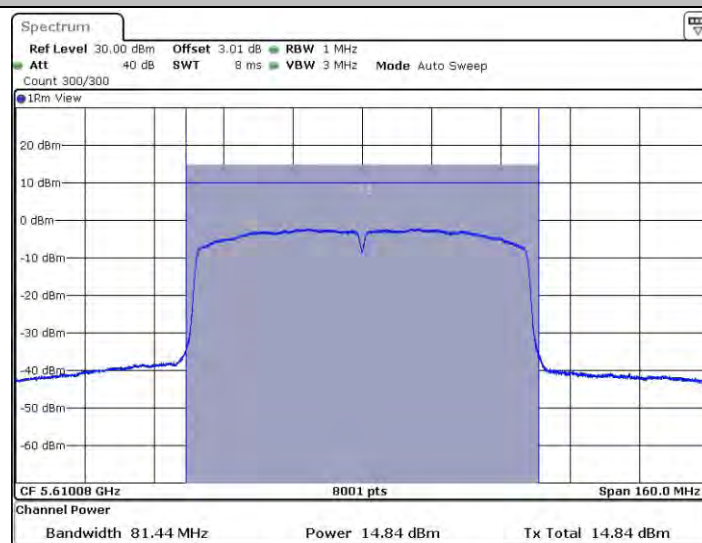
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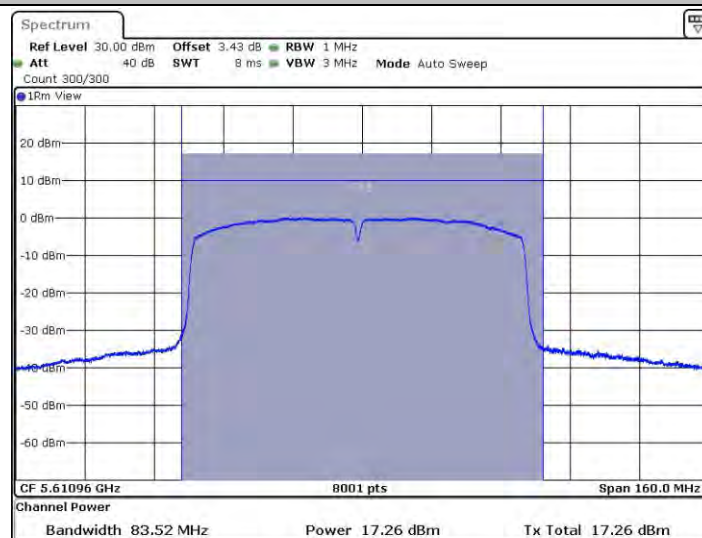
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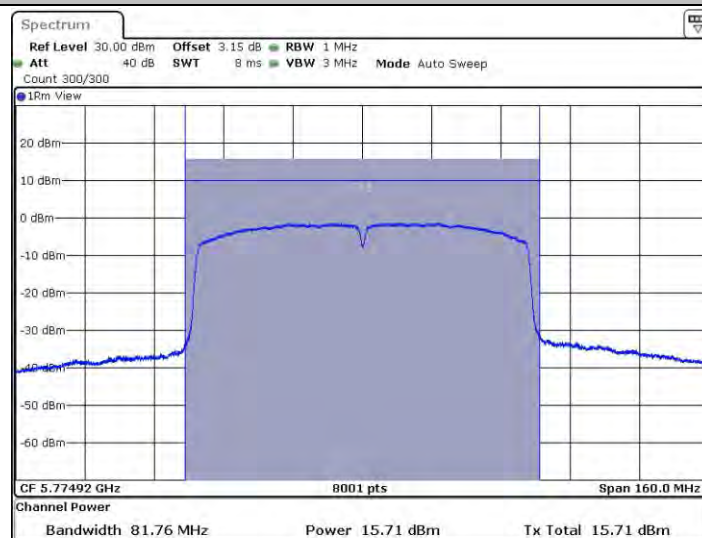
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Date: 27 APR 2021 17:51:17

11AC80SISO_Ant1_5775



Date: 26 APR 2021 17:26:30