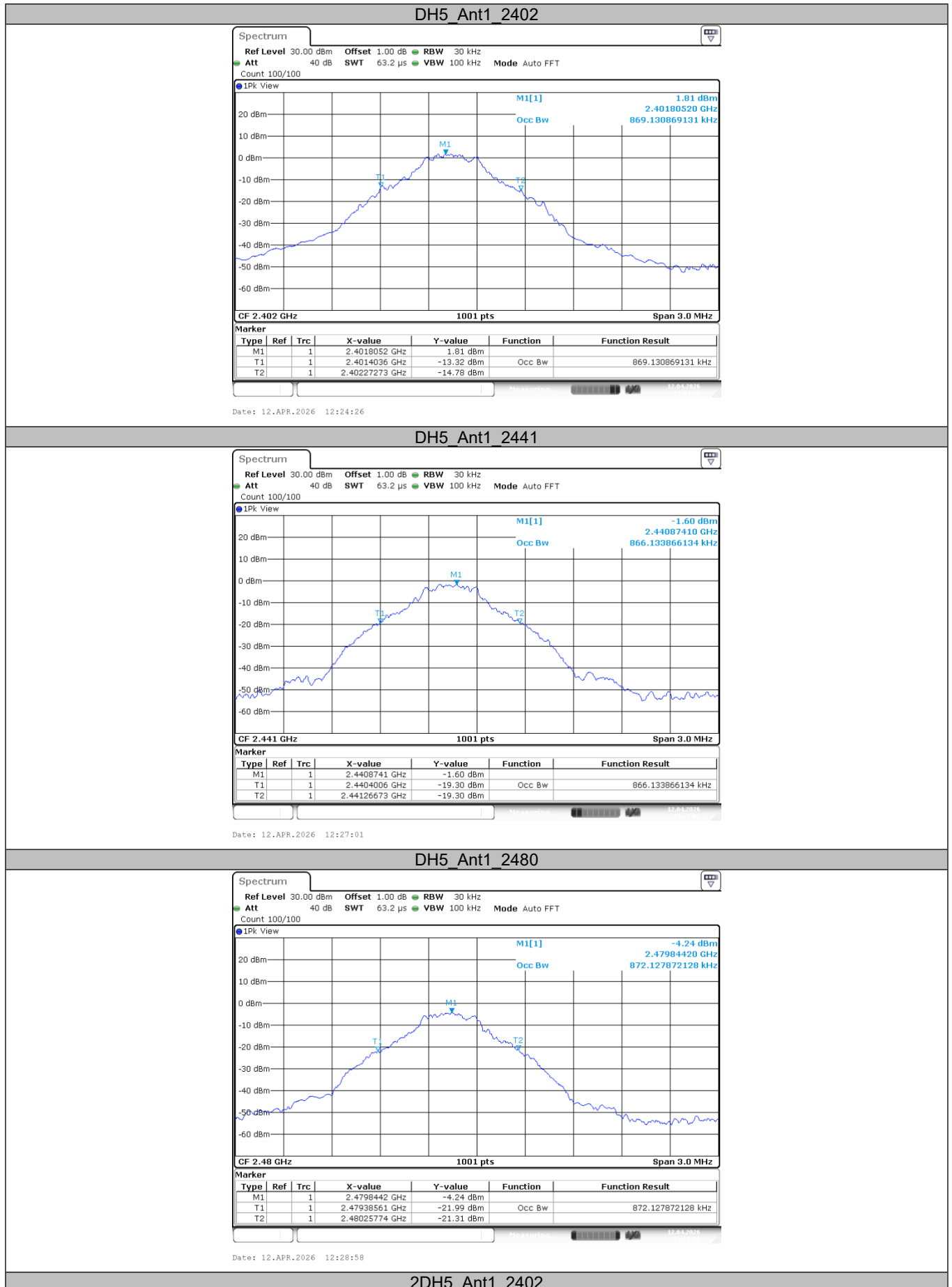


99% Bandwidth:



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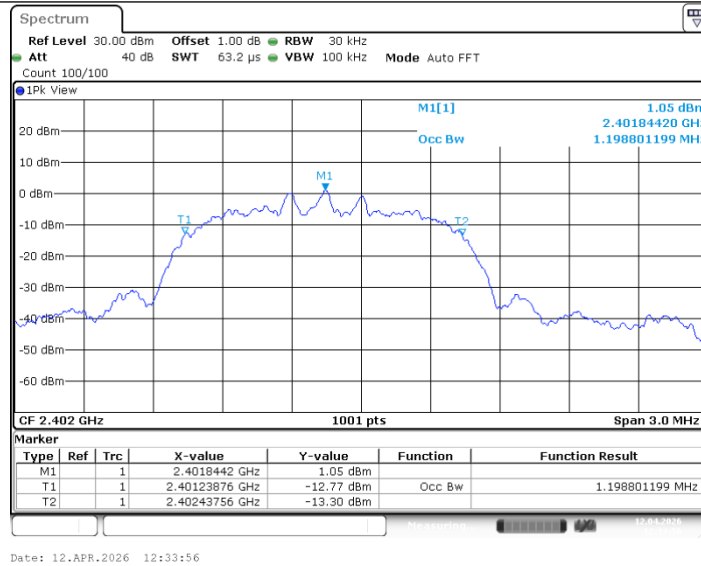
Tel.: (86)755-27521059

Fax: (86)755-27521011

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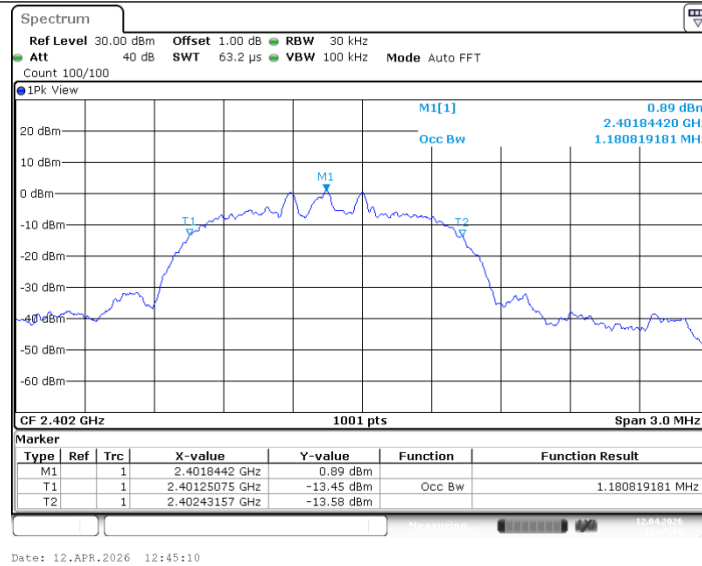
2DH5_Ant1_2441



2DH5_Ant1_2480



3DH5_Ant1_2402



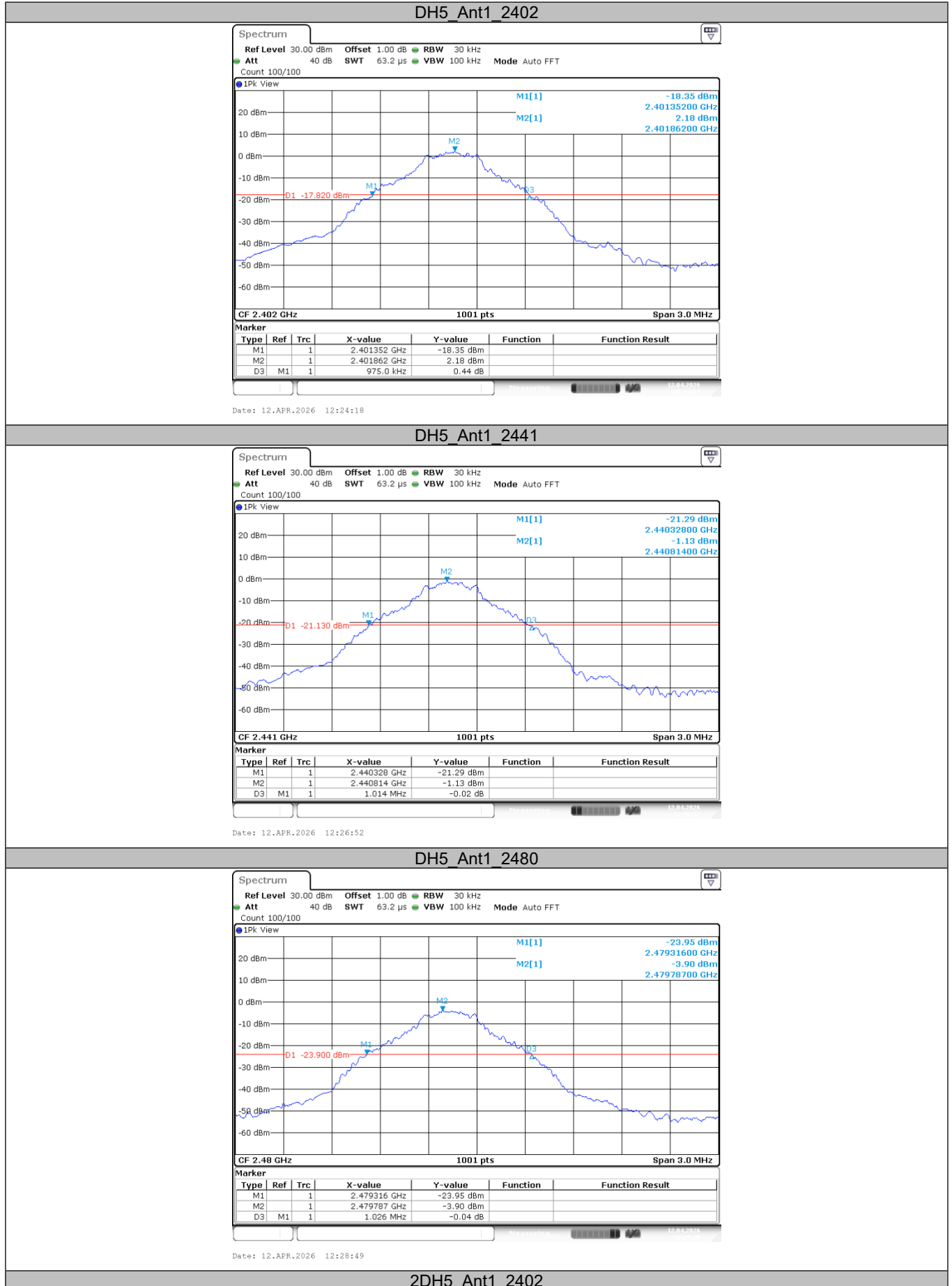
3DH5_Ant1_2441



3DH5_Ant1_2480



20dB Bandwidth:



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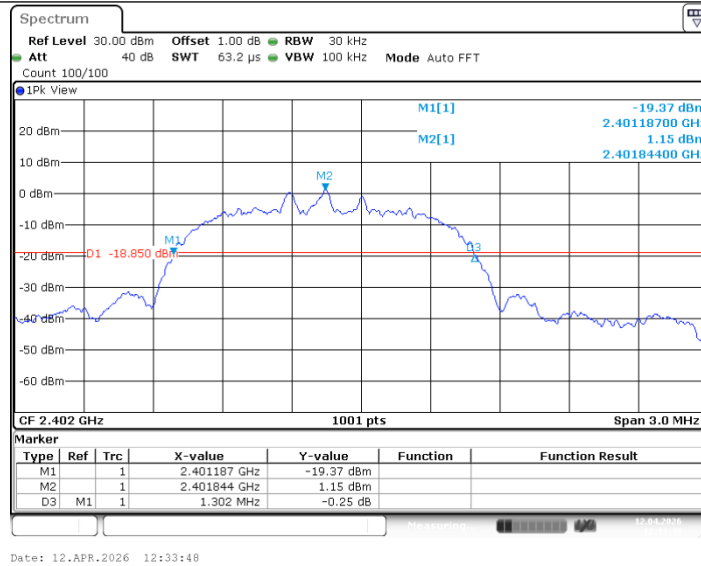
Tel.: (86)755-27521059

Fax: (86)755-27521011

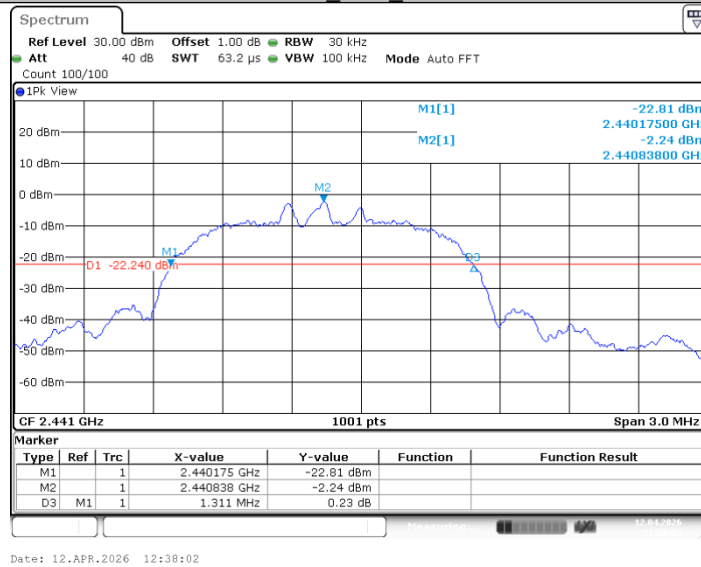
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2DH5_Ant1_2441



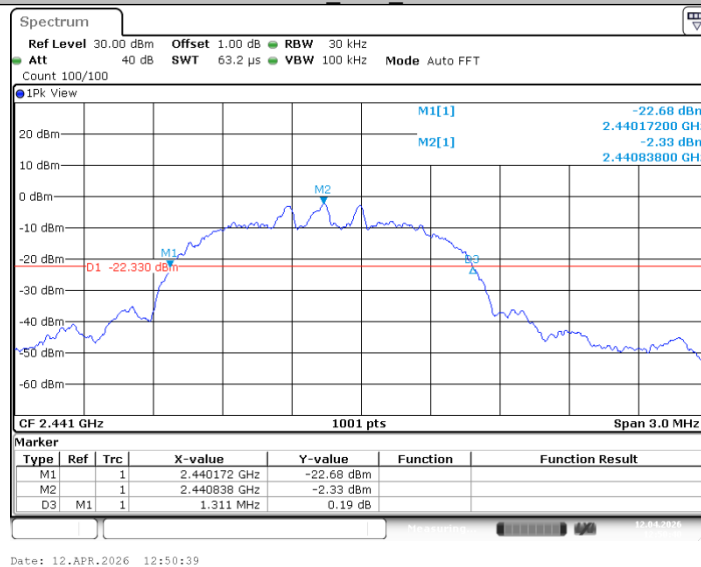
2DH5_Ant1_2480



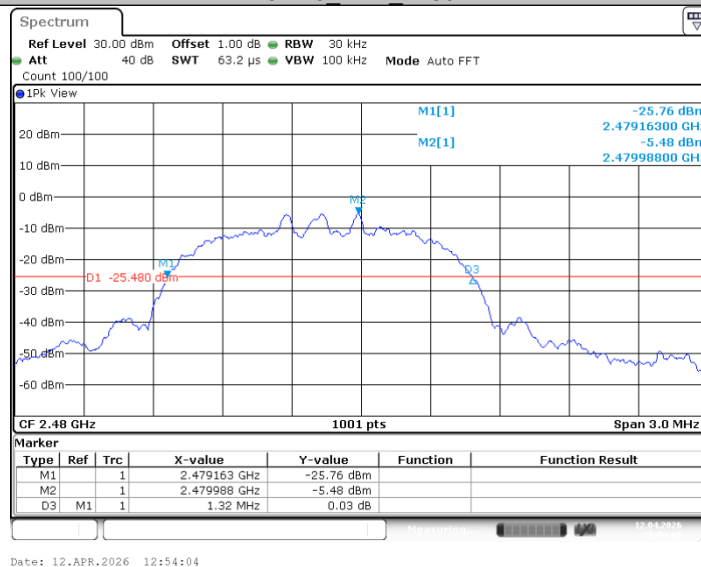
3DH5_Ant1_2402



3DH5_Ant1_2441



3DH5_Ant1_2480



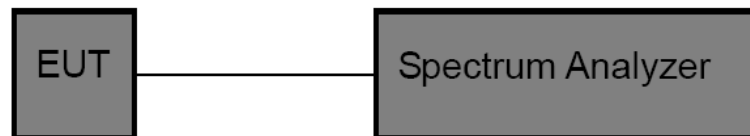
3.5. Channel Separation

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(1)

Test Item	Limit	Frequency Range (MHz)
Channel Separation	>25kHz or >two-thirds of the 20 dB bandwidth Which is greater	2400~2483.5

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
 - (1) Set RBW = 100 kHz.
 - (2) Set the video bandwidth (VBW) ≥ 3 RBW.
 - (3) Detector = Peak.
 - (4) Trace mode = Max hold.
 - (5) Sweep = Auto couple.

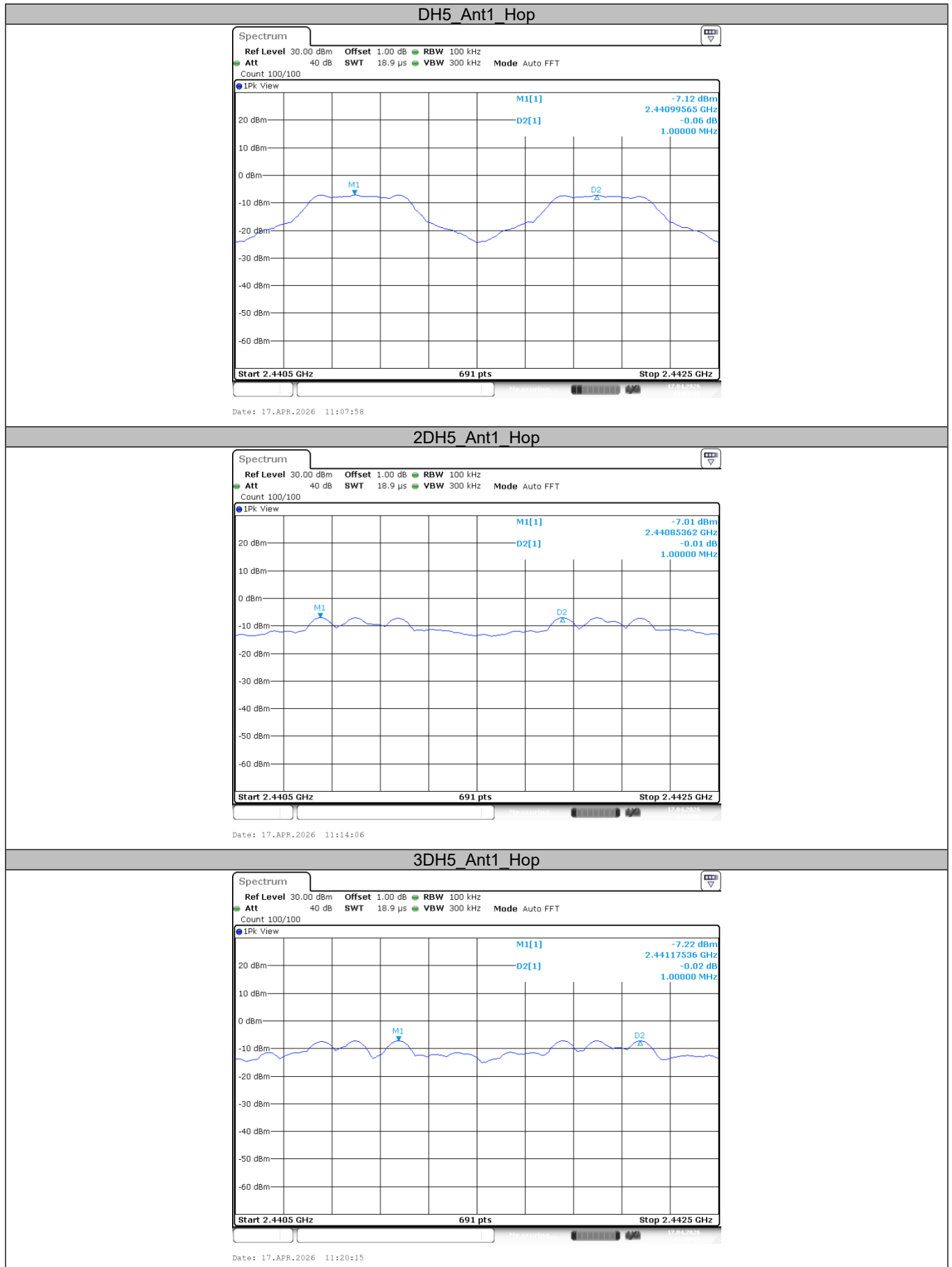
Test Mode

Please refer to the clause 2.4.

Test Result

TestMode	Frequency (MHz)	Carrier Frequencies Separation (MHz)	Limit [MHz]	Verdict
GFSK	Hop_2441	1	≥ 0.687	PASS
$\pi/4$ -DQPSK	Hop_2441	1	≥ 0.880	PASS
8-DPSK	Hop_2441	1	≥ 0.880	PASS

Test plot as follows:



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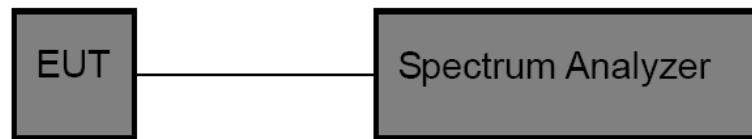
3.6. Number of Hopping Channel

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(iii)

Section	Test Item	Limit
15.247 (a)(iii)	Number of Hopping Channel	≥15

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
 - (1) Peak Detector: RBW=100 kHz, VBW≥RBW, Sweep time= Auto.

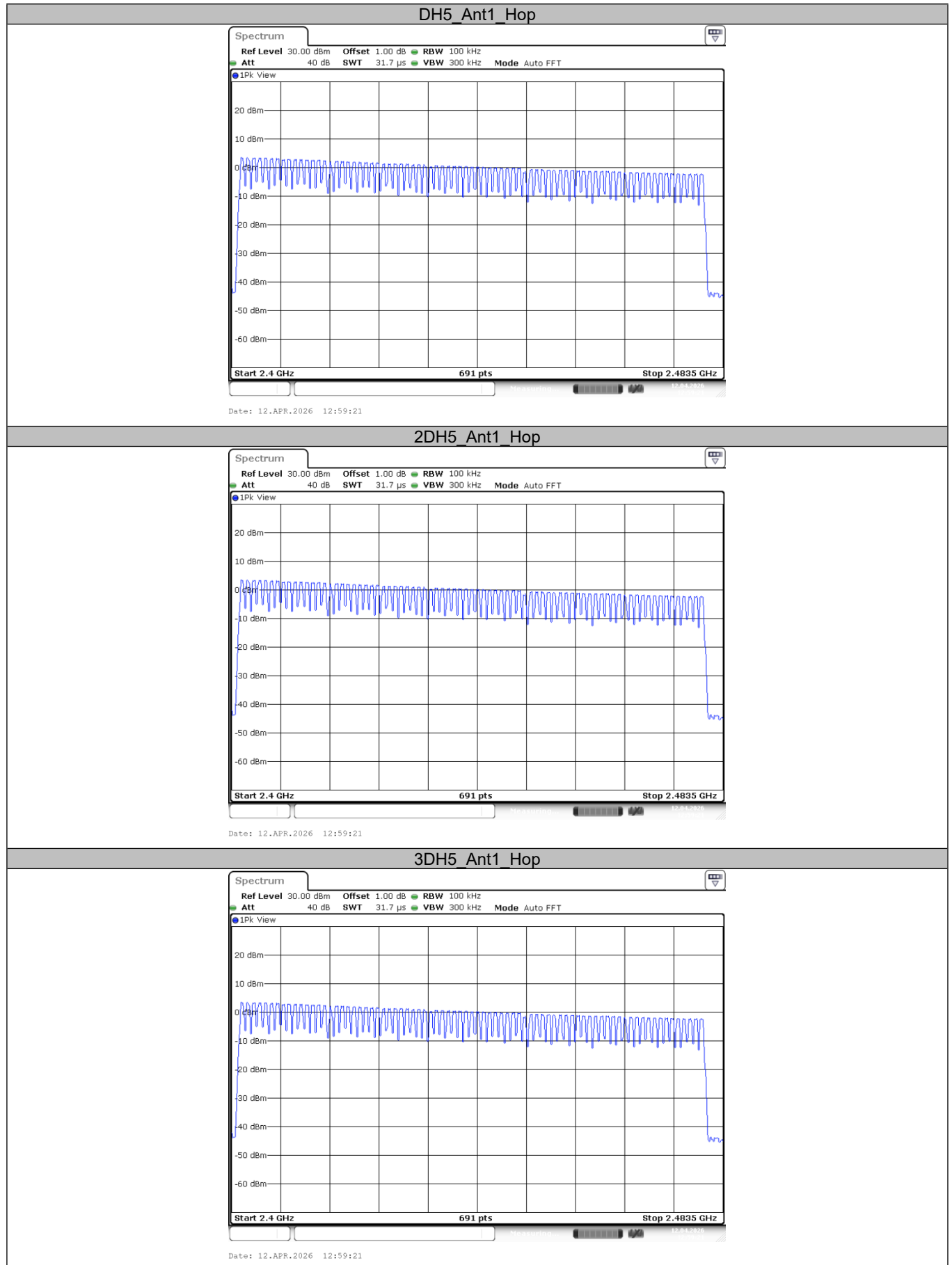
Test Mode

Please refer to the clause 2.4.

Test Result

Test Mode	Channel Number	Limit	Verdict
DH5	79	≥15	Pass
2DH5	79	≥15	Pass
3DH5	79	≥15	Pass

Test plot as follows:



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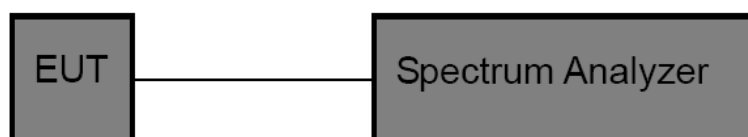
3.7. Dwell Time

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(iii)

Section	Test Item	Limit
15.247 (a)(iii)	Average Time of Occupancy	0.4 sec

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
 - (1) Spectrum Setting: RBW=1MHz, VBW≥RBW.
 - (2) Use video trigger with the trigger level set to enable triggering only on full pulses.
 - (3) Sweep Time is more than once pulse time.
 - (4) Set the center frequency on any frequency would be measure and set the frequency span to zero.
 - (5) Measure the maximum time duration of one single pulse.
 - (6) Set the EUT for packet transmitting.

Test Mode

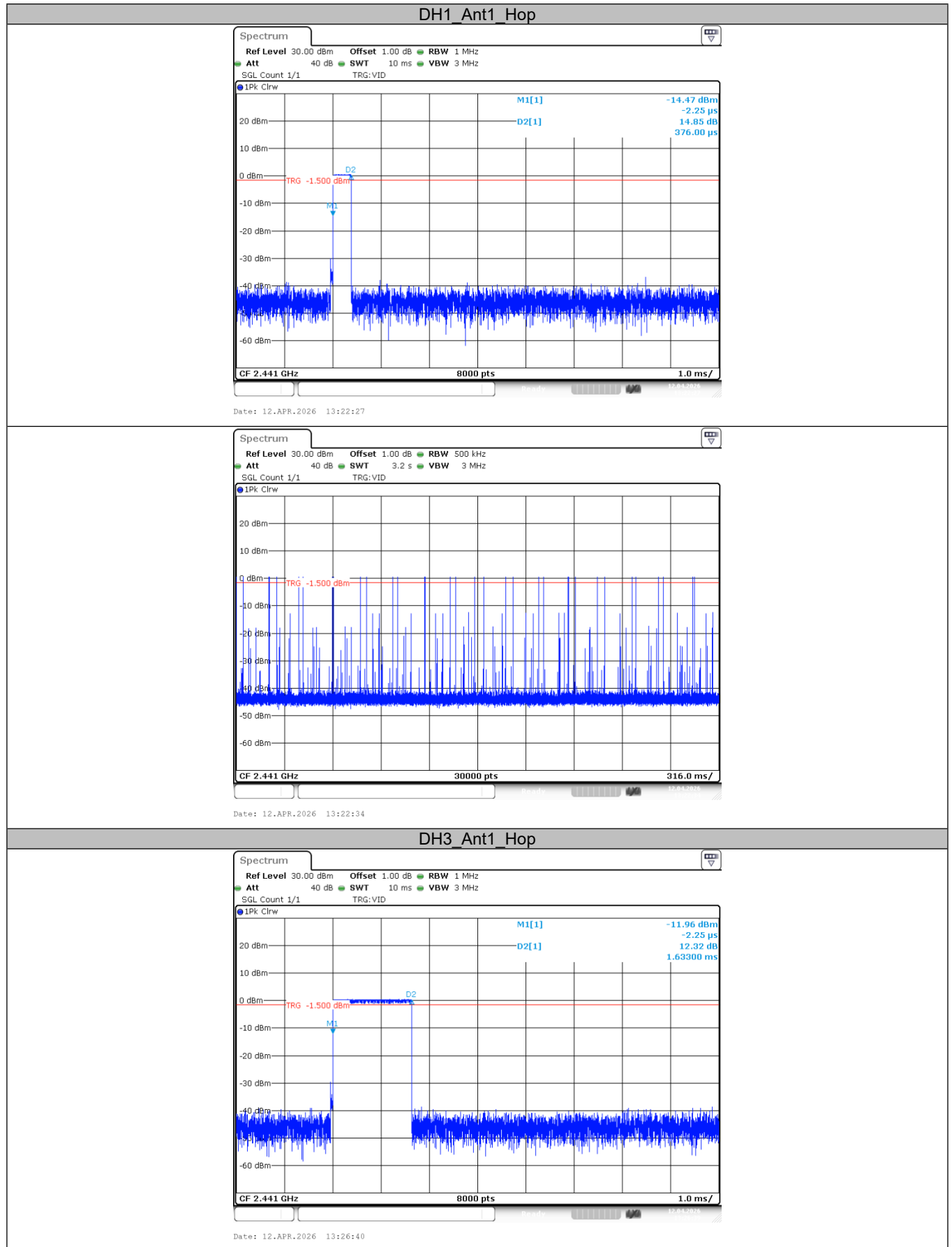
Please refer to the clause 2.4.

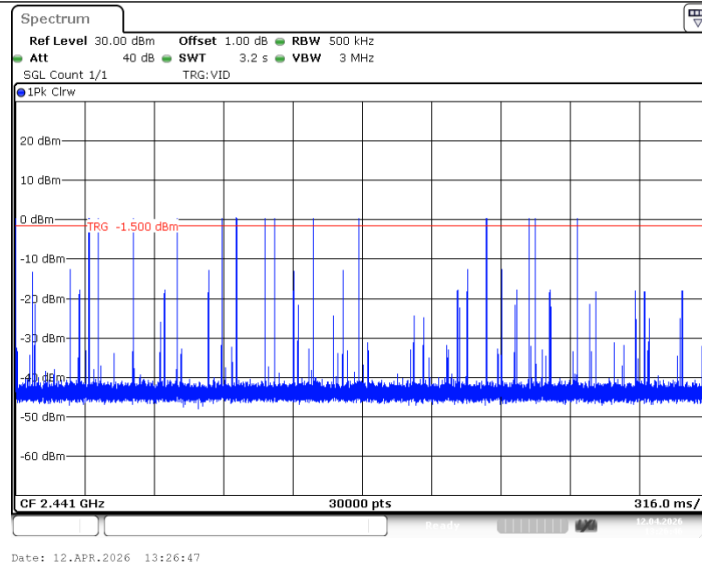
Test Result

Test Mode	Channel	Frequency (MHz)	Pulse Time (ms)	Total of Dwell (ms)	Period Time (ms)	Limit (second)	Verdict
GFSK	DH1	2441	0.376	0.120	31.60	≤0.40	Pass
	DH3	2441	1.633	0.245	31.60		
	DH5	2441	2.873	0.259	31.60		
π/4-DQPSK	2DH1	2441	0.384	0.123	31.60	≤0.40	Pass
	2DH3	2441	1.628	0.244	31.60		
	2DH5	2441	2.869	0.258	31.60		
8-DPSK	3DH1	2441	0.383	0.123	31.60	≤0.40	Pass
	3DH3	2441	1.625	0.244	31.60		
	3DH5	2441	2.869	0.316	31.60		

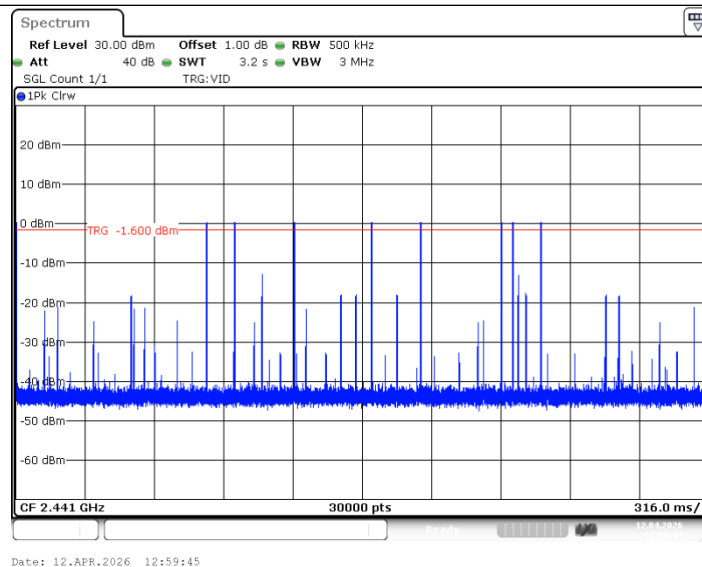
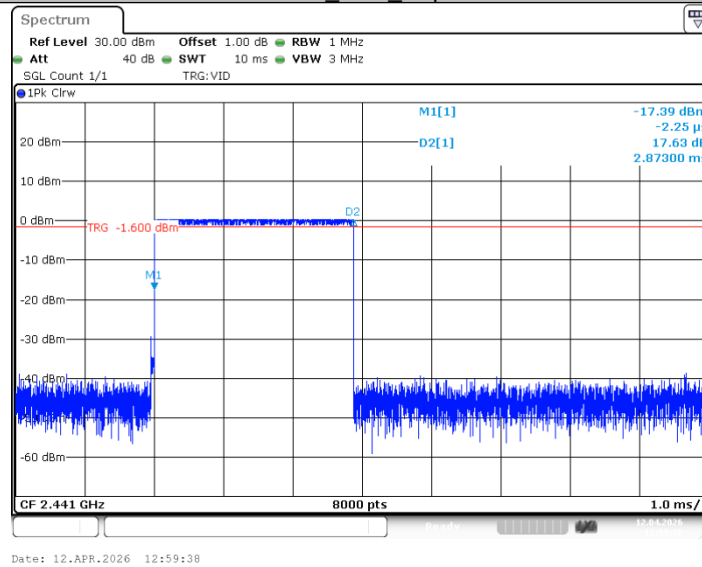
Note: 1DH1/2DH1 Total of Dwell = Pulse Time(1600/2)*31.6/79*
1DH3/2DH3 Total of Dwell = Pulse Time(1600/4)*31.6/79*
1DH5/2DH5 Total of Dwell = Pulse Time(1600/6)*31.6/79*

Test plot as follows:

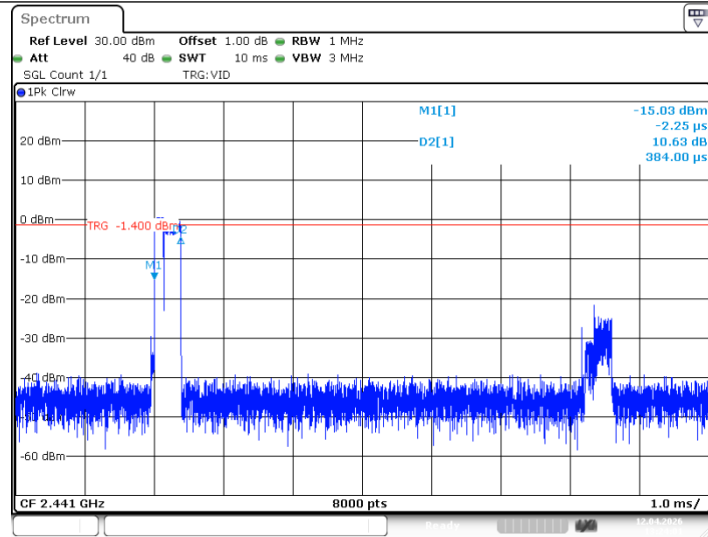




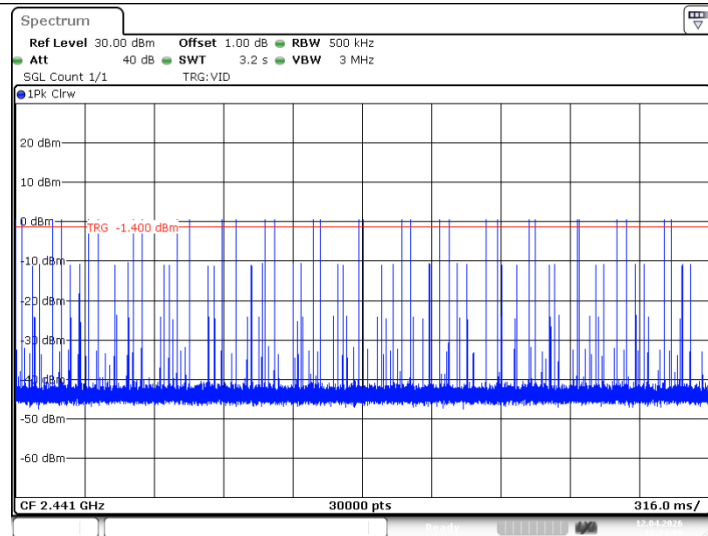
DH5_Ant1_Hop



2DH1_Ant1_Hop

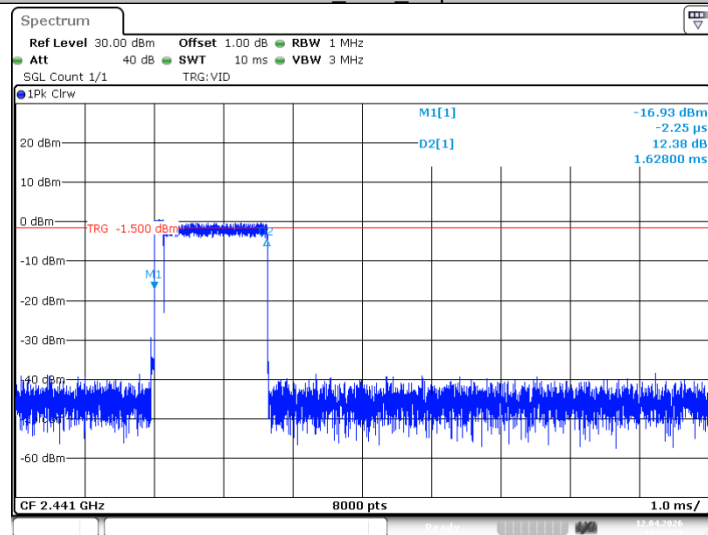


Date: 12.APR.2026 13:24:01

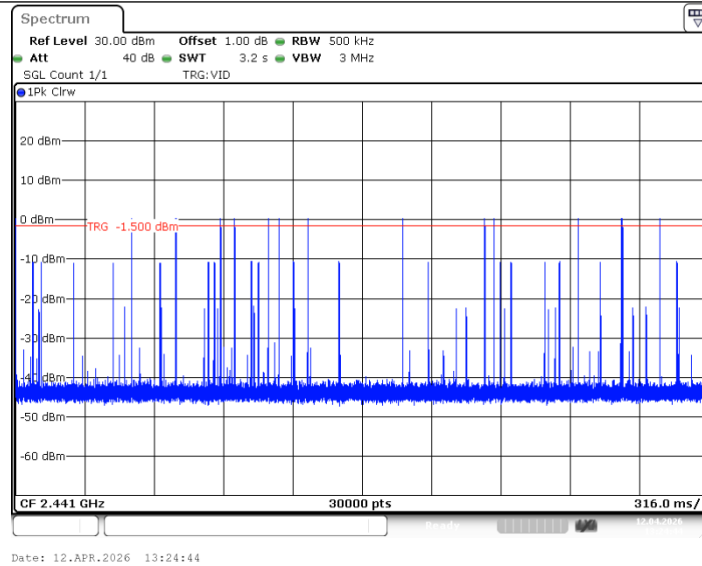


Date: 12.APR.2026 13:24:08

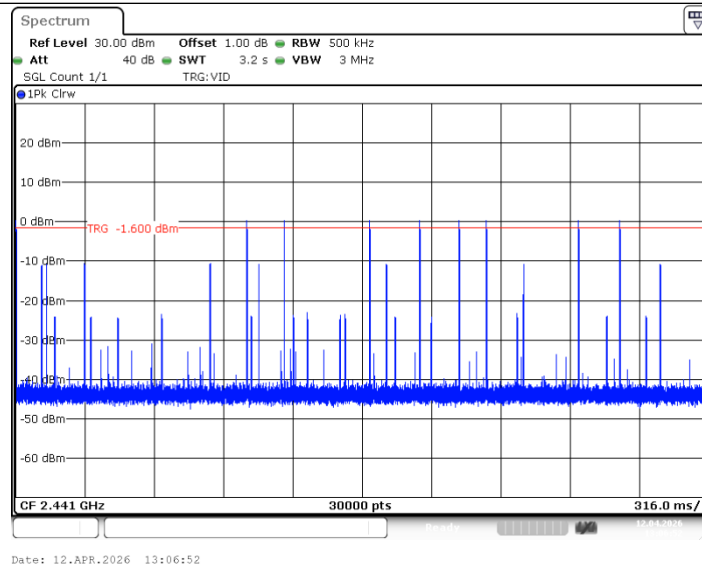
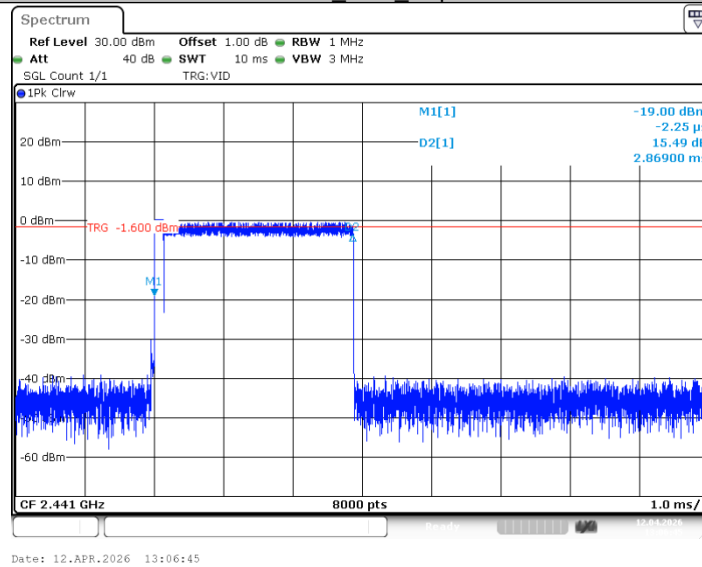
2DH3_Ant1_Hop



Date: 12.APR.2026 13:24:37



2DH5_Ant1_Hop



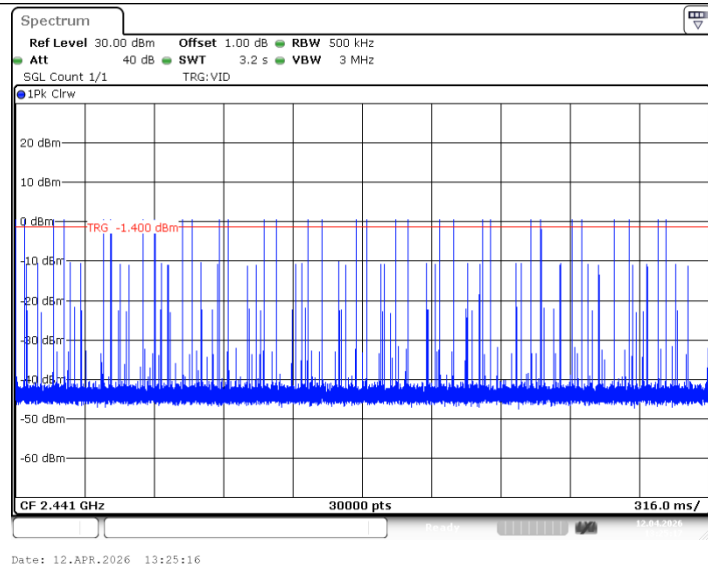
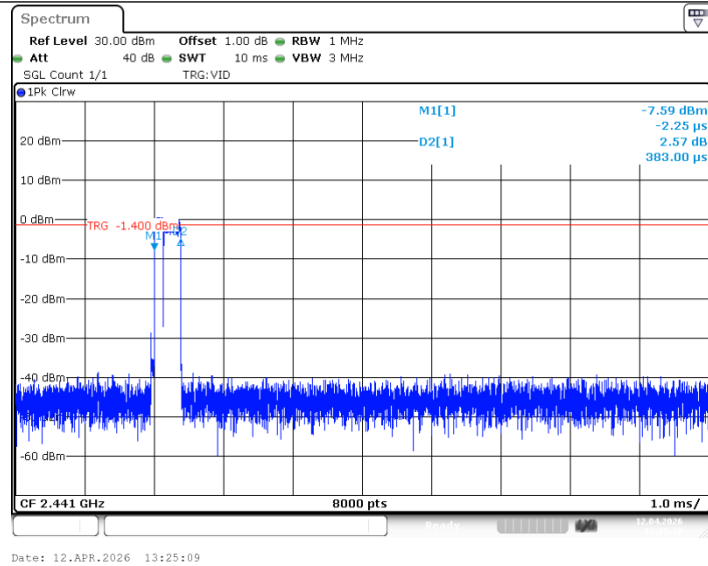
3DH1_Ant1_Hop

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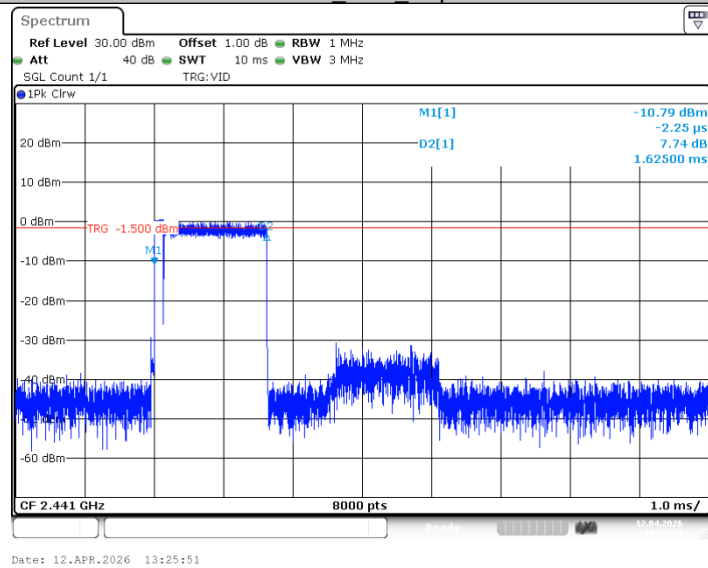
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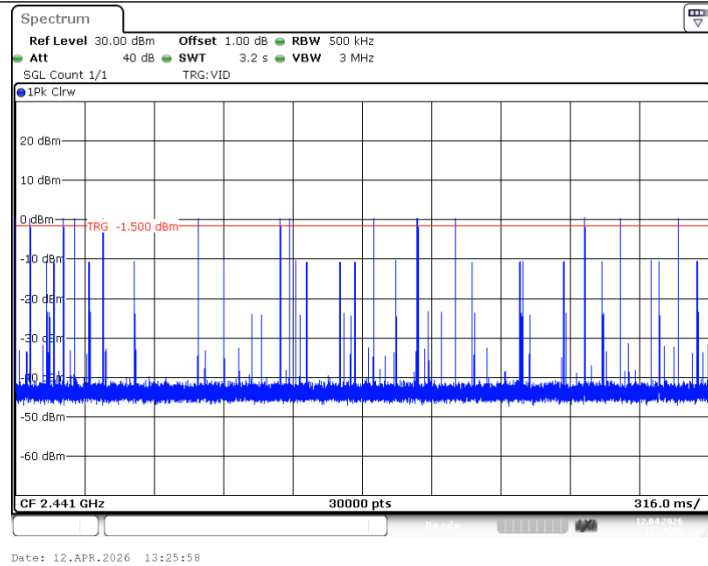
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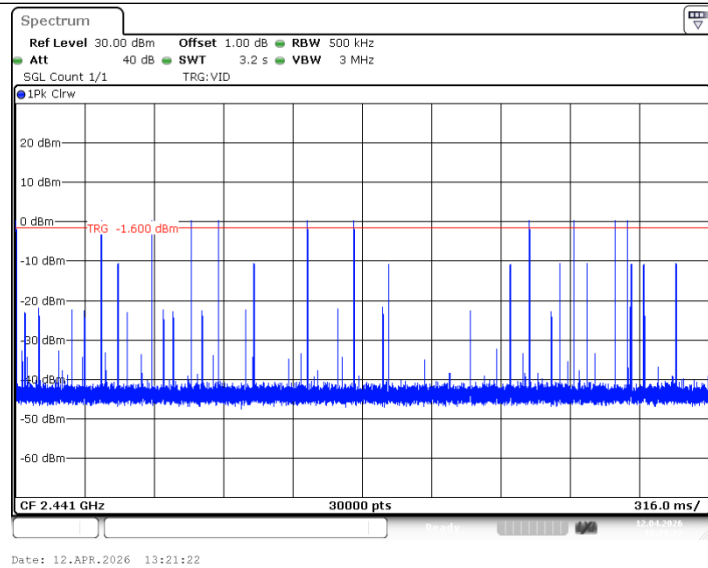
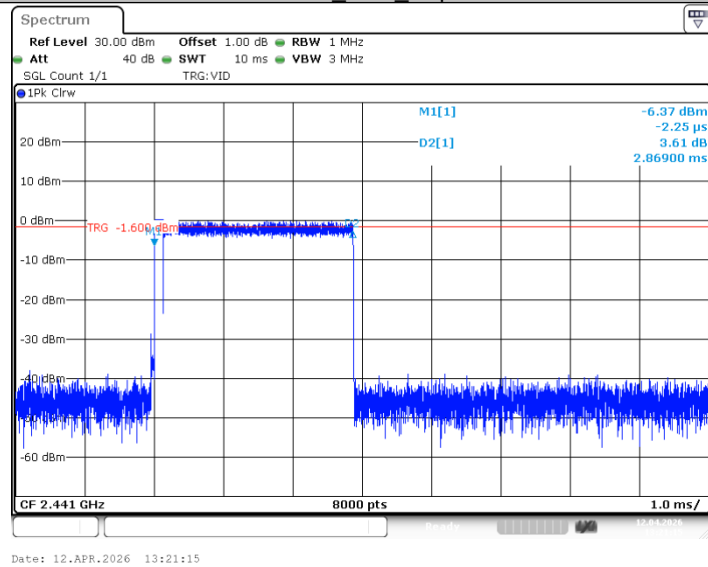


3DH3_Ant1_Hop





3DH5_Ant1_Hop



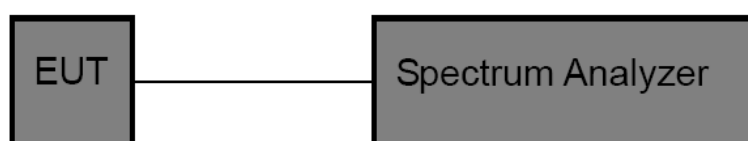
3.8. Peak Output Power

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (b)(1)

Section	Test Item	Limit	Frequency Range (MHz)
FCC CFR 47 Part15.247 (b)(1)	Maximum Conducted Output Power	Hopping Channels $\geq$ 75, Power <1W(30dBm); Others <125mW(21dBm)	2400~2483.5

Test Configuration



Test Procedure

- The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
- Spectrum Setting:
 - Set RBW > 20dB Bandwidth.
 - Set VBW $\geq$ RBW.
 - Detector = Peak.
 - Trace mode = Max hold.
 - Sweep = Auto couple.
 - Span = Approximately five times the 20dB bandwidth, centered on a hopping channel.

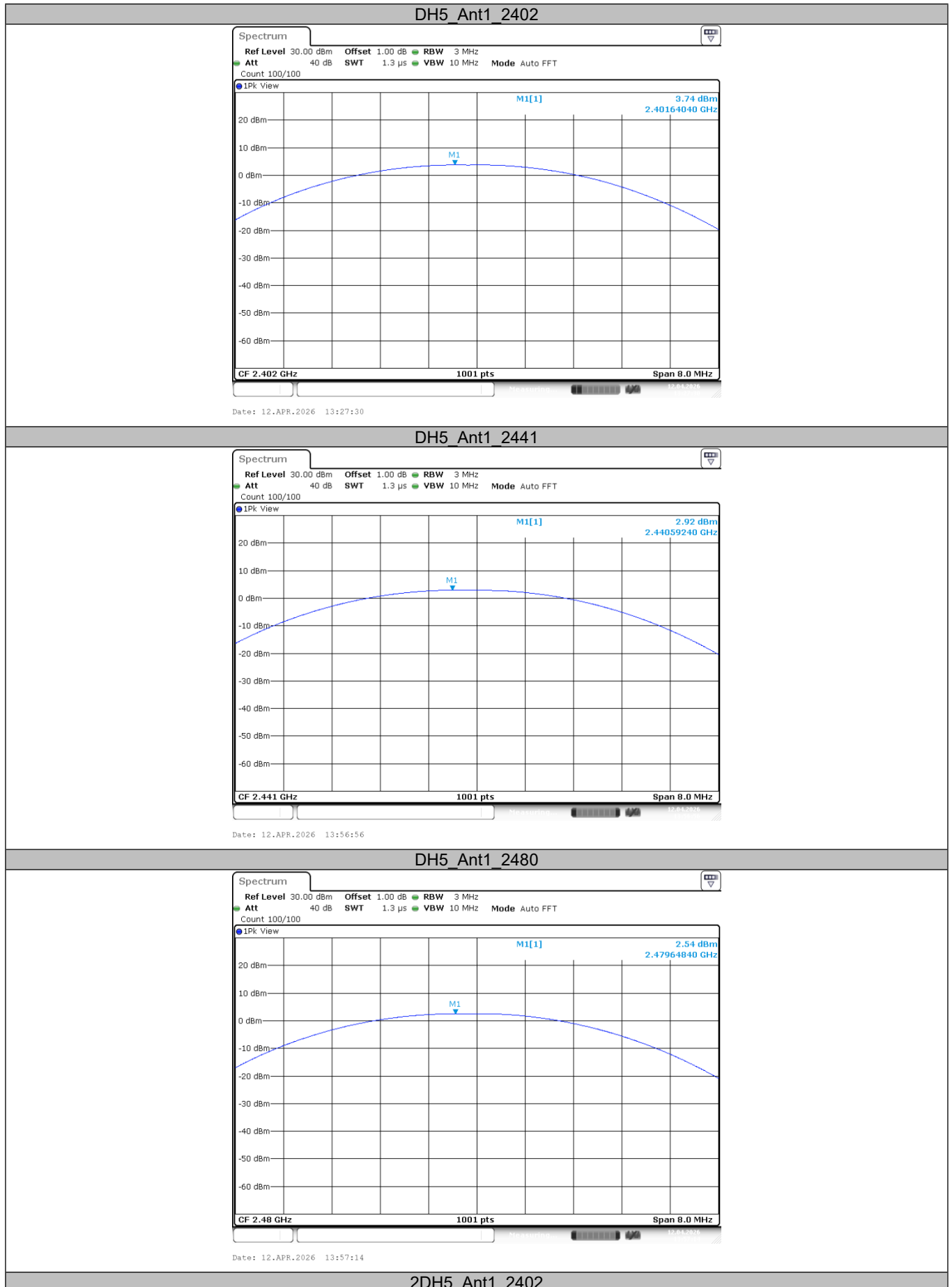
Test Mode

Please refer to the clause 2.4.

Test Result

Test Mode	Frequency (MHz)	Conducted Output Power (dBm)	FCC Limit (dBm)	Verdict
GFSK	2402	3.74	$\leq$ 30	Pass
	2441	2.92	$\leq$ 30	Pass
	2480	2.54	$\leq$ 30	Pass
$\pi/4$ -DQPSK	2402	3.69	$\leq$ 30	Pass
	2441	2.87	$\leq$ 30	Pass
	2480	2.53	$\leq$ 30	Pass
8-DPSK	2402	3.99	$\leq$ 30	Pass
	2441	3.01	$\leq$ 30	Pass
	2480	2.77	$\leq$ 30	Pass

Test plot as follows:

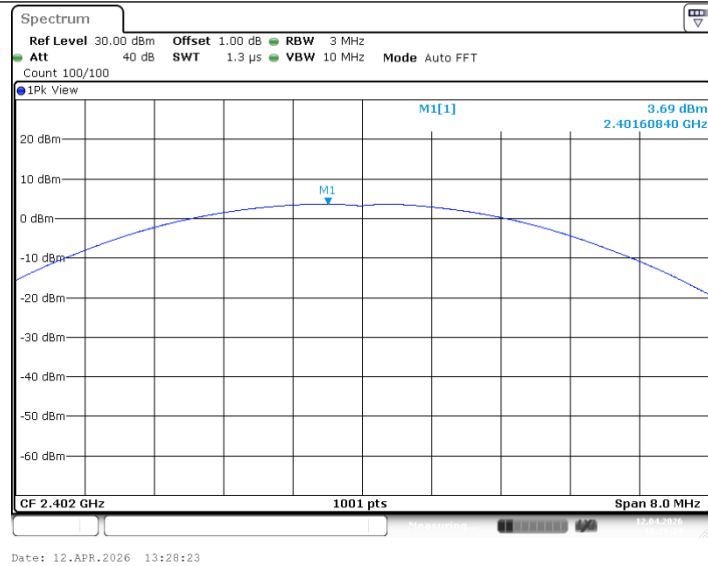


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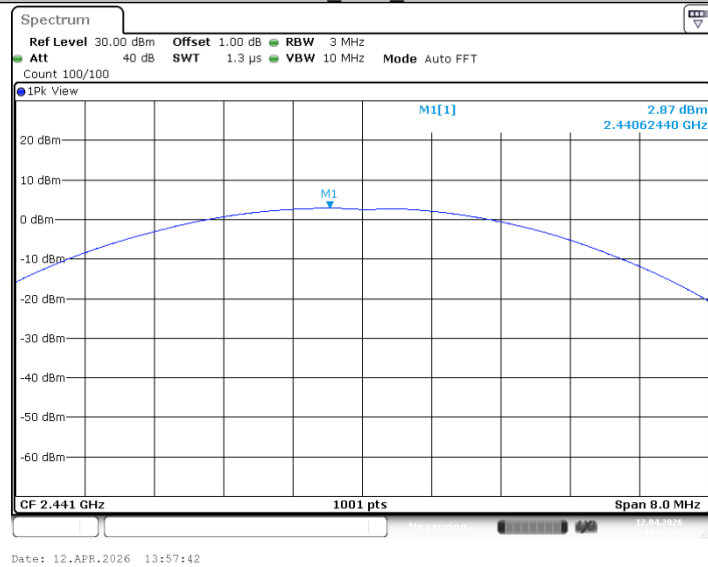
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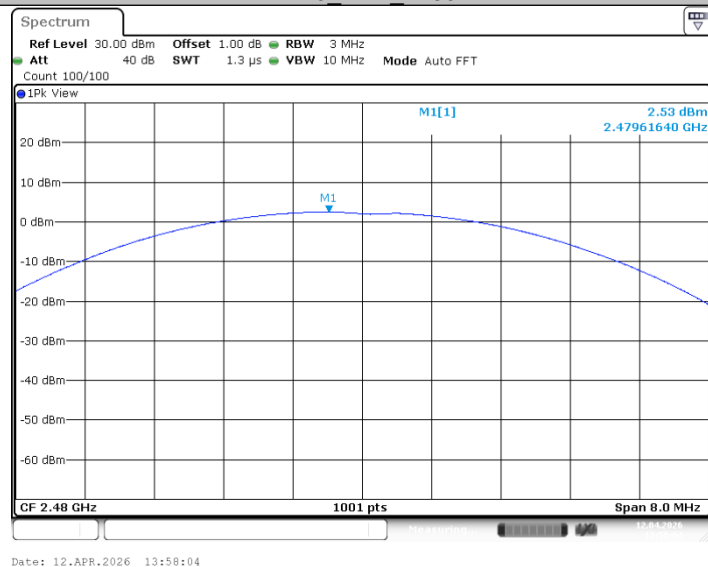
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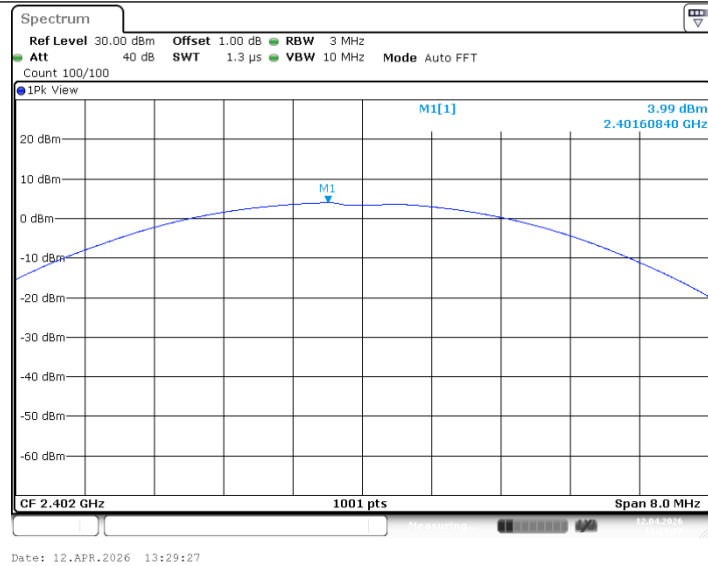
2DH5_Ant1_2441



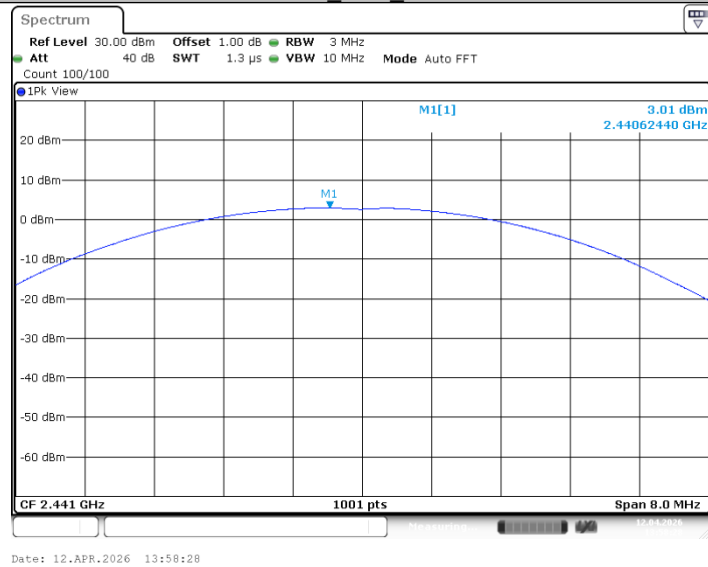
2DH5_Ant1_2480



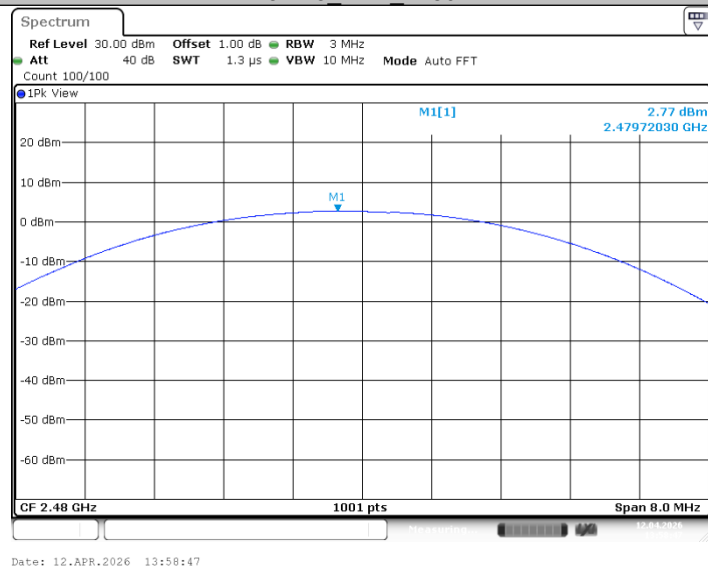
3DH5_Ant1_2402



3DH5_Ant1_2441



3DH5_Ant1_2480

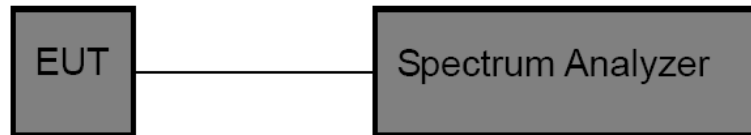


3.9. Duty Cycle

Limit

None, for report purposes only.

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. The EUT was directly connected to the Spectrum Analyzer and antenna output port as show in the block diagram above. The measurement according to section 10.2 of KDB 558074 D01 DTS Meas Guidance v05r02.
3. Spectrum Setting:
Set analyzer center frequency to test channel center frequency.
Set the span to 0Hz.
Set the RBW to 10MHz.
Set the VBW to 10MHz.
Detector: Peak.
Sweep time: Auto.
Allow trace to fully stabilize. Then use the peak marker function to determine the maximum amplitude level.

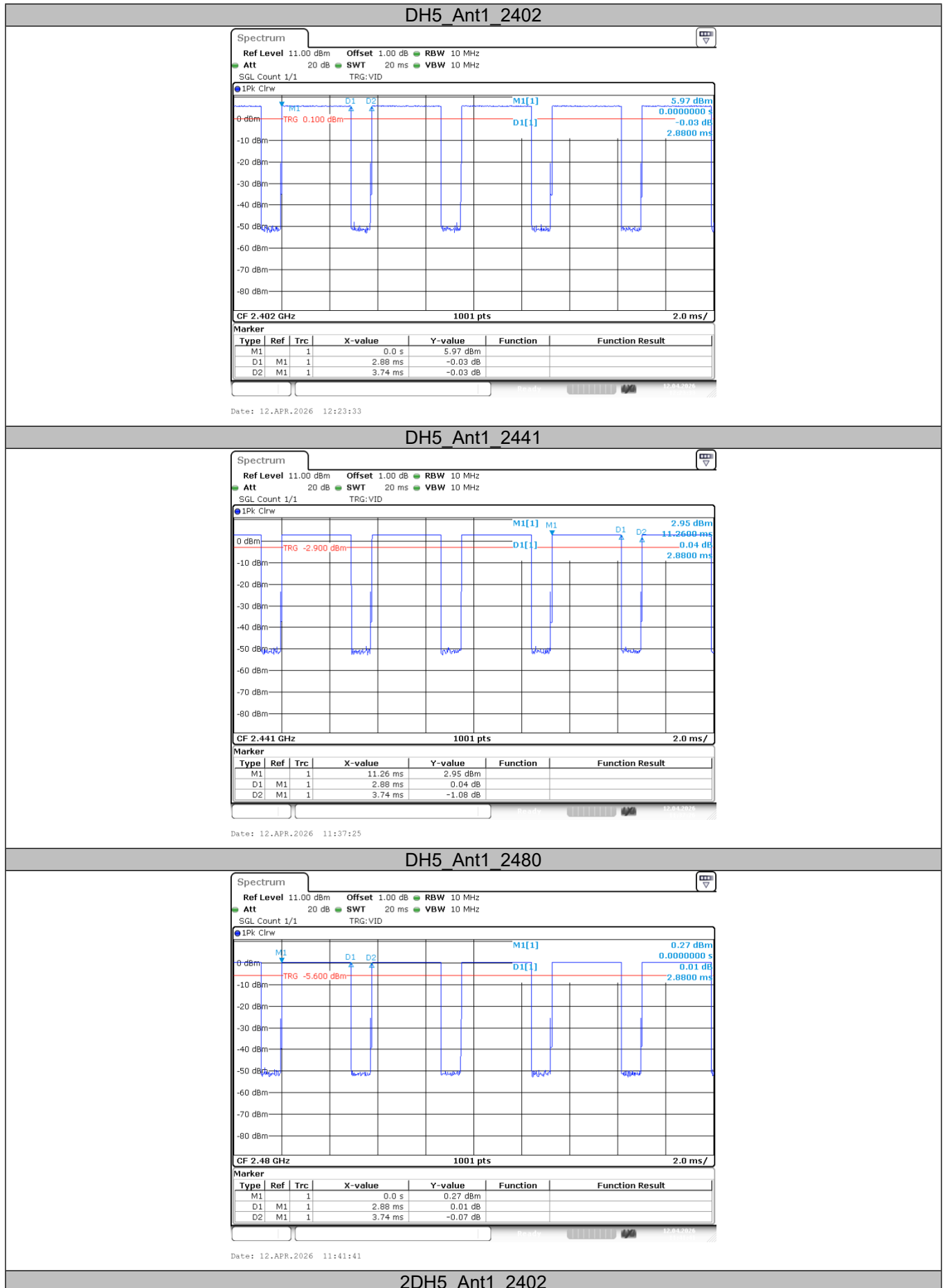
Test Mode

Please refer to the clause 2.4.

Test Result

Test Mode	Frequency (MHz)	Transmission Duration (ms)	Transmission Period (ms)	Duty Cycle (%)	1/T Minimum VBW (kHz)	Final Setting for VBW (kHz)
GFSK	2402	2.88	3.74	77.01	0.35	1
	2441	2.88	3.74	77.01	0.35	1
	2480	2.88	3.74	77.01	0.35	1
$\pi/4$ -DQPSK	2402	2.88	3.74	77.01	0.35	1
	2441	2.88	3.74	77.01	0.35	1
	2480	2.88	3.74	77.01	0.35	1
8-DPSK	2402	2.88	3.74	77.01	0.35	1
	2441	2.88	3.74	77.01	0.35	1
	2480	2.88	3.76	76.60	0.35	1

Test plot as follows:

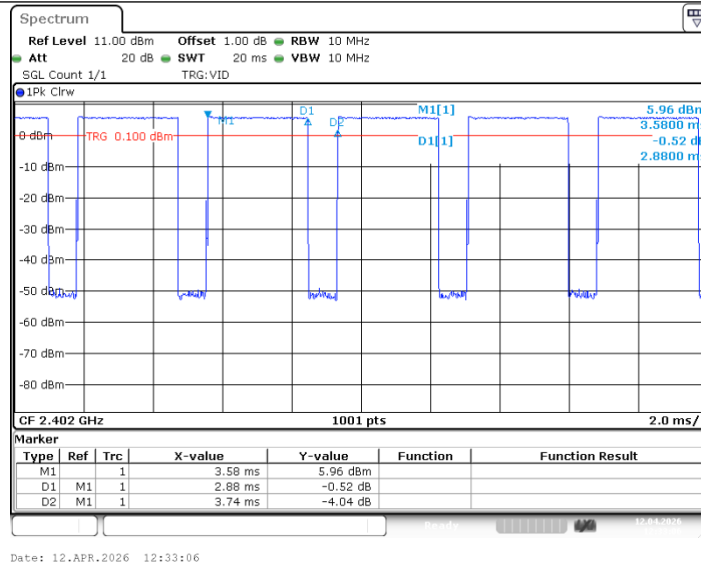


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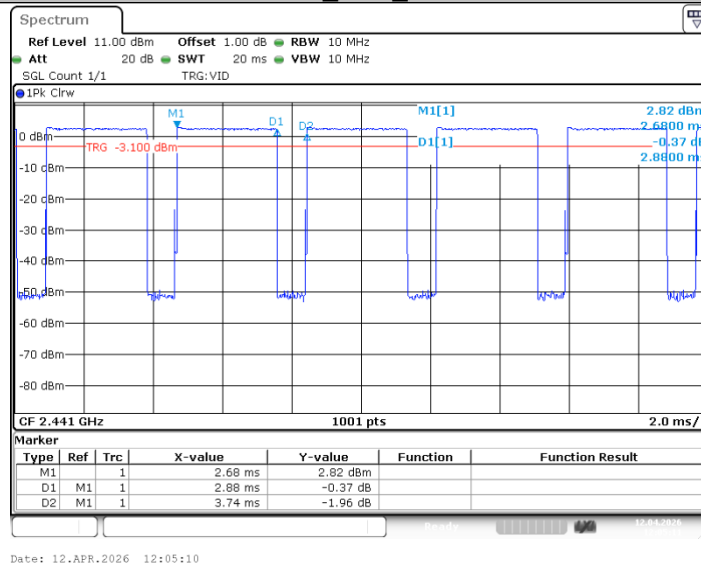
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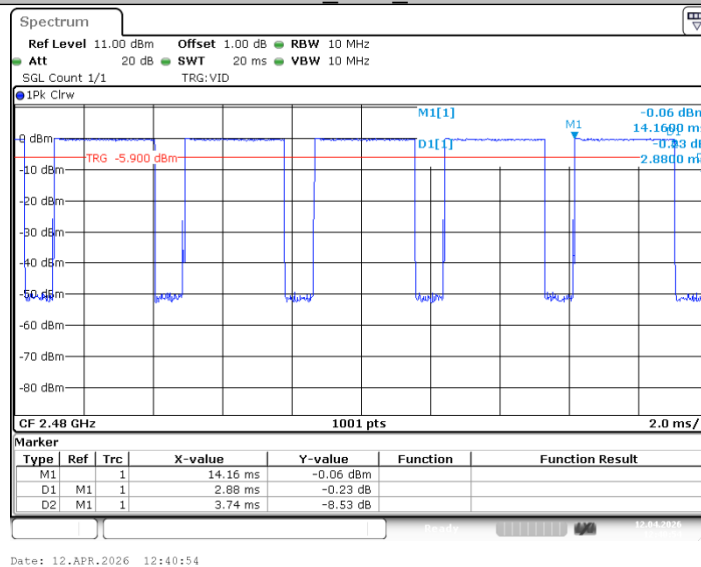
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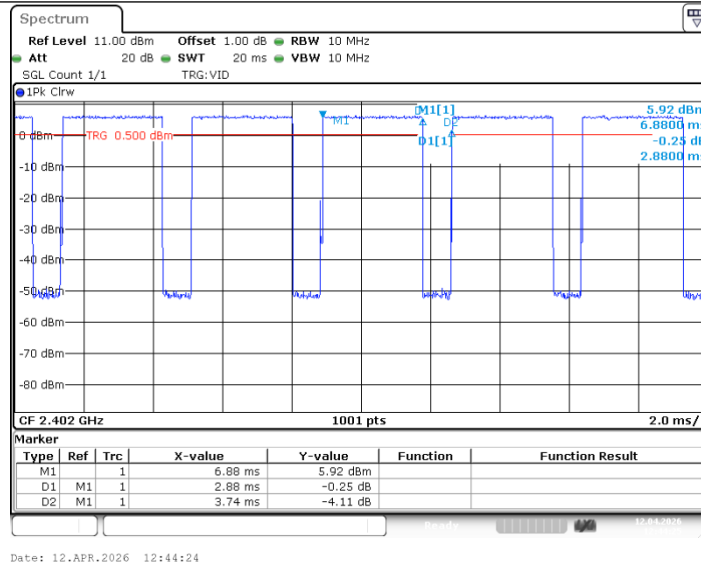
2DH5_Ant1_2441



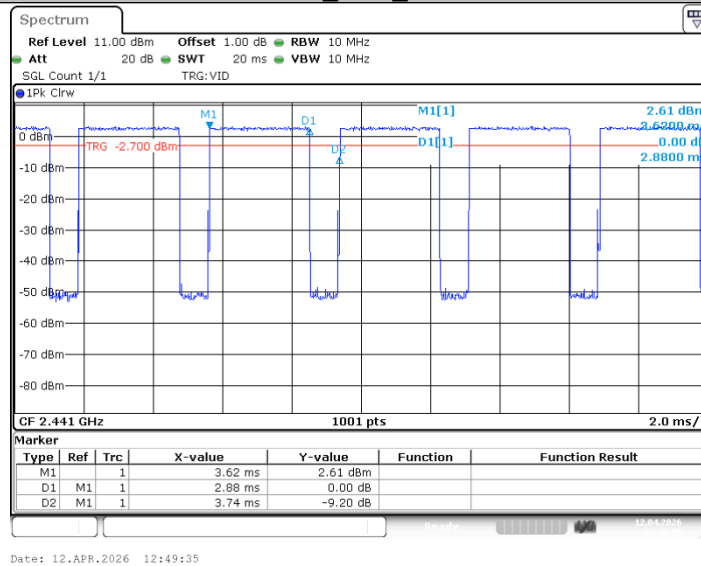
2DH5_Ant1_2480



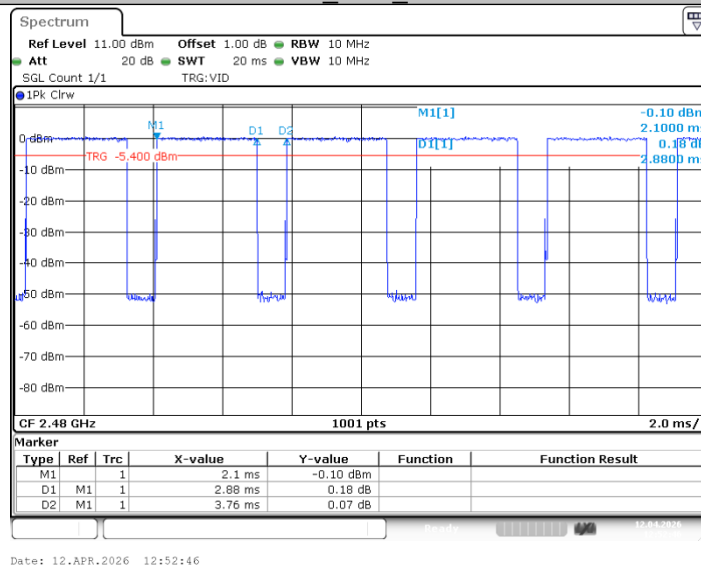
3DH5_Ant1_2402



3DH5_Ant1_2441



3DH5_Ant1_2480



3.10. Antenna Requirement

Requirement

FCC CFR Title 47 Part 15 Subpart C Section 15.203

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

FCC CFR Title 47 Part 15 Subpart C Section 15.247(c) (1)(i)

(i) Systems operating in the 2400~2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.

Test Result

The transmitter employs an integral PCB antenna installed inside the host device. The antenna is permanently attached and cannot be readily replaced by the end user. No standard antenna connector is provided. Therefore, the antenna configuration complies with the requirements of FCC §15.203. The directional gain of the antenna is less than 6dBi, please refer to the EUT internal photographs antenna photo.

*****THE END OF REPORT*****