

Appendix for Appendix for DC_7A_n78A (3450-3550MHz)

Product Name: Smart phone

Model No: Black 5

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	25.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	26.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	24.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	25.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	25.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	26.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.95	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	24.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	25.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	22.99	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	25.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	26.13	PC2	PASS
DC_7A_n78A-34	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.94	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	24.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	25.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.64	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.85	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	25.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	26.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	24.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	25.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	23.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	25.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	26.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	24.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	25.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.47	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	25.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	26.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	24.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	25.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.74	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	25.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	26.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	24.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	25.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.79	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	25.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	26.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.06	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	24.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	25.33	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.35	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	25.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	26.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	24.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	25.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	25.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	26.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.95	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	24.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	25.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	25.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	26.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.31	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	24.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	25.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	25.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	24.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	25.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	25.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	26.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	24.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	25.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.82	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.81	PC2	PASS
DC_7A_n78A-34	30	5+50	DFT-QPSK	M+M	Outer_Full	25.61	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	26.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	24.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	25.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	25.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	26.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.95	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	24.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	25.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.71	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.73	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	25.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	26.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	24.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	25.57	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	25.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	26.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	24.56	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	25.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	25.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	26.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	24.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	25.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	25.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	26.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_7A_n78A-34	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.08	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	24.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	25.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	25.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	26.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.82	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	24.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	25.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.06	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	25.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	26.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.25	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	24.25	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	25.35	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.78	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	25.54	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	26.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	24.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	25.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	25.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	26.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	24.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	25.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	25.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	26.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	24.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	25.37	PC2	PASS
DC_7A_n78A-34	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.07	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	25.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	26.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.74	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	24.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	25.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.80	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	25.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	26.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.84	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	24.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	25.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	25.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	26.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.91	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	24.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	25.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.71	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.75	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	25.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	26.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	24.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	25.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	25.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	26.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	24.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	25.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	25.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	26.54	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	24.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	25.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC2	PASS

Hotspot ON

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	19.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	19.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	19.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.37	PC2	PASS
DC_7A_n78A-34	30	5+10	DFT-16QAM	M+M	Outer_Full	19.53	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	19.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	19.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	19.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	19.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	19.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	19.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	19.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	19.48	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	19.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	19.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	19.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	19.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	19.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.25	PC2	PASS
DC_7A_n78A-34	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.32	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	19.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	19.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	19.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	19.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	19.58	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	19.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	19.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	19.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	19.82	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	19.73	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	19.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	19.99	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	19.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	19.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	19.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	19.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	19.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	19.57	PC2	PASS
DC_7A_n78A-34	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.45	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	19.57	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	19.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	19.56	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	19.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	19.66	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	19.79	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	19.74	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	19.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	19.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.20	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	19.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	19.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	19.64	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	19.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	19.81	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	19.78	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	19.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.56	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	19.51	PC2	PASS
DC_7A_n78A-34	30	5+60	DFT-16QAM	M+H	Inner_Full	19.45	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	19.57	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	19.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	19.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	19.58	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	19.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	19.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.63	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.53	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	19.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	19.66	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	19.67	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	19.66	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.57	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-34	30	5+80	DFT-QPSK	M+H	Outer_Full	19.53	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	19.72	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	19.62	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	19.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	19.67	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.56	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	19.68	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	19.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	19.67	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	19.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	19.66	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	19.55	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	19.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	19.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	19.64	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	19.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	19.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	19.70	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	19.70	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.39	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.34	PC2	PASS
DC_7A_n78A-34	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	19.47	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	19.71	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	19.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	19.70	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	19.66	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	19.67	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.42	PC2	PASS

Receiver ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	16.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	16.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	16.28	PC2	PASS
DC_7A_n78A-34	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	16.41	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	16.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	16.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	16.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	16.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	16.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	16.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	15.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	15.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	16.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	16.35	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	16.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	16.37	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	16.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	16.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	16.35	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	16.80	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	16.75	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	16.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	16.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	16.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	16.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	16.06	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	16.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	16.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	16.37	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	16.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	16.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	16.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	16.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	16.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	16.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	16.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	16.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	16.25	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	16.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	15.95	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	16.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	16.55	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	16.63	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	16.57	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	16.81	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	16.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	16.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	16.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	16.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	16.50	PC2	PASS
DC_7A_n78A-34	30	5+40	DFT-16QAM	M+H	Outer_Full	16.16	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	16.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	16.27	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	16.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	16.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	16.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	16.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	16.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	16.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	16.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	16.77	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	16.64	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	16.26	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	16.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	16.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	16.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	16.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	16.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	16.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	16.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	16.68	PC2	PASS
DC_7A_n78A-34	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	16.58	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	16.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	16.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	16.34	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	16.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	16.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	16.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	16.41	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	16.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	16.48	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	16.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	16.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	16.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	16.38	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	16.64	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	16.37	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	16.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	16.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	16.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	16.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	16.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	16.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	16.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	16.47	PC2	PASS
DC_7A_n78A-34	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	16.33	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	16.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	16.49	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	16.78	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	16.59	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	16.40	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	16.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	16.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	16.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	16.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	16.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	16.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	16.25	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	16.00	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	16.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	16.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	16.30	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	16.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	16.43	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	16.47	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	16.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	16.45	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	16.36	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	16.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	16.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	16.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	16.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	16.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	16.55	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	16.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	16.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	16.46	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	16.28	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	16.54	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	16.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	16.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	16.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	16.29	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	16.32	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	16.42	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	16.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	16.24	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	16.24	PC2	PASS

Sensor ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-34	30	5+10	DFT-QPSK	M+L	Outer_Full	20.18	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	20.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	20.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	20.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	20.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	20.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	19.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	20.06	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	20.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	20.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	20.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	20.21	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.50	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.52	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	19.81	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	19.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.97	PC2	PASS
DC_7A_n78A-34	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.18	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.67	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.81	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.81	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	20.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	19.91	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.82	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	19.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	19.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.65	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	20.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	19.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	20.26	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	20.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	20.00	PC2	PASS
DC_7A_n78A-34	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.42	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.33	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	19.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	20.17	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	19.85	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	19.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	20.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	20.04	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	20.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.79	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	19.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	20.04	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	19.92	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.79	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.51	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	20.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	19.99	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.85	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	20.07	PC2	PASS
DC_7A_n78A-34	30	5+60	DFT-QPSK	M+M	Inner_Full	19.95	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	19.79	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	20.01	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.22	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	19.95	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	19.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.86	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.98	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	20.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.73	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	19.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	19.97	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	19.76	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	19.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	20.05	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	19.93	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	20.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.94	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	19.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.70	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	20.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	19.98	PC2	PASS
DC_7A_n78A-34	30	5+80	DFT-16QAM	M+L	Outer_Full	20.14	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	20.20	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.99	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	20.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.35	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	20.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	20.03	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	20.07	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.02	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	20.09	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.87	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	20.16	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.77	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.56	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	20.15	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	20.10	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	19.61	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	20.19	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.00	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	19.91	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.96	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	20.14	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.58	PC2	PASS
DC_7A_n78A-34	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.67	PC2	PASS

50-3550								
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	20.11	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	20.23	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	20.60	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	20.09	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	19.88	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	20.08	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	20.25	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.83	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	20.12	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	20.13	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.89	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.90	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	20.08	PC2	PASS

DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.82	PC2	PASS
DC_7A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.73	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_7A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.77	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.57	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.56	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.63	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.32	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.05	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.83	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.88	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.86	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.88	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.61	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.98	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.52	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.84	≤13	PASS

DC_7A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.83	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.38	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	9.15	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.74	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.93	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.45	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.29	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.42	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.49	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.75	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.15	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.38	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	9.44	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.66	≤13	PASS
DC_7A_n78A-3	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	0.00	≤13	PASS

450-3550										
DC_7A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.62	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.99	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	9.27	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.74	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.53	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.95	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.61	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.70	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.07	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.69	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.02	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.11	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.48	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.29	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.42	≤13	PASS
DC_7A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.42	≤13	PASS

Test Graphs











