

Appendix for Appendix for DC_7A_n78A (3700-3800MHz)

Product Name: Smart phone

Model No: Black 5

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	24.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	26.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	24.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	24.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	25.65	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	26.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	24.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	25.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	25.25	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	26.10	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	24.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	25.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	25.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	26.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	23.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	25.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.94	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	25.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	26.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	24.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	25.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.69	PC2	PASS
DC_7A_n78A-37	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.66	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	25.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	26.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	24.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	25.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	25.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	26.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	24.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	25.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	25.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	26.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	24.71	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	25.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	25.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	26.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.93	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	24.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	25.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	25.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	26.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	24.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	25.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	25.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	26.70	PC2	PASS
DC_7A_n78A-37	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.17	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	24.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	25.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	25.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	26.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	24.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	25.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	25.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	26.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	24.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	25.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.75	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	25.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	26.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	24.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	25.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	25.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	26.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	24.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	25.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	25.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	26.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	24.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	25.31	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	25.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	26.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	24.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	25.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	25.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	26.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.01	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	24.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	25.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	25.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	26.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	22.72	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	24.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	25.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	25.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	26.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	24.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	25.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	25.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	26.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	22.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	24.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	25.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.82	PC2	PASS
DC_7A_n78A-37	30	5+80	DFT-QPSK	M+L	Outer_Full	25.68	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	26.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	24.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	25.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	25.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	26.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	24.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	25.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.43	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	25.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	26.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	24.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	25.58	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	25.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	26.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	24.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	25.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.94	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	25.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	26.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	24.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	25.62	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	25.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	26.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.06	PC2	PASS
DC_7A_n78A-37	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.20	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	24.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	25.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	25.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	26.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	24.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	25.65	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	25.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	26.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	24.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	25.65	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.94	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	25.67	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	26.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	24.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	25.65	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.95	PC2	PASS

Hotspot ON

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	19.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	19.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	19.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	19.00	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-37	30	5+10	DFT-16QAM	M+M	Outer_Full	19.85	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	19.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	19.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	19.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	19.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	19.25	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	19.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	19.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	19.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	19.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.25	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	19.76	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	19.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	19.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	19.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	19.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	19.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	19.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	19.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	19.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	19.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	18.93	PC2	PASS
DC_7A_n78A-37	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.15	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	19.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	19.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	19.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	19.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	19.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	19.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	19.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	19.38	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	19.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.08	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	19.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	19.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	19.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	19.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	19.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	19.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	19.62	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	19.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	19.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	19.44	PC2	PASS
DC_7A_n78A-37	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.15	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.47	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	19.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	19.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	19.94	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	19.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	19.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	19.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	19.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.27	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	19.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	19.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	19.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	19.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	19.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	19.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	19.27	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	19.47	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	19.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	19.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	19.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	19.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.53	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	19.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	19.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	19.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	19.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	19.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.02	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.08	PC2	PASS
DC_7A_n78A-37	30	5+80	DFT-QPSK	M+H	Outer_Full	19.74	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	19.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	19.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	19.73	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	19.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-37	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	19.77	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.47	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.75	PC2	PASS

Receiver ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	15.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	15.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	16.02	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	16.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	16.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	15.89	PC2	PASS
DC_7A_n78A-37	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	15.96	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	16.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	16.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	16.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	16.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	16.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	16.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	15.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	16.01	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	16.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	16.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	15.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	16.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	15.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	15.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	16.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	15.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	15.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	16.17	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	15.93	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	15.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	16.02	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	16.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	16.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	16.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	17.00	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	16.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	16.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	16.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	16.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	16.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	16.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	15.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	16.00	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	16.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	16.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	16.04	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	15.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	16.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	16.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	16.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	15.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	15.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	16.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	16.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	16.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	16.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	16.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	16.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	16.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	16.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	16.03	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	15.79	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	15.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	16.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	15.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	15.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	16.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	16.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	16.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	15.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	16.08	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	16.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	16.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	16.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	16.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	16.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	16.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	16.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	16.47	PC2	PASS
DC_7A_n78A-37	30	5+40	DFT-16QAM	M+H	Outer_Full	16.08	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	16.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	16.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	16.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	16.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	16.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	15.92	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	16.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	16.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	16.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	15.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	15.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	16.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	16.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	16.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	16.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	16.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	16.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	16.10	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	16.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	16.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	16.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	16.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	15.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	15.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	16.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	16.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	15.93	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	16.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	16.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	16.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	15.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	16.00	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	16.41	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	16.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	16.36	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	16.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	16.71	PC2	PASS
DC_7A_n78A-37	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	16.75	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	16.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	16.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	16.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	16.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	16.06	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	16.08	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	16.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	16.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	16.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	16.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	15.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	16.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	16.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	16.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	15.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	16.02	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	16.47	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	16.44	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	16.41	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	16.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	16.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	16.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	16.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	16.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	16.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	16.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	15.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	16.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	16.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	16.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	16.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	16.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	16.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	16.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	16.46	PC2	PASS
DC_7A_n78A-37	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	16.44	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	16.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	16.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	16.48	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	16.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	16.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	16.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	16.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	16.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	16.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	16.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	16.32	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	16.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	16.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	16.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	16.43	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	16.40	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	16.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	16.46	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	16.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	16.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	16.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	16.08	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	16.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	17.02	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	16.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	16.57	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	16.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	16.43	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	16.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	16.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	16.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	16.51	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	16.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	16.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	16.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	16.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	16.55	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	16.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	16.53	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	16.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	16.59	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	16.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	16.42	PC2	PASS

Sensor ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_7A_n78A-37	30	5+10	DFT-QPSK	M+L	Outer_Full	19.66	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	19.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.62	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	19.57	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.70	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	20.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	20.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	20.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.25	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	19.63	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	19.73	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	19.78	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.62	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	19.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	19.60	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	19.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	20.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	20.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	20.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.61	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.93	PC2	PASS
DC_7A_n78A-37	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.02	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	19.77	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	19.65	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	20.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	20.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	20.25	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.24	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	19.82	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	19.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.54	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	19.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.49	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.45	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	20.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	20.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	20.15	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	20.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	20.21	PC2	PASS
DC_7A_n78A-37	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.30	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	19.71	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	20.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	20.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	19.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.78	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	19.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	19.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.58	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.85	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	19.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	20.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	20.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	20.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	20.12	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	20.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	19.91	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.64	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.74	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	19.86	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.56	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.68	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	19.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.50	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	20.23	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	20.11	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	20.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	20.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.90	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.97	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.79	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.80	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	19.87	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.81	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.93	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	19.96	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.51	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.67	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	20.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	20.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	20.10	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	20.26	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	20.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	20.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	20.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.99	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	20.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	20.05	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.69	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.75	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	20.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	20.25	PC2	PASS
DC_7A_n78A-37	30	5+80	DFT-16QAM	M+L	Outer_Full	20.27	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	20.38	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.04	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.20	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	20.07	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	20.09	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	20.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	20.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.42	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.52	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	20.19	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	20.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	20.39	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	20.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	20.26	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.89	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	20.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.82	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.98	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	20.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.95	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	20.13	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	20.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	20.37	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.11	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	20.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	20.33	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	20.01	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	20.21	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	20.31	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.78	PC2	PASS
DC_7A_n78A-37	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.99	PC2	PASS

00-3800								
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	20.32	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	20.22	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.84	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	20.17	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	20.29	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	20.34	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.72	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.01	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	20.30	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	20.12	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.88	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	20.16	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	20.27	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	20.28	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	20.00	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	20.18	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	20.66	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.83	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	20.14	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	20.28	PC2	PASS

DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	20.35	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.76	PC2	PASS
DC_7A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	20.06	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_7A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.47	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.14	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.95	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.40	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.72	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.47	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.77	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.65	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.80	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.06	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.76	≤13	PASS

DC_7A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.83	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.47	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.26	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.38	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.69	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.54	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.72	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.43	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.52	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.24	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	9.02	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.53	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.44	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_7A_n78A-3	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.55	≤13	PASS

700-3800										
DC_7A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.87	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.08	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.77	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.59	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.77	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.91	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.53	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.59	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.88	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.72	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.64	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.23	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.84	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	9.53	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.73	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.74	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.60	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_7A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	9.24	≤13	PASS

Test Graphs











