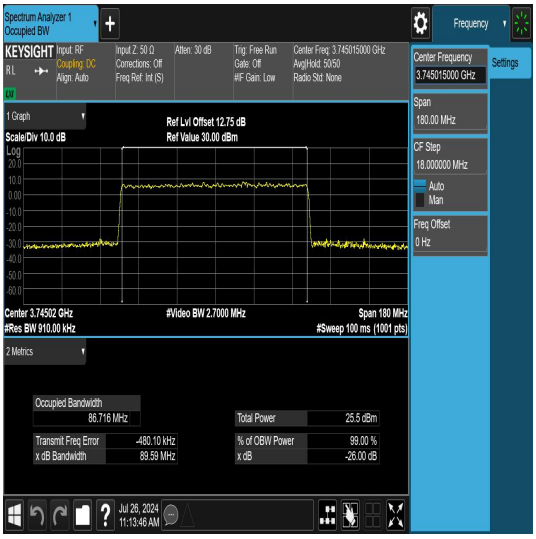
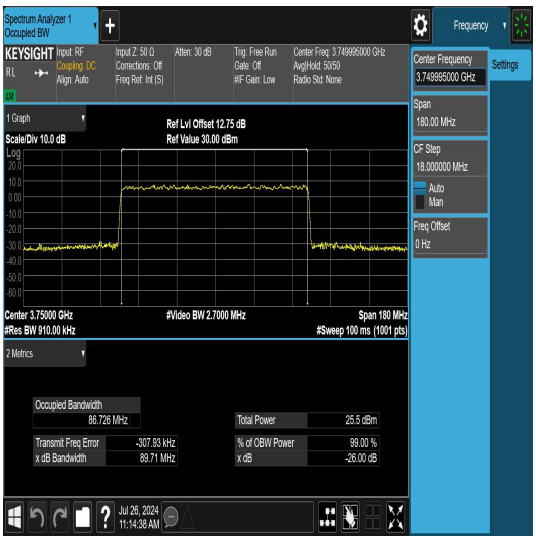
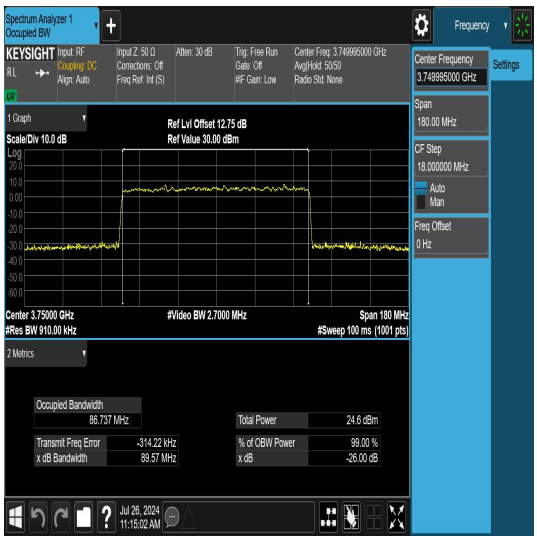
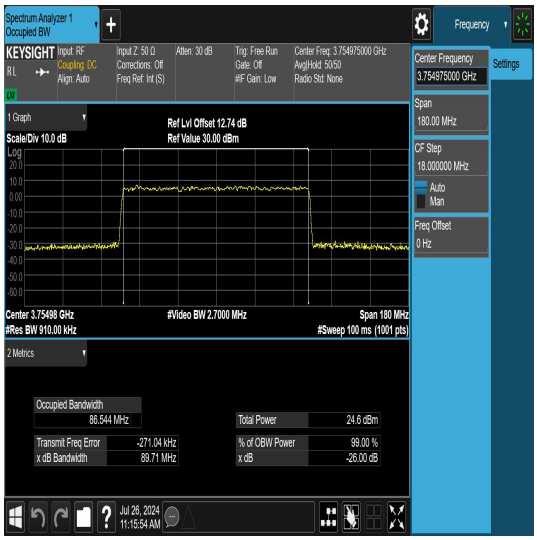
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+80_M+H_TID1_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+80_M+H_TID3_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+L_TID1_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+L_TID3_Ant1#1



NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+M_TID1_Ant1#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+M_TID3_Ant1#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+H_TID1_Ant1#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+90_M+H_TID3_Ant1#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+L_TID1_Ant1#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+L_TID3_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+M_TID1_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+M_TID3_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+H_TID1_Ant1#1

NTNV_DC_7A_n78A-3700-3800_PC2_30_5+100_M+H_TID3_Ant1#1

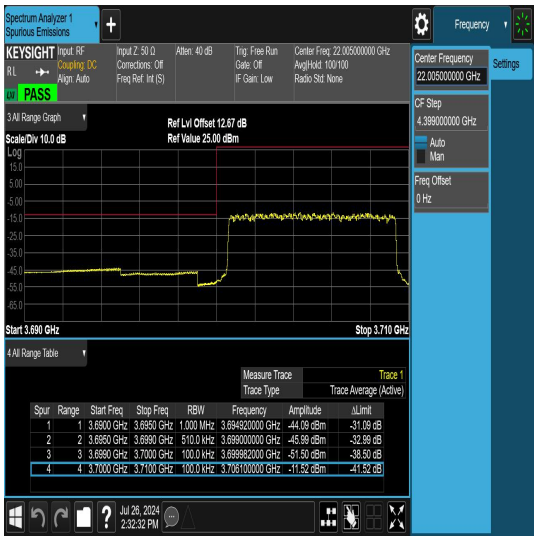
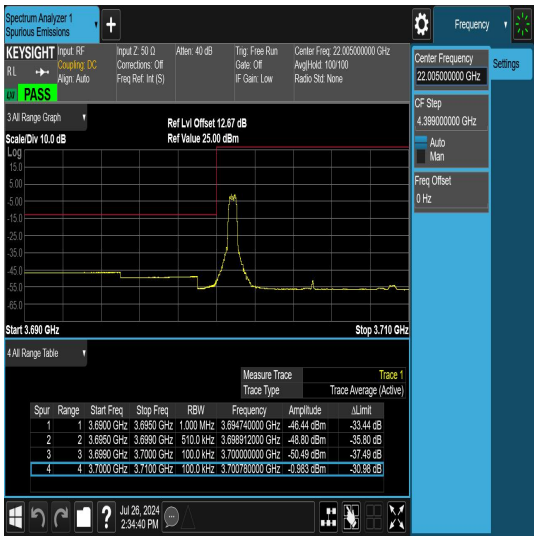
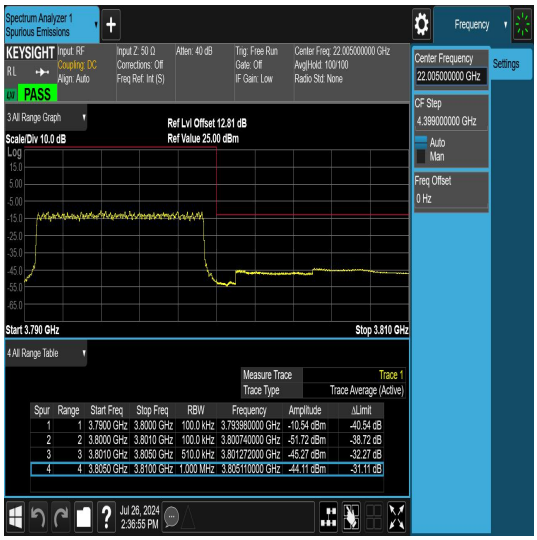
Appendix D: Band Edge for NSA

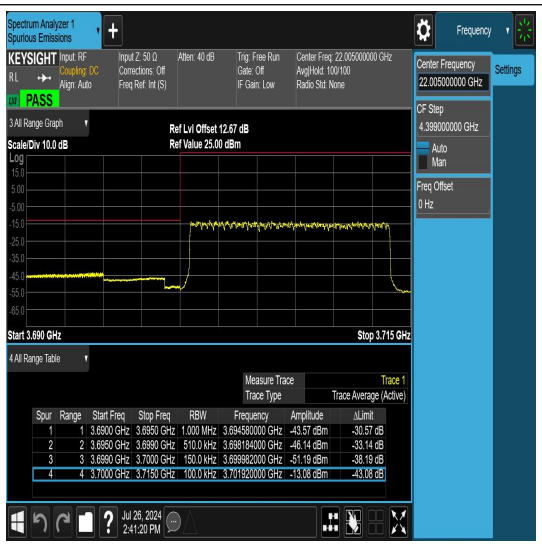
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	-44.09	31.09	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-46.44	33.44	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	-44.11	31.11	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-47.32	34.32	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	-43.57	30.57	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-46.37	33.37	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	-44.84	31.84	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-47.01	34.01	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	-42.97	29.97	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-46.29	33.29	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	-45.09	32.09	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-47.08	34.08	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	-40.36	27.36	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	-43.71	30.71	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	-45.77	32.77	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	-47.22	34.22	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	-44.71	31.71	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	-46.35	33.35	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	-46.05	33.05	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	-47.32	34.32	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	-44.83	31.83	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	-46.27	33.27	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	-45.59	32.59	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	-47.35	34.35	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	-45.08	32.08	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	-46.26	33.26	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	-46.29	33.29	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	-47.36	34.36	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	-45.21	32.21	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	-44.65	31.65	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	-42.35	29.35	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	-44.65	31.65	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	-42.32	29.32	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	-43.46	30.46	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	-42.48	29.48	PASS

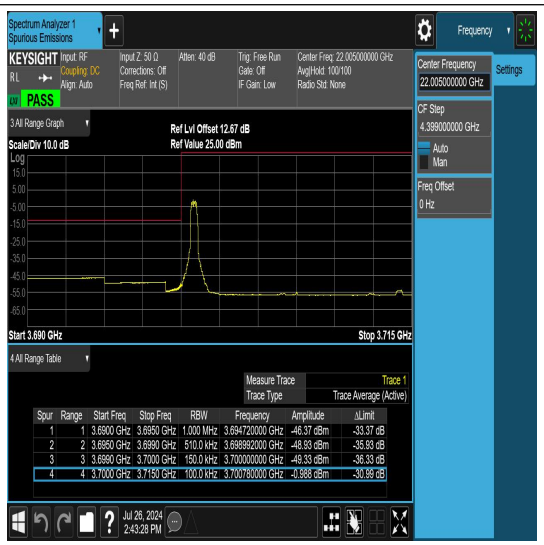
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	-44.35	31.35	PASS
DC_7A_n78A-3700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	-42.01	29.01	PASS
DC_7A_n78A-3700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	-43.24	30.24	PASS
DC_7A_n78A-3700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	-42.54	29.54	PASS
DC_7A_n78A-3700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	-43.57	30.57	PASS

Test Graphs

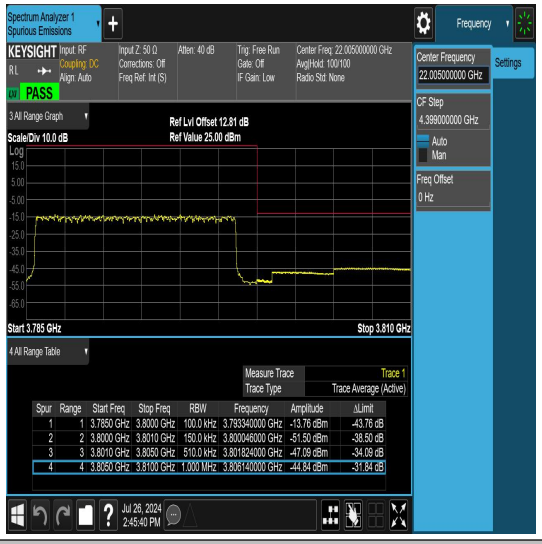
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+10_M+L_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+10_M+L_TID7__#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+10_M+H_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+10_M+H_TID8__#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+15_M+L_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+15_M+L_TID7__#1



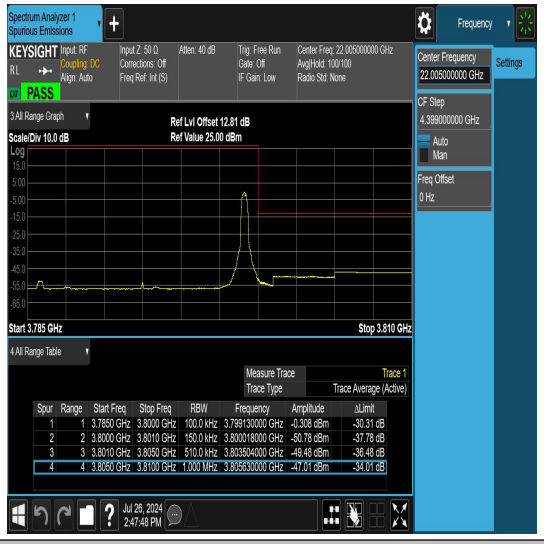
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+15_M+H_TID9__#1



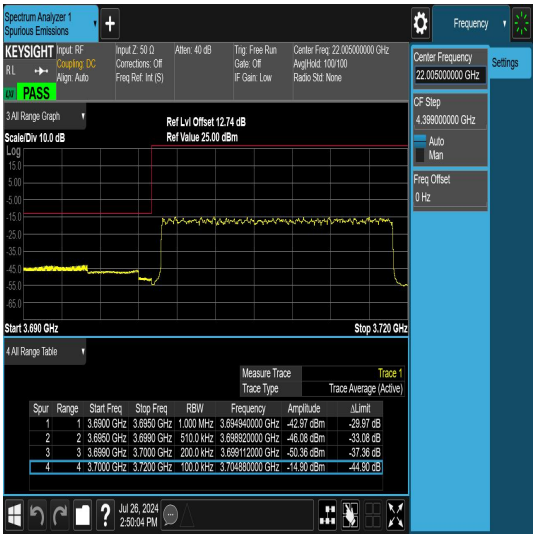
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+15_M+H_TID8__#1

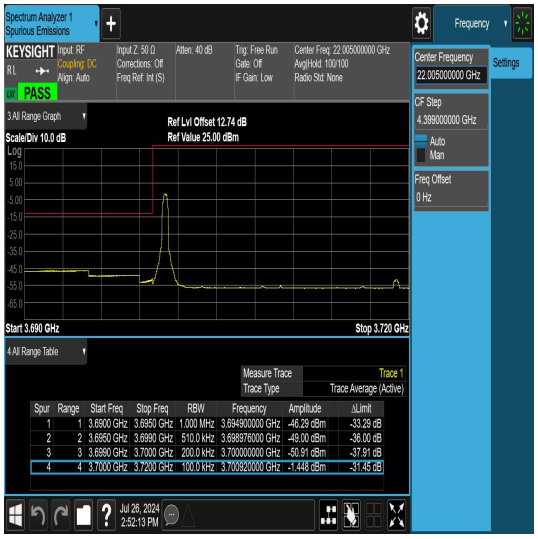


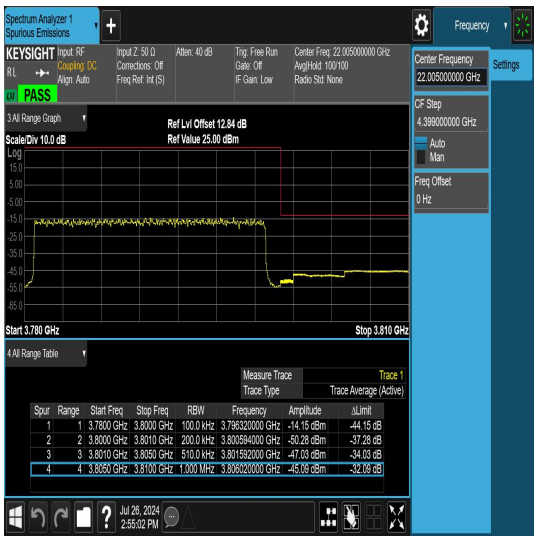
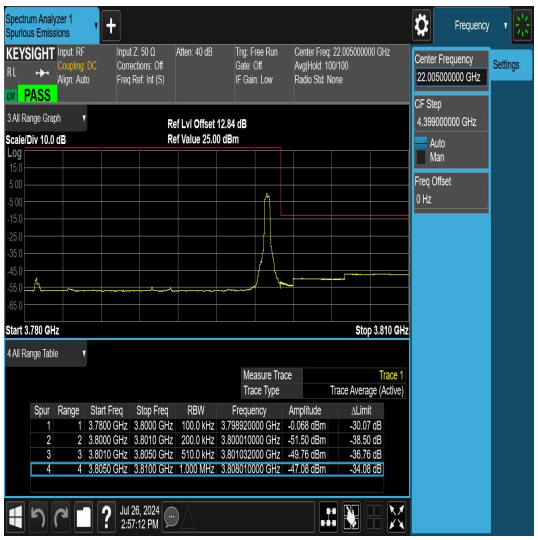
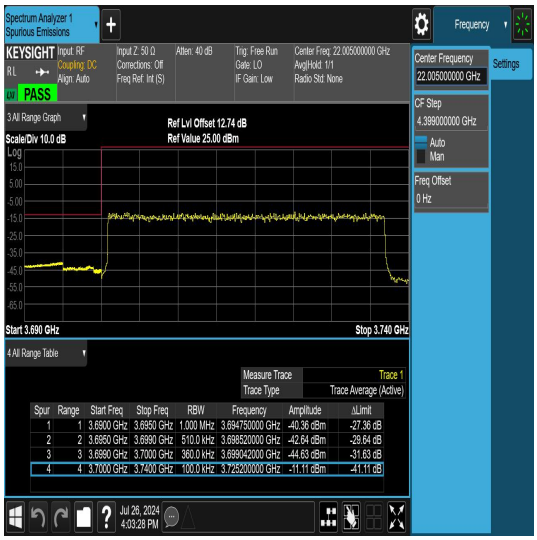
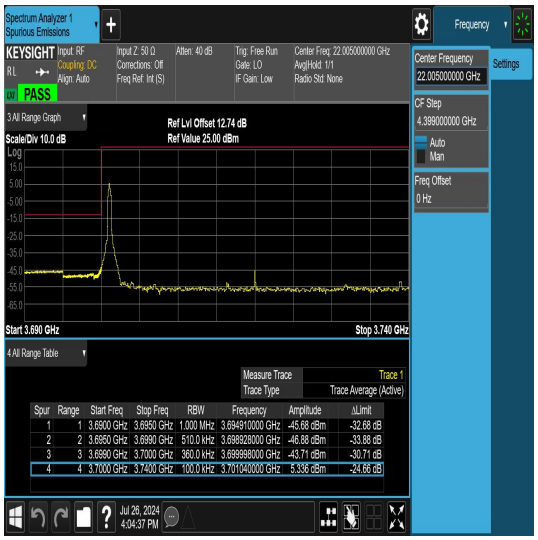
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+20_M+L_TID9__#1

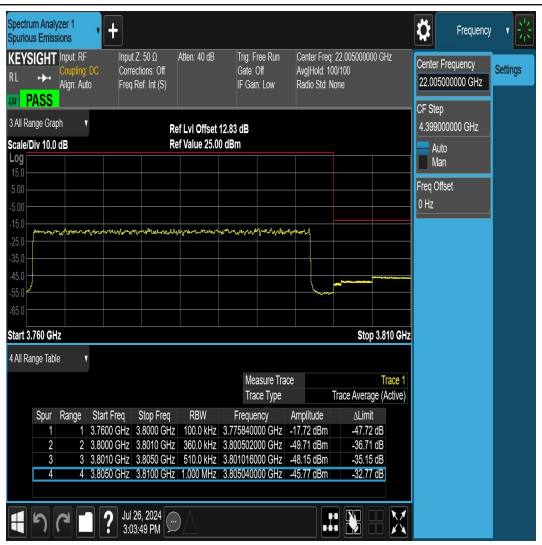


NTNV_DC_7A_n78A-3700-3800_PC2_30_5+20_M+L_TID7__#1

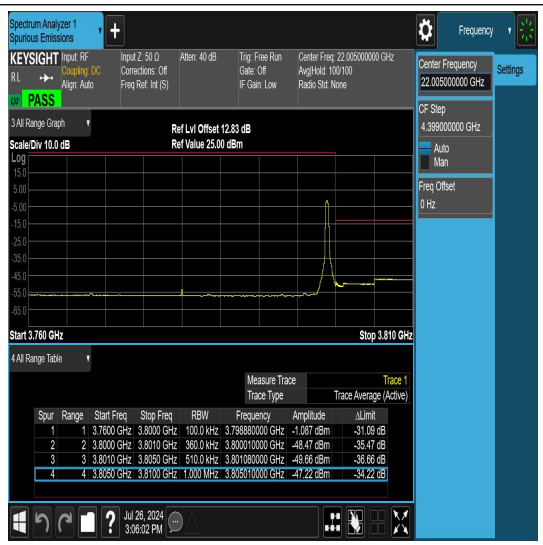




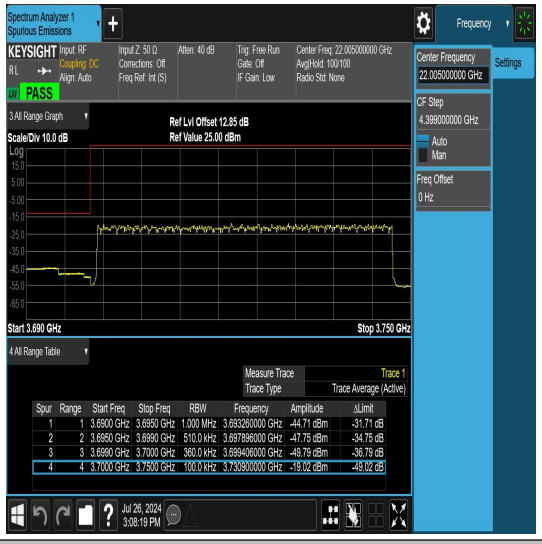
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+20_M+H_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+20_M+H_TID8__#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+40_M+L_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+40_M+L_TID7__#1
	
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+40_M+H_TID9__#1	NTNV_DC_7A_n78A-3700-3800_PC2_30_5+40_M+H_TID8__#1



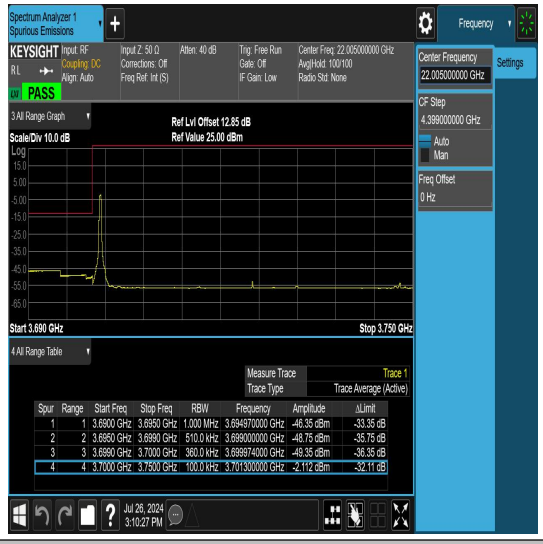
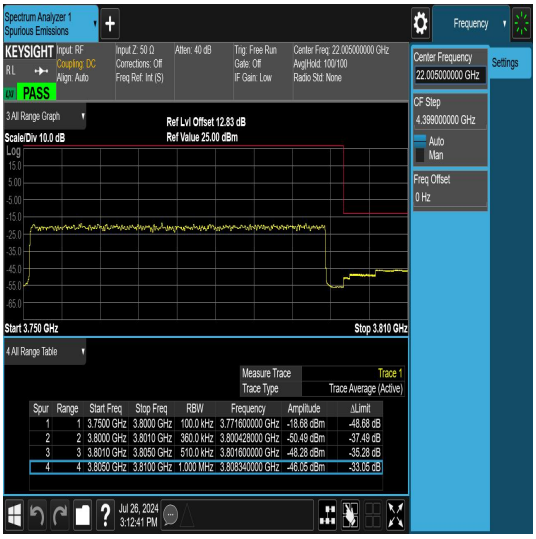
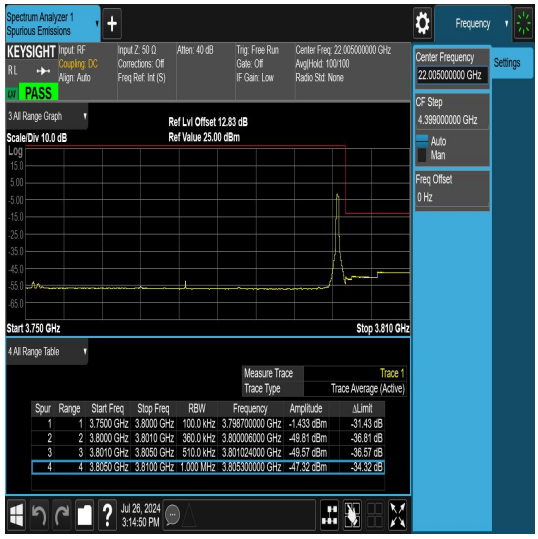
NTNV_DC_7A_n78A-3700-3800_PC2_30_5+50_M+L_TID9__#1



NTNV_DC_7A_n78A-3700-3800_PC2_30_5+50_M+L_TID7__#1



NTNV_DC_7A_n78A-3700-3800_PC2_30_5+50_M+H_TID9__#1



NTNV_DC_7A_n78A-3700-3800_PC2_30_5+50_M+H_TID8__#1