

Appendix for Appendix for DC_2A_n78A (3450-3550MHz)

Product Name: Smart phone

Model No: Black 5

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	25.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	26.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB _Left	22.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB _Right	23.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	24.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	25.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB _Left	22.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB _Right	22.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	25.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	26.28	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB _Left	22.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB _Right	22.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	24.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	25.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB _Left	23.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB _Right	23.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	25.10	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	25.94	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB _Left	22.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB _Right	23.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	24.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	25.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB _Left	22.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB _Right	22.75	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	25.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	26.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB _Left	22.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB _Right	22.94	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	24.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	25.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB _Left	23.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB _Right	23.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	25.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	26.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB _Left	23.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB _Right	23.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	24.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	25.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB _Left	23.42	PC2	PASS
DC_2A_n78A-34	30	5+15	DFT-16QAM	M+M	Edge_1RB	23.36	PC2	PASS

50-3550					_Right			
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	25.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	26.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB _Left	22.90	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB _Right	23.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	24.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	24.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB _Left	22.65	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB _Right	22.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	25.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	26.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB _Left	22.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB _Right	23.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	24.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	25.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB _Left	22.63	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB _Right	22.69	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	25.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	26.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB _Left	23.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB _Right	22.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	24.32	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	25.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB _Left	23.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB _Right	23.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	25.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	26.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB _Left	22.78	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB _Right	22.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	24.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	25.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB _Left	22.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB _Right	22.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	25.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	26.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB _Left	22.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB _Right	23.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	24.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	25.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB _Left	22.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB _Right	23.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	25.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	26.31	PC2	PASS
DC_2A_n78A-34	30	5+40	DFT-QPSK	M+M	Edge_1RB	23.21	PC2	PASS

50-3550					_Left			
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB _Right	23.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	24.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	25.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB _Left	23.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB _Right	23.10	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	25.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	26.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB _Left	23.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB _Right	23.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	24.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	25.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB _Left	22.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB _Right	22.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	25.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	26.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB _Left	22.90	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB _Right	22.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	24.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	25.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB _Left	22.71	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB _Right	22.68	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	25.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	26.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB _Left	23.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB _Right	22.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	24.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB _Left	23.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB _Right	23.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	25.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	26.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB _Left	22.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB _Right	22.83	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	24.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	25.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB _Left	22.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB _Right	22.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	25.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	26.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB _Left	22.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB _Right	22.90	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	24.42	PC2	PASS
DC_2A_n78A-34	30	5+60	DFT-16QAM	M+L	Inner_Full	25.42	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB _Left	22.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB _Right	22.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	26.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB _Left	22.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB _Right	22.78	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	24.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	25.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB _Left	23.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB _Right	23.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	25.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB _Left	23.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB _Right	22.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	24.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	25.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB _Left	22.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB _Right	22.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	25.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	26.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB _Left	22.96	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB _Right	22.97	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	24.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	25.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB _Left	22.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB _Right	22.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	25.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	26.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB _Left	22.96	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB _Right	22.73	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	24.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	25.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB _Left	23.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB _Right	23.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	25.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB _Left	23.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB _Right	22.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	24.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	25.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB _Left	22.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB _Right	22.69	PC2	PASS
DC_2A_n78A-34	30	5+80	DFT-QPSK	M+L	Outer_Full	25.41	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	26.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB _Left	23.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB _Right	22.78	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	24.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	25.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB _Left	22.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB _Right	22.90	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	26.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB _Left	22.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB _Right	22.80	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	24.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	25.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB _Left	23.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB _Right	23.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	25.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	26.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB _Left	23.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB _Right	23.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	24.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	25.27	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB _Left	22.97	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB _Right	22.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	25.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	26.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB _Left	22.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB _Right	22.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	24.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	25.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB _Left	22.68	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB _Right	22.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	25.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	26.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB _Left	22.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB _Right	22.72	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	24.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	25.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB _Left	23.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB _Right	22.83	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	25.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	26.28	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB _Left	22.84	PC2	PASS
DC_2A_n78A-34	30	5+90	DFT-QPSK	M+H	Edge_1RB	22.86	PC2	PASS

50-3550					_Right			
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	24.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	25.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB _Left	22.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB _Right	22.67	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	25.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	26.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB _Left	22.82	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB _Right	22.82	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	24.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	25.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB _Left	22.79	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB _Right	22.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	26.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB _Left	22.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB _Right	22.80	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	24.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB _Left	22.79	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB _Right	22.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	25.32	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	26.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB _Left	22.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB _Right	22.82	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	24.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	25.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB _Left	22.79	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB _Right	22.77	PC2	PASS

Hotspot ON

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	19.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	19.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.22	PC2	PASS
DC_2A_n78A-34	30	5+10	DFT-16QAM	M+M	Outer_Full	19.49	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	19.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	19.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	19.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	19.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	19.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	19.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	19.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	19.40	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	19.72	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	19.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	19.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	19.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	19.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	19.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	19.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.26	PC2	PASS
DC_2A_n78A-34	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.27	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	19.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	19.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.70	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	19.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	19.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	19.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	19.56	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.67	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	19.65	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	19.63	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	19.57	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	19.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	19.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.57	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	19.68	PC2	PASS
DC_2A_n78A-34	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.46	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	19.68	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	19.66	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.43	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.28	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	19.44	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.75	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	19.67	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.63	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.65	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	19.66	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	19.57	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.64	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	19.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	19.68	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	19.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.72	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	19.65	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	19.72	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	19.68	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	19.57	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.58	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	19.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	19.64	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	19.37	PC2	PASS
DC_2A_n78A-34	30	5+80	DFT-QPSK	M+H	Outer_Full	19.53	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	19.58	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	19.93	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	19.63	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	19.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	19.69	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	19.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	19.66	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	19.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	19.65	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	19.60	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	19.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	19.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	19.60	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.58	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.61	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	20.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	19.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	19.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	19.69	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	19.59	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	19.74	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.51	PC2	PASS
DC_2A_n78A-34	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	19.49	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	19.69	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	19.67	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	19.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	19.73	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	19.71	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.30	PC2	PASS

Receiver ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	16.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	16.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	16.23	PC2	PASS
DC_2A_n78A-34	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	16.30	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	16.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	16.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	16.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	16.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	16.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	16.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	16.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	15.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	16.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	15.72	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	16.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	16.35	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	16.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	16.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	16.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	16.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	16.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	16.75	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	16.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	15.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	16.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	16.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	16.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	16.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	16.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	16.33	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	16.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	16.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	16.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	16.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	16.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	16.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	16.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	16.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	16.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	16.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	16.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	16.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	16.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	15.87	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	16.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	16.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	16.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	16.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	16.57	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	16.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	16.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	16.66	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	16.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	16.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	16.38	PC2	PASS
DC_2A_n78A-34	30	5+40	DFT-16QAM	M+H	Outer_Full	16.20	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	16.10	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	16.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	16.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	16.19	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	16.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	16.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	16.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	16.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	16.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	16.58	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	16.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	16.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	16.25	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	16.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	15.94	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	15.93	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	16.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	16.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	16.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	16.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	16.61	PC2	PASS
DC_2A_n78A-34	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	16.59	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	16.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	16.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	16.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	16.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	16.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	16.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	16.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	16.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	16.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	16.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	16.45	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	16.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	16.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	16.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	16.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	16.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	16.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	16.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	16.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	16.41	PC2	PASS
DC_2A_n78A-34	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	16.41	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	16.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	16.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	16.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	16.74	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	16.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	16.38	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	16.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	16.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	16.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	16.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	16.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	16.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	16.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	16.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	16.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	16.01	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	16.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	16.29	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	16.16	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	16.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	16.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	16.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	16.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	16.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	16.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	16.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	16.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	16.28	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	16.51	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	16.19	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	16.37	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	16.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	16.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	16.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	16.17	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	16.14	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	16.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	16.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	16.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	16.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	16.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	16.15	PC2	PASS

Sensor ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34	30	5+10	DFT-QPSK	M+L	Outer_Full	20.01	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	20.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	20.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	20.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	20.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	19.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	20.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	19.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	19.94	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.88	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	20.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	19.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	19.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	19.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	20.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	19.70	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	20.00	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.62	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	20.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	19.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	20.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	20.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	19.94	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	20.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	20.09	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	19.97	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	19.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	20.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	20.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	19.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	19.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.93	PC2	PASS
DC_2A_n78A-34	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.16	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	19.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	19.82	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	20.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	20.00	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	20.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	20.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	20.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	20.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.80	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.80	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	20.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	19.97	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	20.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Inner_Full	20.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.11	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	19.24	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	20.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18.90	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	18.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	19.10	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	17.76	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	18.87	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	20.42	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	17.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	17.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	19.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	16.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	17.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	19.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	20.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	17.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	17.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	18.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	19.35	PC2	PASS
DC_2A_n78A-34	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	17.46	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	17.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	20.20	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	18.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	19.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	17.92	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	16.89	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	20.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	18.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	18.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	18.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	18.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	18.78	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	20.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	18.26	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	18.18	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	18.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	18.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	18.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	20.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	18.95	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	18.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	18.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	19.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	18.74	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	18.75	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	19.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	20.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	18.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	19.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	16.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	16.97	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	19.49	PC2	PASS
DC_2A_n78A-34	30	5+60	DFT-QPSK	M+M	Inner_Full	20.36	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18.83	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	19.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	19.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	18.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	18.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	19.23	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	20.33	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18.10	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	18.31	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	19.24	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.05	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	18.92	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	20.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.22	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.28	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	19.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	18.13	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	18.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	20.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	18.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	18.91	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	19.44	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	18.93	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	18.79	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	20.32	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	18.25	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	18.07	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	18.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	18.27	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	20.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	20.53	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	19.21	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18.10	PC2	PASS
DC_2A_n78A-34	30	5+80	DFT-16QAM	M+L	Outer_Full	19.45	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	20.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	18.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	18.92	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	20.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	20.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.18	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	19.51	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	20.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	19.13	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	19.98	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	20.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	20.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	19.15	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18.08	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	19.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	20.45	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	19.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.35	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	20.55	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	20.46	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.12	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18.89	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	20.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.04	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	18.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	20.40	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	20.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.06	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	18.96	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	19.41	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	20.34	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	18.92	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	18.77	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	20.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	20.36	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	18.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	19.43	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	20.39	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.20	PC2	PASS
DC_2A_n78A-34	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.27	PC2	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	20.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	20.56	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.01	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	19.54	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	20.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.85	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.81	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	20.46	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	20.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	18.99	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	19.50	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	20.49	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	18.84	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	18.78	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	20.52	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	20.47	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.02	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.03	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	19.46	PC2	PASS

DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	20.48	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.86	PC2	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.82	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_2A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.38	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.40	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.60	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.15	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.65	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.78	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.71	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.85	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.76	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.06	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.01	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.67	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.14	≤13	PASS

DC_2A_n78A-3 450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.87	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.29	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.41	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.07	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.51	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.47	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.74	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.16	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.49	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.67	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.51	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.36	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.69	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.54	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.55	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	9.38	≤13	PASS
DC_2A_n78A-3	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.88	≤13	PASS

450-3550										
DC_2A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	2.45	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.90	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.67	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.86	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.81	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	9.32	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.86	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	9.14	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.72	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	9.29	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.33	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	9.08	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.92	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.49	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	9.03	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_2A_n78A-3 450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.09	≤13	PASS

Test Graphs











