

Appendix for Appendix for DC_2A_n78A (3700-3800MHz)

Product Name: Smart phone

Model No: Black 5

Appendix A: Average Power Output Data for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	25.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	26.04	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	24.02	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	24.95	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	25.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	26.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	24.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	25.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	25.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	26.24	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	24.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	25.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	25.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	26.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	23.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	25.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	25.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	26.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	24.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	25.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.70	PC2	PASS
DC_2A_n78A-37	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.67	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	25.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	26.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	24.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	25.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	25.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	26.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	24.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	25.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	25.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	26.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	24.70	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	25.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	25.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	26.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	24.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	25.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	25.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	26.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	24.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	25.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	25.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	26.74	PC2	PASS
DC_2A_n78A-37	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.15	PC2	PASS

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DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	24.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	25.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	25.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	26.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	24.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	25.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	25.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	26.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	24.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	25.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.76	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	25.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	26.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	24.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	25.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	25.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	26.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.04	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	24.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	25.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	25.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	26.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	24.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	25.35	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	25.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	26.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.04	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	24.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	25.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	25.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	26.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	24.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	25.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	25.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	26.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	22.76	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	22.93	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	24.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	25.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	25.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	26.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	24.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	25.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	25.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	26.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	22.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.99	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	24.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	25.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.86	PC2	PASS
DC_2A_n78A-37	30	5+80	DFT-QPSK	M+L	Outer_Full	25.64	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	26.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	24.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	25.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	25.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	26.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	24.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	25.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	25.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	26.61	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	24.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	25.64	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	25.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	26.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.02	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	24.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	25.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	25.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	26.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.00	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	24.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	25.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	25.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	26.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.08	PC2	PASS
DC_2A_n78A-37	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.20	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	24.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	25.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	25.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	26.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	24.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	25.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.90	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	23.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	25.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	26.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.90	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	24.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	25.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.90	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	25.63	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	26.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	24.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	25.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.12	PC2	PASS

Hotspot ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	19.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	19.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	19.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	19.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	19.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	19.59	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	19.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	19.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	19.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	19.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	19.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	19.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	19.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	19.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	19.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	19.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	19.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	19.67	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	19.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	19.18	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	19.18	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	19.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	19.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	19.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	19.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	19.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	19.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	18.84	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	19.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	19.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	19.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	19.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	18.99	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	19.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	19.24	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	18.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	19.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	19.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	19.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	19.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	19.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	19.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	19.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	19.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	19.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	19.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	19.31	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	19.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	19.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	18.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	19.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	19.58	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	19.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	19.61	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	19.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	19.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	19.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	19.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.05	PC2	PASS
DC_2A_n78A-37	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.13	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	19.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	19.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	19.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	19.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.00	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	19.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	19.58	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	19.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	19.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	19.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	19.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.27	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	19.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	19.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	19.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	18.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	19.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	19.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	19.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	19.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	19.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	19.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	19.35	PC2	PASS
DC_2A_n78A-37	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.43	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	19.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	19.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	19.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	19.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	19.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	19.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	19.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	19.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	19.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	19.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	19.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	19.97	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	19.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	19.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	19.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	19.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	19.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	19.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	19.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	19.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	19.58	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	19.67	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	19.61	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	19.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	19.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	19.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	19.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	19.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	19.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	19.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	19.61	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	19.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	20.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.32	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	19.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	19.67	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	19.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	19.65	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	19.63	PC2	PASS

Receiver ON

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	15.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	15.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	15.99	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	16.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	15.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	15.86	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	15.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	16.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	16.54	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	16.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	16.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	16.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	16.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	16.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	15.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	15.99	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	16.04	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	16.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	15.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	16.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	15.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	15.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	15.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	15.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	15.84	PC2	PASS
DC_2A_n78A-37	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	16.10	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	15.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	15.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	16.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	16.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	16.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	16.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	16.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	16.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	15.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	16.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	15.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	16.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	16.01	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	16.00	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	15.95	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	16.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	16.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	16.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	15.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	15.93	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	16.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	16.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	16.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	16.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	16.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	16.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	16.02	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	16.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	16.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	15.78	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	15.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	15.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	16.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	15.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	16.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	16.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	16.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	16.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	16.79	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	16.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	16.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	16.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	16.33	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	16.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	16.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	16.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	15.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	16.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	16.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	16.18	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	16.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	16.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	16.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	16.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	16.03	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	16.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	16.18	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	16.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	16.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	16.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	16.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	16.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	15.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	16.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	16.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	16.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	15.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	15.95	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	16.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	16.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	16.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	16.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	16.32	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	16.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	16.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	16.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	16.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	16.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	16.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	16.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	16.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	16.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	16.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	16.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	16.39	PC2	PASS
DC_2A_n78A-37	30	5+70	DFT-16QAM	M+M	Outer_Full	16.43	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	16.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	16.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	16.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	16.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	16.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	16.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	16.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	16.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	16.58	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	16.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	16.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	16.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	16.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	16.48	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	16.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	16.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	16.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	16.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	16.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	16.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	16.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	16.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	16.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	16.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	16.28	PC2	PASS
DC_2A_n78A-37	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	16.42	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	16.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	16.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	16.43	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	16.46	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	16.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	16.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	16.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	16.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	16.48	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	16.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	16.50	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	16.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	16.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	16.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	16.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	16.55	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	16.59	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	16.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	16.38	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	16.49	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	16.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	16.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	16.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	16.53	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	16.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	16.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	16.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	16.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	16.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	16.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	16.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	16.61	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	16.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	16.42	PC2	PASS

Sensor ON

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
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DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	19.58	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	19.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	19.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	20.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	20.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	20.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	20.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	20.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	19.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	19.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	20.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	19.88	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	19.56	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	19.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	19.69	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	19.79	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	19.71	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	19.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	20.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	20.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	20.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	19.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	19.94	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	20.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	19.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	19.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	19.68	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	19.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	19.80	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	19.40	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	19.60	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	20.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	20.18	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	20.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Inner_Full	20.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.30	PC2	PASS
DC_2A_n78A-37	30	5+20	DFT-QPSK	M+H	Outer_Full	19.85	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	19.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	19.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	19.64	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	19.74	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	19.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	19.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	19.51	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	19.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	20.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	20.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	20.16	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	20.19	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	20.20	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	19.90	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	19.79	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	20.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	19.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	19.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	19.93	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	19.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	19.62	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	19.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	19.97	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	19.47	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	19.75	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	20.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	20.17	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	20.12	PC2	PASS
DC_2A_n78A-37	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	20.14	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	20.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	20.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.39	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	19.98	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	19.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	19.95	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	19.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	19.89	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	19.63	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.72	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	19.76	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	19.57	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	19.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	19.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	19.90	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	19.52	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	19.78	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	20.24	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	20.11	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	20.06	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	20.08	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	20.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	20.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.42	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	19.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	19.77	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	19.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	19.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	19.81	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	19.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	19.91	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	19.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	19.87	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	19.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	20.02	PC2	PASS
DC_2A_n78A-37	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	19.56	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	19.70	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	20.28	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	20.15	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	20.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	20.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	20.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	20.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	20.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	20.09	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	19.92	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	19.96	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	20.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	20.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	20.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	19.73	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	20.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	20.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	20.10	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	20.25	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	20.41	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.03	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	20.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	20.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	20.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.45	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.55	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	20.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	20.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	20.30	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	20.44	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	20.26	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	20.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.22	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	20.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	20.31	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	20.27	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	19.95	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	20.14	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	20.29	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	20.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	19.84	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.01	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	20.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	19.94	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	20.12	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	20.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	20.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	20.36	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	20.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	20.05	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	20.24	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	20.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	20.34	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	19.81	PC2	PASS

DC_2A_n78A-37 00-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	20.02	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	20.66	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	20.23	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	19.88	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	20.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	20.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	19.83	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	20.07	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	20.33	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	20.20	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	19.84	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	20.21	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	20.27	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	20.35	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	19.85	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	20.13	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	20.32	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	20.25	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	19.82	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	20.19	PC2	PASS
DC_2A_n78A-37	30	5+100	DFT-16QAM	M+H	Outer_Full	20.31	PC2	PASS

00-3800								
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	20.37	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	19.86	PC2	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	20.20	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth h	Modulation	Chan nel	RB Config	DutyCy cle	Factor	Result	Limi t	Verdict
DC_2A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.10	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.28	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.39	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	100%	0.00	7.70	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.74	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.88	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.29	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	100%	0.00	9.26	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.33	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.12	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.74	≤13	PASS

DC_2A_n78A-3 700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.52	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.87	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.71	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.65	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.61	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	100%	0.00	9.45	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.96	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.86	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.81	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.93	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	9.19	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.27	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.92	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	100%	0.00	9.11	≤13	PASS
DC_2A_n78A-3	30	5+70	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.79	≤13	PASS

700-3800										
DC_2A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.40	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.28	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	9.01	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.49	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.52	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.83	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.86	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.57	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.65	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.79	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.77	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+90	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.42	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.33	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	19.6%	7.08	1.50	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	19.6%	7.08	1.45	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	19.6%	7.08	1.56	≤13	PASS
DC_2A_n78A-3 700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	19.6%	7.08	1.26	≤13	PASS

Test Graphs











