
Appendix for 66A_N78A(3450-3550MHz)

Product Name: Smartphone

Model No: Black 6

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26.09	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+L	Inner_Full	26.03	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.54	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.62	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	25.32	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+L	Inner_Full	25.90	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.61	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.70	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	25.67	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+M	Inner_Full	25.84	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.15	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	24.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+M	Inner_Full	25.77	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.35	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.91	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	25.42	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+H	Inner_Full	25.63	PC2	PASS
DC_66A_n78	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.68	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.97	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	24.41	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+H	Inner_Full	25.29	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.64	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.87	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	25.99	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+L	Inner_Full	25.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.26	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.26	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	25.13	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+L	Inner_Full	25.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.19	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	23.18	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	25.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+M	Inner_Full	25.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.31	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.68	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	24.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+M	Inner_Full	25.63	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.55	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.94	PC2	PASS

DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	25.41	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+H	Inner_Full	25.52	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.88	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.04	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	24.51	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+H	Inner_Full	25.24	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.91	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.11	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	25.83	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	25.84	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.26	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.14	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	25.02	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	25.84	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.48	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	25.71	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	25.70	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.43	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	24.80	PC2	PASS
DC_66A_n78	30	5+20	DFT-16QAM	M+M	Inner_Full	25.66	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.44	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	25.41	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	25.42	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.90	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.85	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	24.40	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+H	Inner_Full	25.29	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.95	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	25.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	25.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.06	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.15	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+L	Outer_Full	24.97	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+L	Inner_Full	25.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	25.68	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+M	Inner_Full	25.74	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.14	PC2	PASS

DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	22.90	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	24.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+M	Inner_Full	25.78	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.12	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	22.79	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	25.44	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	25.71	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.57	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.63	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+H	Outer_Full	24.54	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+H	Inner_Full	25.61	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.26	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.33	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	25.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	25.75	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.28	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.86	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+L	Outer_Full	25.02	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+L	Inner_Full	25.65	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.44	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.06	PC2	PASS
DC_66A_n78	30	5+50	DFT-QPSK	M+M	Outer_Full	25.67	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	25.71	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.08	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	24.89	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+M	Inner_Full	25.69	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.19	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	25.43	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	25.59	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.77	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+H	Outer_Full	24.49	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+H	Inner_Full	25.54	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.86	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.77	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	25.63	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	25.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.12	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.47	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+L	Outer_Full	24.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+L	Inner_Full	25.75	PC2	PASS

DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	22.38	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	25.61	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	25.64	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.98	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	22.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	24.75	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+M	Inner_Full	25.64	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.10	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.98	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	25.47	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	25.60	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.22	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.69	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+H	Outer_Full	24.56	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+H	Inner_Full	25.57	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.09	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.70	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	25.84	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+L	Inner_Full	25.96	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.07	PC2	PASS
DC_66A_n78	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	22.77	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+L	Outer_Full	24.90	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+L	Inner_Full	25.94	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.30	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	25.89	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+M	Inner_Full	25.86	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.01	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	22.53	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+M	Outer_Full	24.82	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+M	Inner_Full	25.85	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.94	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.45	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	25.66	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+H	Inner_Full	25.70	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	22.60	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+H	Outer_Full	24.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+H	Inner_Full	25.67	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.11	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.64	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	25.85	PC2	PASS

DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	25.92	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.94	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.59	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+L	Outer_Full	24.82	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+L	Inner_Full	25.85	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.77	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.33	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	25.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	25.93	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.05	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.23	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	24.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+M	Inner_Full	25.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.09	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.36	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	25.72	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	25.83	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.76	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.47	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+H	Outer_Full	24.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+H	Inner_Full	25.83	PC2	PASS
DC_66A_n78	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.67	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.36	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	25.58	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	25.62	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.70	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.97	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+L	Outer_Full	24.71	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+L	Inner_Full	25.60	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.91	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.10	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	25.61	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	25.64	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.89	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.01	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	24.74	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+M	Inner_Full	25.60	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.80	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	21.90	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	25.55	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	25.62	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.81	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.24	PC2	PASS

DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+H	Outer_Full	24.65	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+H	Inner_Full	25.59	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.74	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.29	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	25.57	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	25.66	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.49	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.97	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+L	Outer_Full	24.68	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+L	Inner_Full	25.65	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.30	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.77	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	25.56	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	26.15	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.47	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.98	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	24.69	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+M	Inner_Full	25.65	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.25	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	25.55	PC2	PASS
DC_66A_n78	30	5+100	DFT-QPSK	M+H	Inner_Full	25.65	PC2	PASS

A-3450-3550								
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.48	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.03	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+H	Outer_Full	24.73	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+H	Inner_Full	25.61	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.26	PC2	PASS
DC_66A_n78 A-3450-3550	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.77	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_66A_n 78A-3450-3 550	30	5+10	DFT-QPSK	M+L	Outer_Full	5.22	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+10	DFT-16QAM	M+L	Outer_Full	5.90	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+10	DFT-QPSK	M+M	Outer_Full	5.18	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+10	DFT-16QAM	M+M	Outer_Full	5.85	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+10	DFT-QPSK	M+H	Outer_Full	5.29	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+10	DFT-16QAM	M+H	Outer_Full	5.98	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-QPSK	M+L	Outer_Full	5.18	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-16QAM	M+L	Outer_Full	5.92	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-QPSK	M+M	Outer_Full	5.14	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-16QAM	M+M	Outer_Full	5.74	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-QPSK	M+H	Outer_Full	5.15	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+15	DFT-16QAM	M+H	Outer_Full	6.07	≤13	PASS

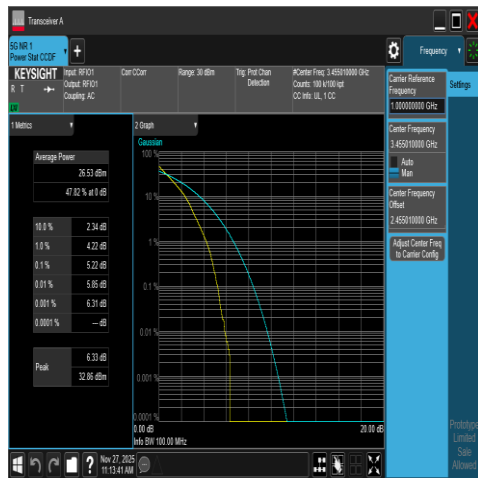
DC_66A_n 78A-3450-3 550	30	5+20	DFT-QPSK	M+L	Outer_Full	5.12	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+20	DFT-16QAM	M+L	Outer_Full	5.96	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+20	DFT-QPSK	M+M	Outer_Full	5.23	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+20	DFT-16QAM	M+M	Outer_Full	5.84	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+20	DFT-QPSK	M+H	Outer_Full	5.19	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+20	DFT-16QAM	M+H	Outer_Full	6.07	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-QPSK	M+L	Outer_Full	5.40	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-16QAM	M+L	Outer_Full	5.86	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-QPSK	M+M	Outer_Full	5.21	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-16QAM	M+M	Outer_Full	5.95	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-QPSK	M+H	Outer_Full	5.12	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+40	DFT-16QAM	M+H	Outer_Full	5.94	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+50	DFT-QPSK	M+L	Outer_Full	5.20	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+50	DFT-16QAM	M+L	Outer_Full	5.97	≤13	PASS
DC_66A_n 78A-3450-3	30	5+50	DFT-QPSK	M+M	Outer_Full	5.24	≤13	PASS

550								
DC_66A_n 78A-3450-3 550	30	5+50	DFT-16QAM	M+M	Outer_Full	5.98	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+50	DFT-QPSK	M+H	Outer_Full	5.17	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+50	DFT-16QAM	M+H	Outer_Full	5.90	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-QPSK	M+L	Outer_Full	5.66	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-16QAM	M+L	Outer_Full	6.16	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-QPSK	M+M	Outer_Full	5.19	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-16QAM	M+M	Outer_Full	6.06	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+60	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+70	DFT-QPSK	M+L	Outer_Full	5.25	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+70	DFT-16QAM	M+L	Outer_Full	6.01	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+70	DFT-QPSK	M+M	Outer_Full	5.24	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+70	DFT-16QAM	M+M	Outer_Full	5.99	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+70	DFT-QPSK	M+H	Outer_Full	5.35	≤13	PASS
DC_66A_n	30	5+70	DFT-16QAM	M+H	Outer_Full	5.91	≤13	PASS

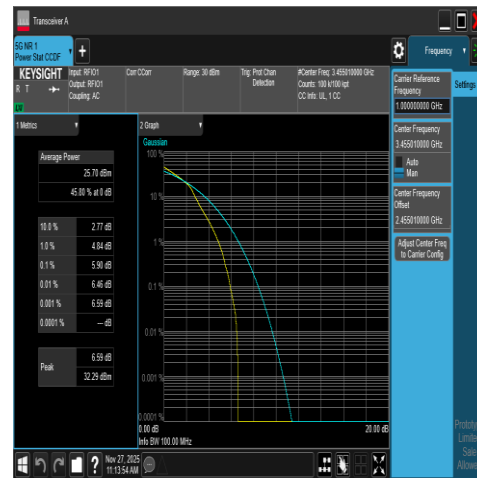
78A-3450-3 550								
DC_66A_n 78A-3450-3 550	30	5+80	DFT-QPSK	M+L	Outer_Full	5.28	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+80	DFT-16QAM	M+L	Outer_Full	5.98	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+80	DFT-QPSK	M+M	Outer_Full	5.31	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.06	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+80	DFT-QPSK	M+H	Outer_Full	5.28	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+80	DFT-16QAM	M+H	Outer_Full	6.03	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-QPSK	M+L	Outer_Full	5.31	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-16QAM	M+L	Outer_Full	6.15	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-QPSK	M+M	Outer_Full	5.60	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-16QAM	M+M	Outer_Full	6.12	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-QPSK	M+H	Outer_Full	5.35	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+90	DFT-16QAM	M+H	Outer_Full	6.05	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+100	DFT-QPSK	M+L	Outer_Full	5.35	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+100	DFT-16QAM	M+L	Outer_Full	6.05	≤13	PASS

DC_66A_n 78A-3450-3 550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.30	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+100	DFT-16QAM	M+M	Outer_Full	5.98	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+100	DFT-QPSK	M+H	Outer_Full	5.34	≤13	PASS
DC_66A_n 78A-3450-3 550	30	5+100	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS

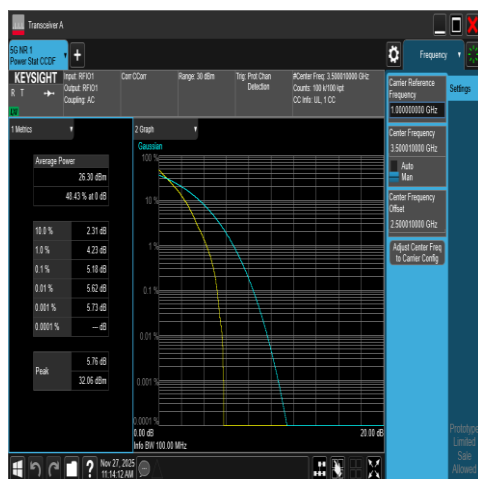
Test Graphs



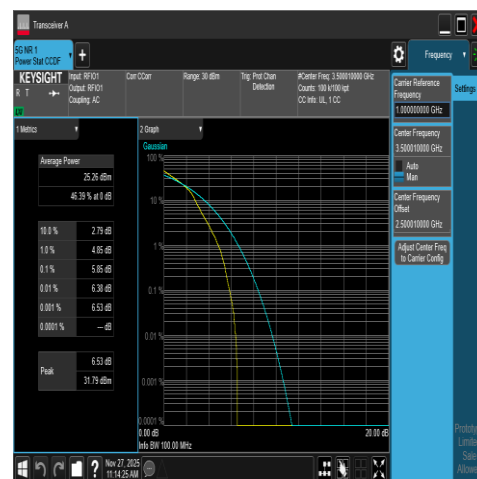
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS



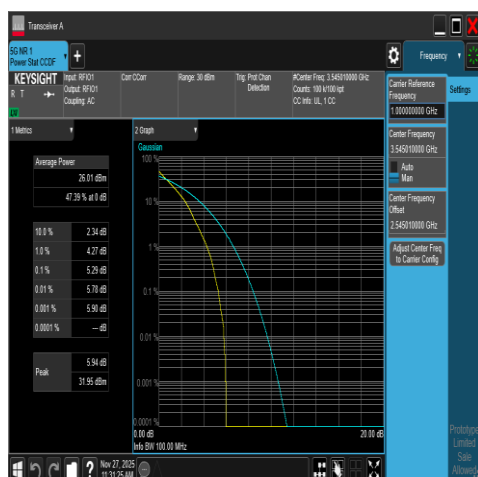
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS



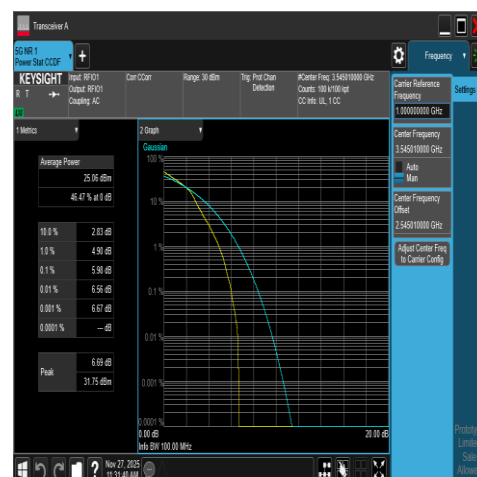
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS



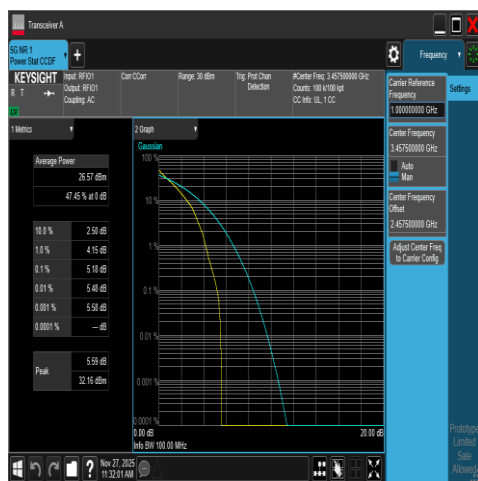
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS



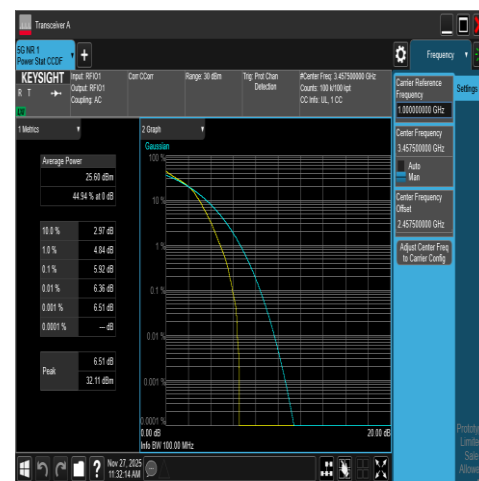
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS



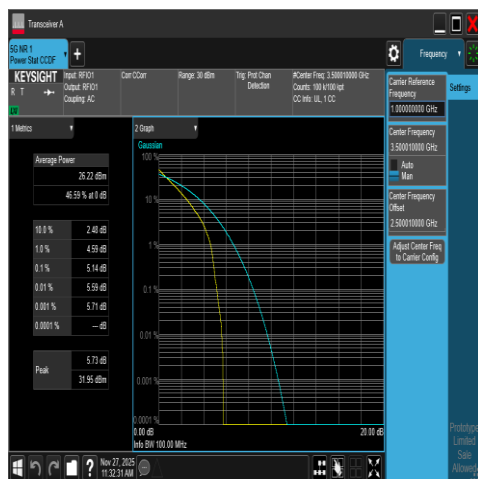
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS



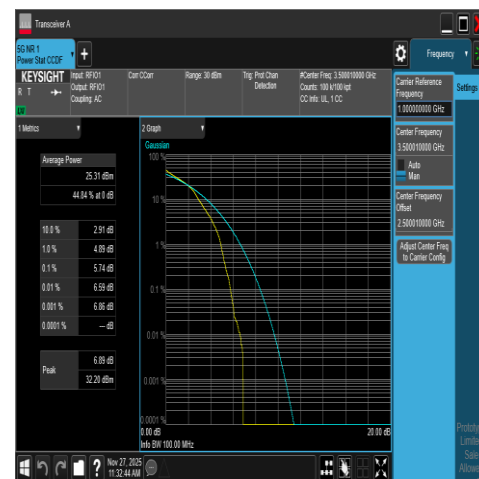
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS



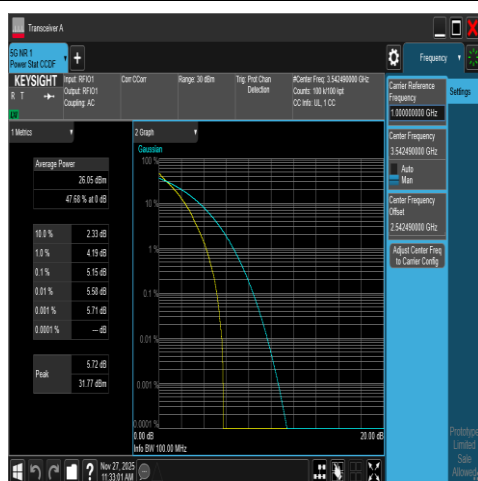
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS



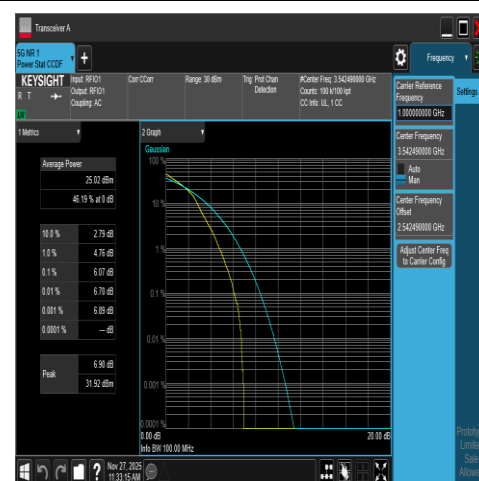
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS



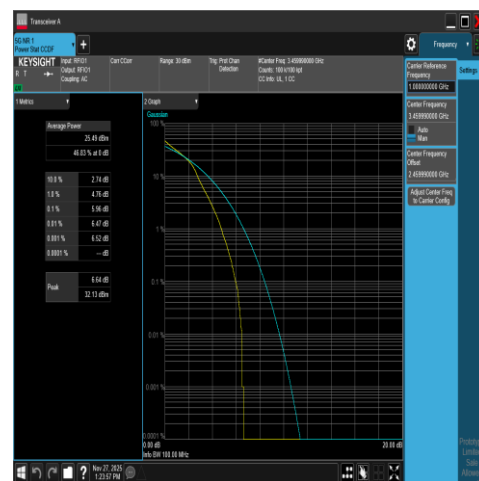
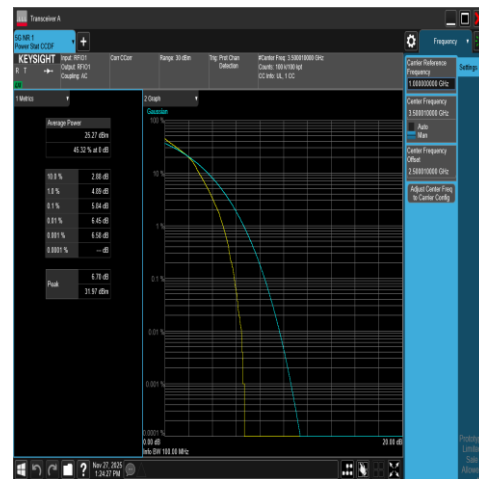
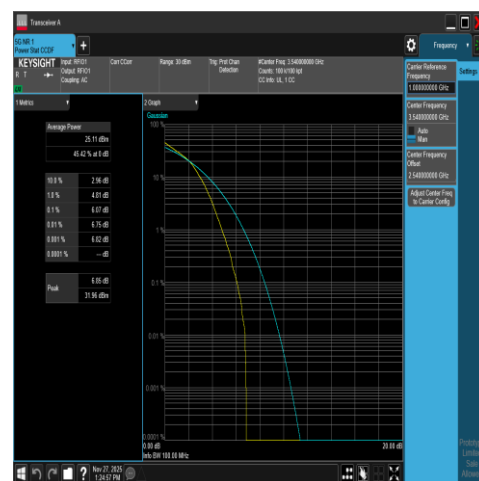
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS

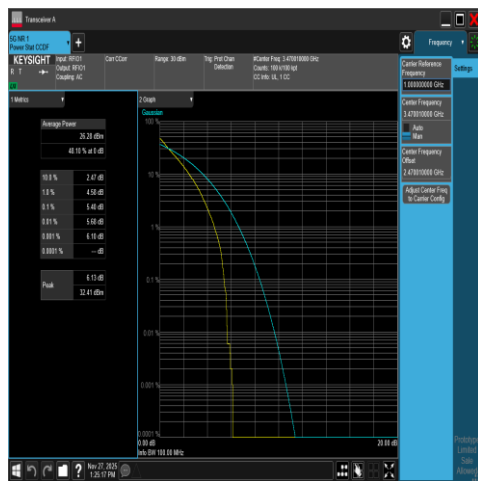


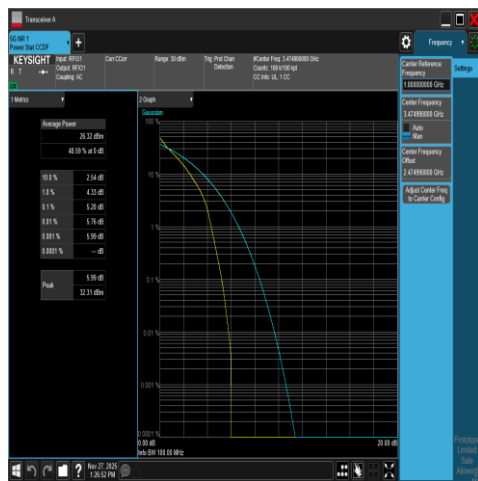
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS



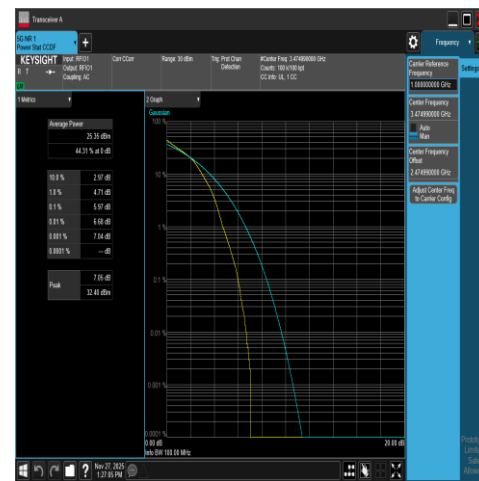
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+15-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

NTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+H-N/A-N/A-DFT-16QAM-Outer Full-PASS

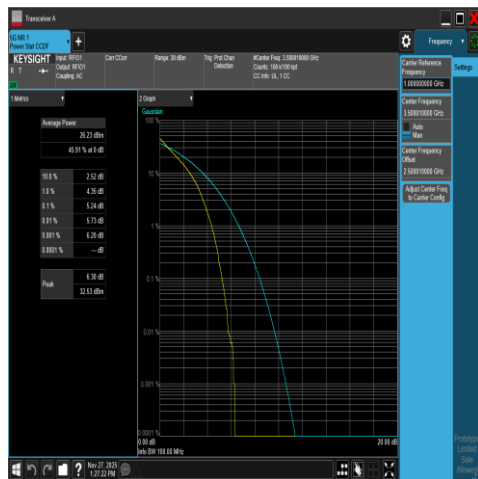




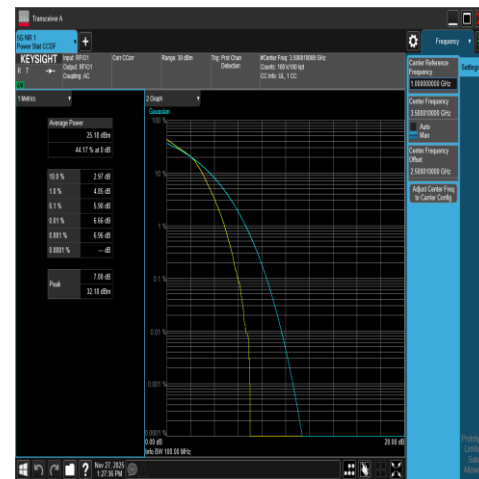
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+L-N/A-N/A-DFT-QPSK-Outter_Full-PASS



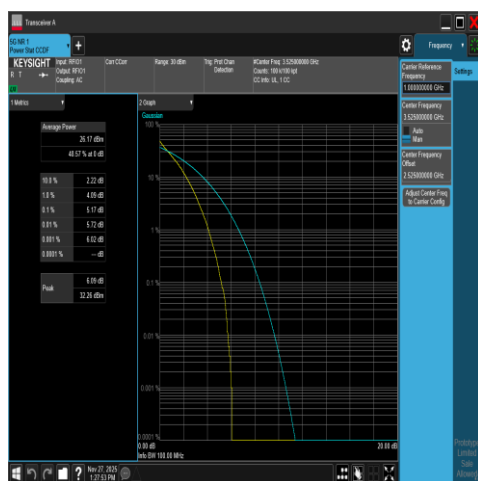
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+L-N/A-N/A-DFT-16QAM-Outter_Full-PASS



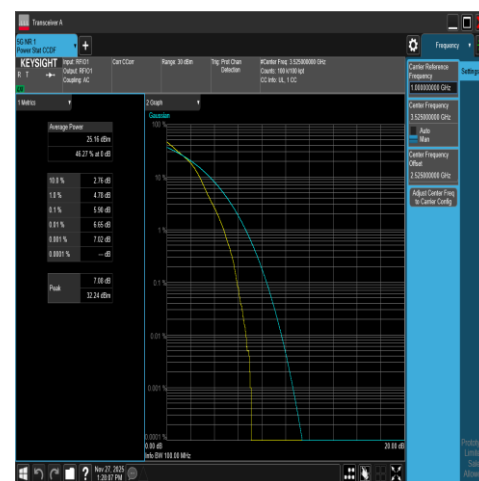
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+M-N/A-N/A-DFT-QPSK-Outter_Full-PASS



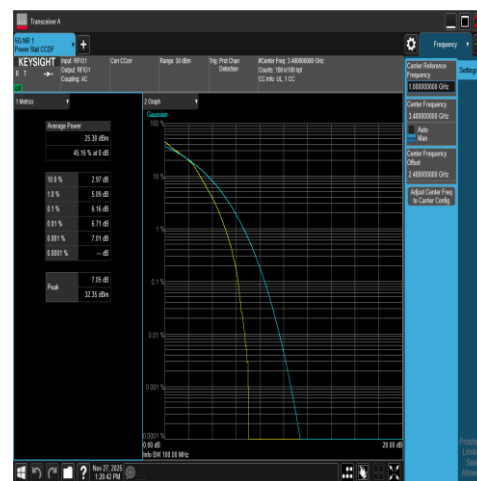
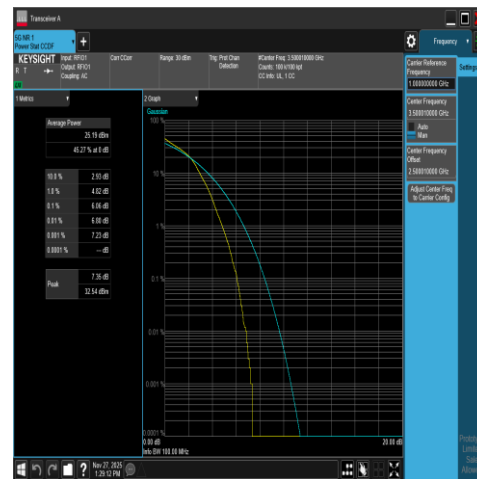
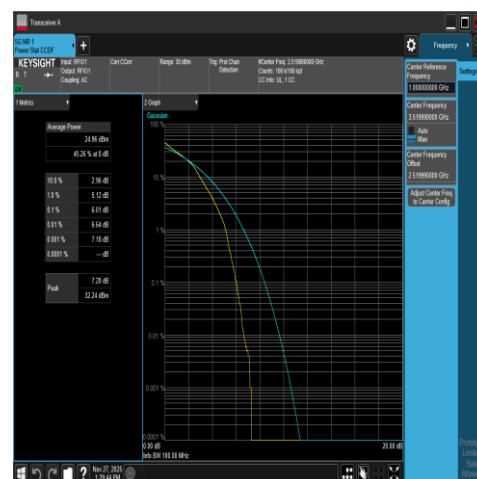
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+M-N/A-N/A-DFT-16QAM-Outter_Full-PASS

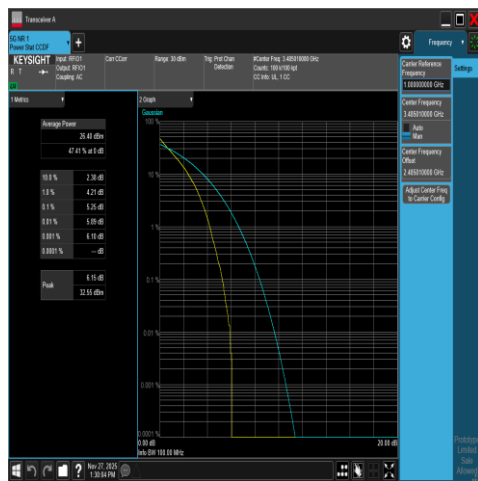


NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+H-N/A-N/A-DFT-QPSK-Outter_Full-PASS

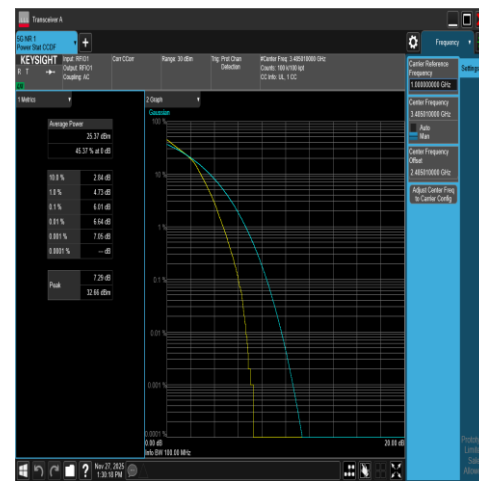


NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+H-N/A-N/A-DFT-16QAM-Outter_Full-PASS

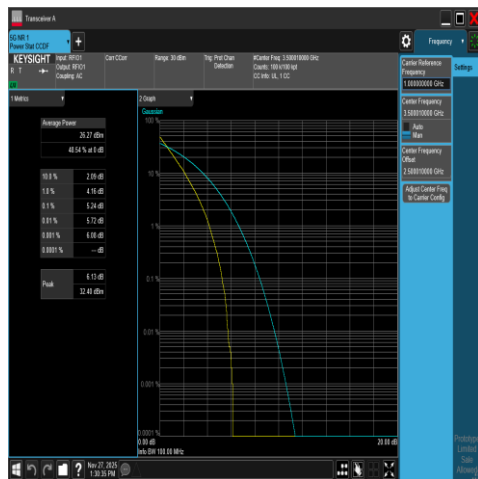
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+60-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+60-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+60-M
+H-N/A-N/A-DFT-16QAM-Outer Full-PASS



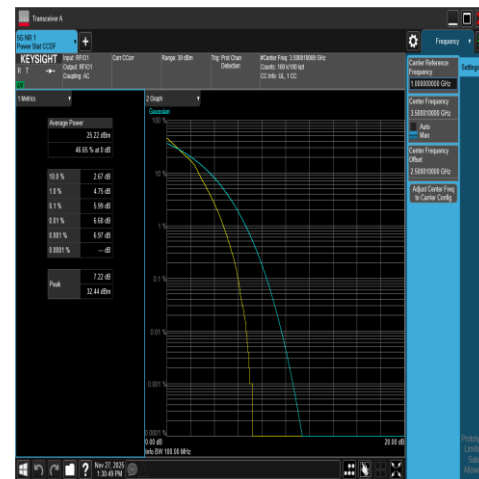
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+L-N/A-N/A-DFT-QPSK-Outter_Full-PASS



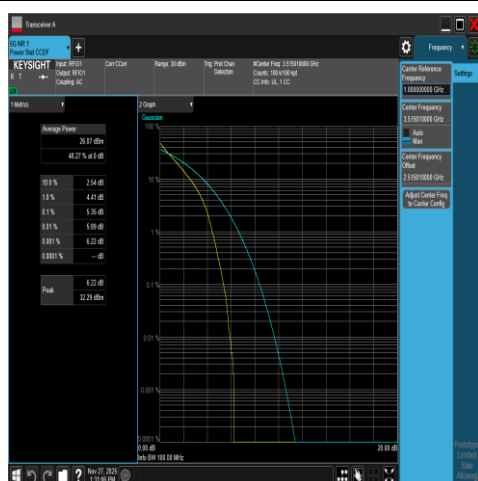
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+L-N/A-N/A-DFT-16QAM-Outter_Full-PASS



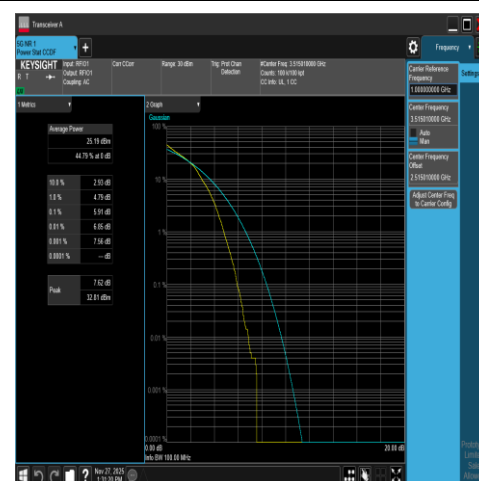
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+M-N/A-N/A-DFT-QPSK-Outter_Full-PASS



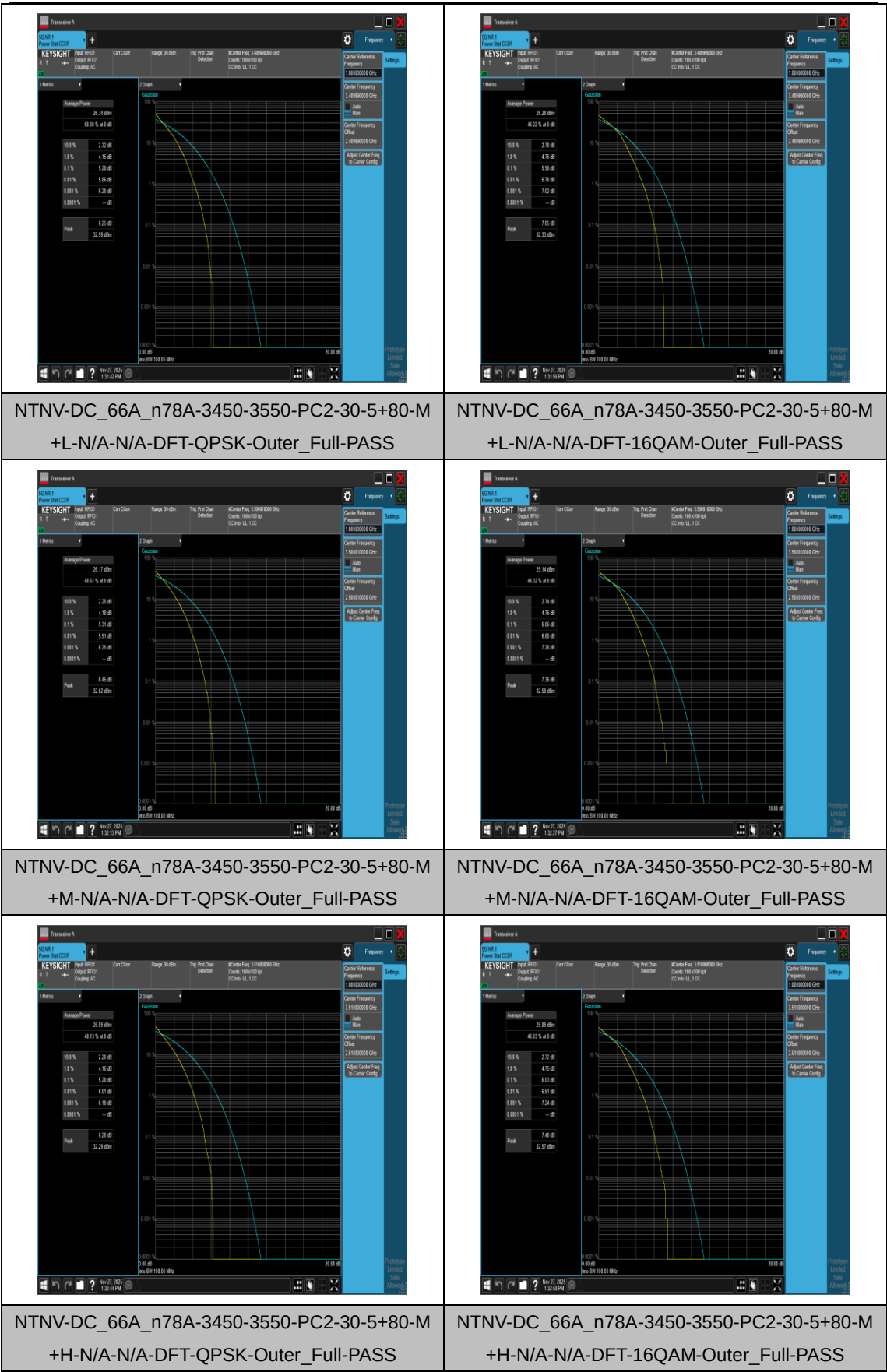
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+M-N/A-N/A-DFT-16QAM-Outter_Full-PASS

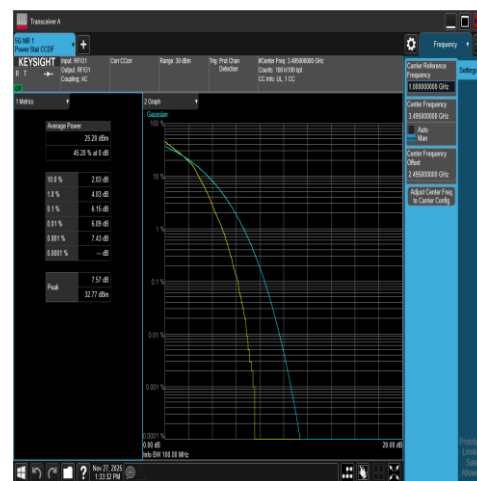
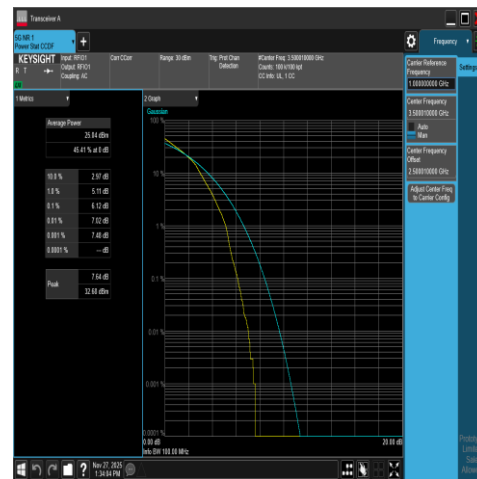
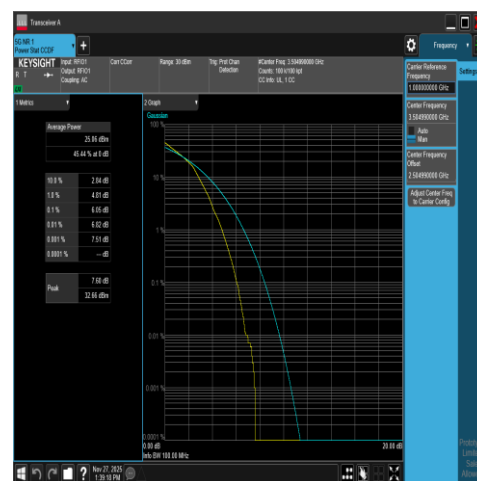


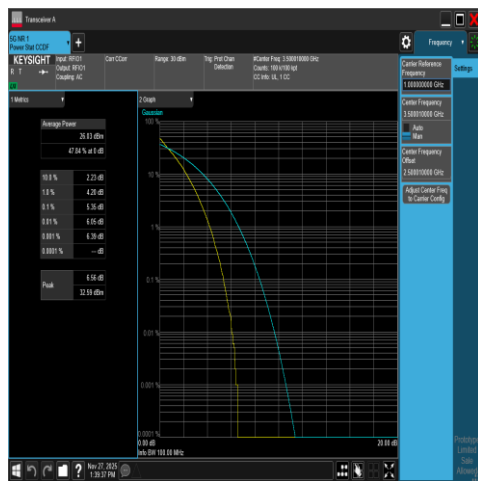
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+H-N/A-N/A-DFT-QPSK-Outter_Full-PASS



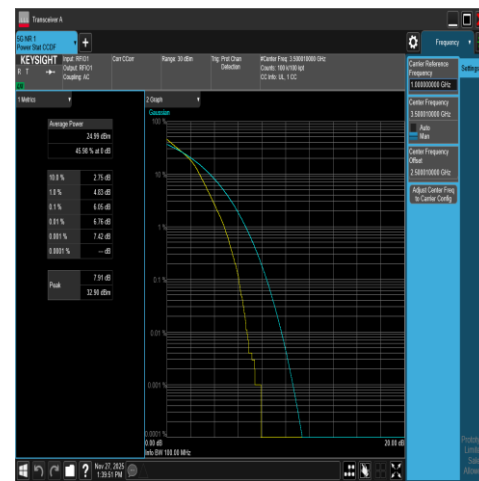
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+70-M
+H-N/A-N/A-DFT-16QAM-Outter_Full-PASS



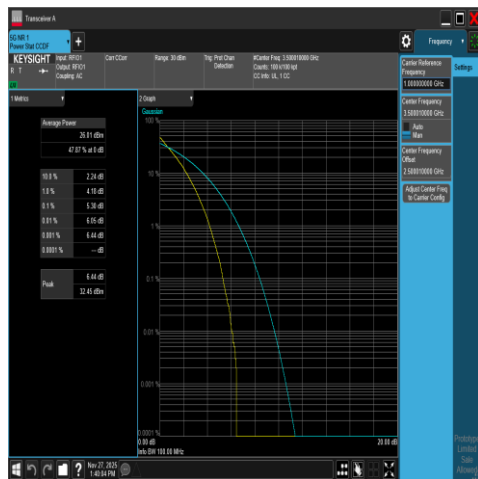
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+90-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+90-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+90-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS



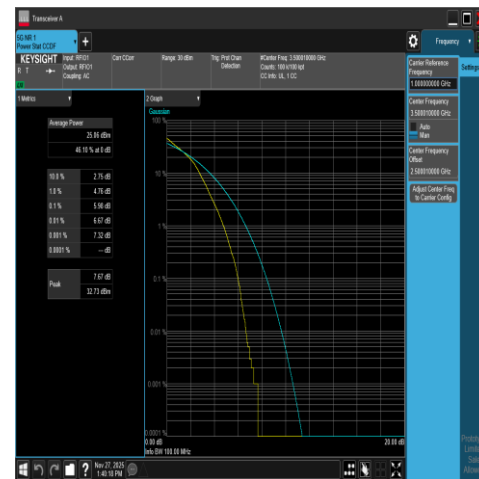
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+L-N/A-N/A-DFT-QPSK-Outer_Full-PASS



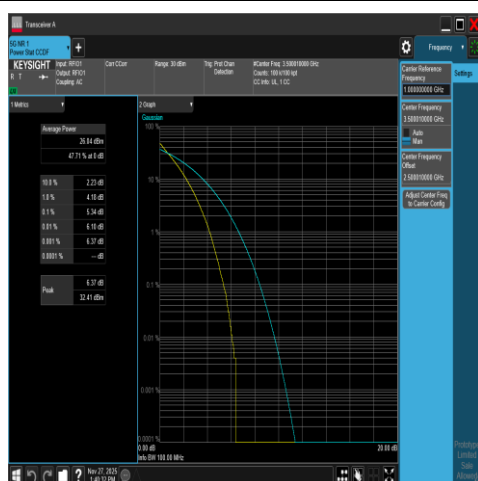
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+L-N/A-N/A-DFT-16QAM-Outer_Full-PASS



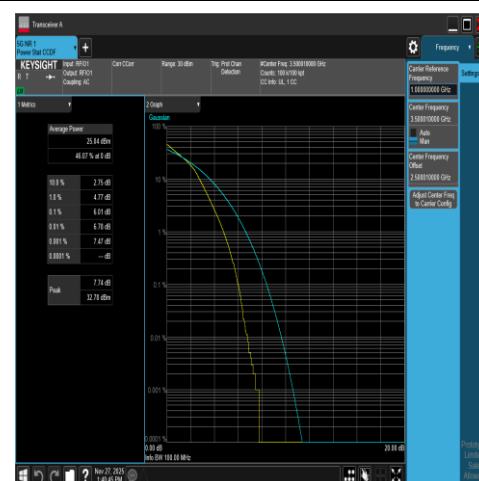
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+M-N/A-N/A-DFT-QPSK-Outer_Full-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+M-N/A-N/A-DFT-16QAM-Outer_Full-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+H-N/A-N/A-DFT-QPSK-Outer_Full-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+100-M+H-N/A-N/A-DFT-16QAM-Outer_Full-PASS

Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

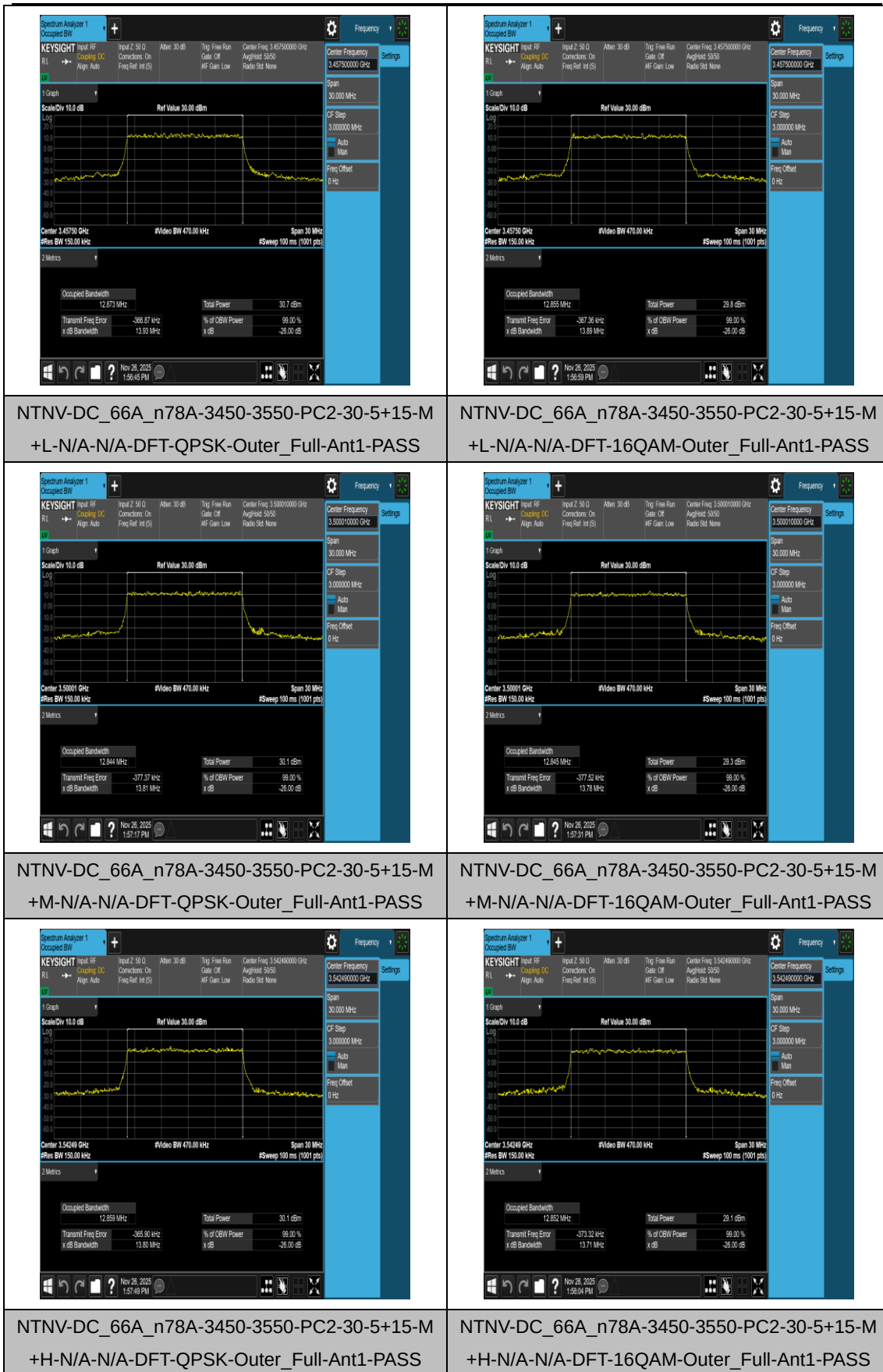
Test Result

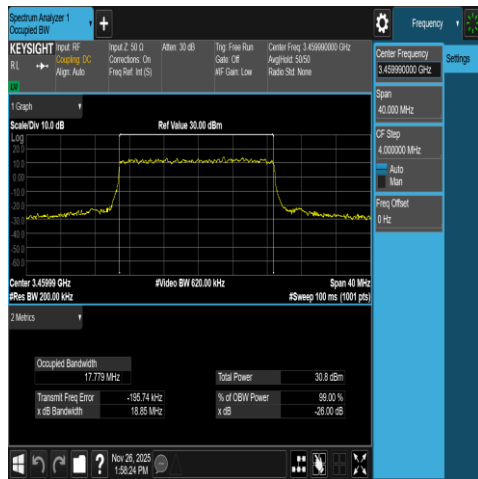
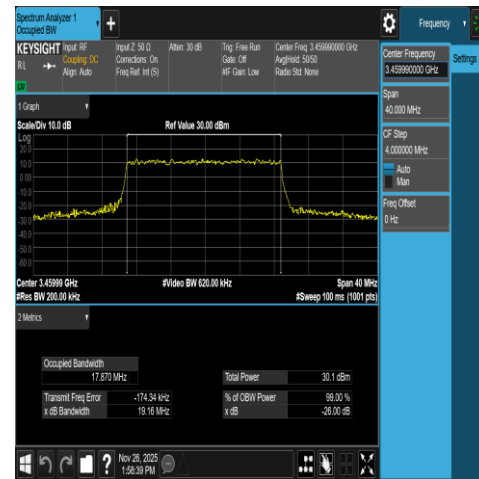
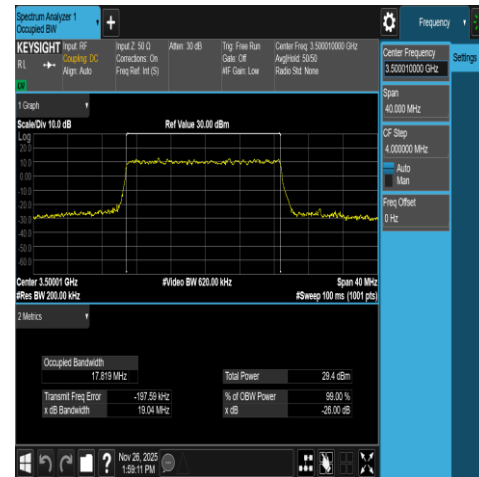
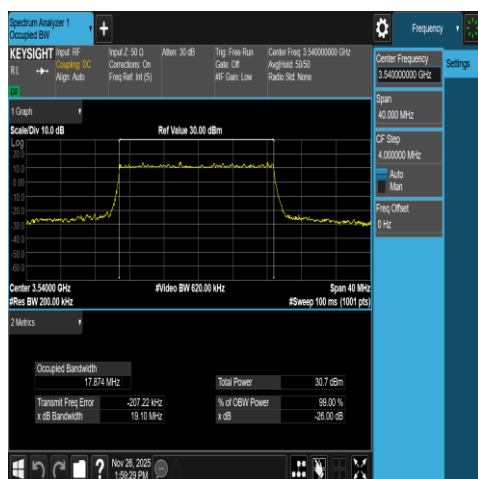
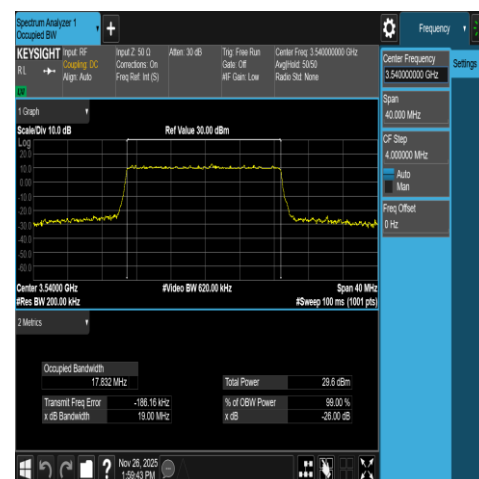
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_66A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	8.5975	9.429	PASS
DC_66A_n78A-3450-3550	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5664	9.208	PASS
DC_66A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	8.6036	9.375	PASS
DC_66A_n78A-3450-3550	30	5+10	DFT-16QAM	M+M	Outer_Full	8.5633	9.074	PASS
DC_66A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	8.6106	9.414	PASS
DC_66A_n78A-3450-3550	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5888	9.375	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	12.873	13.93	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-16QAM	M+L	Outer_Full	12.855	13.89	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	12.844	13.81	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-16QAM	M+M	Outer_Full	12.845	13.78	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	12.859	13.80	PASS
DC_66A_n78A-3450-3550	30	5+15	DFT-16QAM	M+H	Outer_Full	12.852	13.71	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	17.779	18.85	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	17.870	19.16	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	17.863	18.96	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	17.819	19.04	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	17.874	19.10	PASS
DC_66A_n78A-3450-3550	30	5+20	DFT-16QAM	M+H	Outer_Full	17.832	19.00	PASS

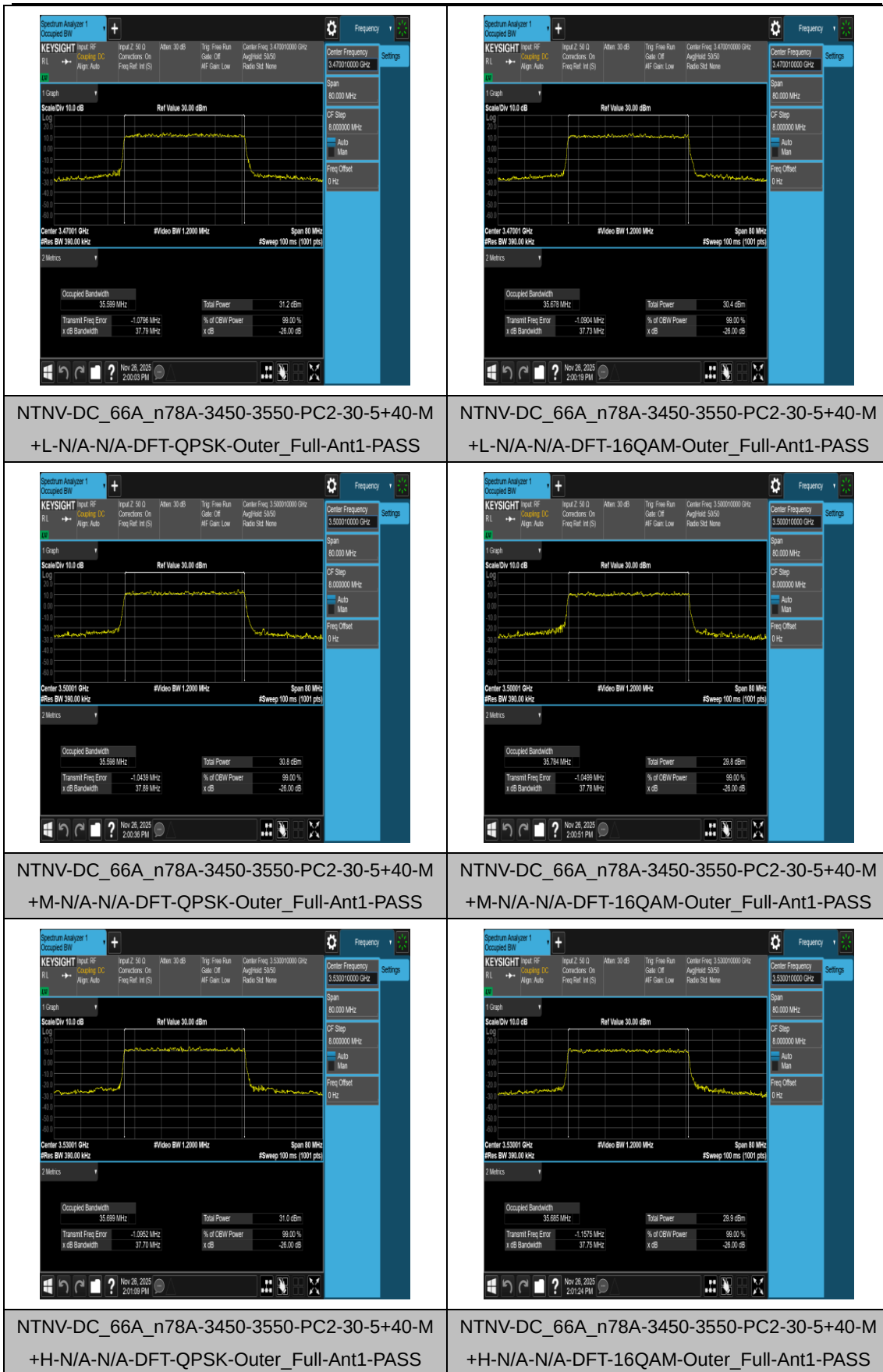
550								
DC_66A_n78A-3450-3 550	30	5+40	DFT-QPSK	M+L	Outer_Full	35.599	37.79	PASS
DC_66A_n78A-3450-3 550	30	5+40	DFT-16QAM	M+L	Outer_Full	35.678	37.73	PASS
DC_66A_n78A-3450-3 550	30	5+40	DFT-QPSK	M+M	Outer_Full	35.598	37.89	PASS
DC_66A_n78A-3450-3 550	30	5+40	DFT-16QAM	M+M	Outer_Full	35.784	37.78	PASS
DC_66A_n78A-3450-3 550	30	5+40	DFT-QPSK	M+H	Outer_Full	35.699	37.70	PASS
DC_66A_n78A-3450-3 550	30	5+40	DFT-16QAM	M+H	Outer_Full	35.685	37.75	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-QPSK	M+L	Outer_Full	45.544	47.91	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-16QAM	M+L	Outer_Full	45.756	48.45	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-QPSK	M+M	Outer_Full	45.727	48.40	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-16QAM	M+M	Outer_Full	45.667	48.12	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-QPSK	M+H	Outer_Full	45.706	48.18	PASS
DC_66A_n78A-3450-3 550	30	5+50	DFT-16QAM	M+H	Outer_Full	45.616	48.06	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+L	Outer_Full	57.830	60.16	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-16QAM	M+L	Outer_Full	57.867	60.30	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+M	Outer_Full	57.901	60.10	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-16QAM	M+M	Outer_Full	57.922	59.90	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+H	Outer_Full	57.623	60.09	PASS
DC_66A_n78A-3450-3 550	30	5+60	DFT-16QAM	M+H	Outer_Full	57.641	59.99	PASS
DC_66A_n78A-3450-3 550	30	5+70	DFT-QPSK	M+L	Outer_Full	64.345	67.68	PASS
DC_66A_n78A-3450-3 550	30	5+70	DFT-16QAM	M+L	Outer_Full	64.152	67.43	PASS
DC_66A_n78A-3450-3 550	30	5+70	DFT-QPSK	M+M	Outer_Full	64.274	67.27	PASS

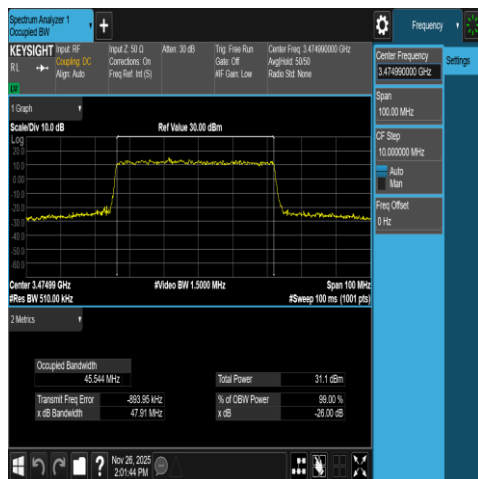
DC_66A_n78A-3450-3 550	30	5+70	DFT-16QAM	M+M	Outer_Full	64.388	67.63	PASS
DC_66A_n78A-3450-3 550	30	5+70	DFT-QPSK	M+H	Outer_Full	64.219	67.25	PASS
DC_66A_n78A-3450-3 550	30	5+70	DFT-16QAM	M+H	Outer_Full	64.228	67.11	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+L	Outer_Full	76.855	80.03	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-16QAM	M+L	Outer_Full	76.983	80.12	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+M	Outer_Full	77.125	80.11	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-16QAM	M+M	Outer_Full	76.908	79.99	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+H	Outer_Full	77.267	79.88	PASS
DC_66A_n78A-3450-3 550	30	5+80	DFT-16QAM	M+H	Outer_Full	77.093	80.12	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+L	Outer_Full	86.569	89.74	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-16QAM	M+L	Outer_Full	86.555	90.09	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+M	Outer_Full	86.698	89.91	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-16QAM	M+M	Outer_Full	86.513	89.81	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+H	Outer_Full	86.434	89.91	PASS
DC_66A_n78A-3450-3 550	30	5+90	DFT-16QAM	M+H	Outer_Full	86.598	89.90	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+L	Outer_Full	95.957	99.90	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-16QAM	M+L	Outer_Full	96.141	100.6	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+M	Outer_Full	95.946	99.86	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-16QAM	M+M	Outer_Full	96.155	100.8	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+H	Outer_Full	95.958	99.85	PASS
DC_66A_n78A-3450-3 550	30	5+100	DFT-16QAM	M+H	Outer_Full	96.147	101.0	PASS

Configuration	Occupied Bandwidth (MHz)	Total Power (dBm)	Transmitt Freq Error (kHz)	% of OBW Power (dB)
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +L-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS	8.5975	30.0	-14.301	99.0%
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +L-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS	8.5964	29.4	2.566	99.0%
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +M-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS	8.6038	29.9	-13.169	99.0%
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +M-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS	8.5933	28.7	-17.417	99.0%
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +H-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS	8.6105	29.7	-14.797	99.0%
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+10-M +H-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS	8.5888	28.9	-13.033	99.0%



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+L-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+H-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASSNTNV-DC_66A_n78A-3450-3550-PC2-30-5+20-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS





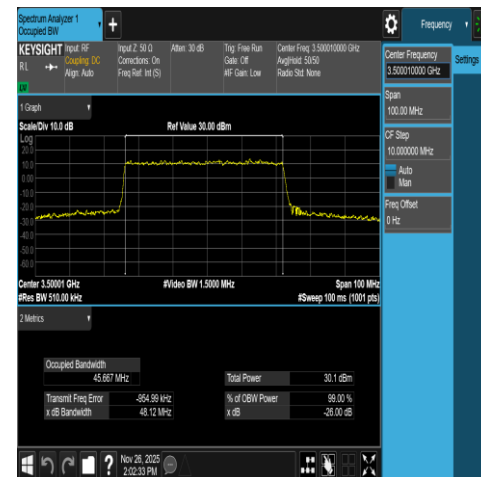
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+L-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS



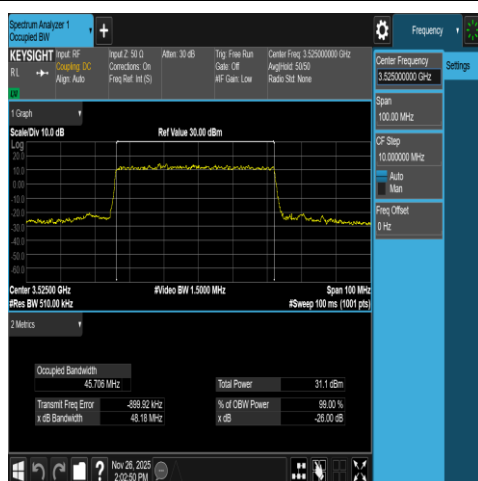
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS



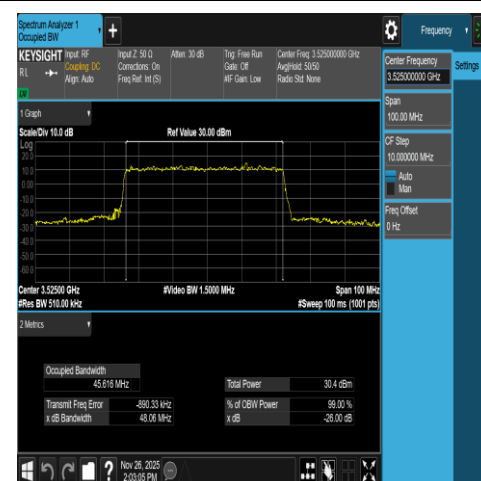
NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+M-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+H-N/A-N/A-DFT-QPSK-Outer_Full-Ant1-PASS



NTNV-DC_66A_n78A-3450-3550-PC2-30-5+50-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-Ant1-PASS

