
Appendix for 66A_N78A(3700-3800MHz)

Product Name: Smartphone

Model No: Black 6

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	26.28	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+L	Inner_Full	26.44	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	23.59	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	23.77	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+L	Outer_Full	25.51	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+L	Inner_Full	26.21	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	23.66	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	23.78	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	26.95	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+M	Inner_Full	26.95	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	24.28	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	24.27	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+M	Outer_Full	26.13	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+M	Inner_Full	26.86	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	24.44	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	24.47	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	26.19	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+H	Inner_Full	26.23	PC2	PASS
DC_66A_n78	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+H	Outer_Full	25.42	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+H	Inner_Full	26.01	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	23.79	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	26.33	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+L	Inner_Full	26.37	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	23.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	23.53	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+L	Outer_Full	25.39	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+L	Inner_Full	26.29	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	23.26	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	23.52	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	26.97	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+M	Inner_Full	26.85	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	24.28	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	24.17	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+M	Outer_Full	26.13	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+M	Inner_Full	26.70	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	24.47	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	24.45	PC2	PASS

DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	26.27	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+H	Inner_Full	26.19	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.83	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.81	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+H	Outer_Full	25.51	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+H	Inner_Full	26.00	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	26.37	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	26.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+L	Outer_Full	25.29	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+L	Inner_Full	26.29	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	23.61	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	23.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	26.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	26.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.34	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.29	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+M	Outer_Full	26.06	PC2	PASS
DC_66A_n78	30	5+20	DFT-16QAM	M+M	Inner_Full	26.76	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	24.28	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	24.18	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	26.19	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	26.19	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.78	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.73	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+H	Outer_Full	25.37	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+H	Inner_Full	26.09	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.72	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.71	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	26.30	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	26.36	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.10	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+L	Outer_Full	25.37	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+L	Inner_Full	26.21	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.86	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	23.34	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	26.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+M	Inner_Full	26.84	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.01	PC2	PASS

DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.90	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+M	Outer_Full	25.88	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+M	Inner_Full	26.76	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.81	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	26.30	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	26.33	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.68	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.48	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+H	Outer_Full	25.55	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+H	Inner_Full	26.20	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	23.43	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.25	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	26.31	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	26.44	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.46	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.94	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+L	Outer_Full	25.54	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+L	Inner_Full	26.30	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.57	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	24.11	PC2	PASS
DC_66A_n78	30	5+50	DFT-QPSK	M+M	Outer_Full	26.70	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	26.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.20	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.18	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+M	Outer_Full	25.96	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+M	Inner_Full	26.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	24.13	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	24.10	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	26.31	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	26.35	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.98	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.66	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+H	Outer_Full	25.58	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+H	Inner_Full	26.21	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	24.02	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	23.75	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	26.33	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	26.49	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	23.27	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.61	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+L	Outer_Full	25.54	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+L	Inner_Full	26.37	PC2	PASS

DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	23.05	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	26.64	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	26.77	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.91	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.93	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+M	Outer_Full	25.95	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+M	Inner_Full	26.66	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.80	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	26.24	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	26.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.48	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+H	Outer_Full	25.58	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+H	Inner_Full	26.24	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.67	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	23.38	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	26.47	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+L	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.11	PC2	PASS
DC_66A_n78	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.41	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+L	Outer_Full	25.54	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+L	Inner_Full	26.47	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	23.44	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.72	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	26.93	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+M	Inner_Full	26.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.75	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.79	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+M	Outer_Full	25.95	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+M	Inner_Full	26.78	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.69	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.68	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	26.58	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+H	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.68	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.48	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+H	Outer_Full	25.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+H	Inner_Full	26.48	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.71	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	23.52	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	26.85	PC2	PASS

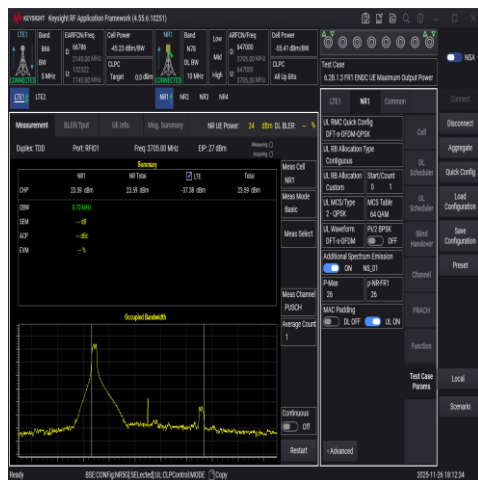
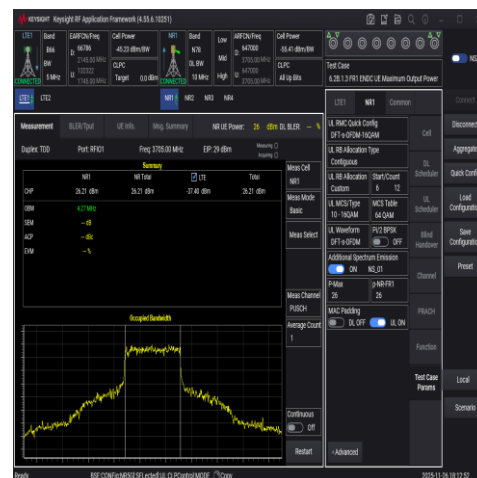
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	26.99	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.42	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.66	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+L	Outer_Full	25.86	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+L	Inner_Full	26.86	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.16	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.45	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	26.84	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	26.92	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.50	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.51	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+M	Outer_Full	25.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+M	Inner_Full	26.80	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.61	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.68	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	26.85	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	26.84	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.74	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.65	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+H	Outer_Full	25.88	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+H	Inner_Full	26.73	PC2	PASS
DC_66A_n78	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.61	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.50	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	26.63	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	26.70	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.16	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.30	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+L	Outer_Full	25.82	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+L	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.36	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	26.59	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	26.66	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.23	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.35	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+M	Outer_Full	25.77	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+M	Inner_Full	26.51	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.14	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.23	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	26.57	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	26.65	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.36	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.40	PC2	PASS

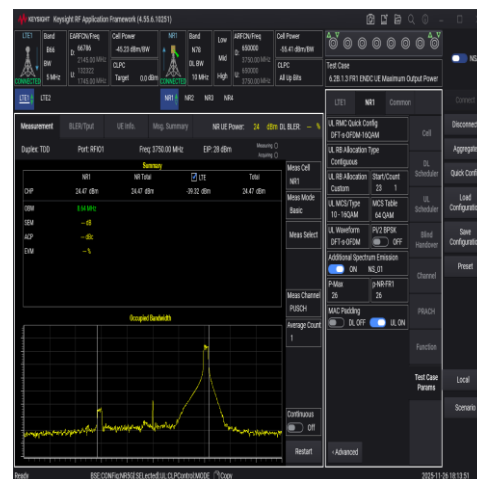
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DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+H	Inner_Full	26.53	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.35	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.43	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	26.58	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	27.01	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.89	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.14	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+L	Outer_Full	25.77	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+L	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.64	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.92	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	26.56	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	26.67	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.88	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.18	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+M	Outer_Full	25.72	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+M	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.73	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.90	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	26.57	PC2	PASS
DC_66A_n78	30	5+100	DFT-QPSK	M+H	Inner_Full	26.69	PC2	PASS

A-3700-3800								
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.90	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.15	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+H	Outer_Full	25.70	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+H	Inner_Full	26.60	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.68	PC2	PASS
DC_66A_n78 A-3700-3800	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.92	PC2	PASS

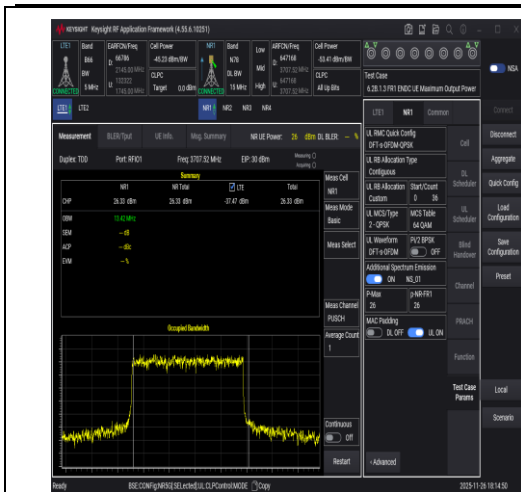
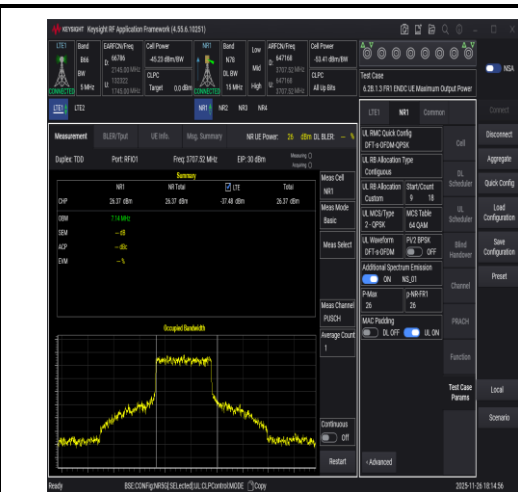
Test Graphs

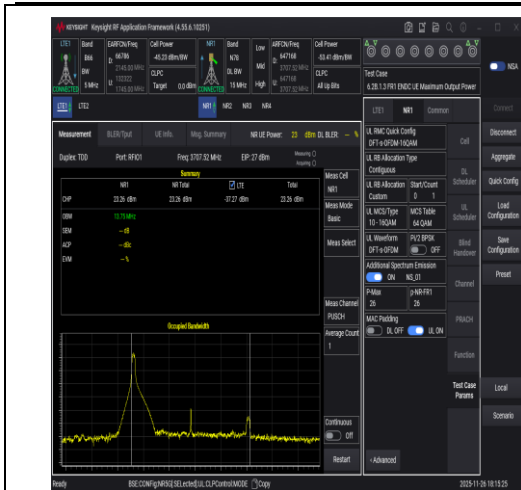
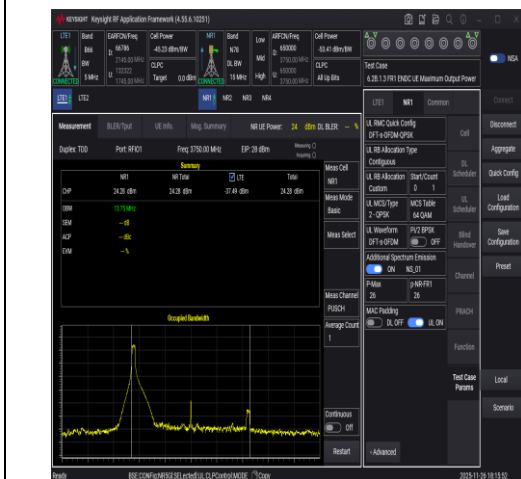
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+L-N/A-N/A-DFT-QPSK-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+L-N/A-N/A-DFT-16QAM-Inner_Full-PASS

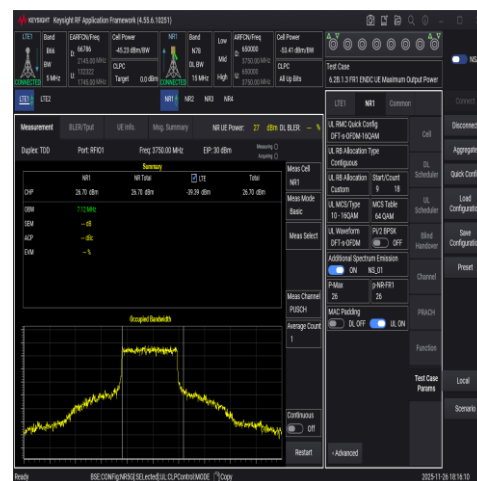
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+L-N/A-N/A-DFT-16QAM-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-QPSK-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-16QAM-Inner Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+M-N/A-N/A-DFT-16OAM-Edge 1RB Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+H-N/A-N/A-DFT-QPSK-Inner Full-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+H-N/A-N/A-DFT-QPSK-Edge 1RB Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+H-N/A-N/A-DFT-16QAM-Inner Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+10-M
+H-N/A-N/A-DFT-16QAM-Edge 1RB Right-PASS

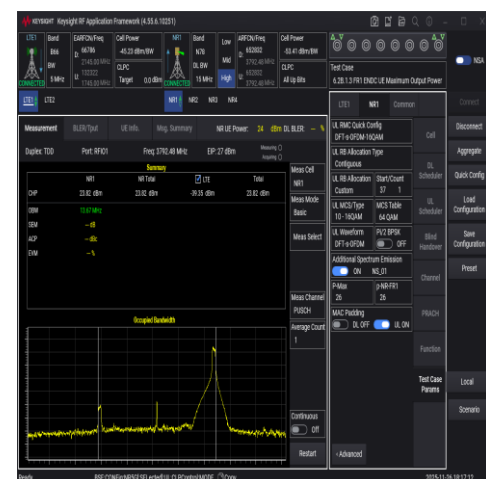
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+L-N/A-N/A-DFT-QPSK-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+L-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+L-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+L-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+L-N/A-N/A-DFT-16QAM-Inner_Full-PASS

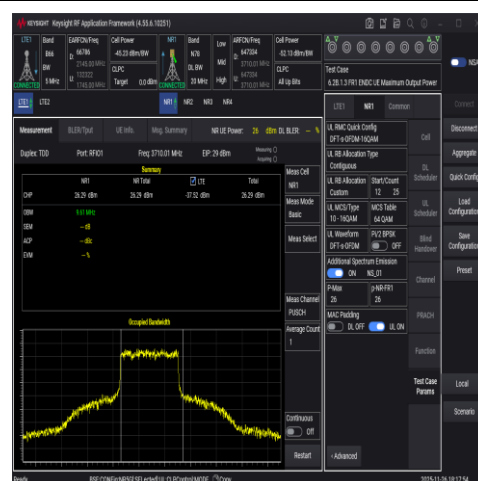
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+L-N/A-N/A-DFT-16QAM-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-QPSK-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS

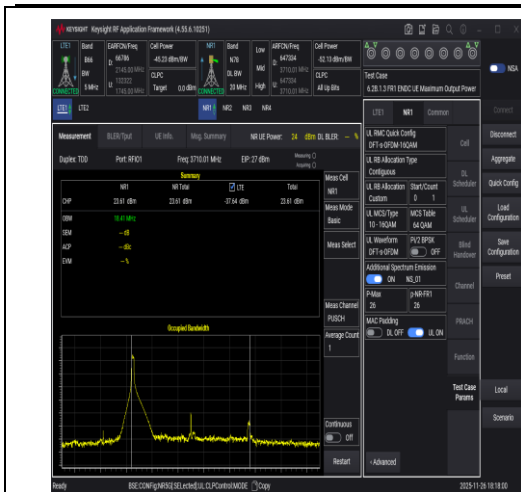
NTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-16QAM-Inner Full-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+M-N/A-N/A-DFT-16OAM-Edge 1RB Right-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-QPSK-Inner Full-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-16QAM-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-16QAM-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-16QAM-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+15-M
+H-N/A-N/A-DFT-16QAM-Edge_1RB_Right-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+L-N/A-N/A-DFT-OPSK-Inner Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+L-N/A-N/A-DFT-QPSK-Edge 1RB Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+L-N/A-N/A-DFT-16OAM-Inner Full-PASS

NTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+L-N/A-N/A-DFT-16QAM-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+L-N/A-N/A-DFT-16QAM-Edge_1RB_Right-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+M-N/A-N/A-DFT-QPSK-Outer_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+M-N/A-N/A-DFT-QPSK-Inner_Full-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Left-PASSNTNV-DC_66A_n78A-3700-3800-PC2-30-5+20-M
+M-N/A-N/A-DFT-QPSK-Edge_1RB_Right-PASS

