



SGS-CSTC Standards Technical Services Co., Ltd. Shenzhen Branch

Page: 31 of 54

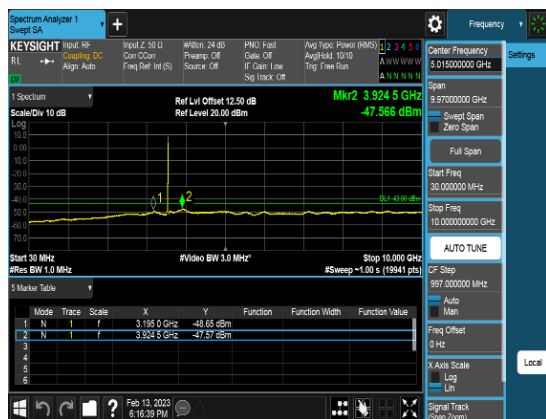
Port1_10M_3555MHz_TM3.1_CHL_CSE0a



Port1_10M_3555MHz_TM3.1_CHL_CSE0b



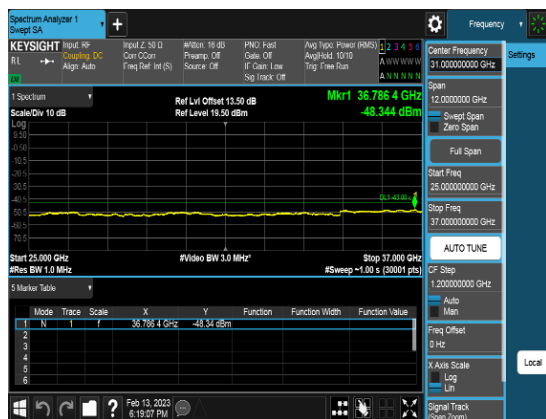
Port1_10M_3555MHz_TM3.1_CHL_CSE1



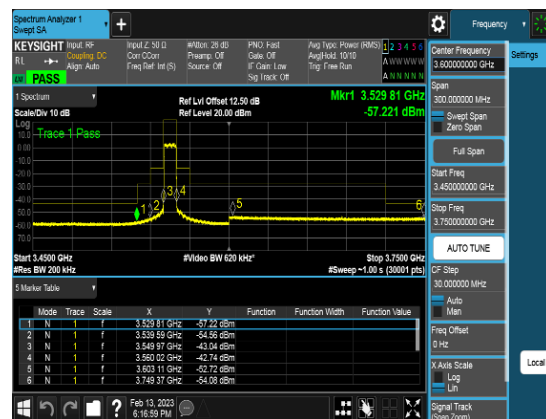
Port1_10M_3555MHz_TM3.1_CHL_CSE2



Port1_10M_3555MHz_TM3.1_CHL_CSE3



Port1_10M_3555MHz_TM3.1_CHL_CSEpart96



Unless otherwise agreed in writing, this document is issued by the Company subject to its General Conditions of Service printed overleaf, available on request or accessible at <http://www.sgs.com/en/Terms-and-Conditions.aspx> and, for electronic format documents, subject to Terms and Conditions for Electronic Documents at <http://www.sgs.com/en/terms-and-conditions/Terms-a-Documents.aspx>. Attention is drawn to the limitation of liability, indemnification and jurisdiction issues defined therein. Any holder of this document is advised that information contained hereon reflects the Company's findings at the time of its intervention only and within the limits of Client's instructions, if any. The Company's sole responsibility is to its Client and this document does not exonerate parties to a transaction from exercising all their rights and obligations under the transaction documents. This document cannot be reproduced except in full, without prior written approval of the Company. Any unauthorized alteration, forgery or falsification of the content or appearance of this document is unlawful and offenders may be prosecuted to the fullest extent of the law. Unless otherwise stated the results shown in this test report refer only to the sample(s) tested and such sample(s) are retained for 30 days only.
Attention: To check the authenticity of testing inspection report & certificate, please contact us at telephone: (86-755) 8307 1443, or email: CN.Doccheck@sgs.com
No.1 Workshop, M-10, Middle Section, Science & Technology Park, Shenzhen, China 518057 t (86-755) 26012053 f (86-755) 26710594 www.sgsgroup.com.cn
中国·深圳·科技园中区M-10栋一号厂房 邮编: 518057 t (86-755) 26012053 f (86-755) 26710594 sgs.china@sgs.com

Member of the SGS Group (SGS SA)

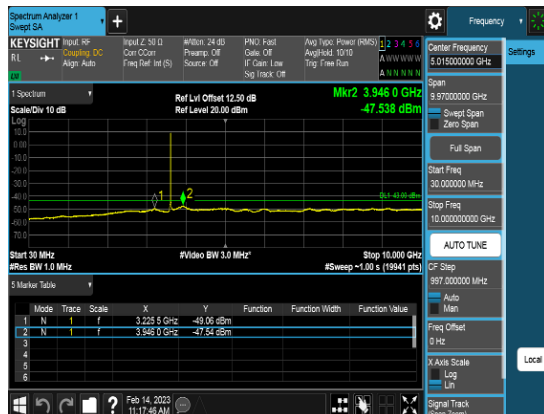
Port1_10M_3625MHz_TM3.1_CHM_CSE0a



Port1_10M_3625MHz_TM3.1_CHM_CSE0b



Port1_10M_3625MHz_TM3.1_CHM_CSE1



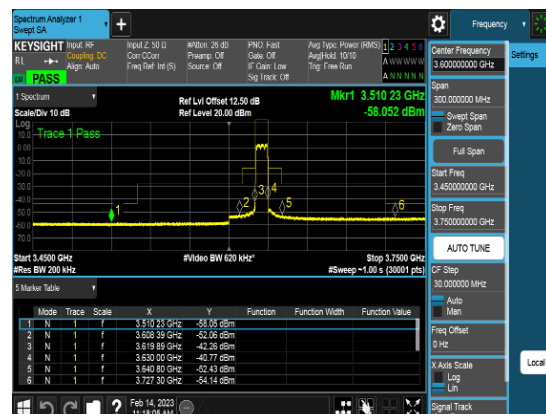
Port1_10M_3625MHz_TM3.1_CHM_CSE2



Port1_10M_3625MHz_TM3.1_CHM_CSE3



Port1_10M_3625MHz_TM3.1_CHM_CSEpart96



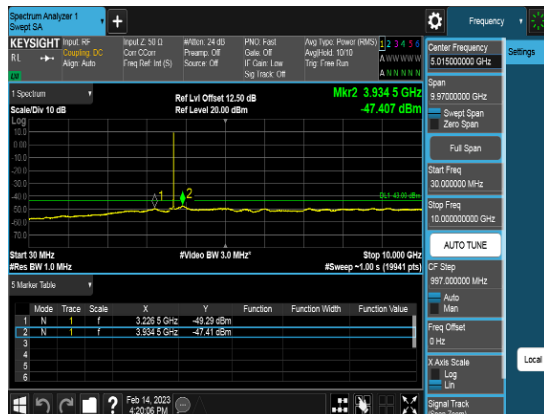
Port1_10M_3695MHz_TM3.1_CHH_CSE0a



Port1_10M_3695MHz_TM3.1_CHH_CSE0b



Port1_10M_3695MHz_TM3.1_CHH_CSE1



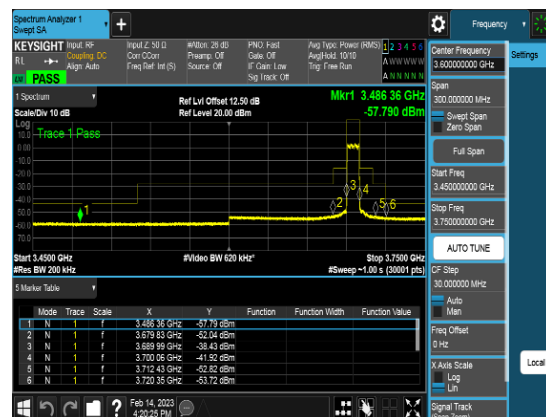
Port1_10M_3695MHz_TM3.1_CHH_CSE2



Port1_10M_3695MHz_TM3.1_CHH_CSE3



Port1_10M_3695MHz_TM3.1_CHH_CSEpart96



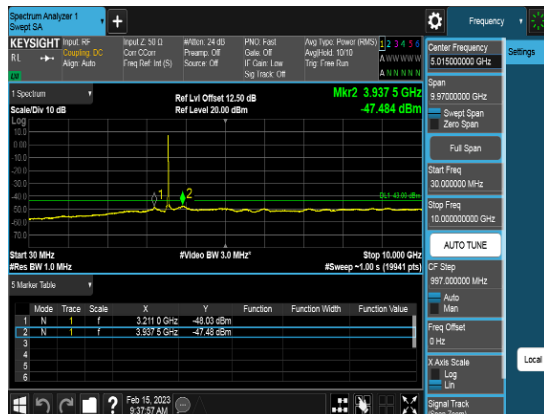
Port1_20M_3560MHz_TM3.1_CHL_CSE0a



Port1_20M_3560MHz_TM3.1_CHL_CSE0b



Port1_20M_3560MHz_TM3.1_CHL_CSE1



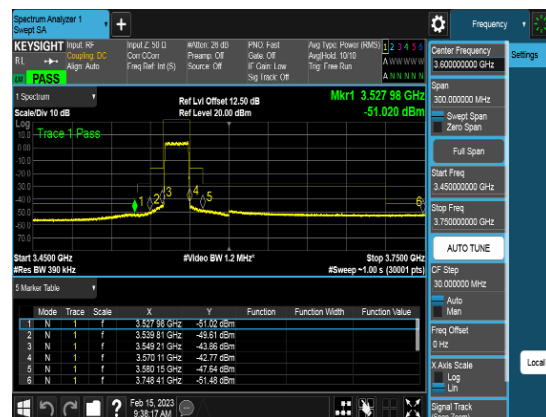
Port1_20M_3560MHz_TM3.1_CHL_CSE2



Port1_20M_3560MHz_TM3.1_CHL_CSE3



Port1_20M_3560MHz_TM3.1_CHL_CSEpart96



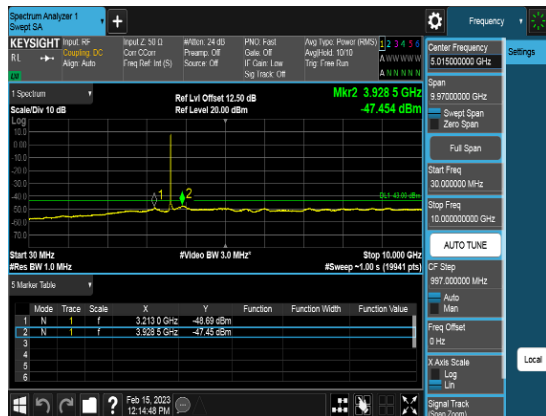
Port1_20M_3625MHz_TM3.1_CHM_CSE0a



Port1_20M_3625MHz_TM3.1_CHM_CSE0b



Port1_20M_3625MHz_TM3.1_CHM_CSE1



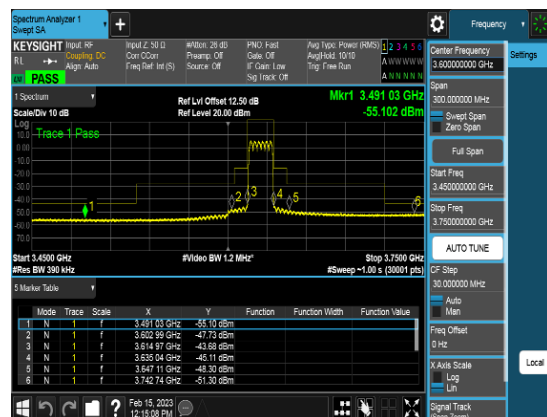
Port1_20M_3625MHz_TM3.1_CHM_CSE2



Port1_20M_3625MHz_TM3.1_CHM_CSE3



Port1_20M_3625MHz_TM3.1_CHM_CSEpart96



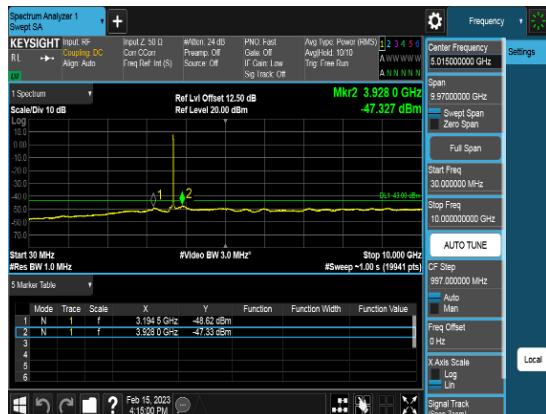
Port1_20M_3690MHz_TM3.1_CHH_CSE0a



Port1_20M_3690MHz_TM3.1_CHH_CSE0b



Port1_20M_3690MHz_TM3.1_CHH_CSE1



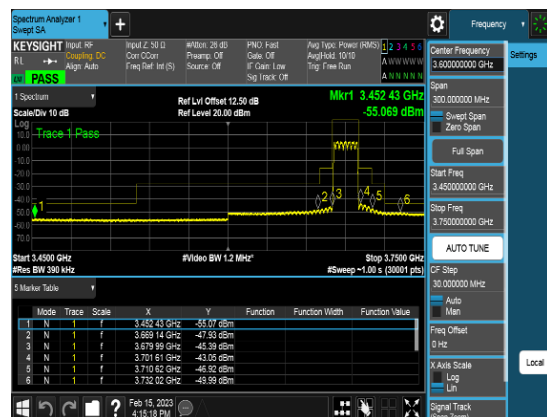
Port1_20M_3690MHz_TM3.1_CHH_CSE2



Port1_20M_3690MHz_TM3.1_CHH_CSE3



Port1_20M_3690MHz_TM3.1_CHH_CSEpart96



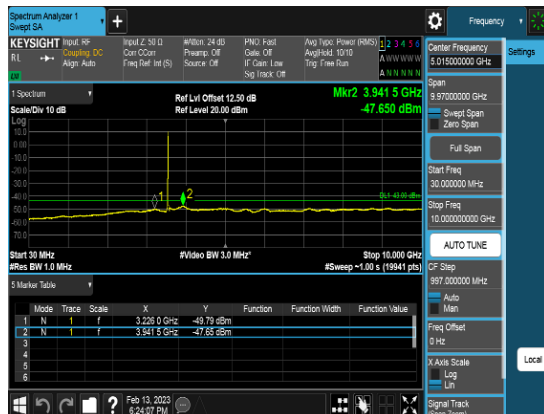
Port2_10M_3555MHz_TM3.1_CHL_CSE0a



Port2_10M_3555MHz_TM3.1_CHL_CSE0b



Port2_10M_3555MHz_TM3.1_CHL_CSE1



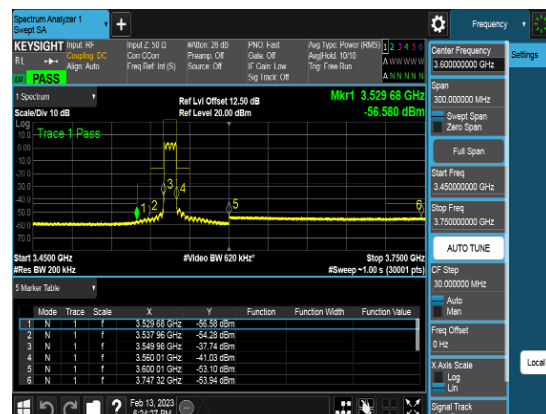
Port2_10M_3555MHz_TM3.1_CHL_CSE2



Port2_10M_3555MHz_TM3.1_CHL_CSE3



Port2_10M_3555MHz_TM3.1_CHL_CSEpart96



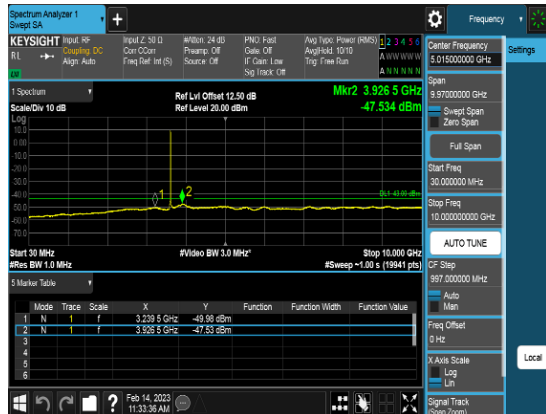
Port2_10M_3625MHz_TM3.1_CHM_CSE0a



Port2_10M_3625MHz_TM3.1_CHM_CSE0b



Port2_10M_3625MHz_TM3.1_CHM_CSE1



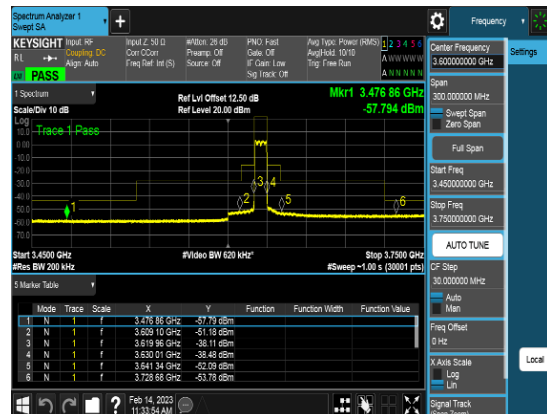
Port2_10M_3625MHz_TM3.1_CHM_CSE2



Port2_10M_3625MHz_TM3.1_CHM_CSE3



Port2_10M_3625MHz_TM3.1_CHM_CSEpart96



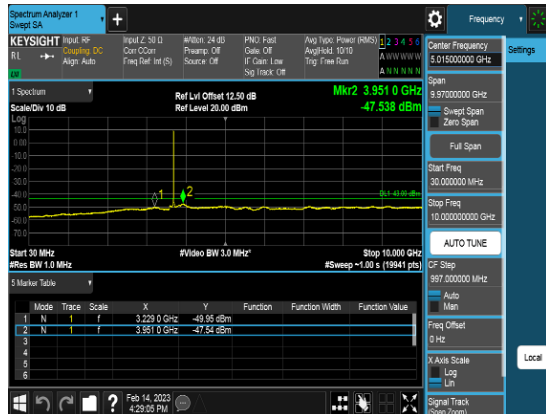
Port2_10M_3695MHz_TM3.1_CHH_CSE0a



Port2_10M_3695MHz_TM3.1_CHH_CSE0b



Port2_10M_3695MHz_TM3.1_CHH_CSE1



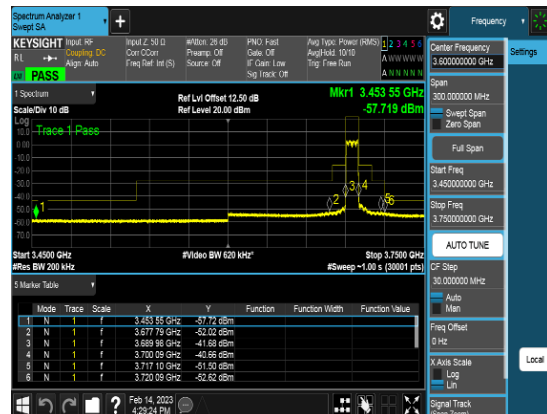
Port2_10M_3695MHz_TM3.1_CHH_CSE2



Port2_10M_3695MHz_TM3.1_CHH_CSE3



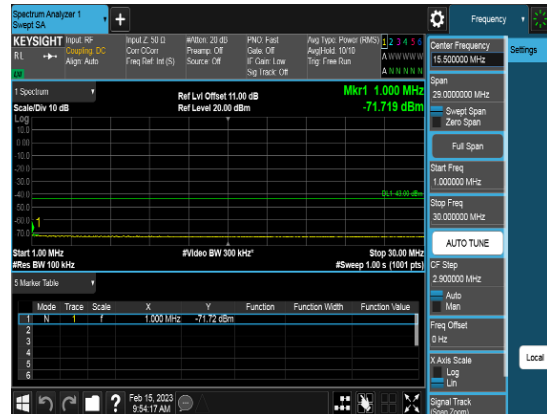
Port2_10M_3695MHz_TM3.1_CHH_CSEpart96



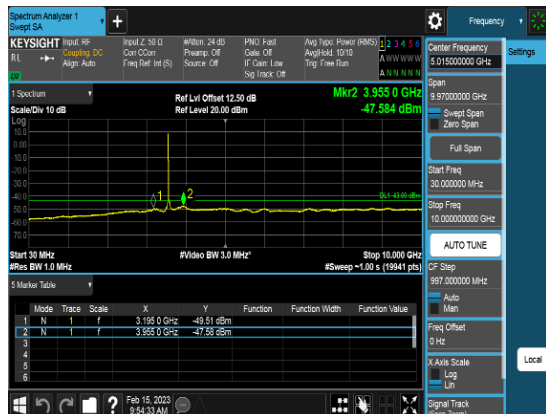
Port2_20M_3560MHz_TM3.1_CHL_CSE0a



Port2_20M_3560MHz_TM3.1_CHL_CSE0b



Port2_20M_3560MHz_TM3.1_CHL_CSE1



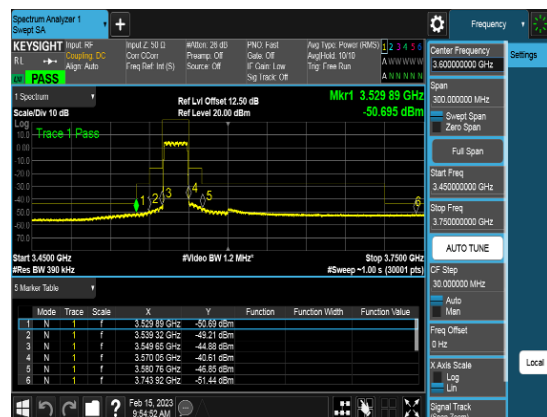
Port2_20M_3560MHz_TM3.1_CHL_CSE2



Port2_20M_3560MHz_TM3.1_CHL_CSE3



Port2_20M_3560MHz_TM3.1_CHL_CSEpart96



Port2_20M_3625MHz_TM3.1_CHM_CSE0a



Port2_20M_3625MHz_TM3.1_CHM_CSE0b



Port2_20M_3625MHz_TM3.1_CHM_CSE1



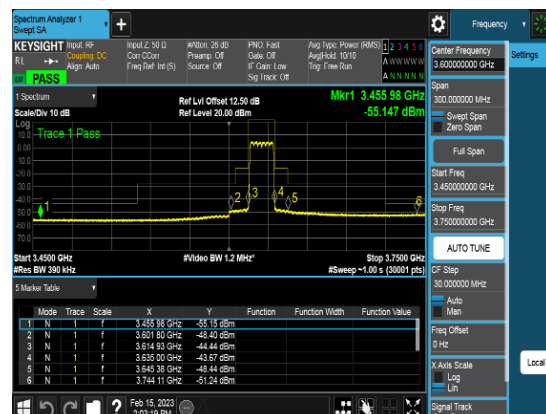
Port2_20M_3625MHz_TM3.1_CHM_CSE2



Port2_20M_3625MHz_TM3.1_CHM_CSE3



Port2_20M_3625MHz_TM3.1_CHM_CSEpart96



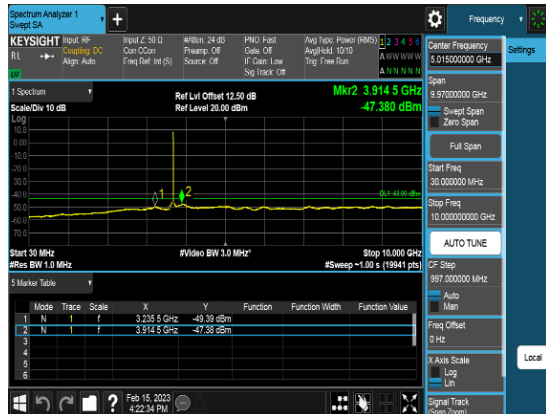
Port2_20M_3690MHz_TM3.1_CHH_CSE0a



Port2_20M_3690MHz_TM3.1_CHH_CSE0b



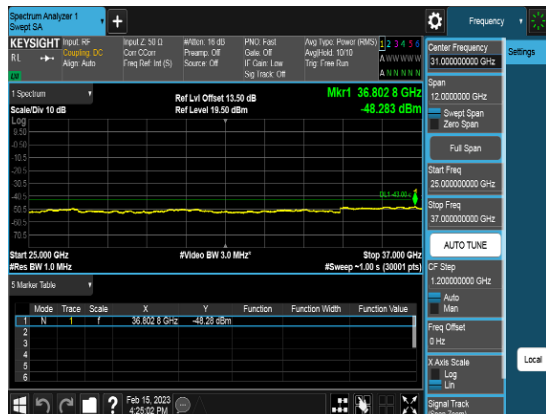
Port2_20M_3690MHz_TM3.1_CHH_CSE1



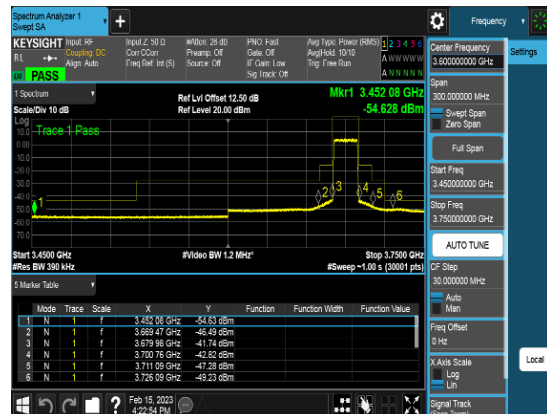
Port2_20M_3690MHz_TM3.1_CHH_CSE2



Port2_20M_3690MHz_TM3.1_CHH_CSE3



Port2_20M_3690MHz_TM3.1_CHH_CSEpart96



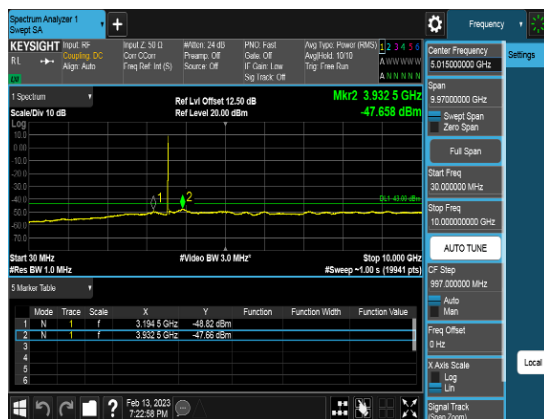
Port1_10M_3555MHz_TM3.2_CHL_CSE0a



Port1_10M_3555MHz_TM3.2_CHL_CSE0b



Port1_10M_3555MHz_TM3.2_CHL_CSE1



Port1_10M_3555MHz_TM3.2_CHL_CSE2



Port1_10M_3555MHz_TM3.2_CHL_CSE3



Port1_10M_3555MHz_TM3.2_CHL_CSEpart96

