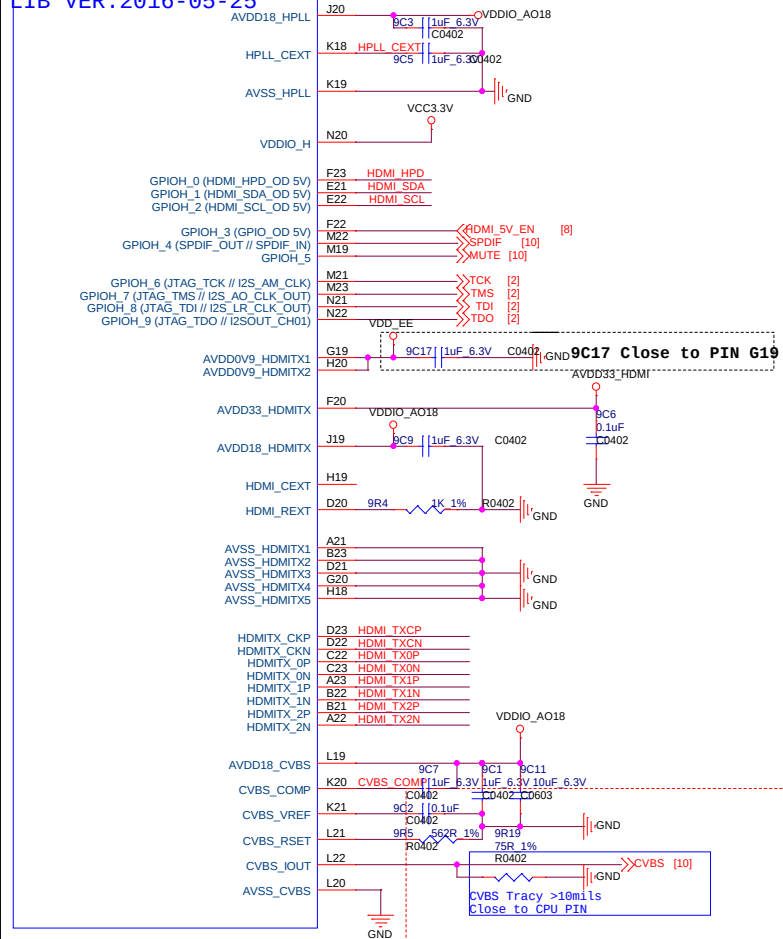


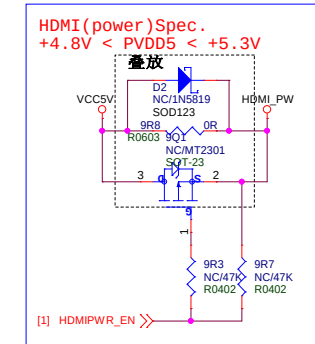
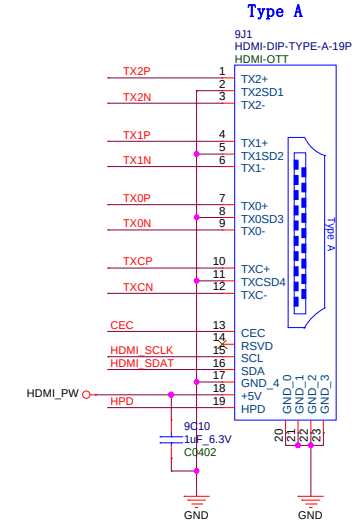
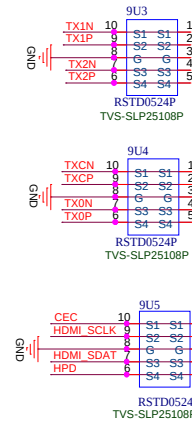
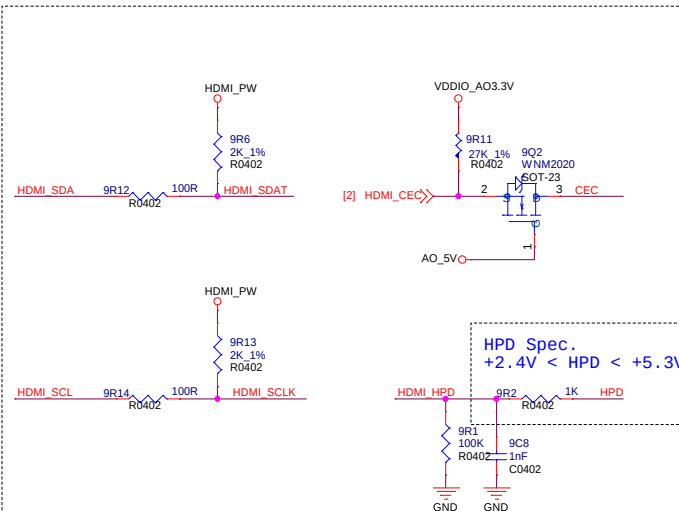
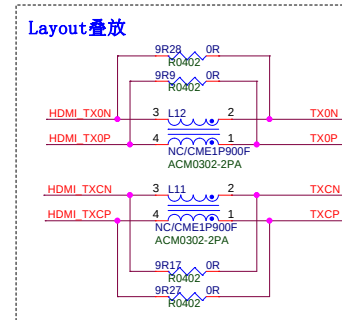
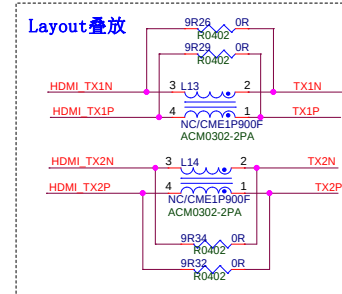
CPU U1G

U1G GXM_S912
LIB VER:2016-05-25



BGA483_OP65

HDMI output

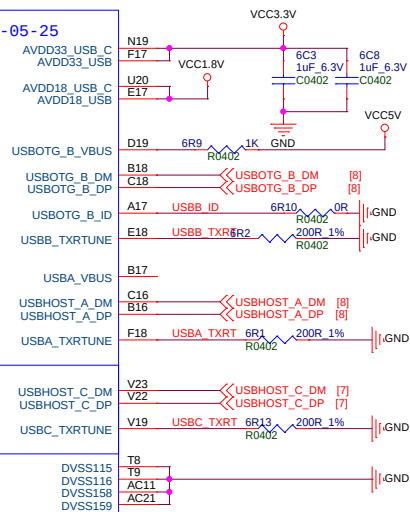


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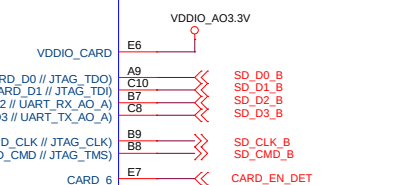
USBOTG B VBUS



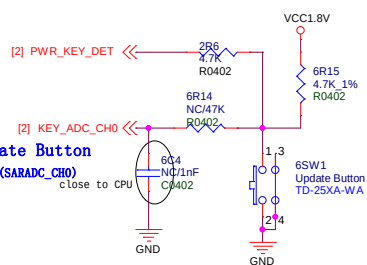
USB_C is only available in S912
Not available in S905D

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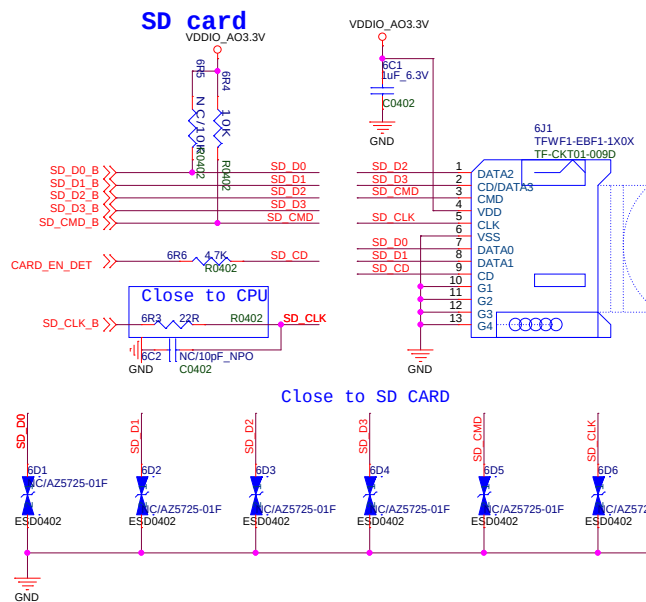
BGA483 0P65



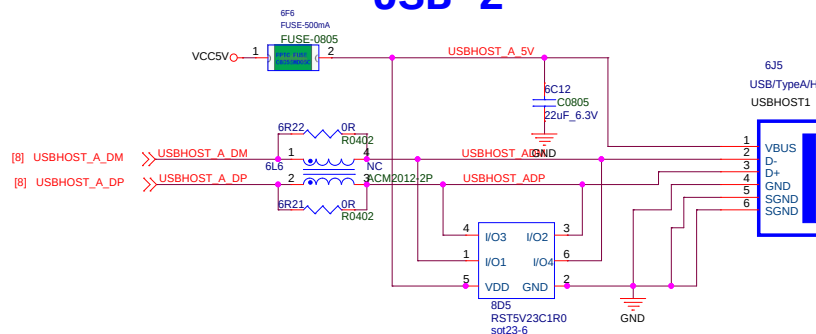
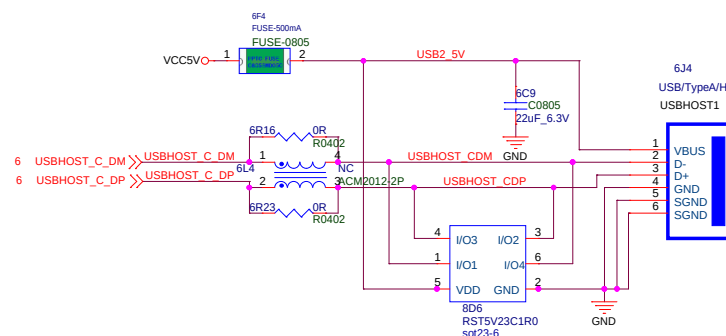
[8] USBOTG B DM



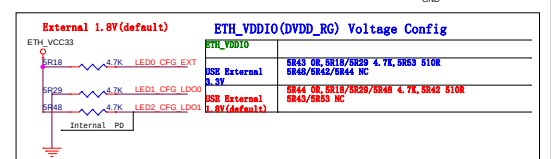
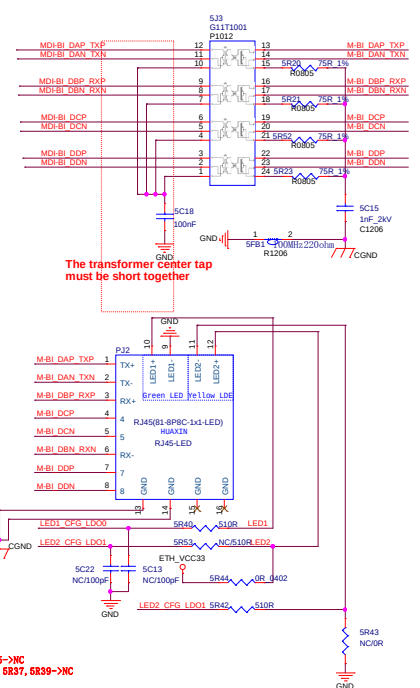
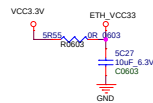
USB 2



6. USBHOST.C

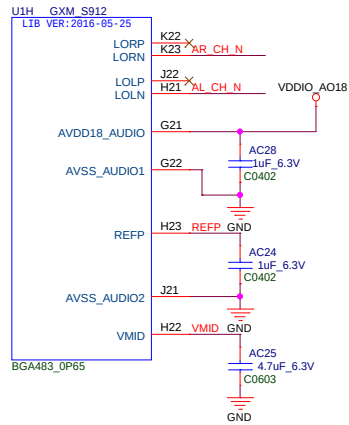


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Title									
USB&CARD&KEY									
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Document Number									
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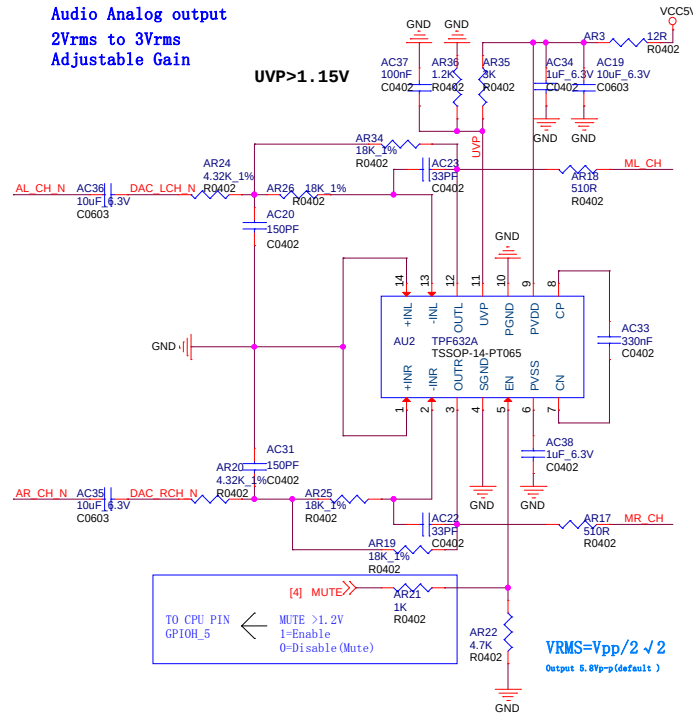
LED Settings

Pin Name	Logical	LED IO active	Link/Blinking(TX/RX)
LED1/CFG LD00	I'b0	LED[1] H	100Mbps
LED2/CFG LD01	I'b1	LED[2] L	100Mbps

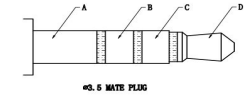
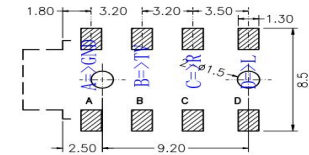
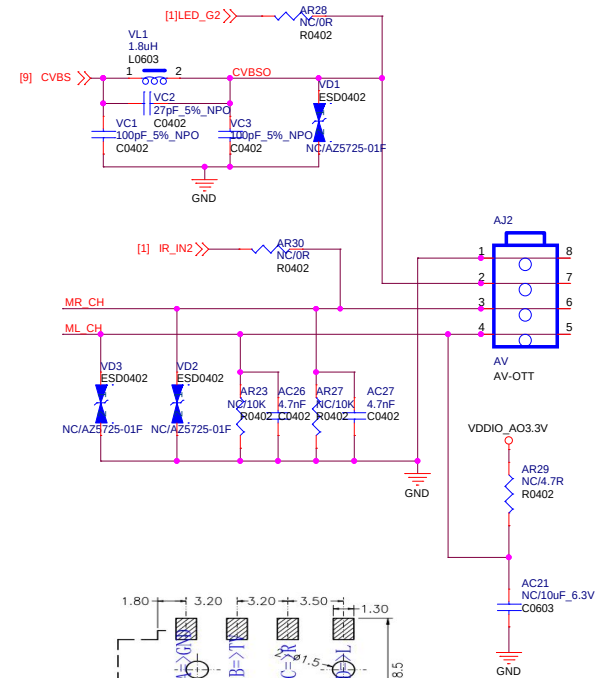


Audio Analog output
2Vrms to 3Vrms
Adjustable Gain

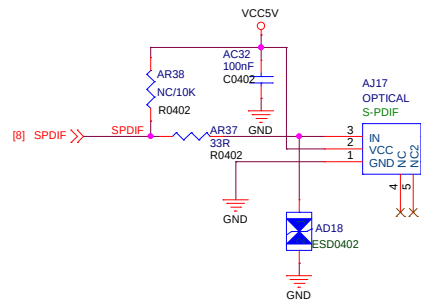
UVP>1.15V



VRMS=Vpp/2√2
Output 6.8Vpp (default)



Optical output



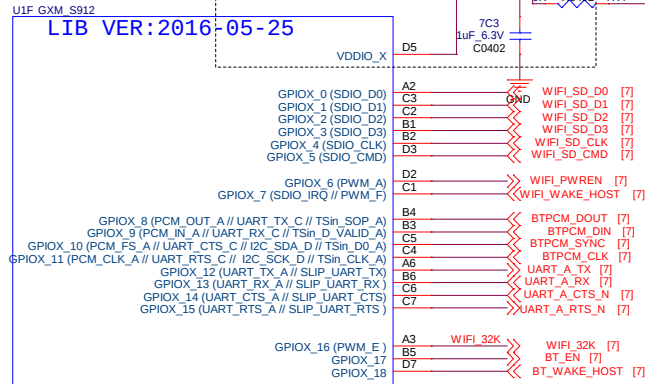
SHENZHEN GIEC ELECTRONICS CO.,LTD			
Title			
Audio			
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AP6181/RTL8189ETV WIFI ONLY
 AP6212/RTL8723BS WIFI+BT
 AP6330 WIFI+BT+FM
 AP6335/6255 WIFI+BT+FM (SDIO 3.0)
 QCA9377 WIFI+BT (SDIO 3.0)

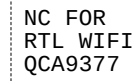
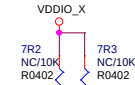
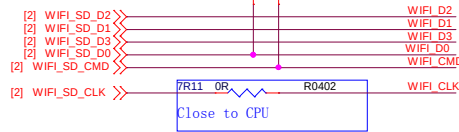
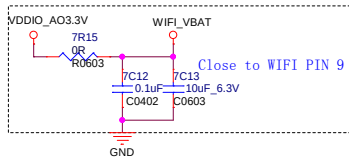
NOTE:
 AP6335/6255 OSC use 37.4MHZ
 Others AMPAK OSC use 26MH
 RTL WIFI/QCA9377 OSC NC

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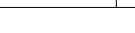
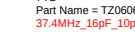
AMPAK WIFI: 7R4 0R,7R1 NC
 RTL WIFI: 7R4 NC,7R1 0R



BGA483_OP65



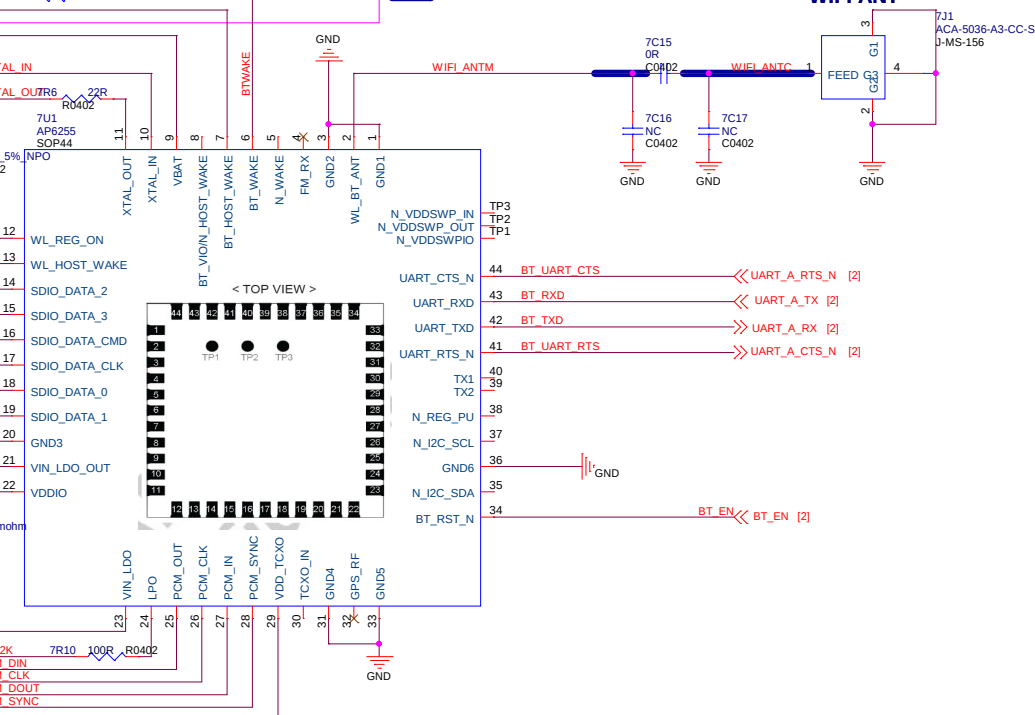
Note: 4.7uH@DCR<=80mΩ



NC 7R7 FOR QCA9377

50 ohm impedance RF trace

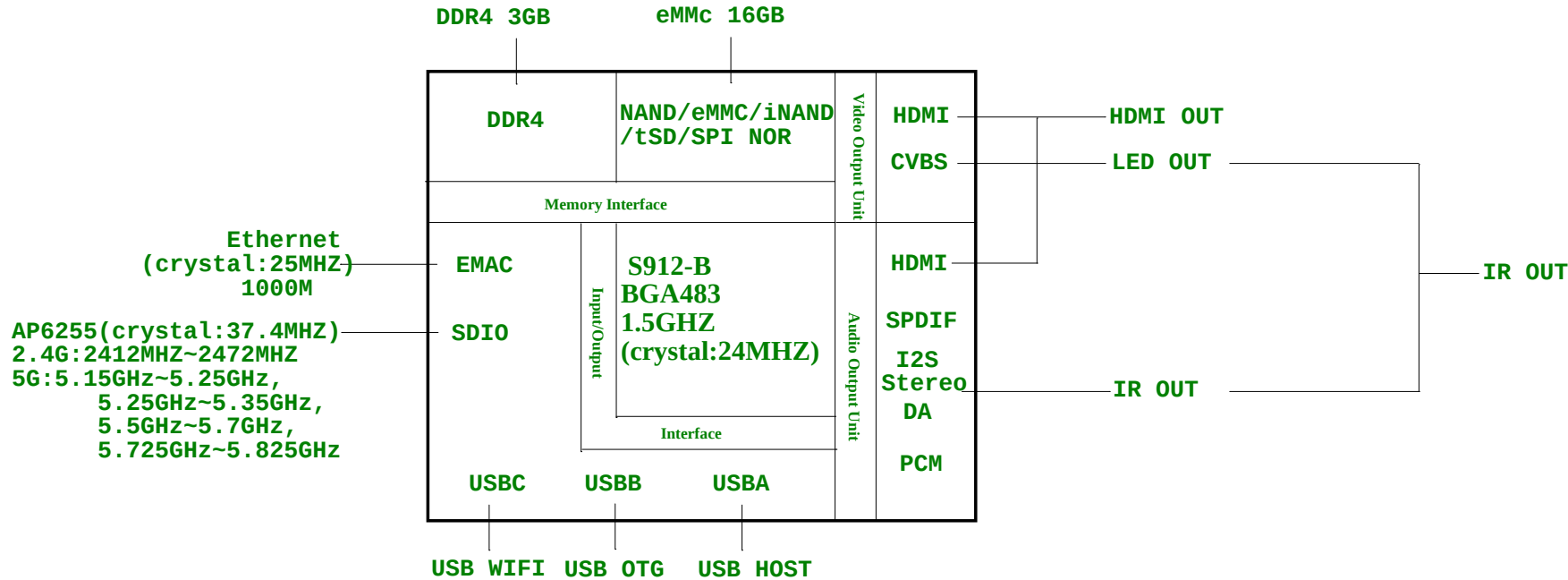
WiFi ANT



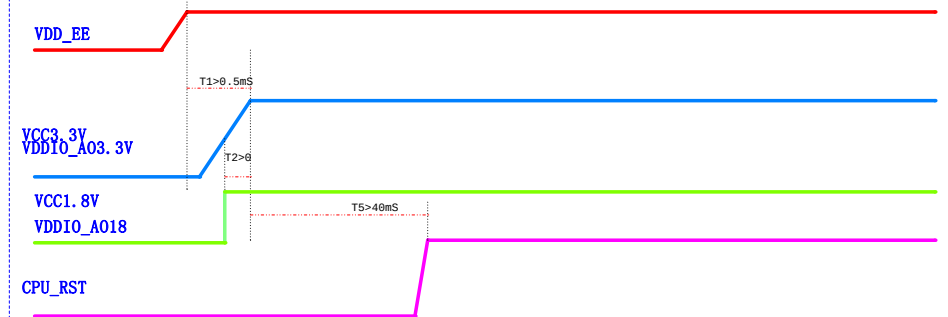
AP6181/6212/6330/6255:
 7R13 NC
 AP6335:
 7R13

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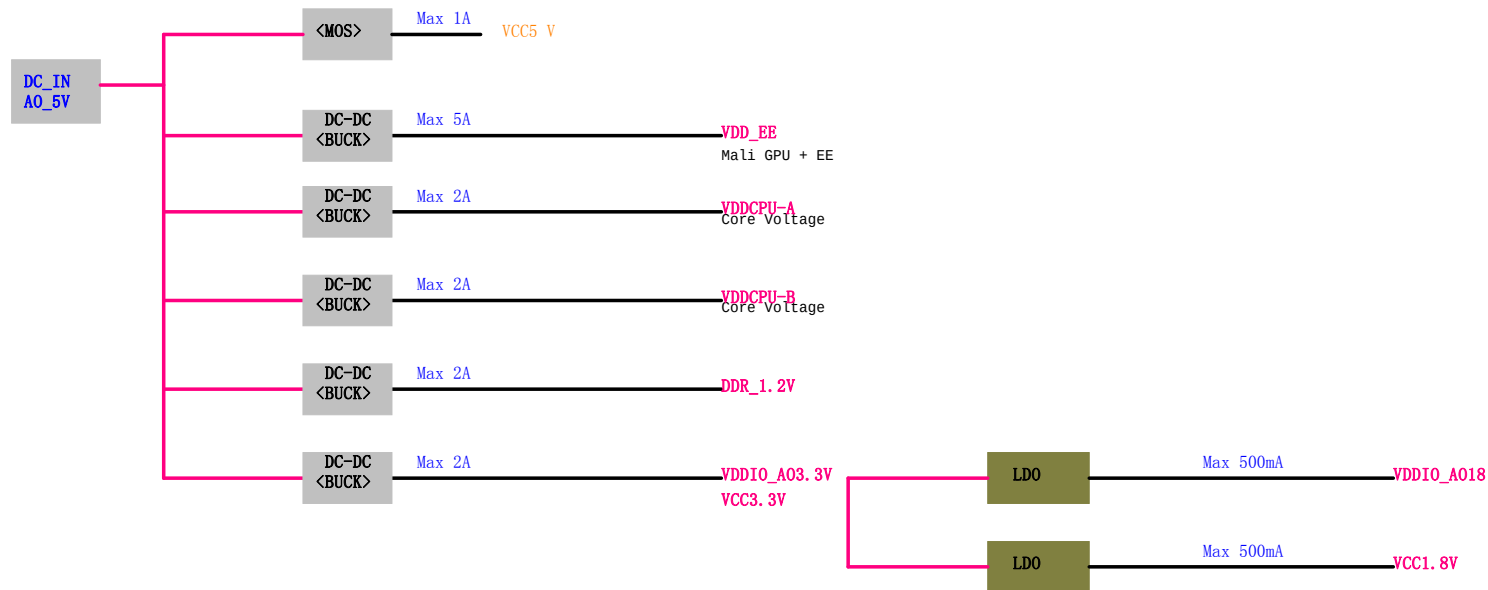
Block Diagram



Power On Sequence :



Power Tree

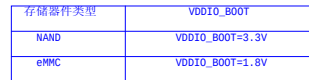


SHENZHEN GIEC ELECTRONICS CO.,LTD			
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```
VDDIO_BOOT1
BOOT_9 (NAND_CE1 // (EMMC_Reset GPIO))
BOOT_15 (NAND_DQS // EMMC_DS // NOR_CS)
BOOT_14 (NAND_REN_W/R)
BOOT_13 (NAND_WEN_CLK // NOR_C)
BOOT_12 (NAND_CLE // NOR_Q)
BOOT_11 (NAND_ALE // NOR_D)
BOOT_10 (NAND_RB0 // EMMC_CMD)
BOOT_8 (NAND_CE0 // EMMC_CLK)
```

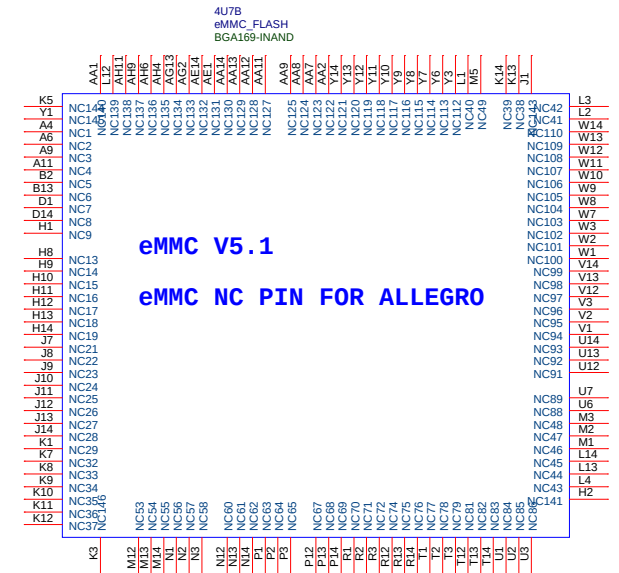
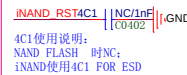


NAND_CLE	
1	boot from NAND/eMMC
0	boot from SPI

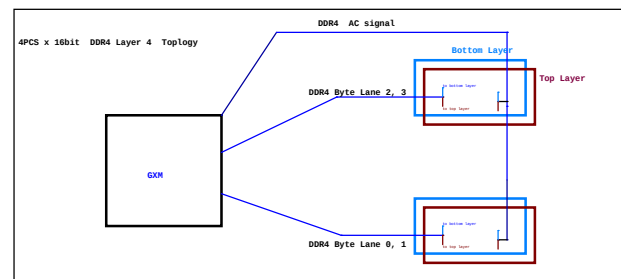
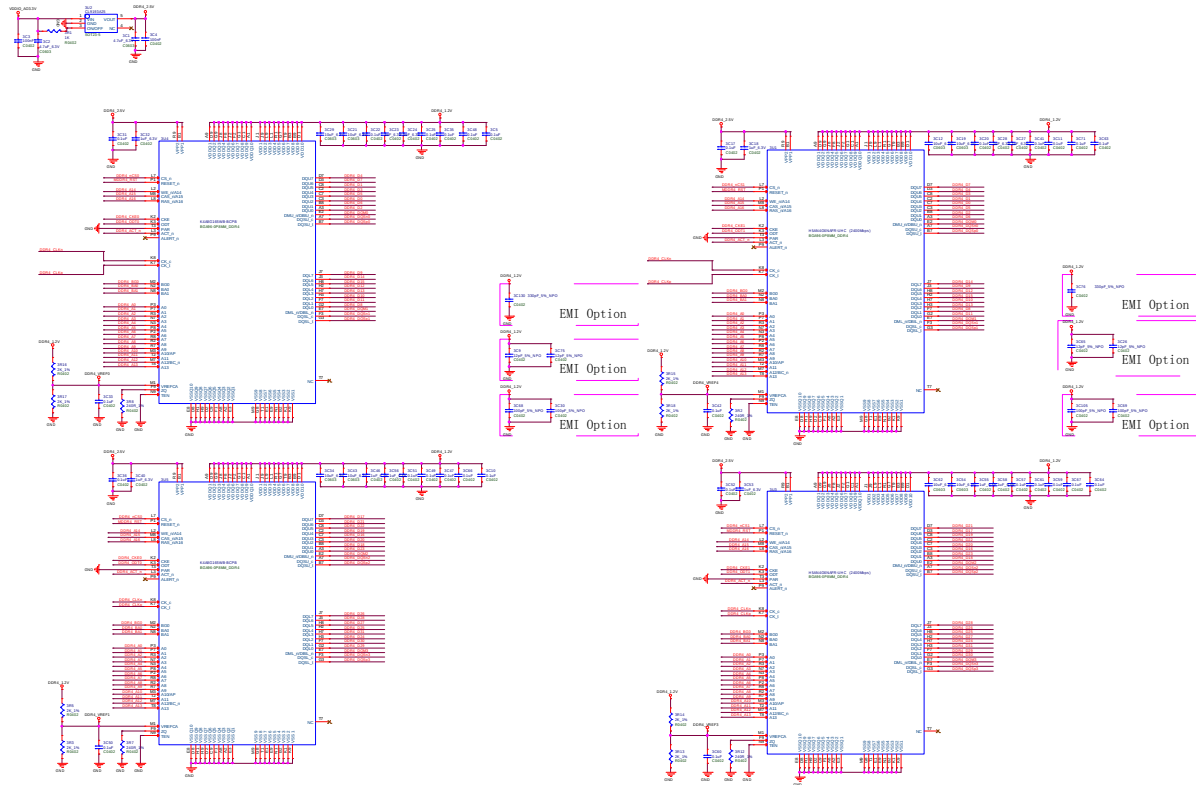
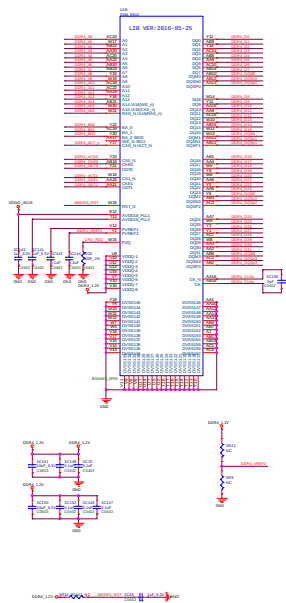


PCB Decal:169pin balls Type(BGA)

必须确认所使用eMMC Flash的VDDQ RMS消耗电流值
当前LDO供电只能满足RMS 电流<220mA
如eMMC VDDQ电流超过上述值,eMMC VDDQ要改为使用DC-DC来供电



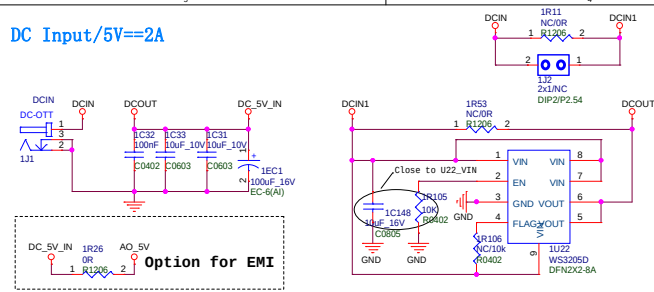
eMMC NC PIN FOR ALLEGRO



DDR4 容量选择表	Rank 0		Rank 1	
	3U4	3U5	3U1	3U3
2GByte	512M Byte	512M Byte	512M Byte	512M Byte
3GByte	1G Byte	1G Byte	512M Byte	512M Byte

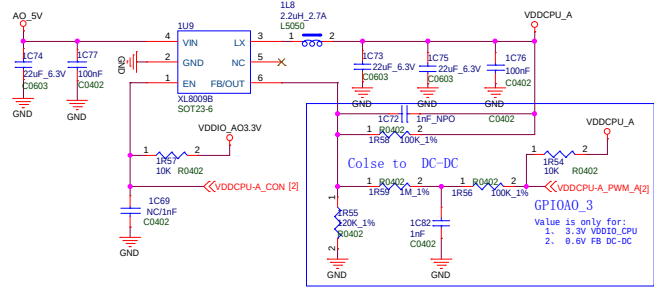
注意：3G Byte容量选择时，Rank0必须2G Byte，Rank1 5G Byte 配置

DC Input/5V==2A



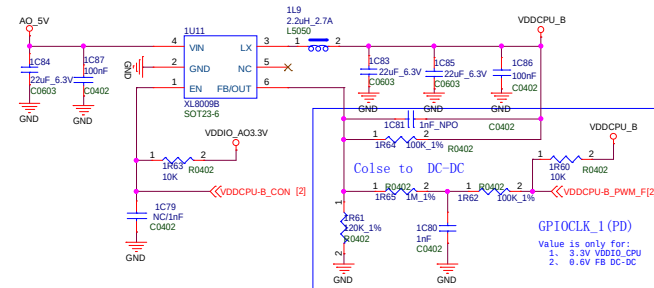
VDDCPU_A

Design Max: 2A

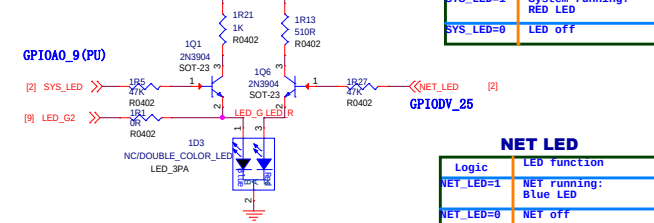


VDDCPU_B

Design Max: 2A



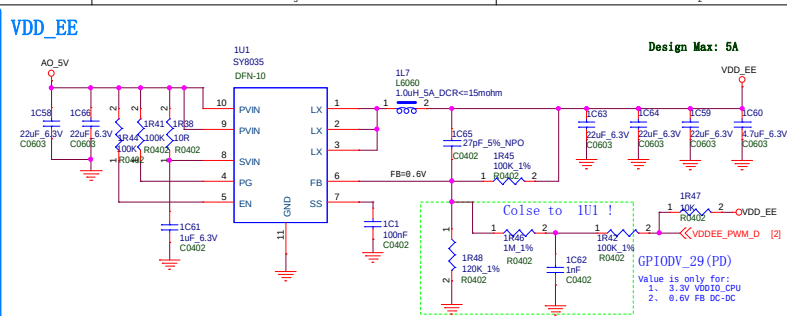
System LED



Logic	LED function
SYS_LED=1	System running: RED LED
SYS_LED=0	LED off

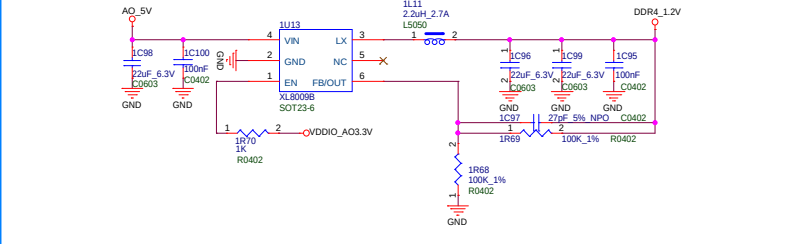
Logic	LED function
NET_LED=1	NET running: Blue LED
NET_LED=0	NET off

VDD_EE



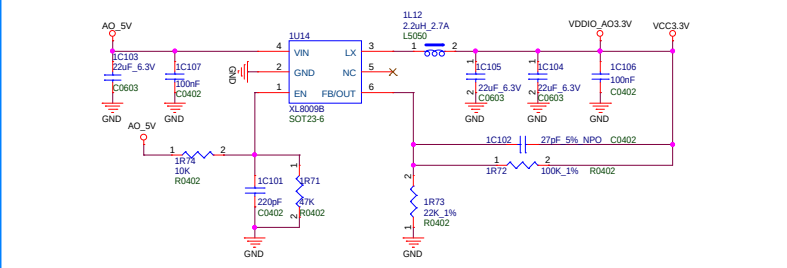
DDR4_1.2V

Design Max: 2A

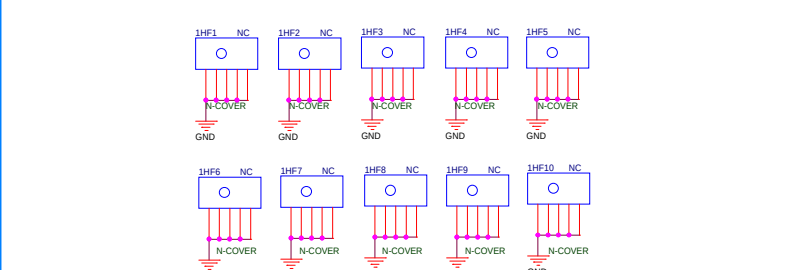


VDDIO_A03.3V/VCC3.3V

Design Max: 2A

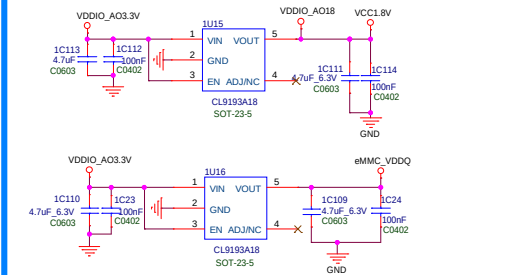


Shield

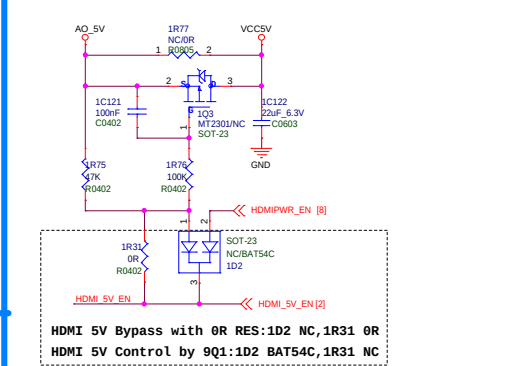


1.8V

Design Max: 300mA



5V Control



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