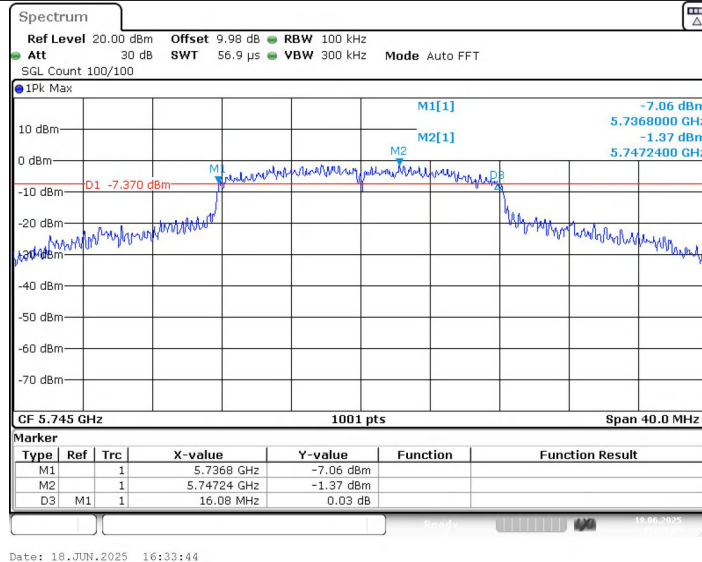
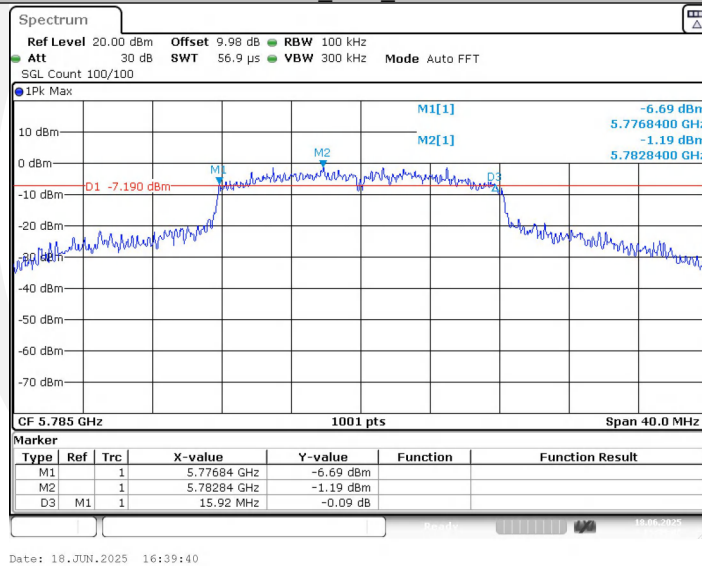


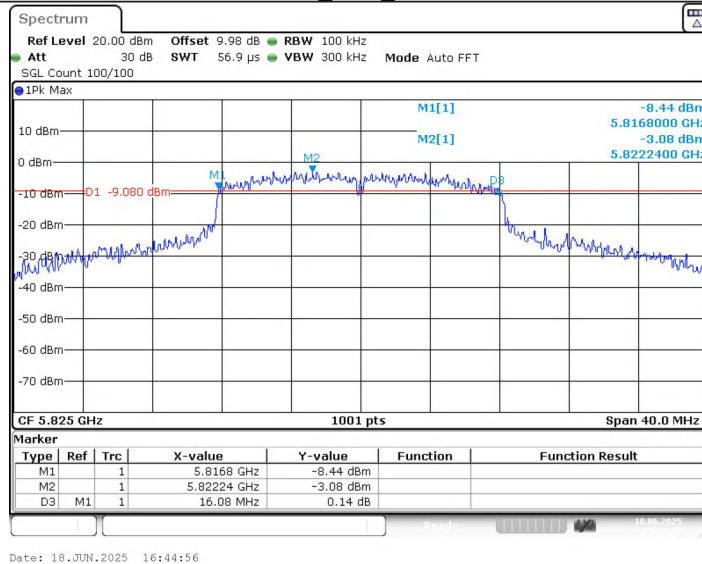
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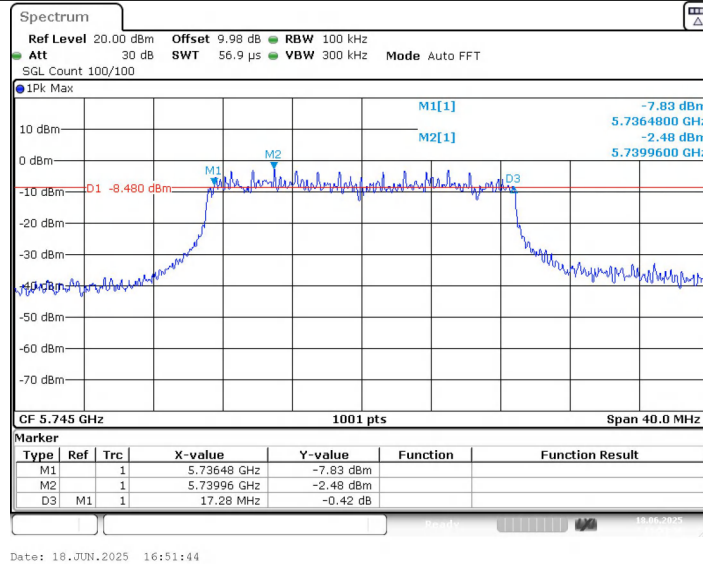
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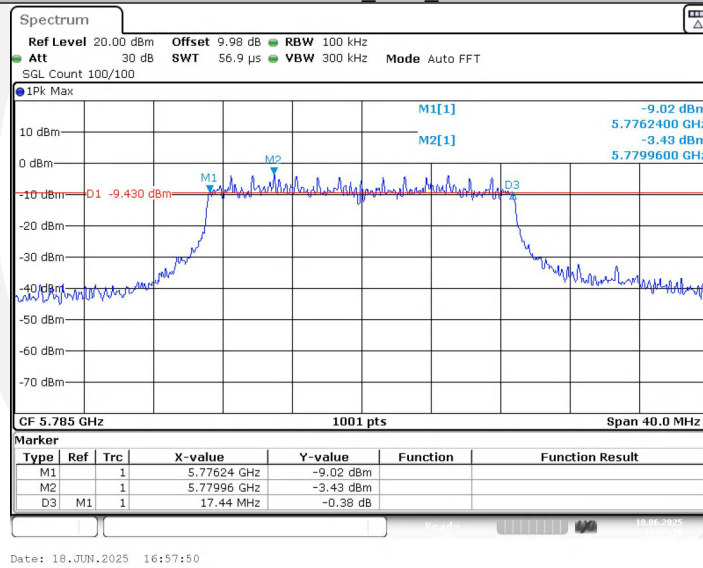
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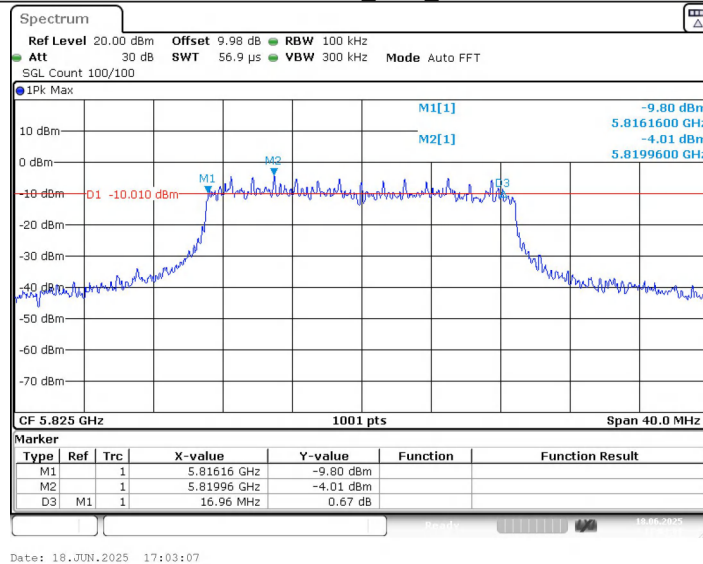
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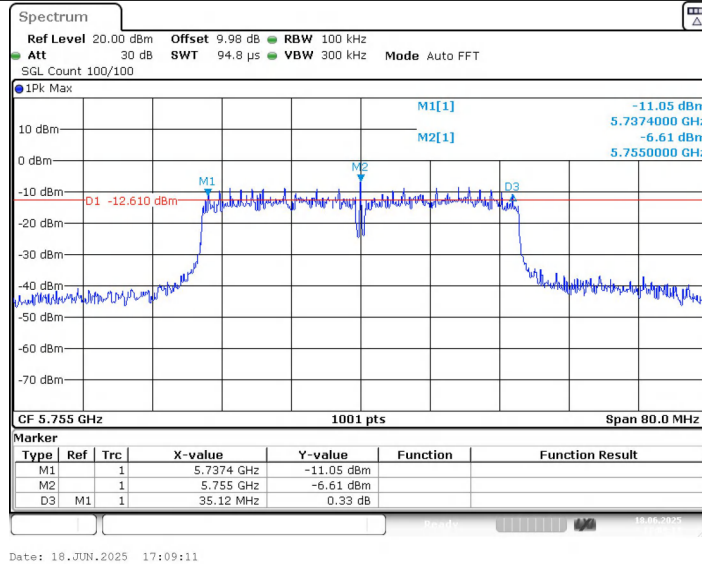
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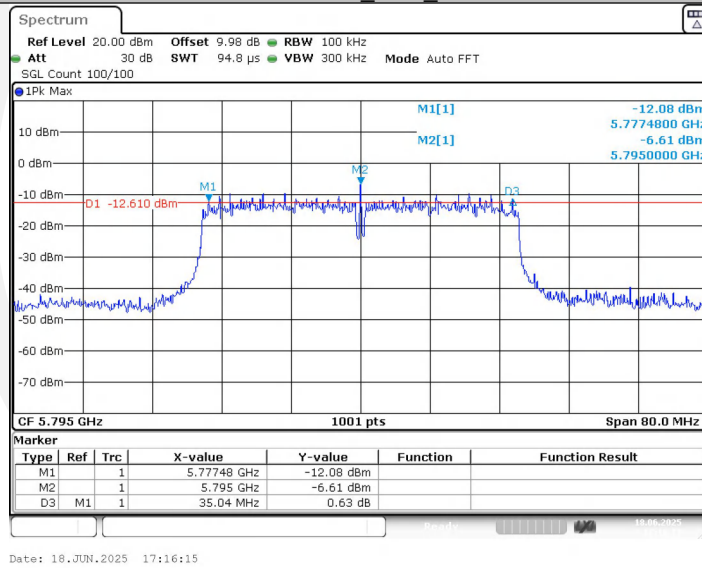
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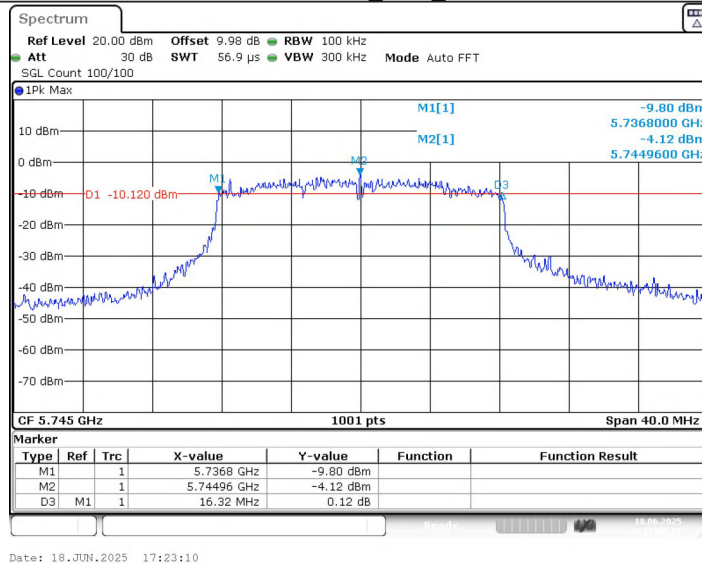
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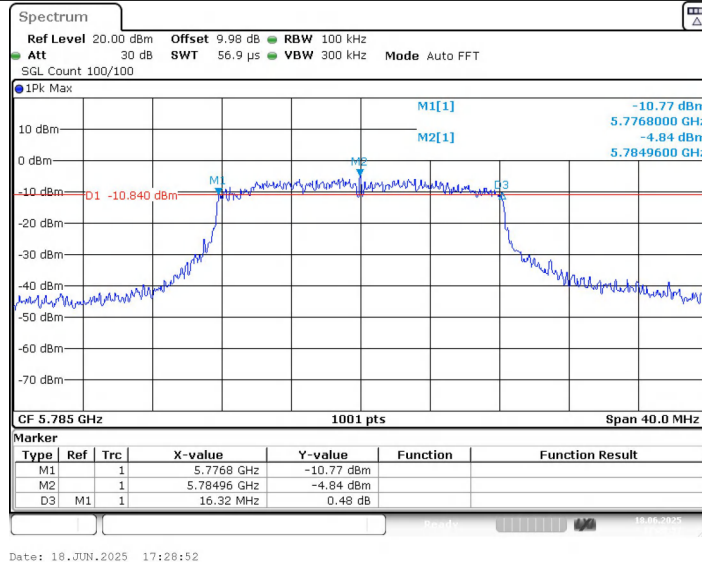
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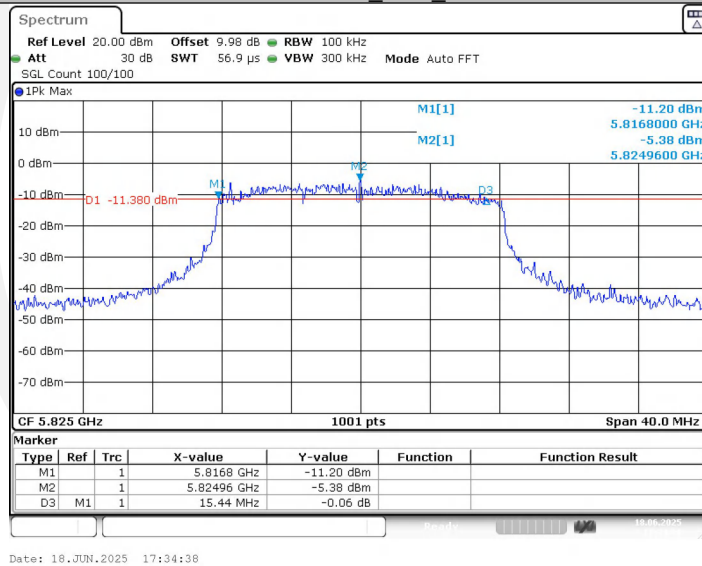
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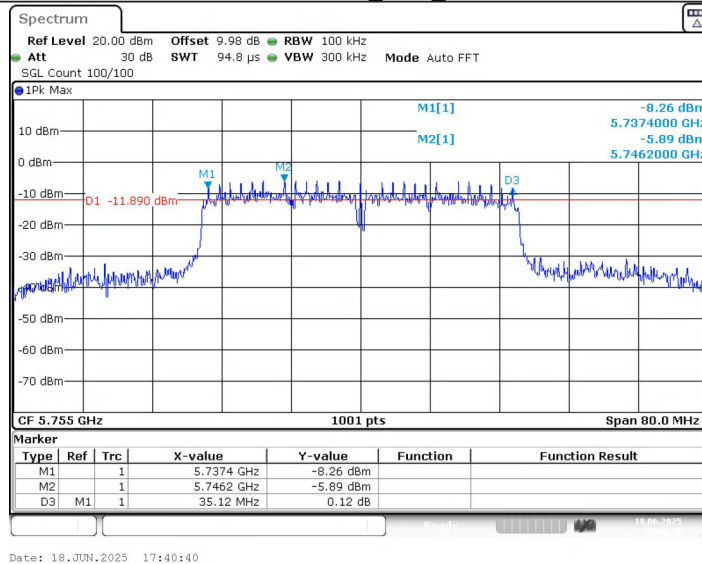
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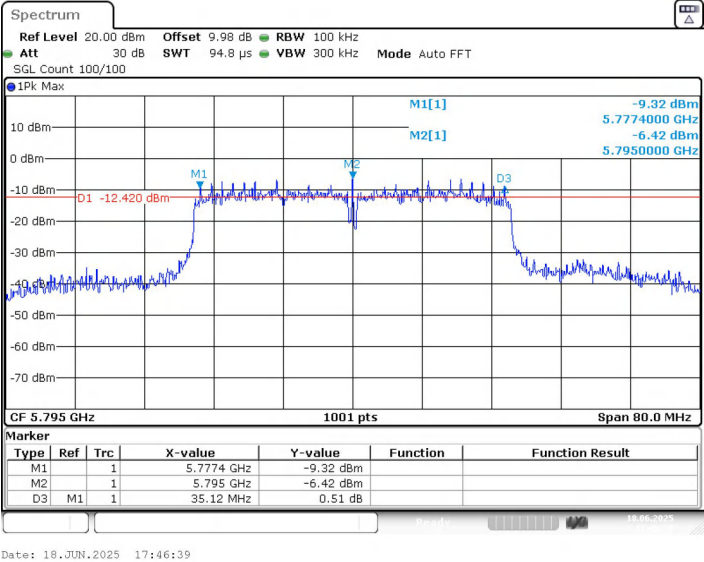
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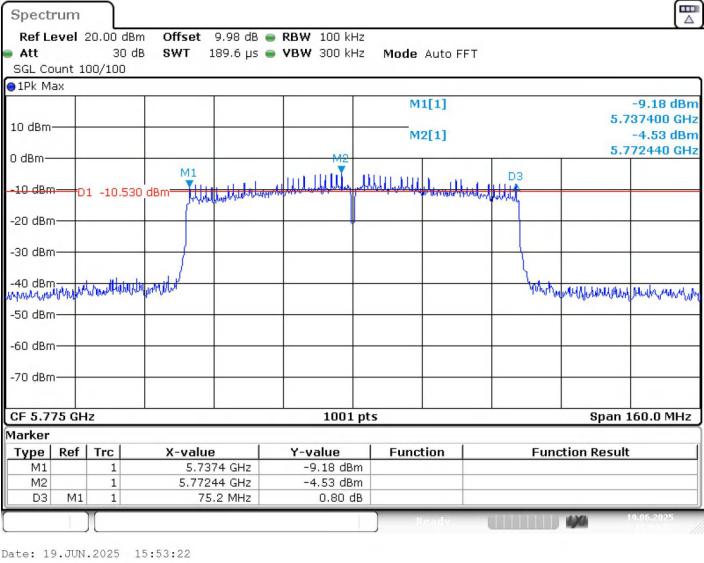
11AC40SISO_Ant1_5755



11AC40SISO_Ant1_5795



11AC80SISO_Ant1_5775



4.4 Maximum Conducted Output Power

Test Requirement:	15.407(a)(1)(iv)-(B1), 15.407(a)(2)(iv)-(B2, B3), 15.407(a)(3)(i)-(B4)
Test Method:	KDB 789033 D02 General UNII Test Procedures New Rules v02r01
Limit:	For the band 5.15-5.25GHz, 5.25-5.35GHz, 5.47-5.725GHz, the maximum conducted output power over the frequency bands of operation shall not exceed 250mW. For the band 5.725-5.85GHz, the maximum conducted output power over the frequency bands of operation shall not exceed 1W. For Straddle Channel, According to KDB 789033 D02 General UNII Test Procedures New Rules v02r01, If the power and PSD of the devices are uniform and comply with the lower limits specified for the U-NII-2 bands, a single measurement over the entire emission bandwidth can be performed to show compliance. If transmitting antennas of directional gain greater than 6 dBi are used, the peak output power shall be reduced by the amount in dB that the directional gain of the antenna exceeds 6 dBi.
Test setup:	 The diagram illustrates the test setup. On the left is a green rectangular box representing the Spectrum Analyzer. A black line (cable) connects it to a yellow rectangular box on the right, which is labeled 'EUT' (Equipment Under Test). Below the Spectrum Analyzer box is the text 'Spectrum Analyzer', and below the EUT box is the text 'EUT'.
Test procedure:	Method SA-1 - Set span to encompass the entire 26 dB EBW or 99% OBW of the signal. - Set RBW = 1 MHz. - Set VBW $\geq$ 3 MHz. - Number of points in sweep $\geq$ $[2 \times \text{span} / \text{RBW}]$. (This gives bin-to-bin spacing $\leq$ $\text{RBW} / 2$, so that narrowband signals are not lost between frequency bins.) - Sweep time = auto. - Detector = RMS (i.e., power averaging), if available. Otherwise, use sample detector mode. - If transmit duty cycle $<$ 98%, use a video trigger with the trigger level set to enable triggering only on full power pulses. The transmitter shall operate at maximum power control level for the entire duration of every sweep. If the EUT transmits continuously (i.e., with no OFF intervals) or at duty cycle $\geq$ 98%, and if each transmission is entirely at the maximum power control level, then the trigger shall be set to "free run." - Trace average at least 100 traces in power averaging (rms) mode. - Compute power by integrating the spectrum across the 26 dB EBW or 99% OBW of the signal using the instrument's band power measurement function, with band limits set equal to the EBW or OBW band edges. If the instrument does not have a band power function, then sum the spectrum levels (in power units) at 1 MHz intervals extending across the 26 dB EBW or 99% OBW of the spectrum.
Test Instruments:	Refer to section 3.0 for details

Test mode:	Refer to section 2.2 for details
Test results:	Pass



Measurement Data

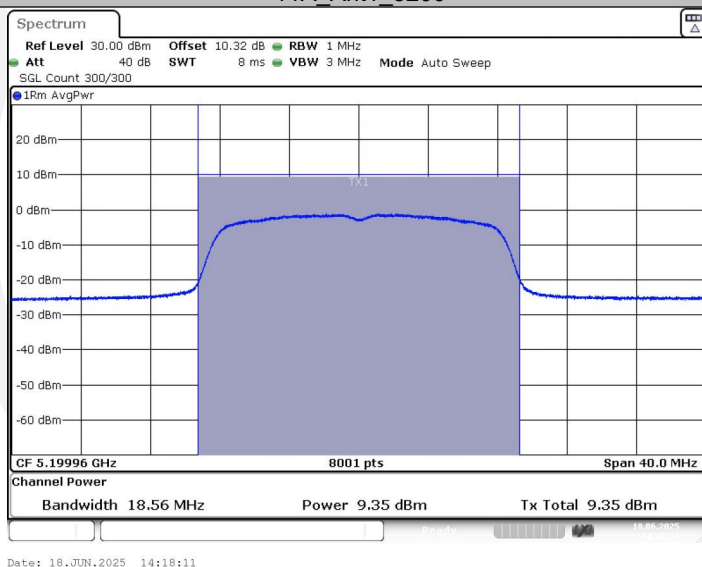
Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	8.00	91.96	0.36	8.36	≤23.98	PASS
		5200	8.99	92.00	0.36	9.35	≤23.98	PASS
		5240	9.53	92.41	0.34	9.87	≤23.98	PASS
		5260	1.80	92.41	0.34	2.14	≤23.98	PASS
		5280	2.44	93.21	0.31	2.75	≤23.82	PASS
		5320	2.10	94.06	0.27	2.37	≤23.98	PASS
		5500	7.52	97.89	0.09	7.61	≤23.87	PASS
		5580	8.71	97.20	0.12	8.83	≤23.95	PASS
		5700	9.62	97.90	0.09	9.71	≤23.94	PASS
		5745	12.41	92.38	0.34	12.75	≤30.00	PASS
		5785	11.78	95.39	0.20	11.98	≤30.00	PASS
		5825	10.71	94.93	0.23	10.94	≤30.00	PASS
11N20SISO	Ant1	5180	8.14	55.00	2.60	10.74	≤23.98	PASS
		5200	7.91	54.76	2.62	10.53	≤23.98	PASS
		5240	7.66	55.00	2.60	10.26	≤23.98	PASS
		5260	1.21	58.97	2.29	3.50	≤23.98	PASS
		5280	2.24	57.50	2.40	4.64	≤23.98	PASS
		5320	2.08	57.50	2.40	4.48	≤23.98	PASS
		5500	7.29	97.74	0.10	7.39	≤23.98	PASS
		5580	8.67	97.74	0.10	8.77	≤23.98	PASS
		5700	9.52	97.01	0.13	9.65	≤23.98	PASS
		5745	9.47	56.10	2.51	11.98	≤30.00	PASS
		5785	6.59	57.89	2.37	8.96	≤30.00	PASS
		5825	5.61	57.89	2.37	7.98	≤30.00	PASS
11N40SISO	Ant1	5190	9.51	95.59	0.20	9.71	≤23.98	PASS
		5230	10.05	94.12	0.26	10.31	≤23.98	PASS
		5270	0.80	44.83	3.48	4.28	≤23.98	PASS
		5310	1.07	44.83	3.48	4.55	≤23.98	PASS
		5510	7.19	95.59	0.20	7.39	≤23.98	PASS
		5550	8.46	95.59	0.20	8.66	≤23.98	PASS
		5670	8.56	95.59	0.20	8.76	≤23.98	PASS
		5755	4.18	46.43	3.33	7.51	≤30.00	PASS
		5795	3.48	44.83	3.48	6.96	≤30.00	PASS
11AC20SISO	Ant1	5180	7.98	89.10	0.50	8.48	≤23.98	PASS
		5200	7.87	89.10	0.50	8.37	≤23.98	PASS

		5240	7.61	88.54	0.53	8.14	≤23.98	PASS
		5260	4.80	92.62	0.33	5.13	≤23.98	PASS
		5280	5.22	92.62	0.33	5.55	≤23.86	PASS
		5320	5.20	90.26	0.45	5.65	≤23.98	PASS
		5500	6.94	97.76	0.10	7.04	≤23.98	PASS
		5580	8.56	97.04	0.13	8.69	≤23.98	PASS
		5700	9.36	97.78	0.10	9.46	≤23.98	PASS
		5745	9.15	92.00	0.36	9.51	≤30.00	PASS
		5785	8.40	93.92	0.27	8.67	≤30.00	PASS
		5825	7.56	91.45	0.39	7.95	≤30.00	PASS
11AC40SISO	Ant1	5190	1.88	41.38	3.83	5.71	≤23.98	PASS
		5230	1.98	41.38	3.83	5.81	≤23.98	PASS
		5270	10.01	40.00	3.98	13.99	≤23.98	PASS
		5310	2.78	37.93	4.21	6.99	≤23.98	PASS
		5510	6.88	94.20	0.26	7.14	≤23.98	PASS
		5550	8.09	94.20	0.26	8.35	≤23.98	PASS
		5670	8.55	95.59	0.20	8.75	≤23.98	PASS
		5755	5.91	37.93	4.21	10.12	≤30.00	PASS
		5795	5.37	40.74	3.90	9.27	≤30.00	PASS
11AC80SISO	Ant1	5210	9.16	91.43	0.39	9.55	≤23.98	PASS
		5290	10.31	28.00	5.53	15.84	≤23.98	PASS
		5530	8.18	88.89	0.51	8.69	≤23.98	PASS
		5610	8.39	88.89	0.51	8.90	≤23.98	PASS
		5775	10.97	91.67	0.38	11.35	≤30.00	PASS

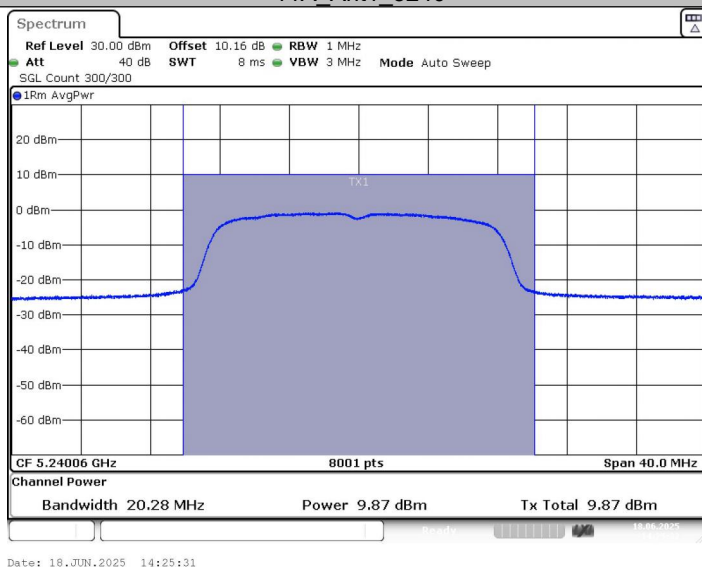
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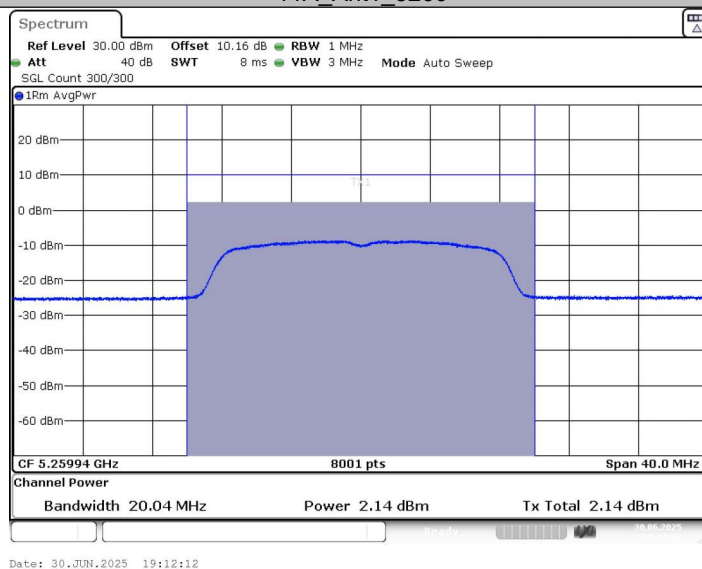
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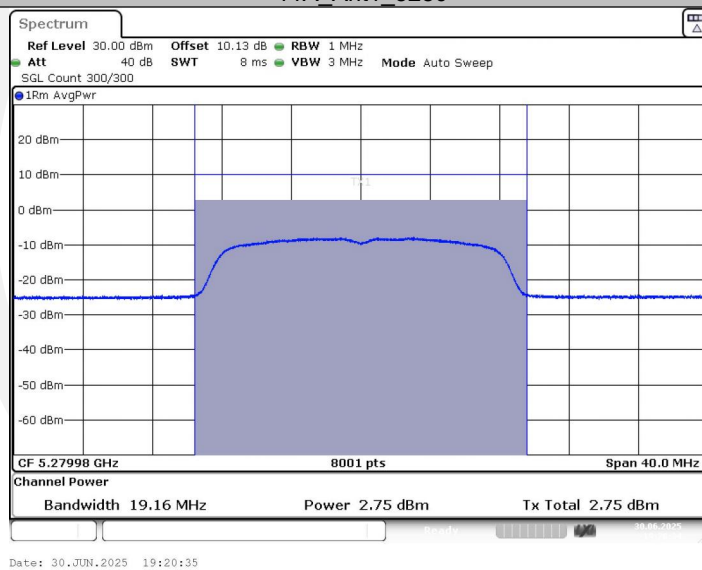
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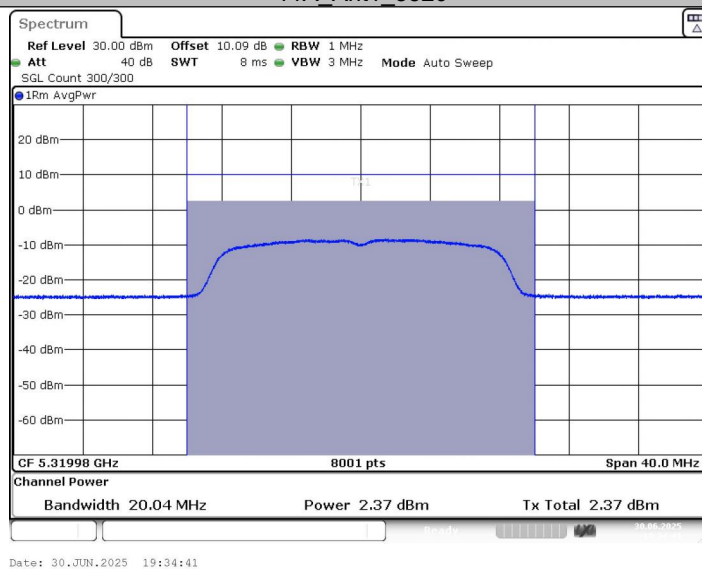
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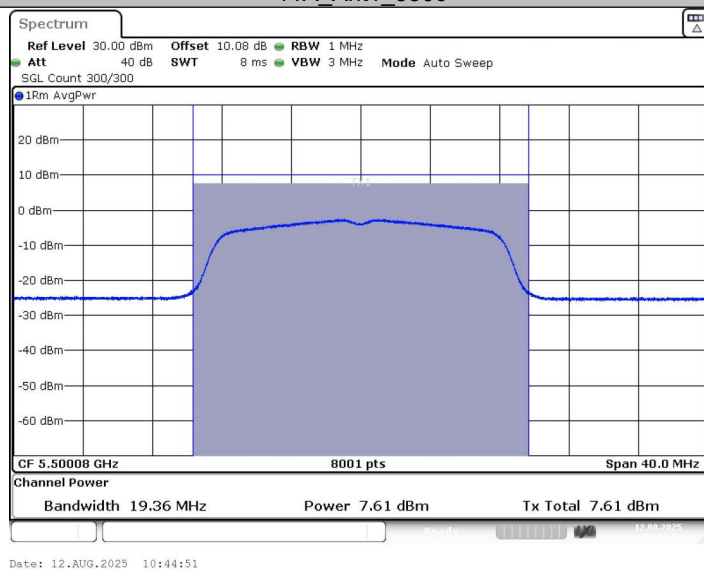
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11A_Ant1_5320



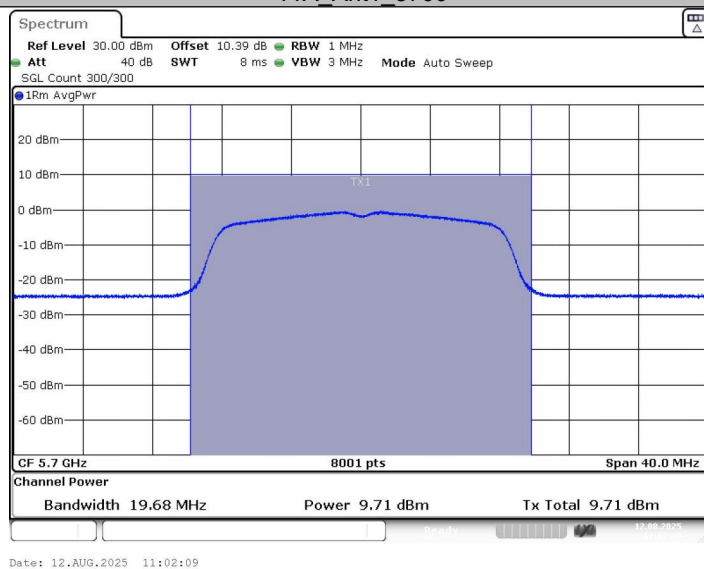
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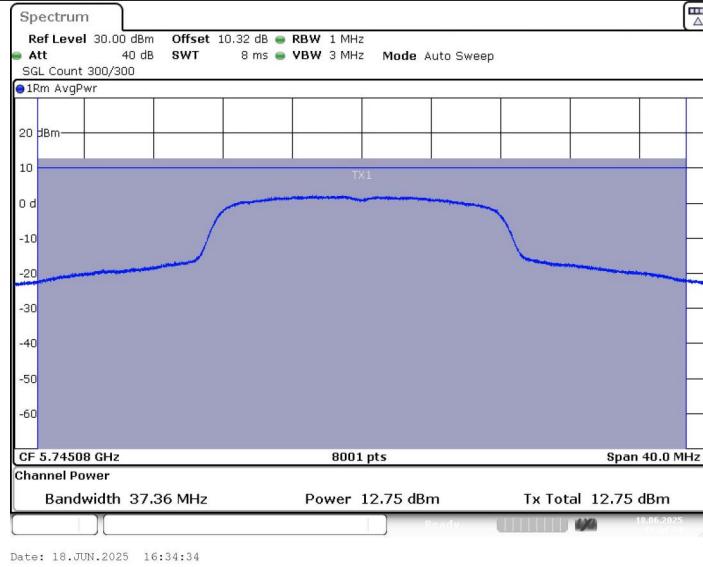
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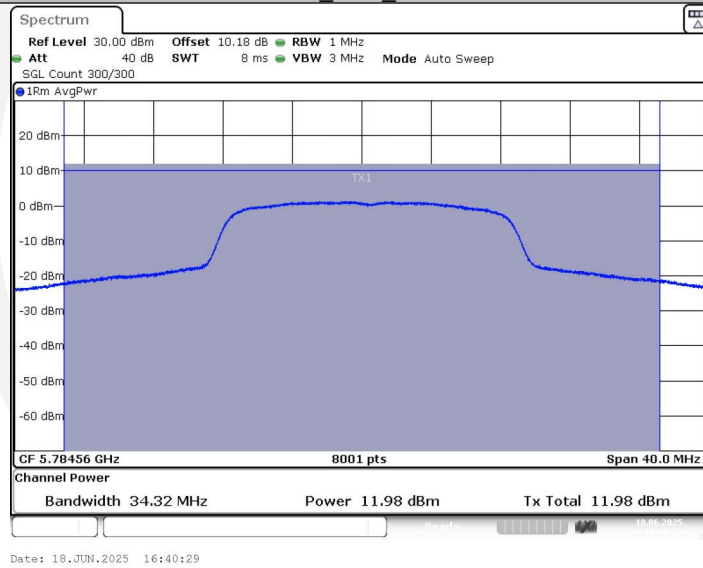
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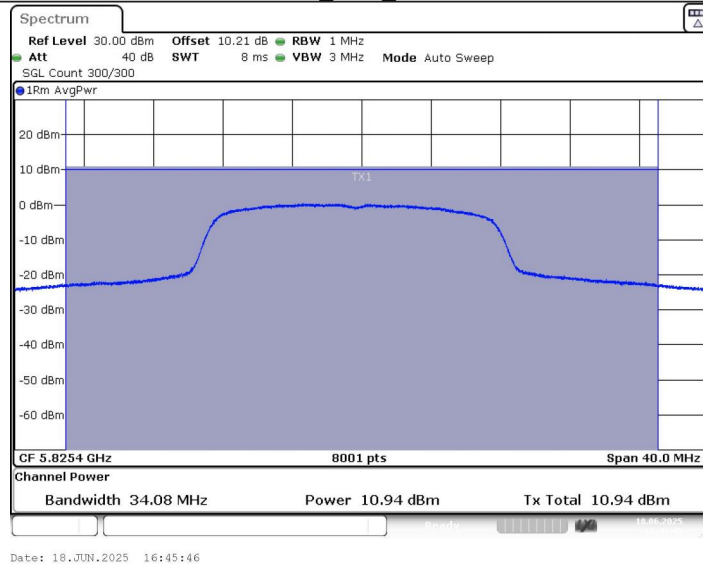
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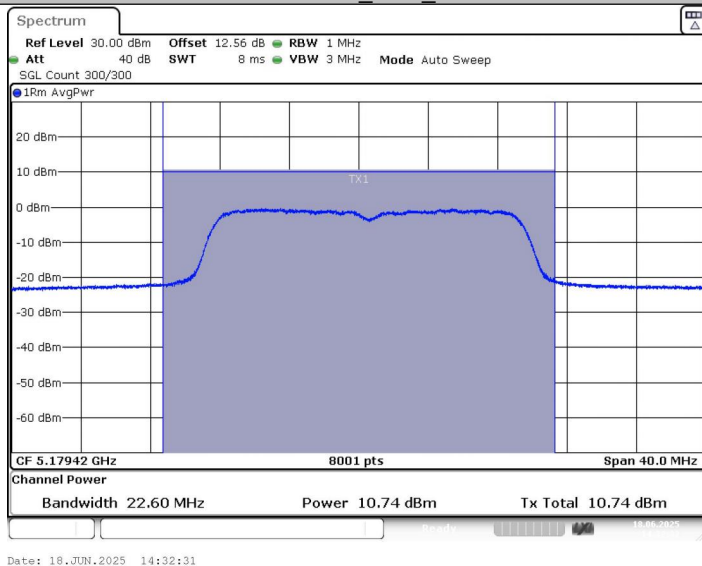
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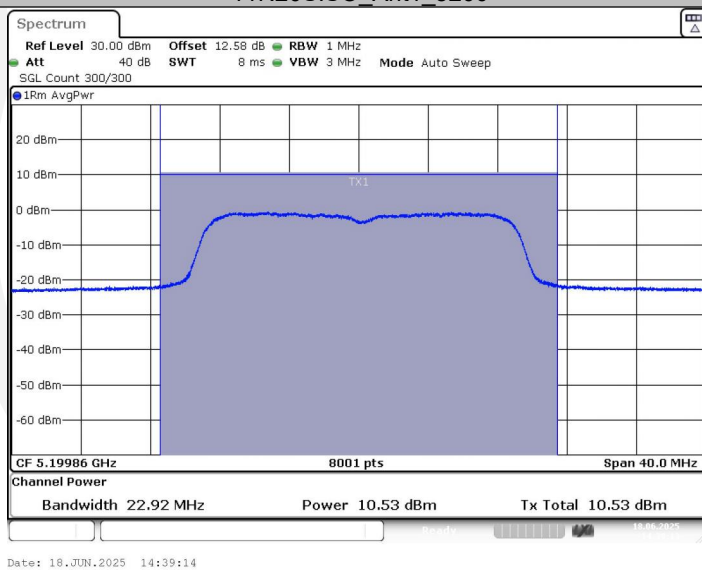
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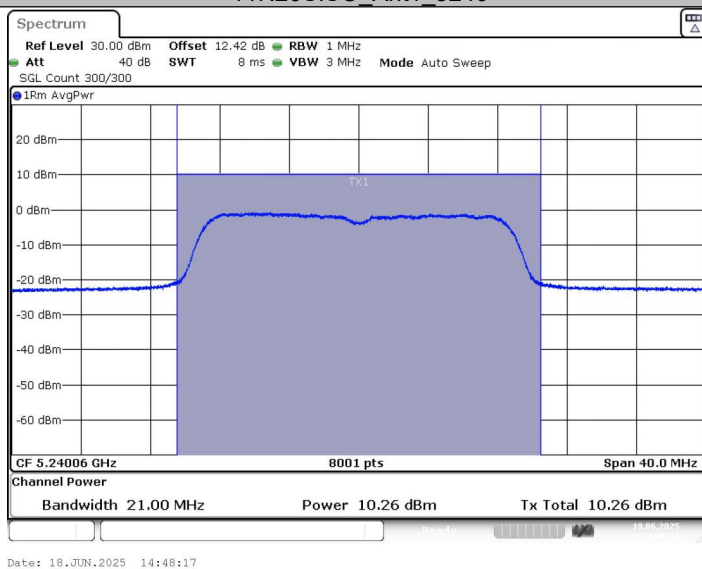
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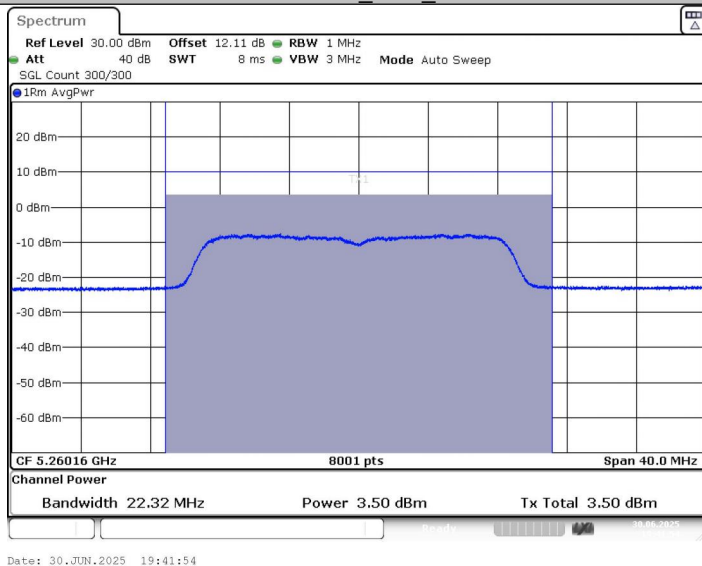
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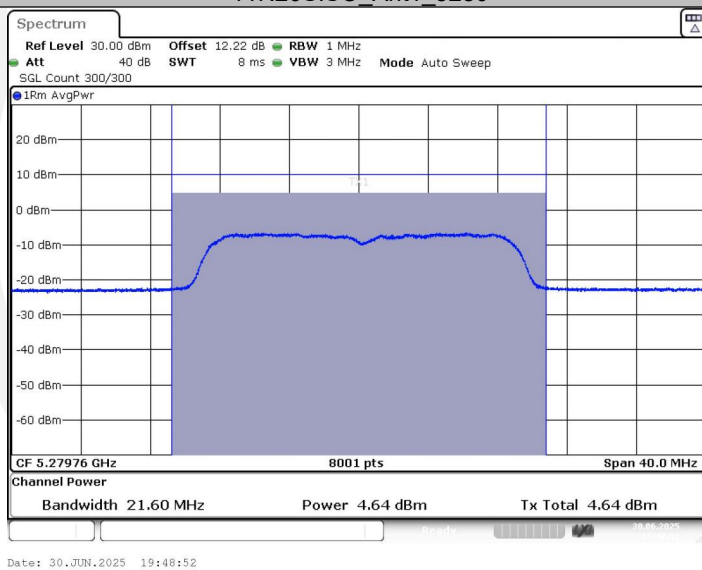
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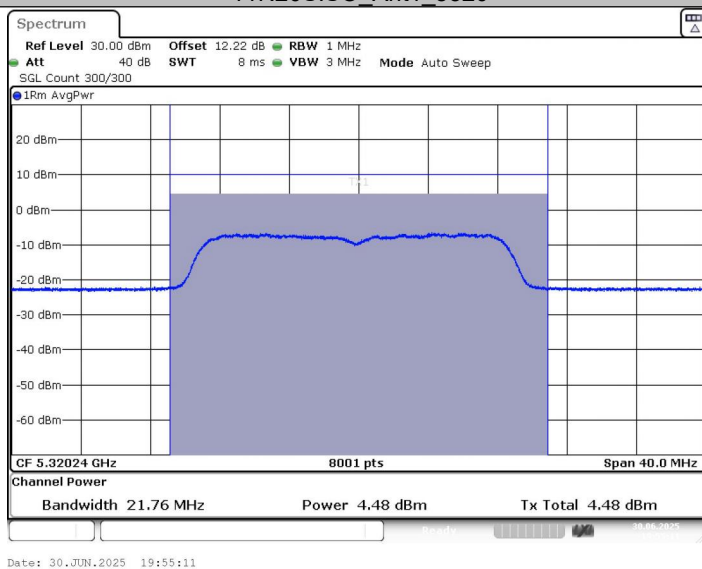
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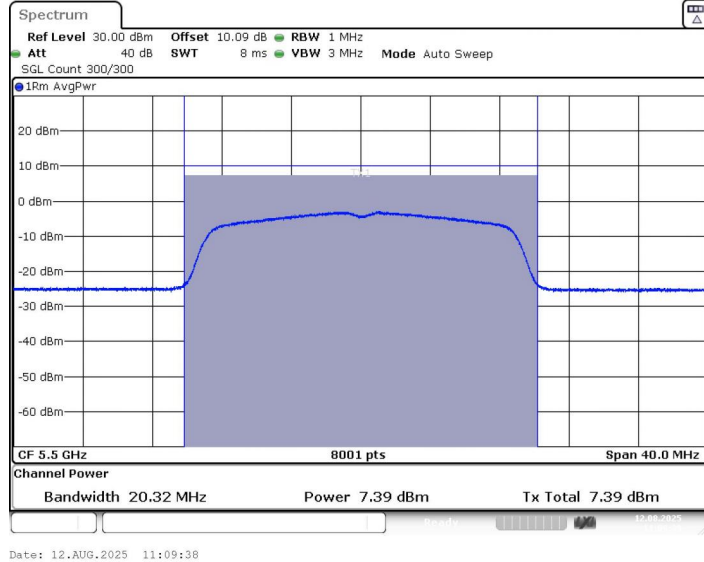
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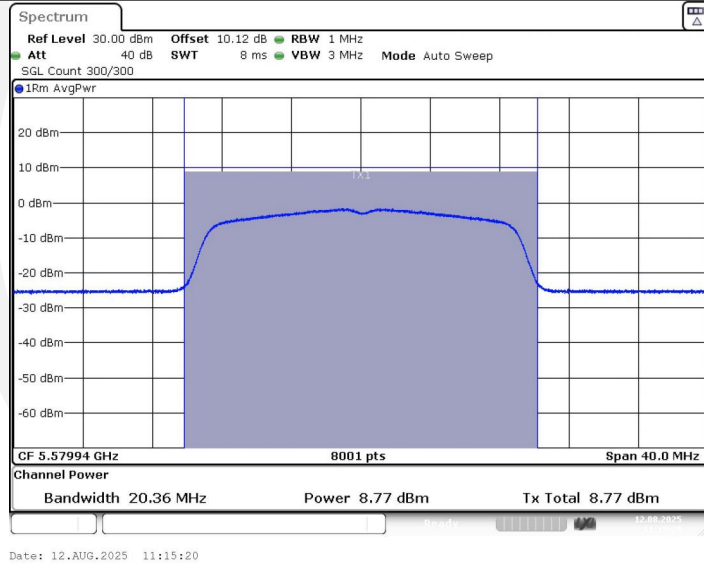
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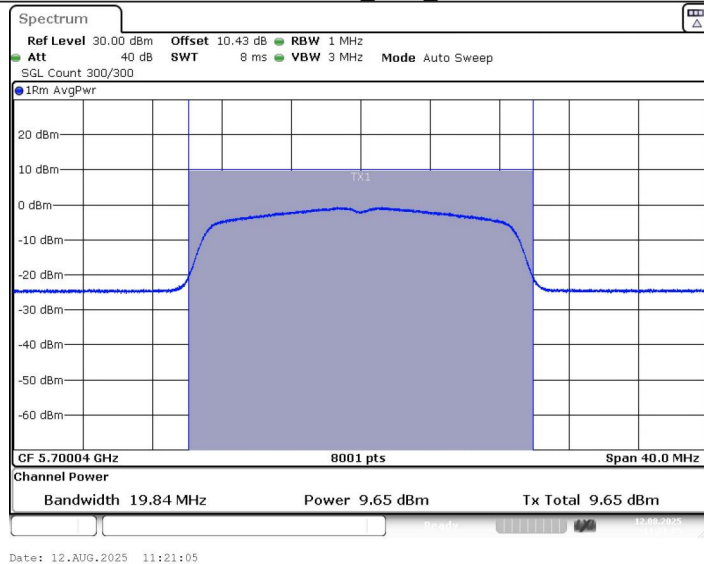
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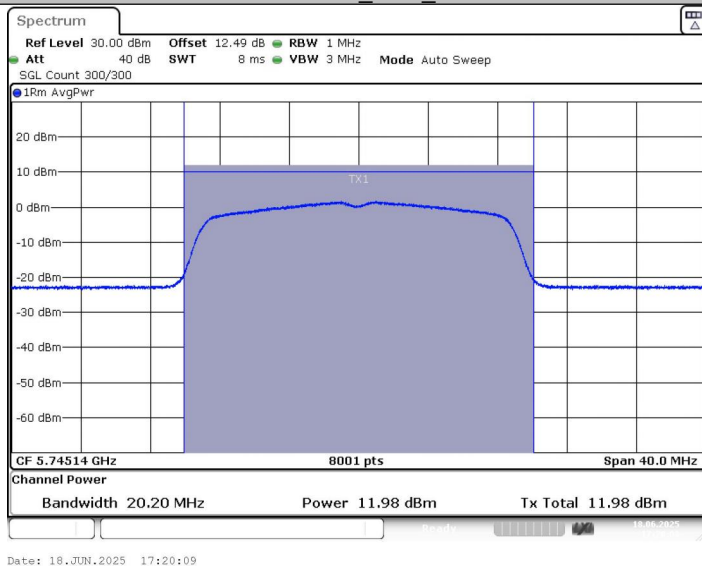
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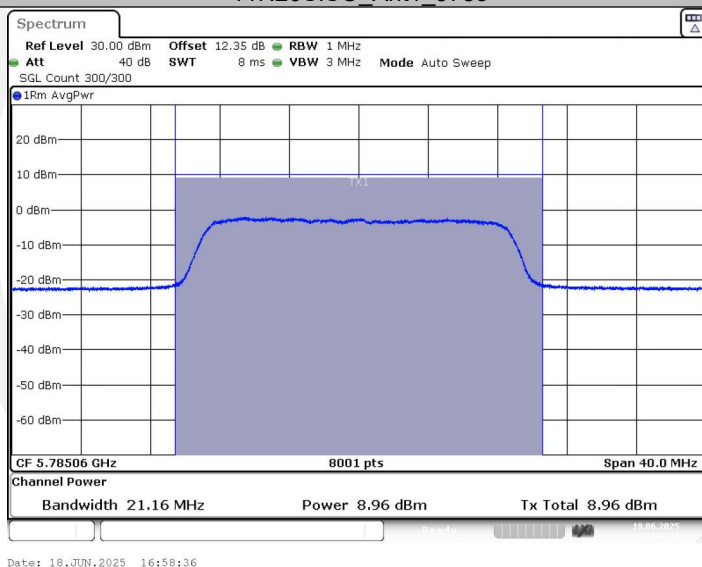
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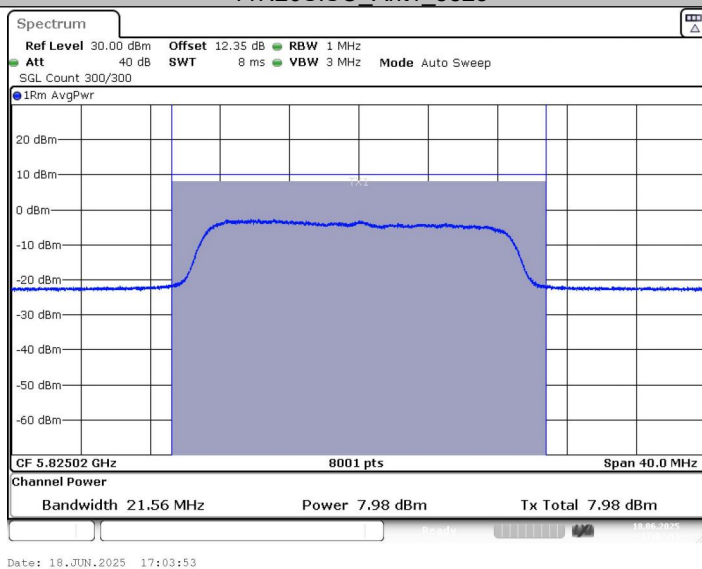
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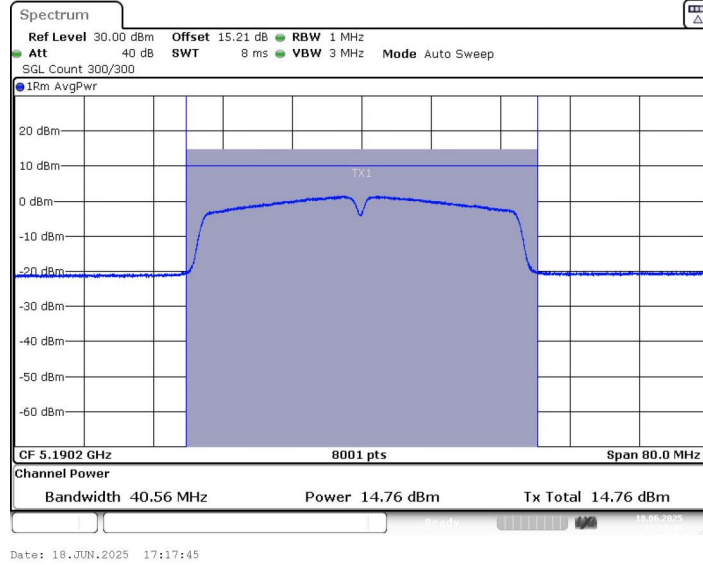
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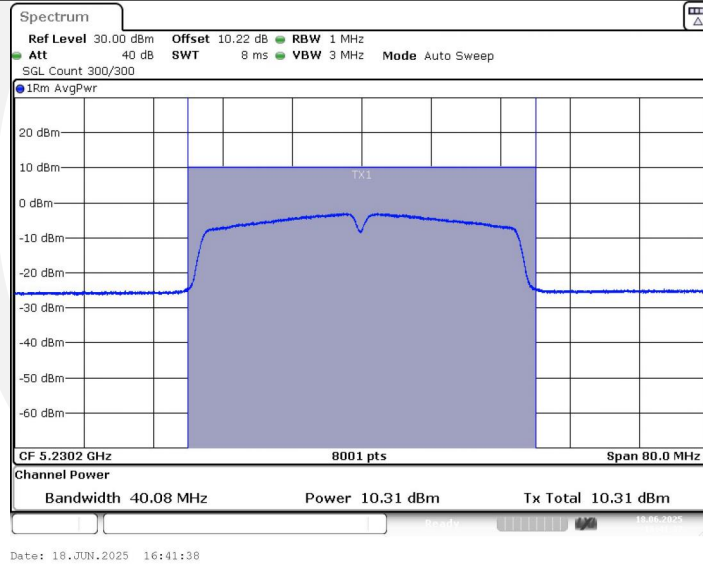
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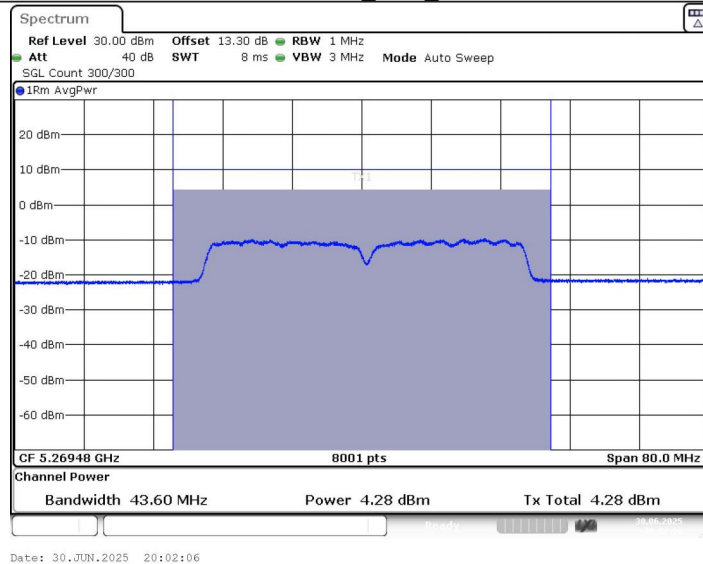
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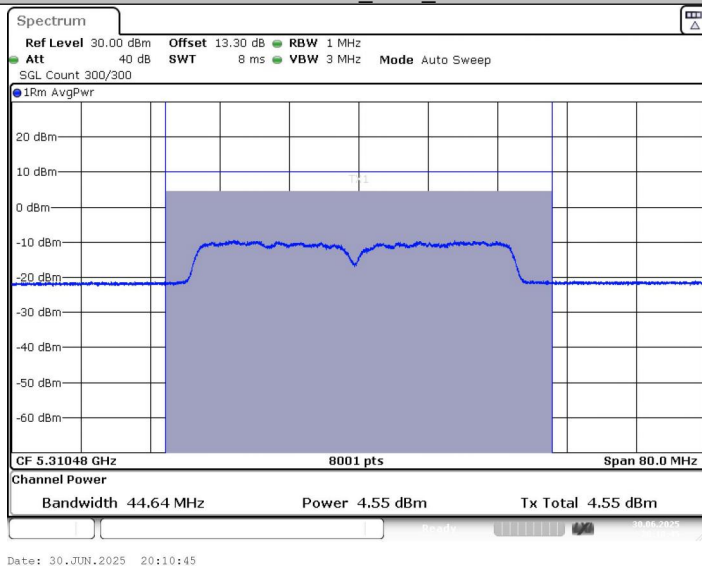
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11N40SISO_Ant1_5270



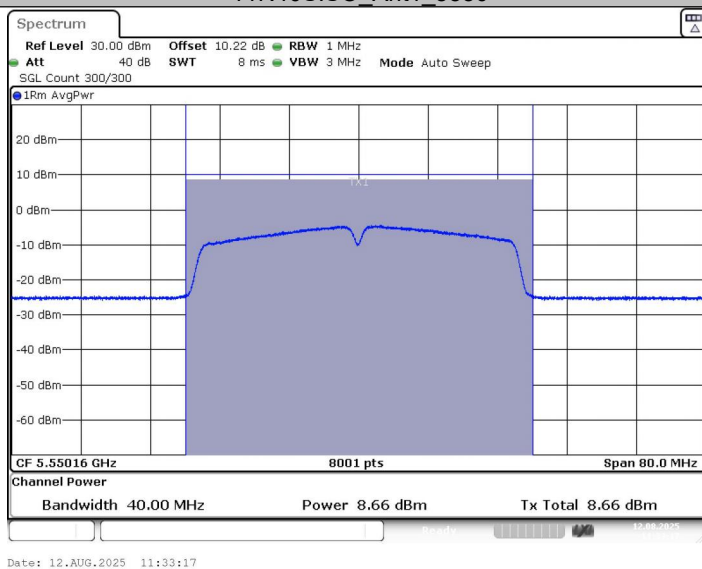
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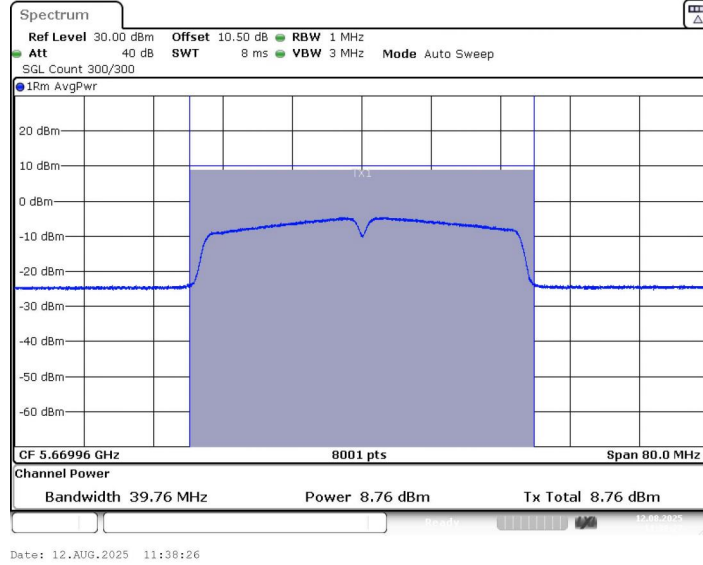
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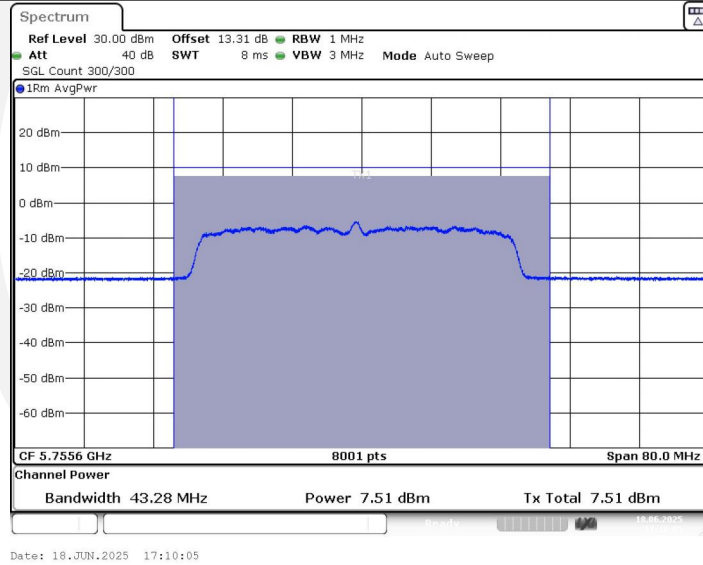
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11N40SISO_Ant1_5670



11N40SISO_Ant1_5755



11N40SISO_Ant1_5795

