



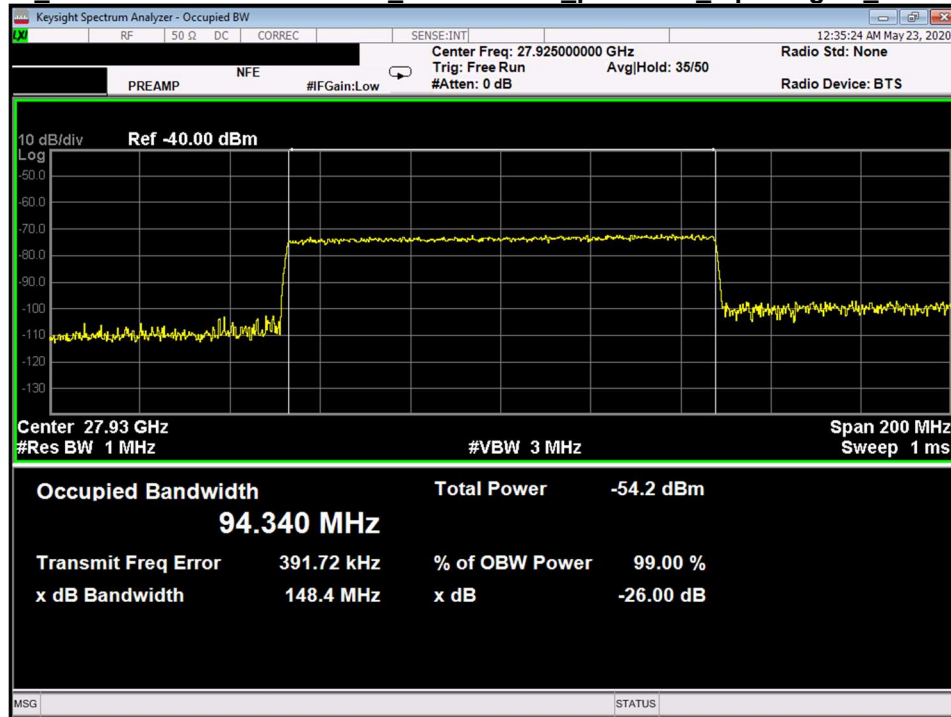
## **Annex B**

### **Input vs Output Signal Comparison**

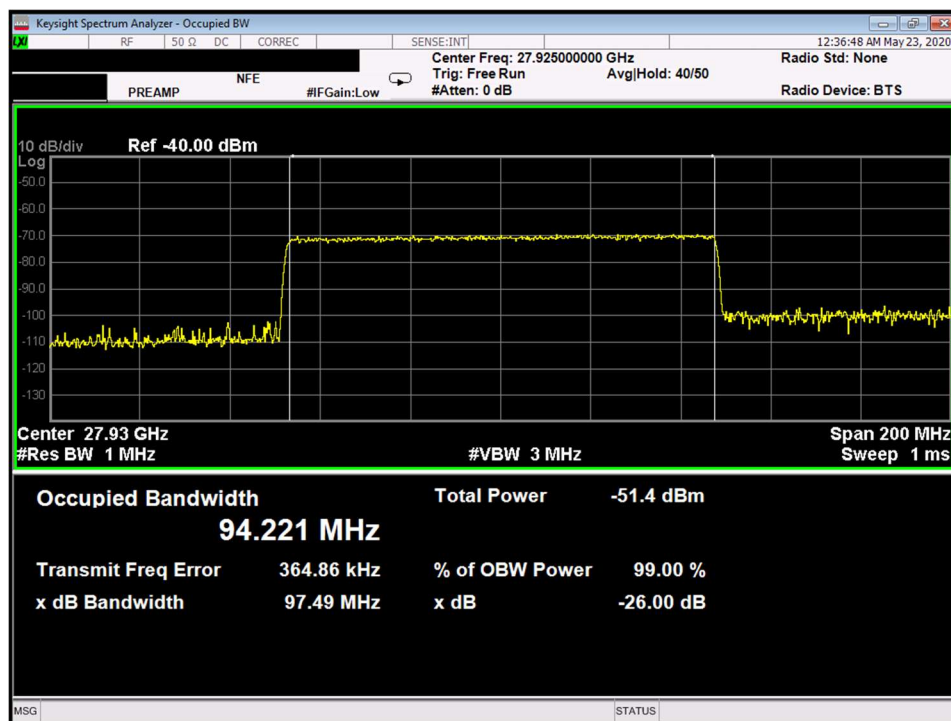




**Occ BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p19c0009\_Input Signal\_Pin - 52 dBm**

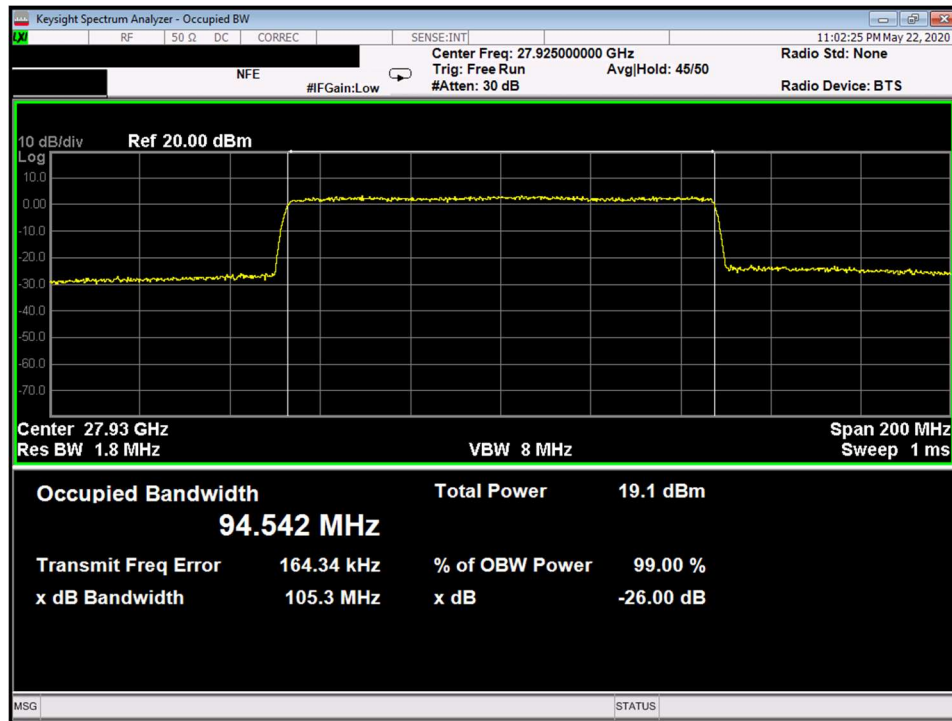


**Occ BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p19c0009\_Input Signal\_Pin - 52 dBm\_+ 3 dB**

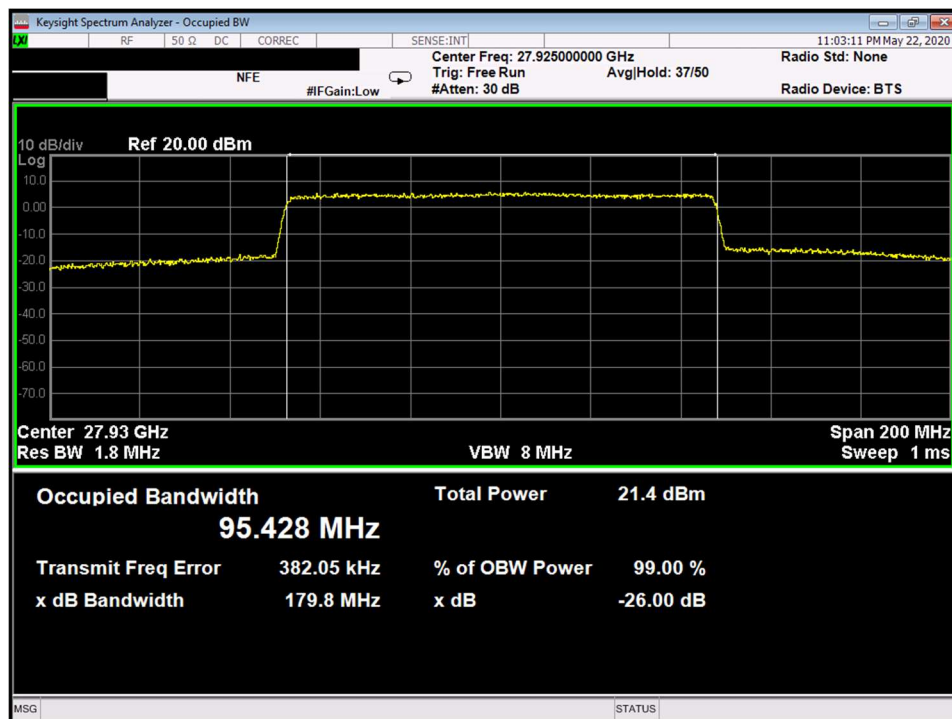




### Occ BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p19c0009\_Output Signal

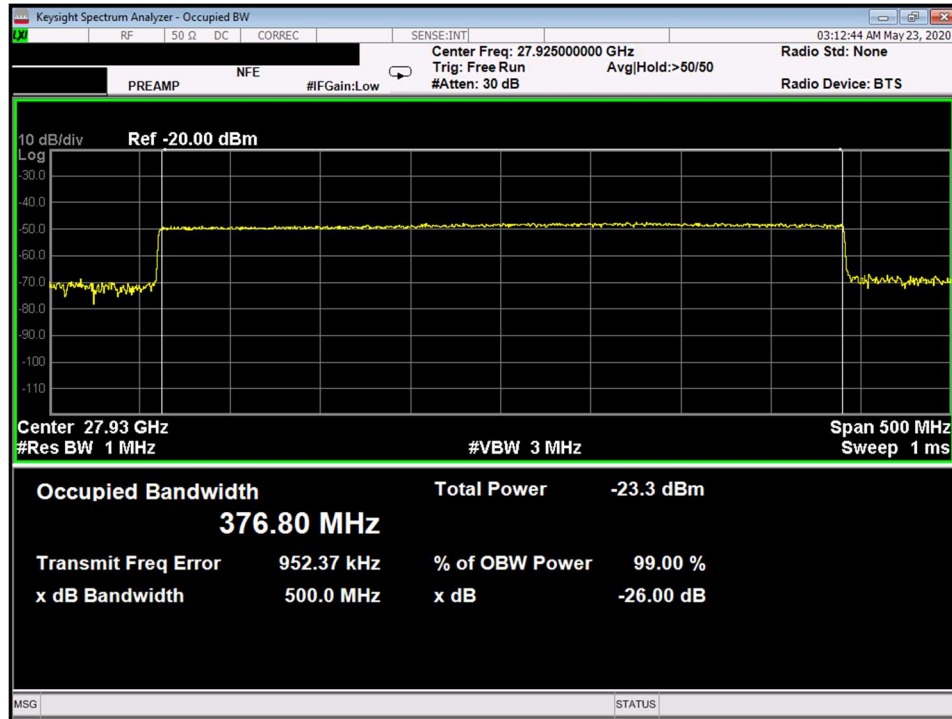


### Occ BW\_MIMO DL In - MIMO DL Out\_100 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

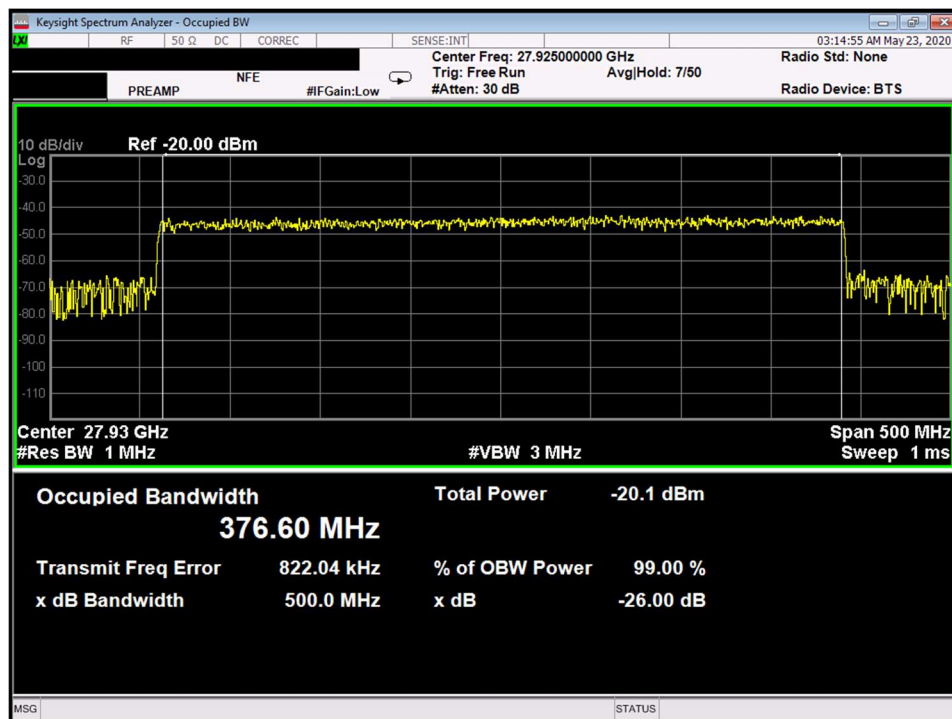




**Occ BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p19c0009\_Input Signal\_-52 dBm**

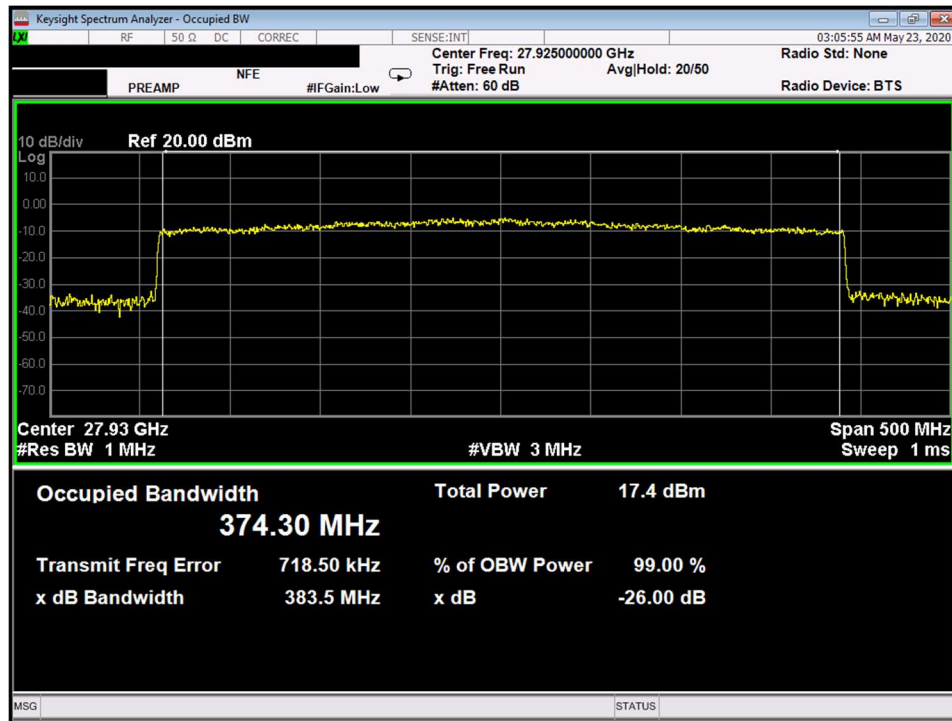


**Occ BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p19c0009\_Input Signal\_-52 dBm\_Pin + 3 dB**

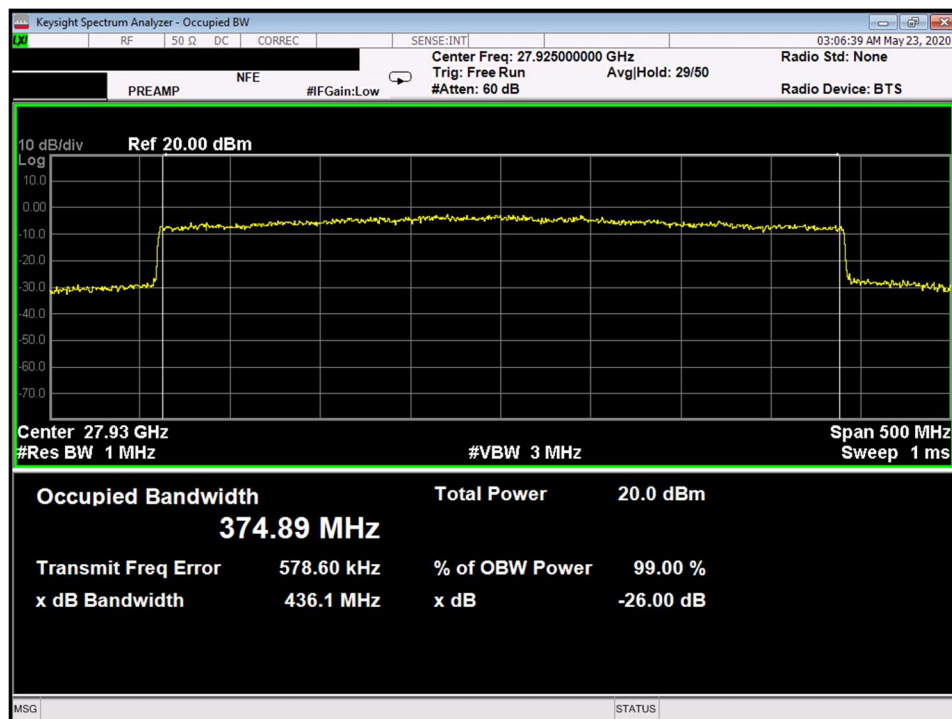




### Occ BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p19c0009\_Output Signal

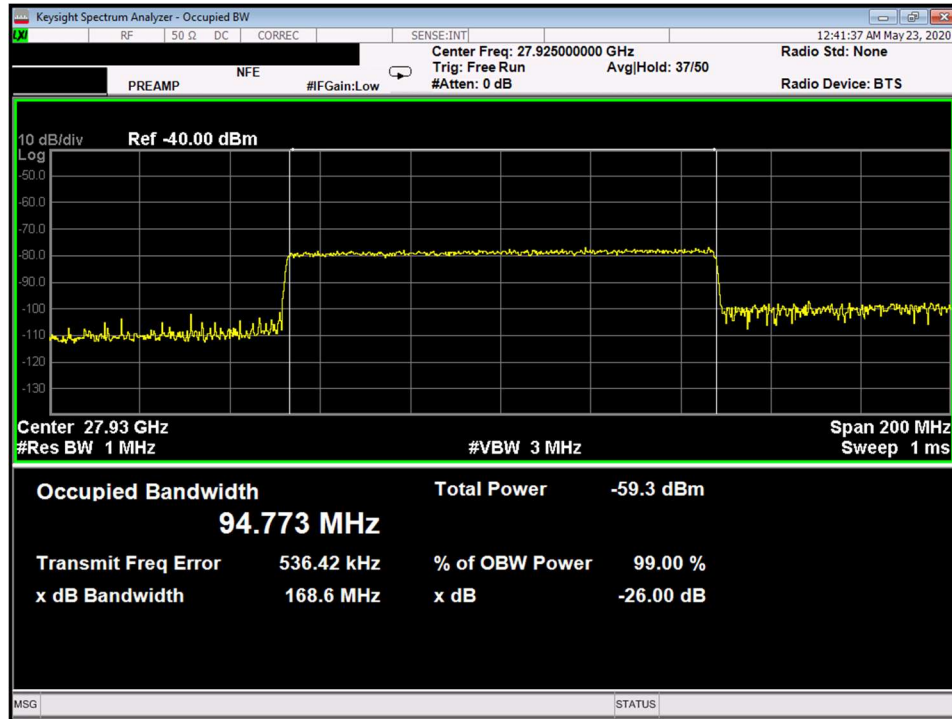


### Occ BW\_MIMO DL In - MIMO DL Out\_400 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

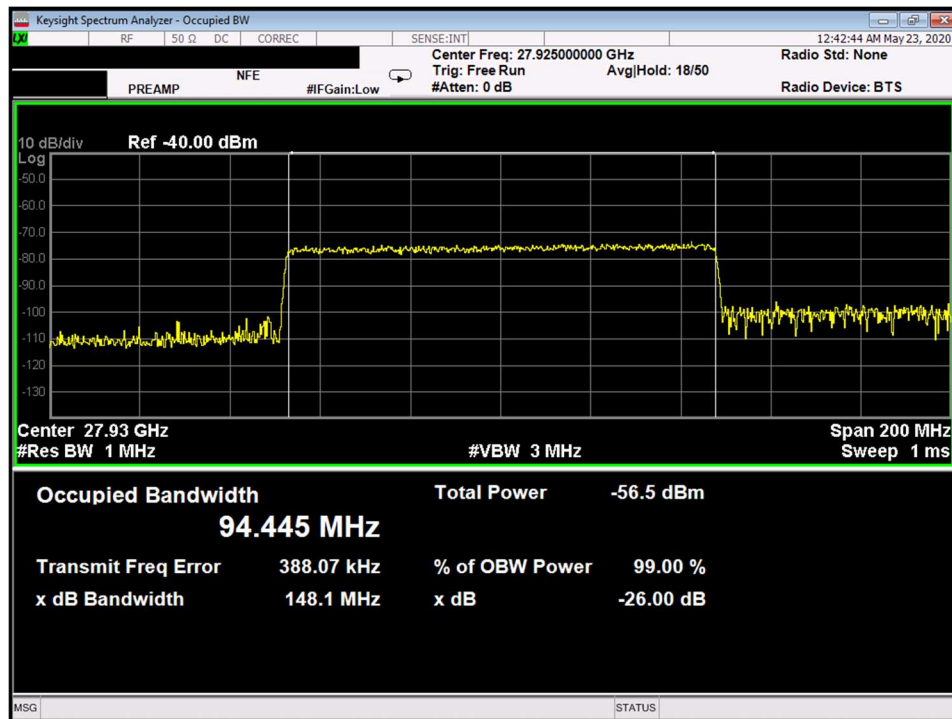




**Occ BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p19c0009\_Input Signal Signal\_Pin - 57 dBm**

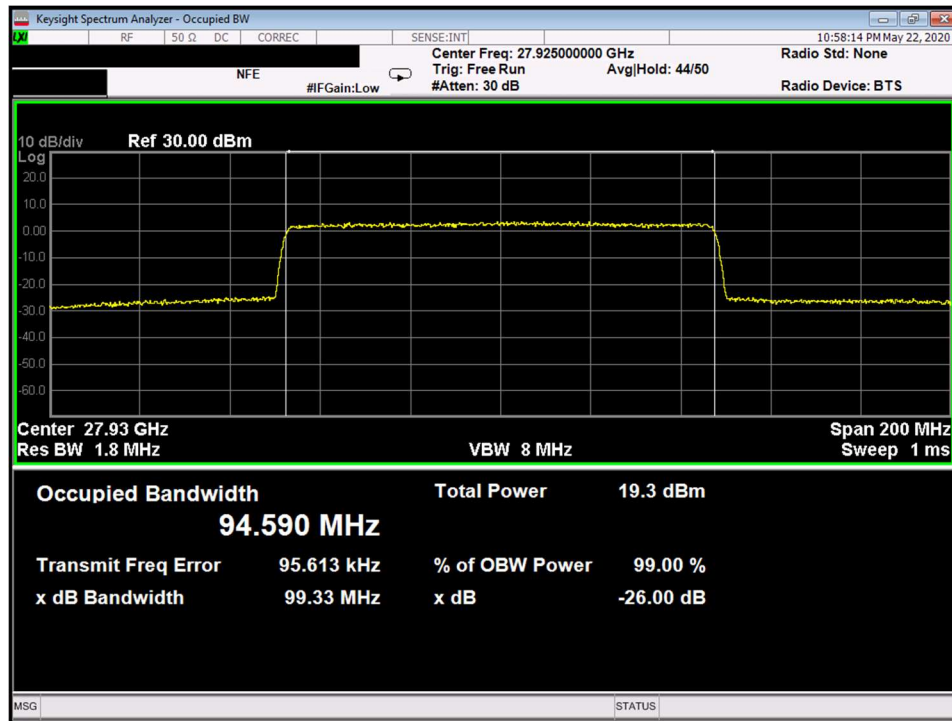


**Occ BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p19c0009\_Input Signal Signal\_Pin - 57 dBm\_+ 3 dB**

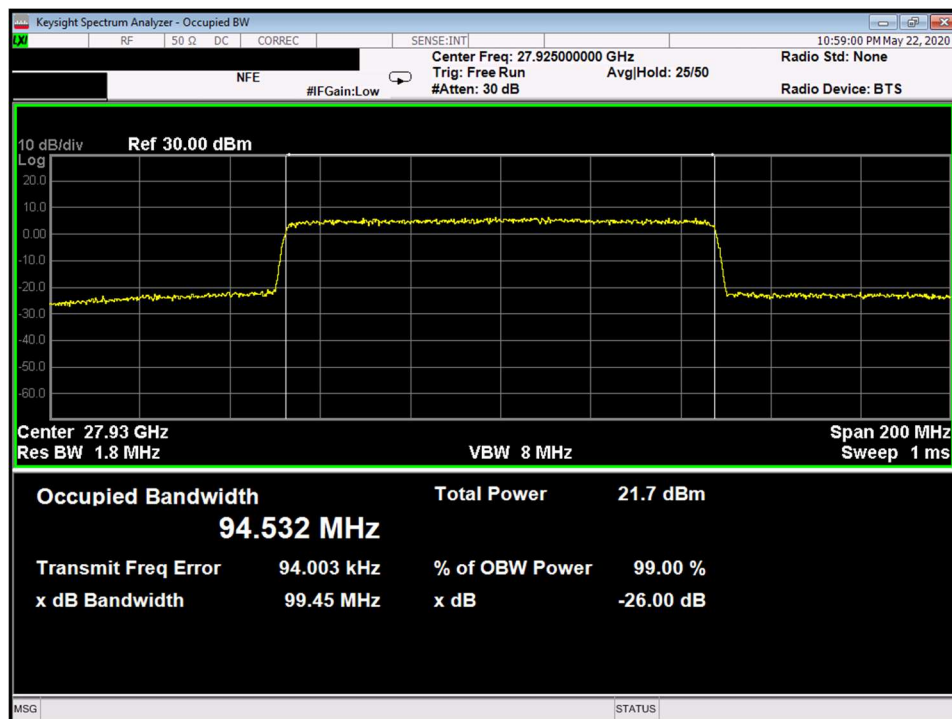




### Occ BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p19c0009\_Output Signal

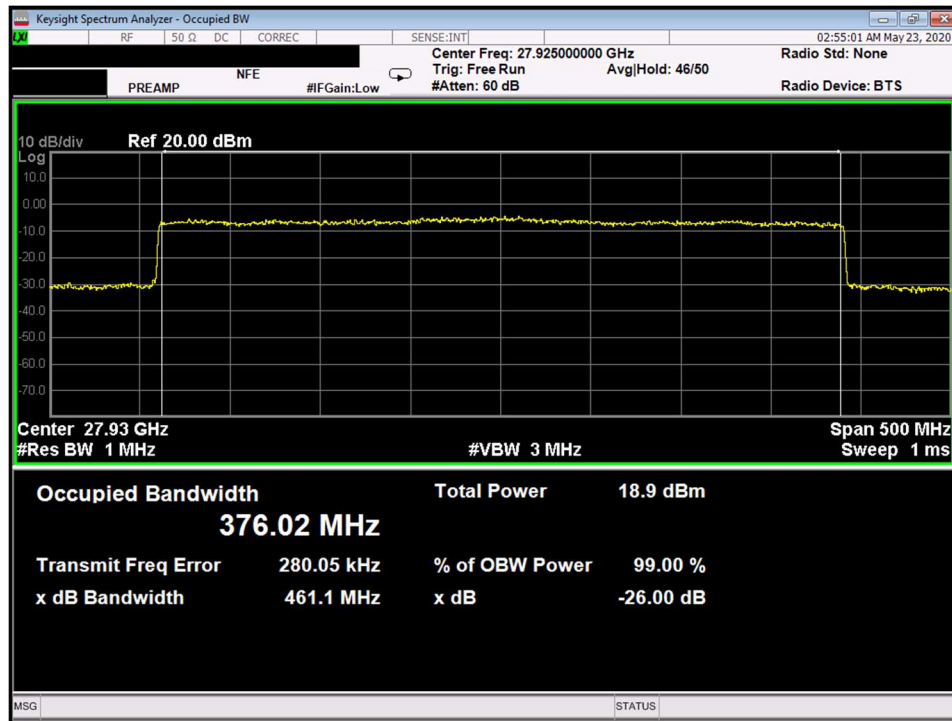


### Occ BW\_MIMO UL In - MIMO UL Out\_100 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

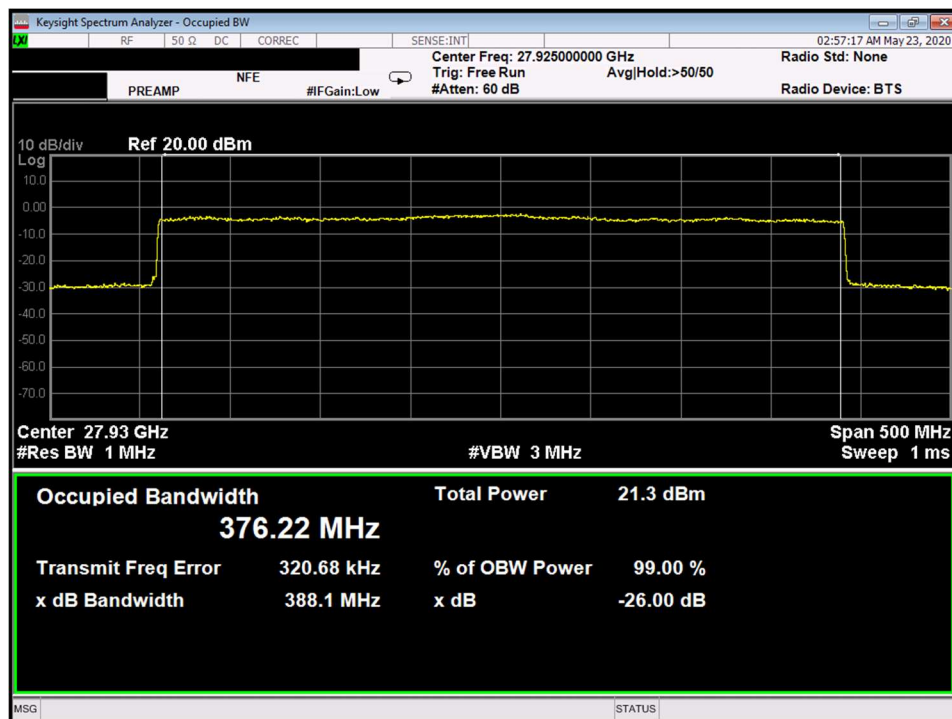




### Occ BW\_MIMO UL In - MIMO UL Out\_400 MHz BW\_p19c0009\_Output Signal

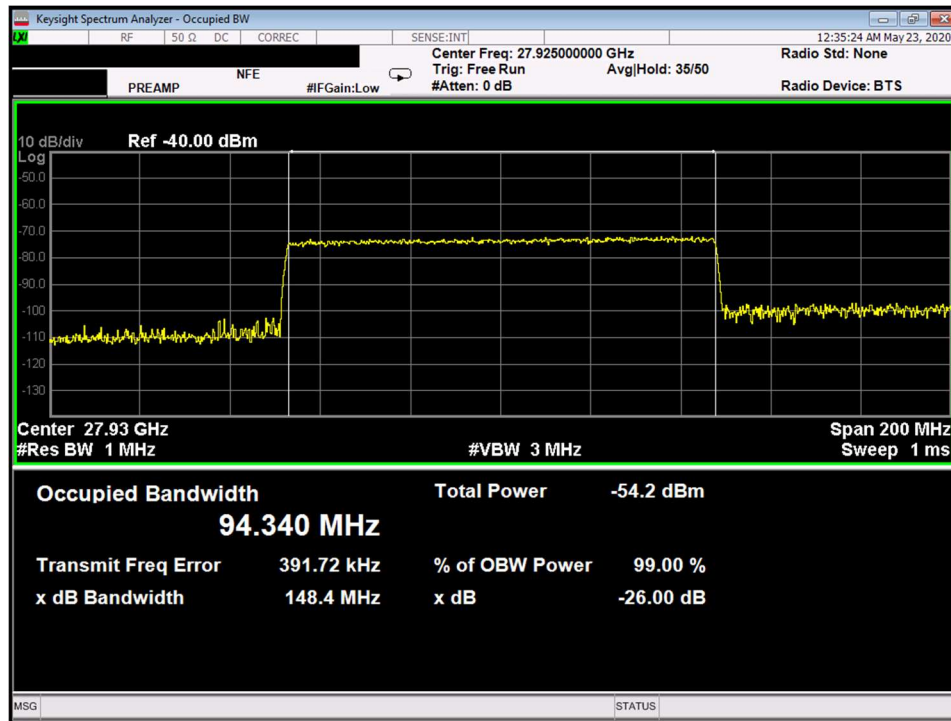


### Occ BW\_MIMO UL In - MIMO UL Out\_400 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

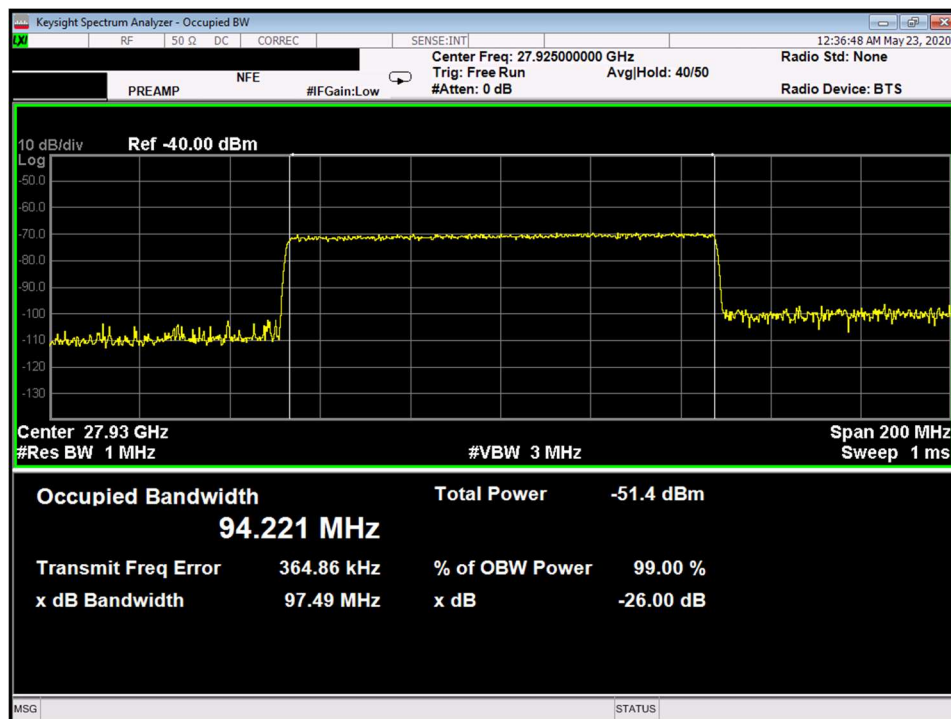




**Occ BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p19c0009\_Input Signal\_Pin - 52 dBm**

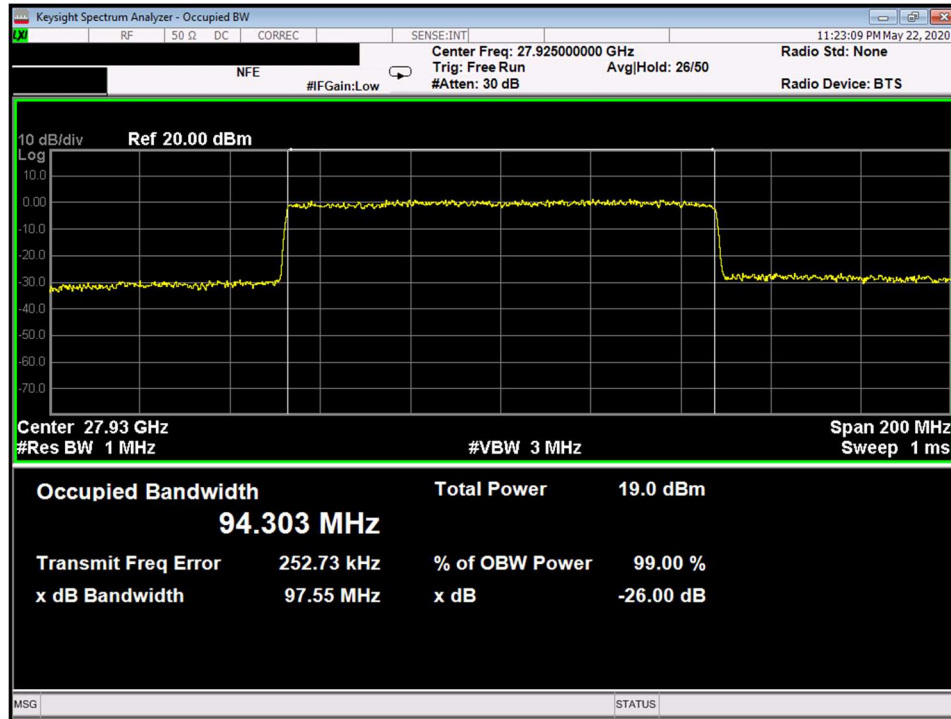


**Occ BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p19c0009\_Input Signal\_Pin - 52 dBm + 3 dB**

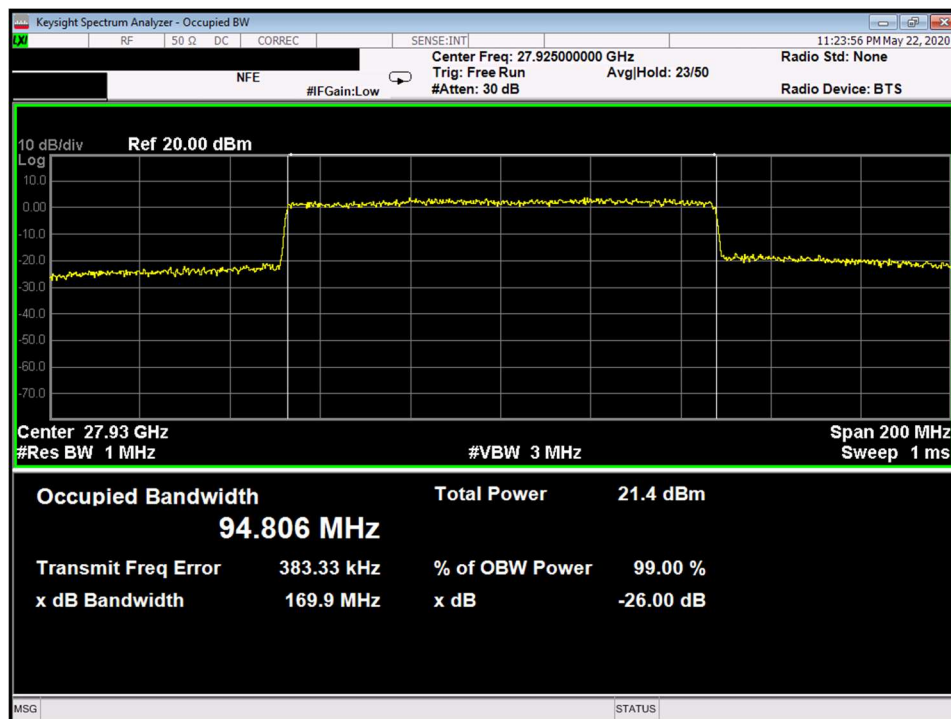




## Occ BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p19c0009\_Output Signal

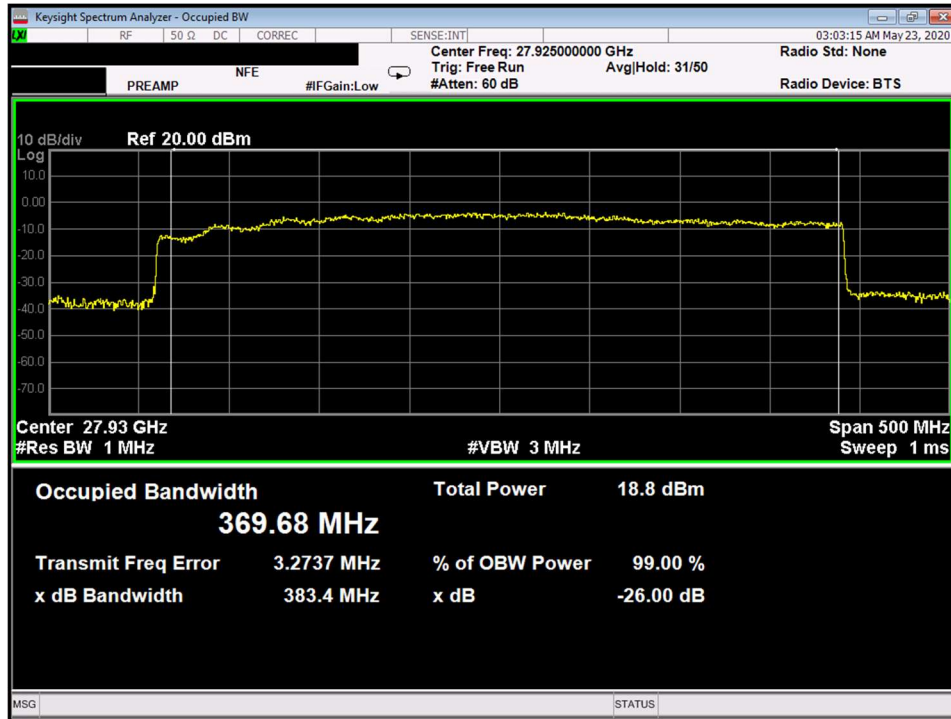


## Occ BW\_SISO DL In - SISO DL Out\_100 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

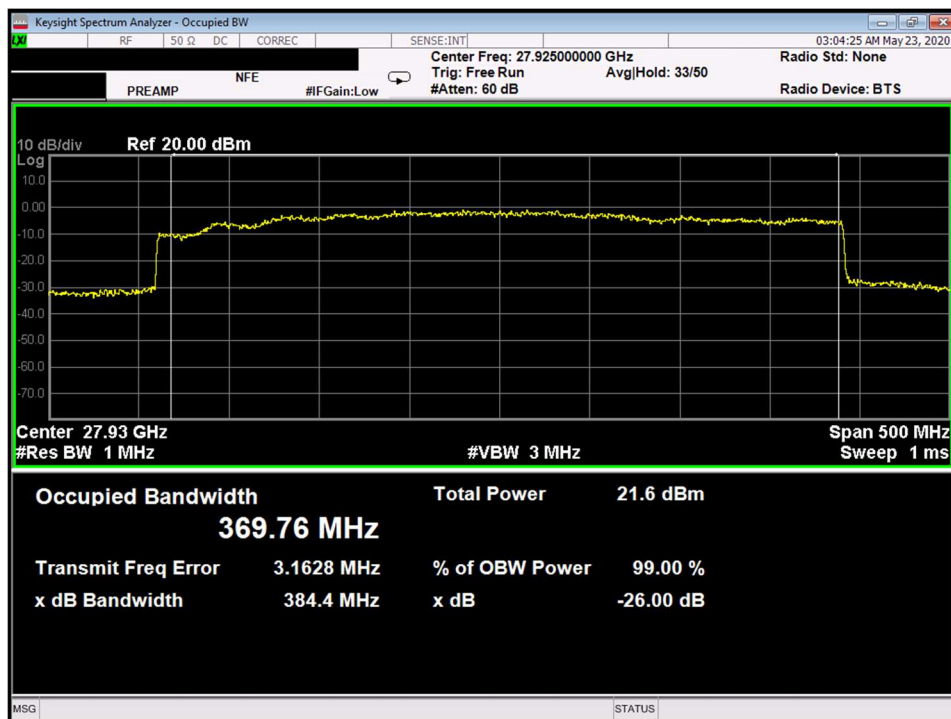




### Occ BW\_SISO DL In - SISO DL Out\_400 MHz BW\_p19c0009\_Output Signal

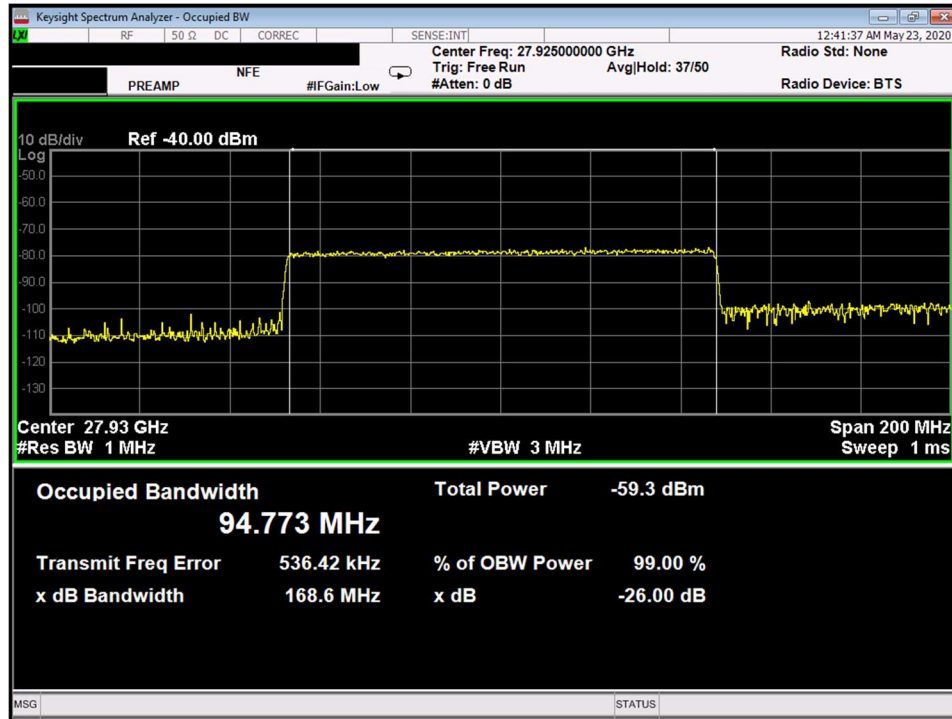


### Occ BW\_SISO DL In - SISO DL Out\_400 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

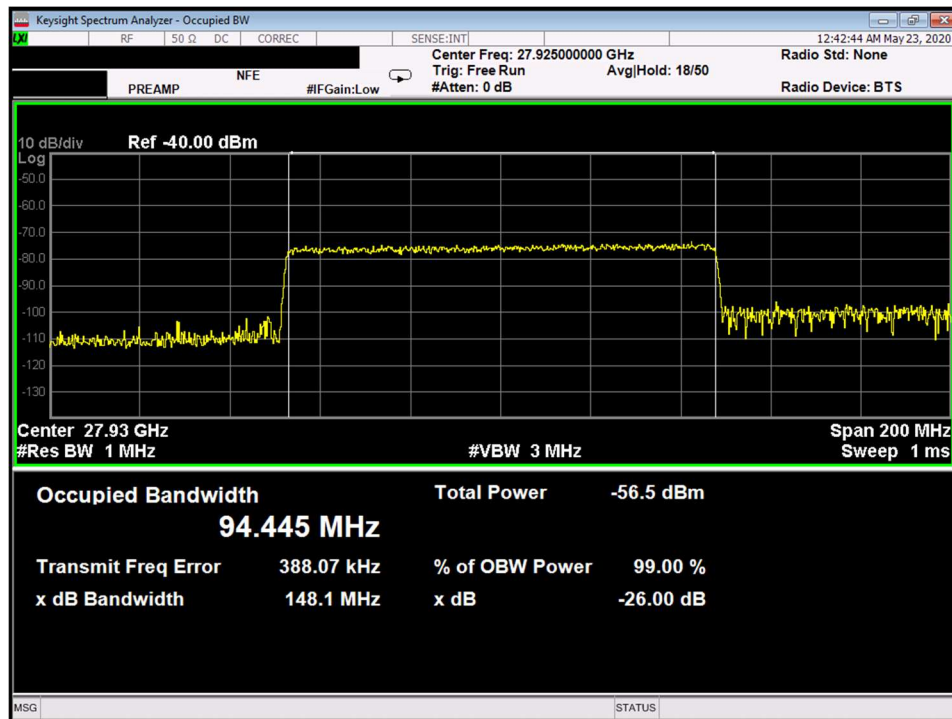




Occ BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p19c0009\_Input Signal Signal\_Pin - 57 dBm

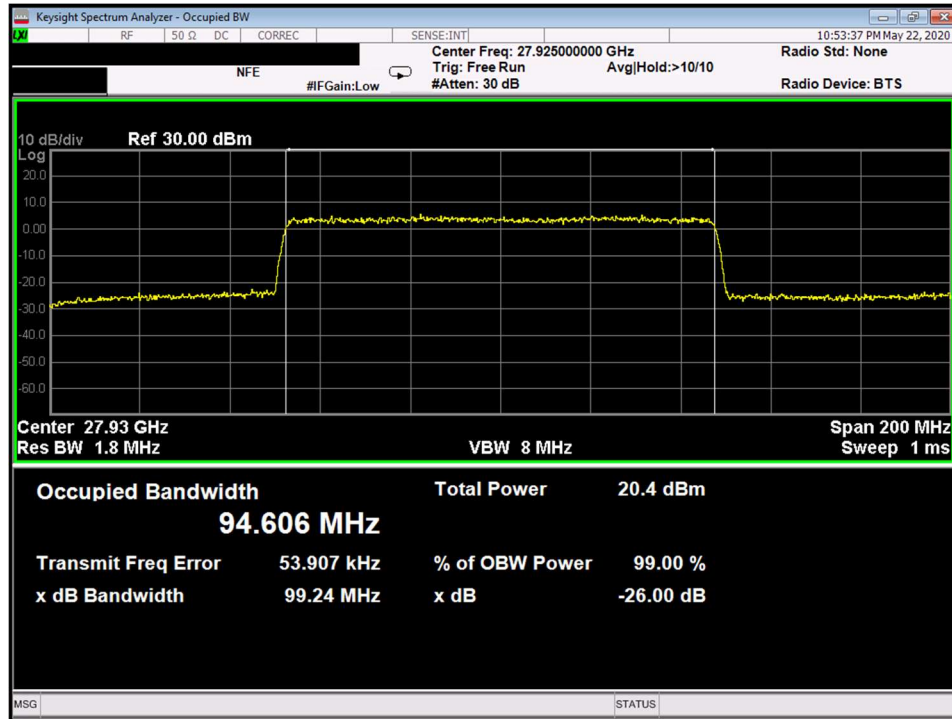


Occ BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p19c0009\_Input Signal Signal\_Pin - 57 dBm\_+ 3 dB

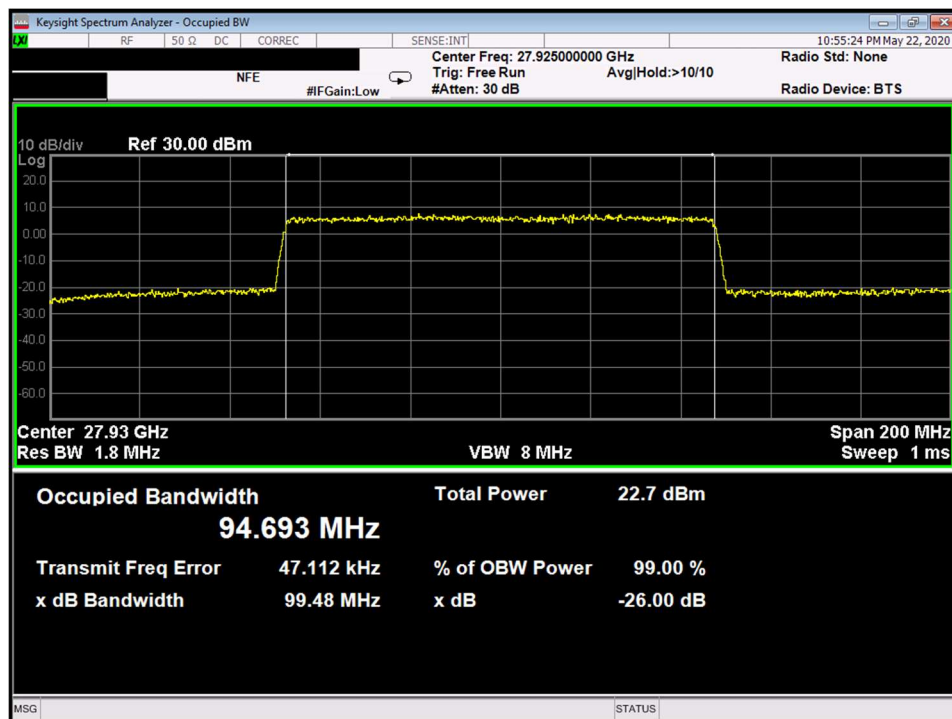




### Occ BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p19c0009\_Output Signal

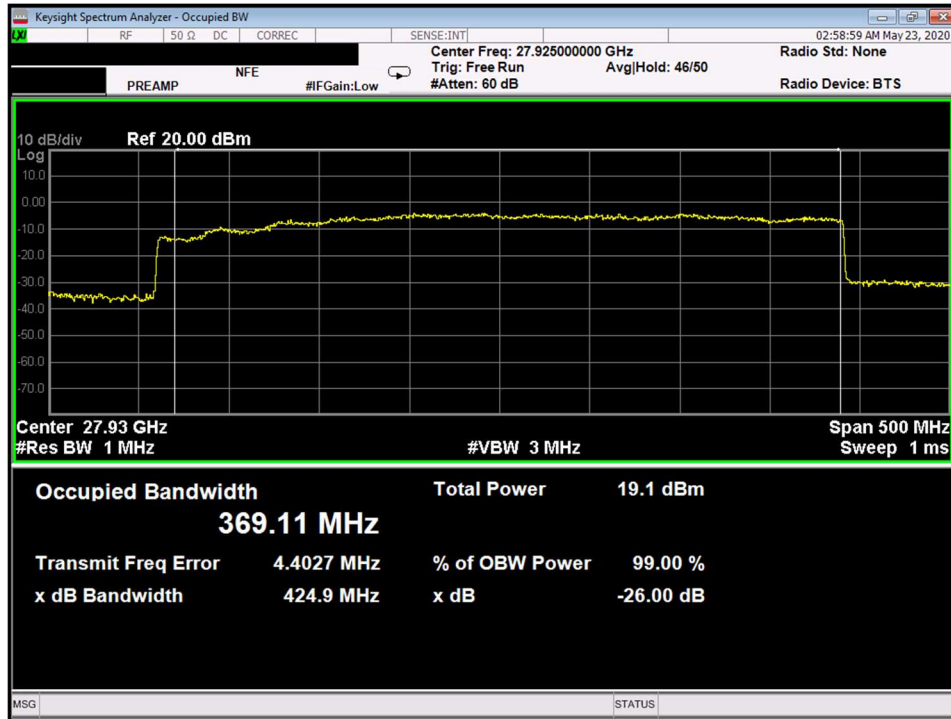


### Occ BW\_SISO UL In - SISO UL Out\_100 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB





### Occ BW\_SISO UL In - SISO UL Out\_400 MHz BW\_p19c0009\_Output Signal



### Occ BW\_SISO UL In - SISO UL Out\_400 MHz BW\_p19c0009\_Output Signal\_Pin + 3 dB

