

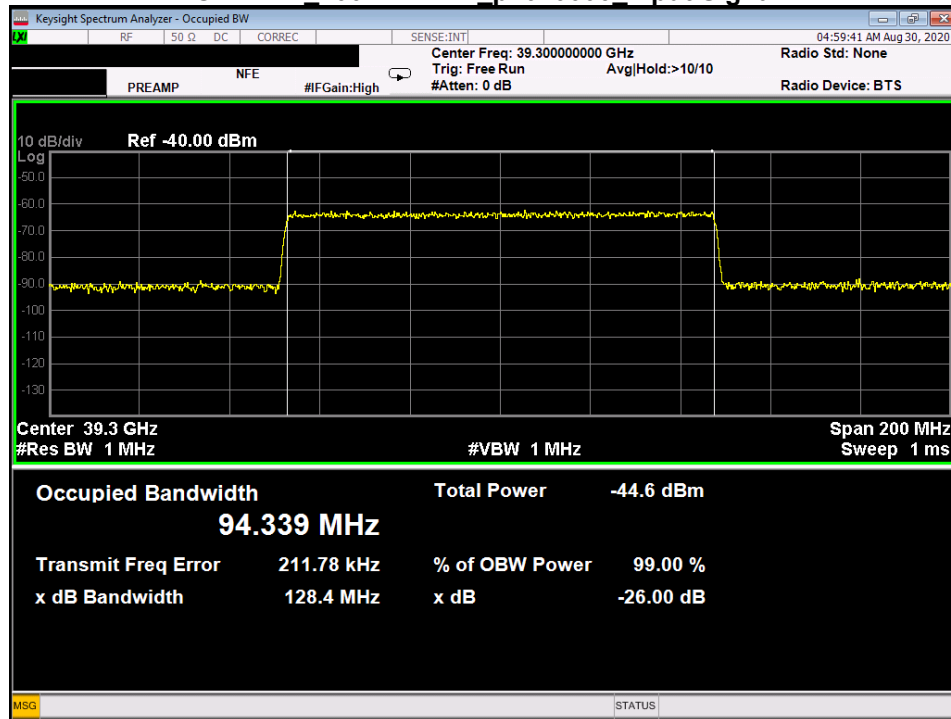


Annex B

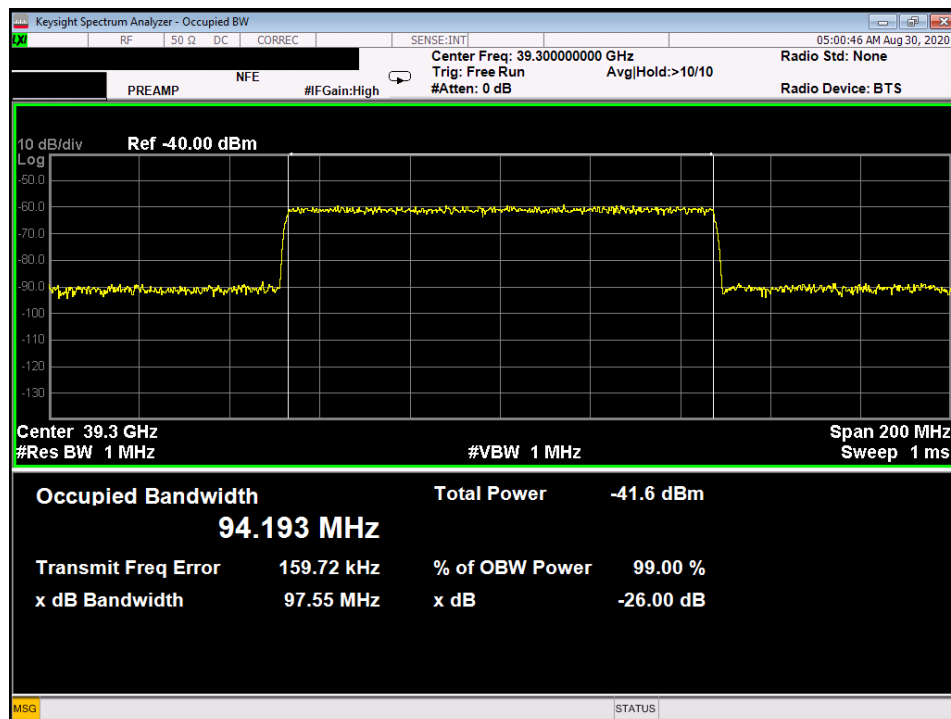
Input vs Output Signal Comparison



OCC BW_100 MHz BW_p2070006_Input Signal

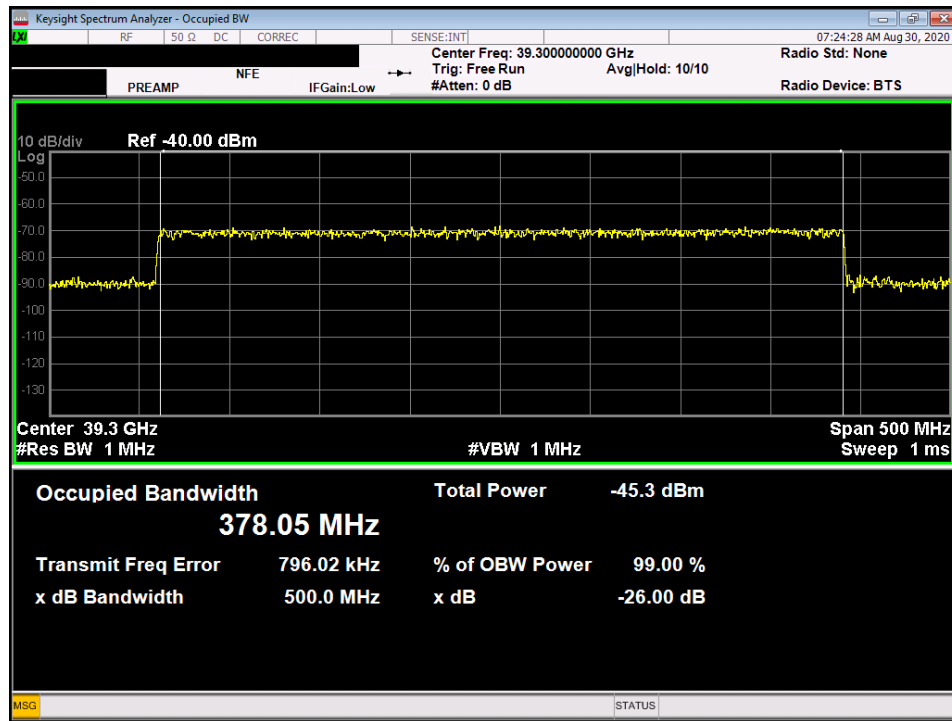


OCC BW_100 MHz BW_p2070006_Input Signal_Pin + 3 dB

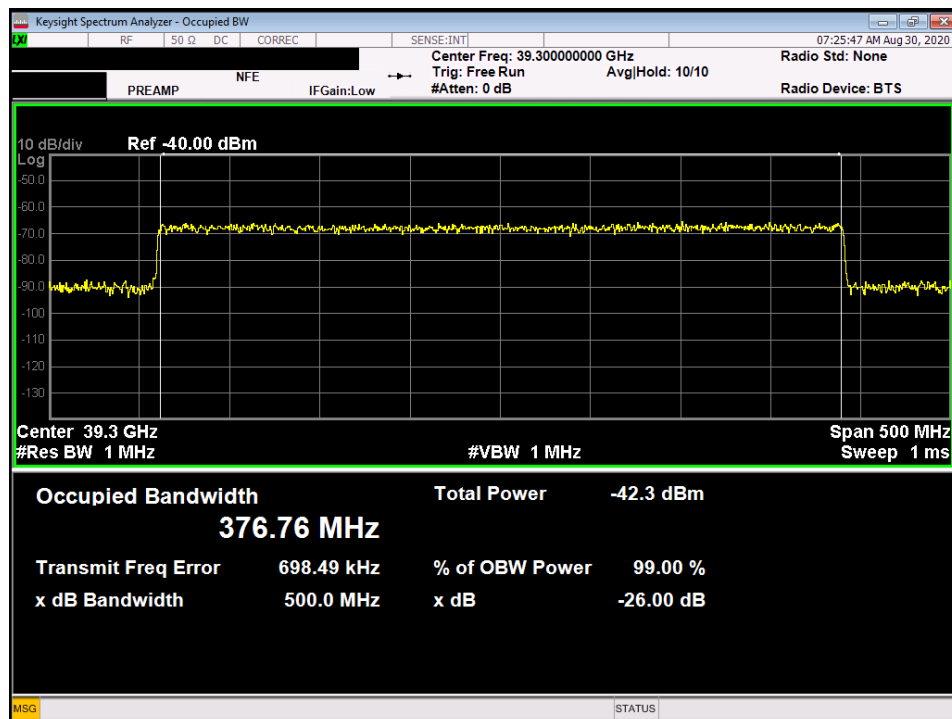




OCC BW_400 MHz BW_p2070006_Input Signal

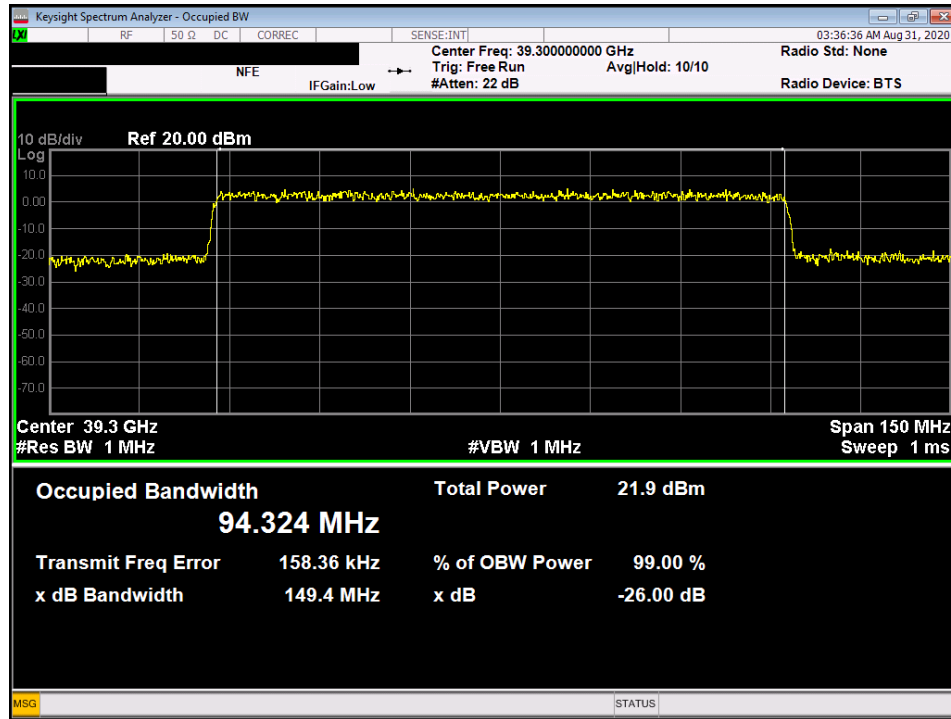


OCC BW_400 MHz BW_p2070006_Input Signal_Pin + 3 dB

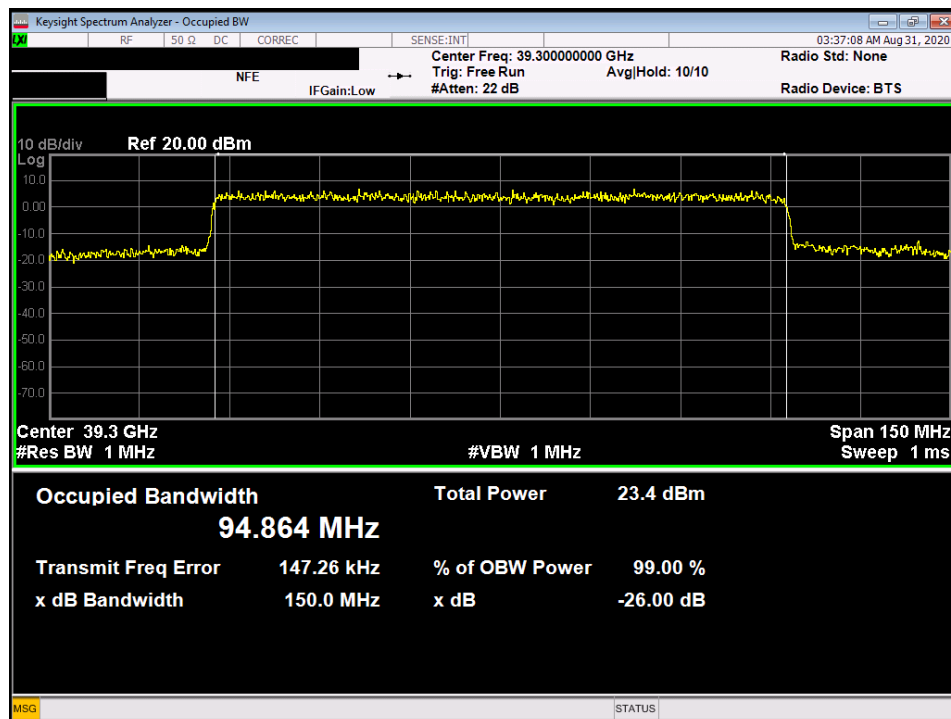




OCC BW_MIMO DL In - MIMO DL Out_100 MHz BW_p2070006_Output Signal

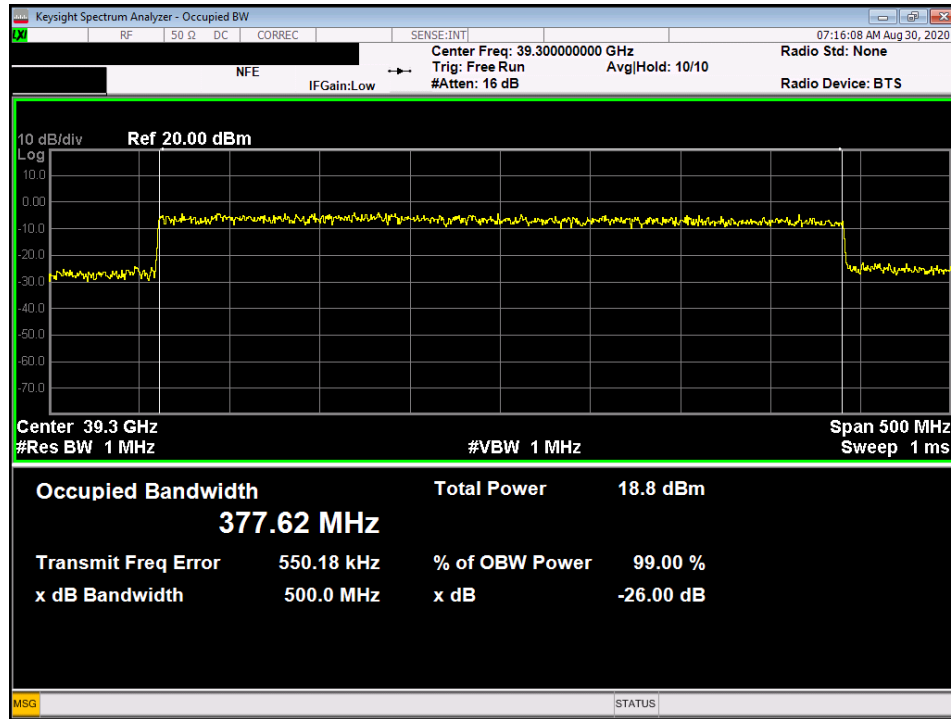


OCC BW_MIMO DL In - MIMO DL Out_100 MHz BW_p2070006_Output Signal_Pin + 3 dB

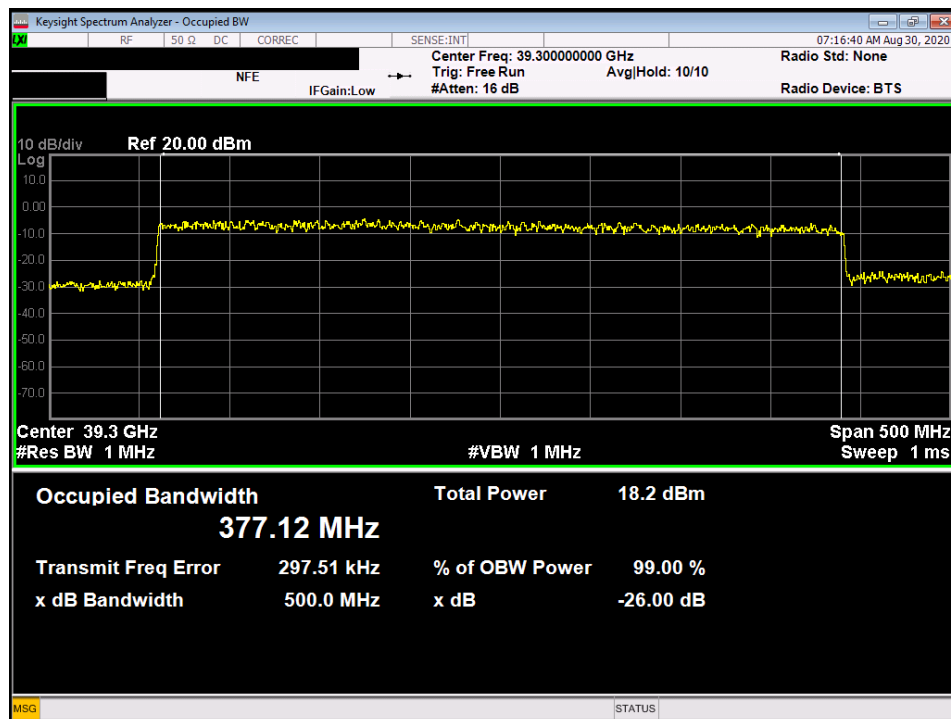




OCC BW_MIMO DL In - MIMO DL Out_400 MHz BW_p2070006_Output Signal

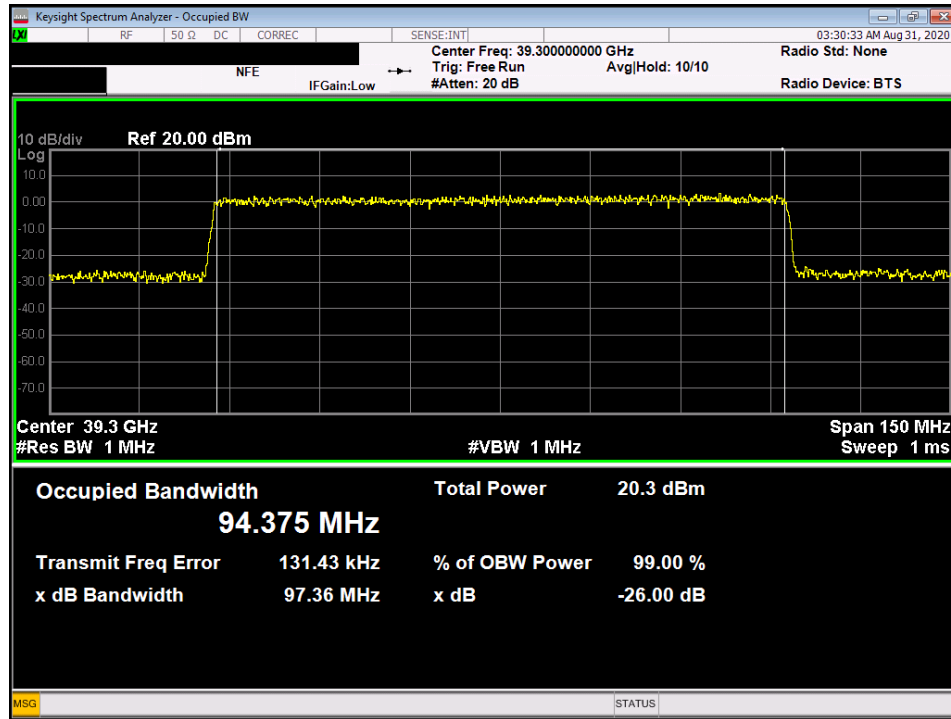


OCC BW_MIMO DL In - MIMO DL Out_400 MHz BW_p2070006_Output Signal_Pin + 3 dB

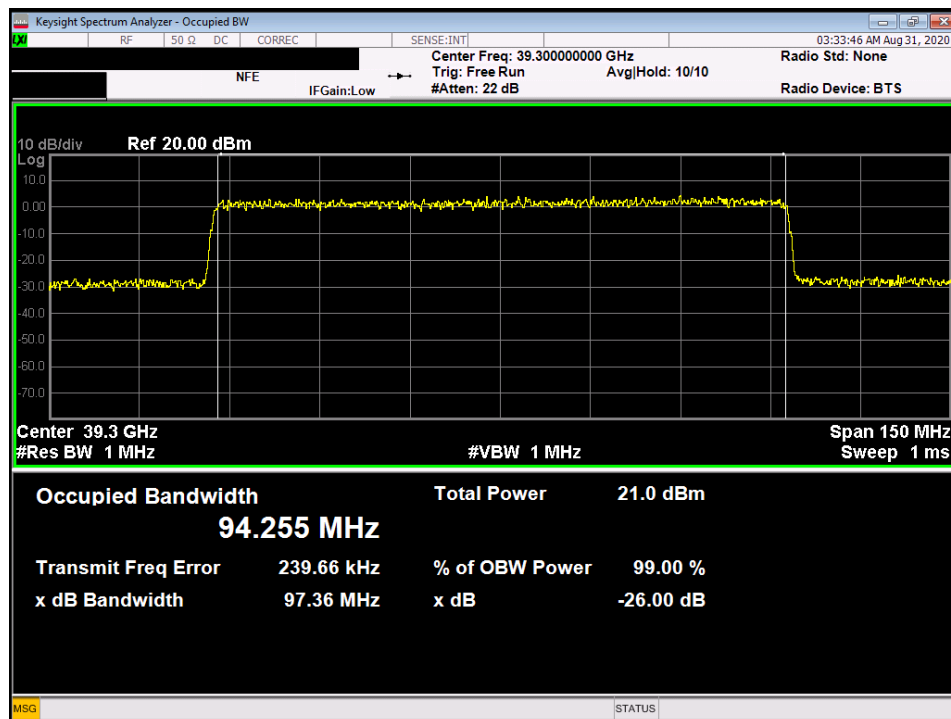




OCC BW_MIMO UL In - MIMO UL Out_100 MHz BW_p2070006_Output Signal

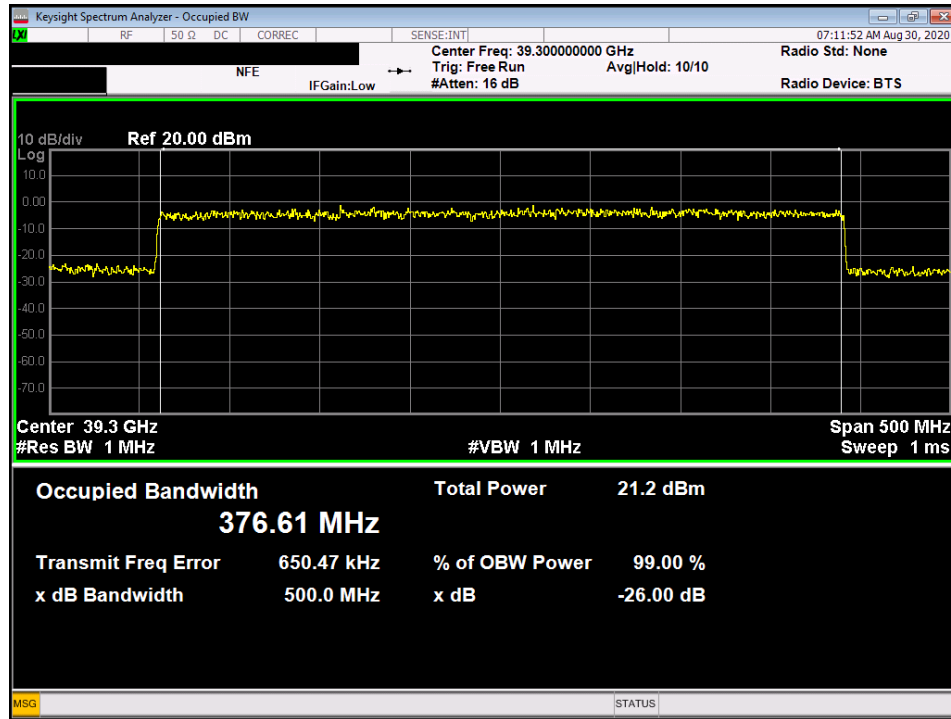


OCC BW_MIMO UL In - MIMO UL Out_100 MHz BW_p2070006_Output Signal_Pin + 3 dB

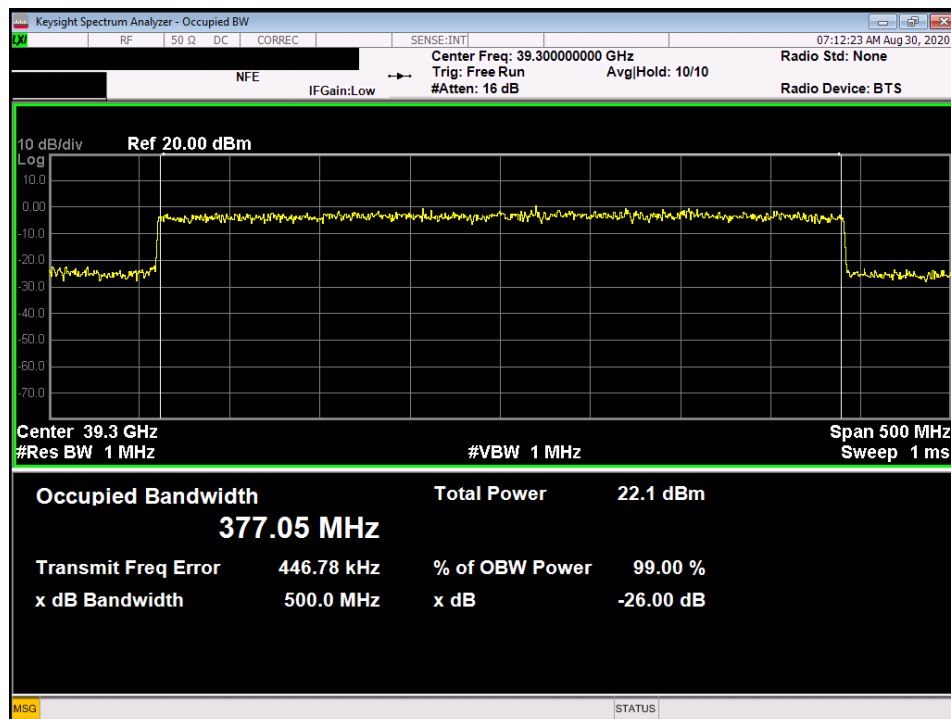




OCC BW_MIMO UL In - MIMO UL Out_400 MHz BW_p2070006_Output Signal

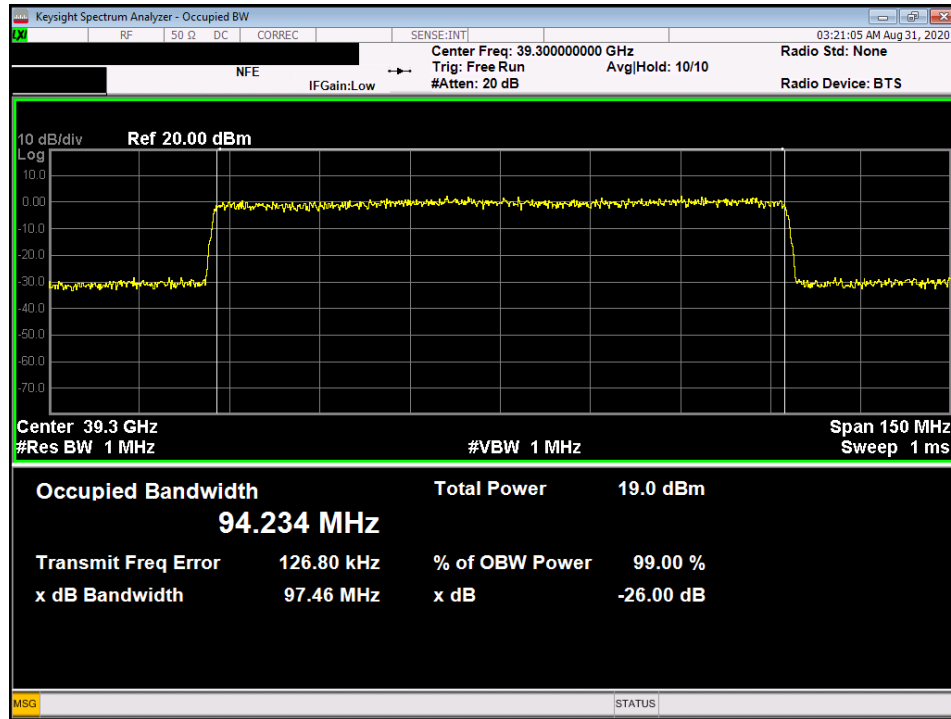


OCC BW_MIMO UL In - MIMO UL Out_400 MHz BW_p2070006_Output Signal_Pin + 3 dB

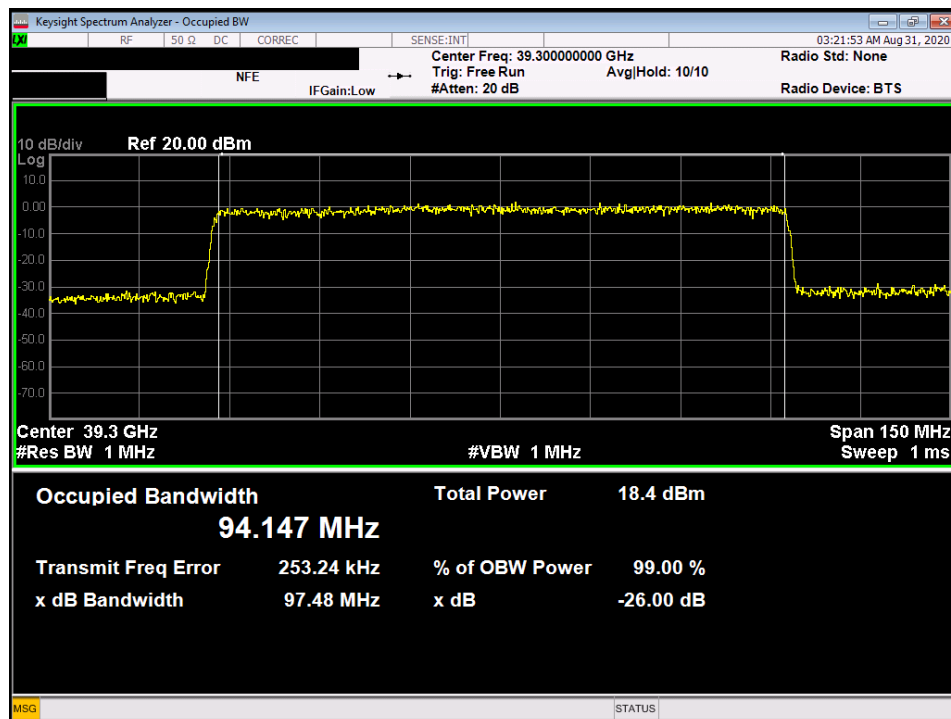




OCC BW_SISO DL In - SISO DL Out_100 MHz BW_p2070006_Output Signal

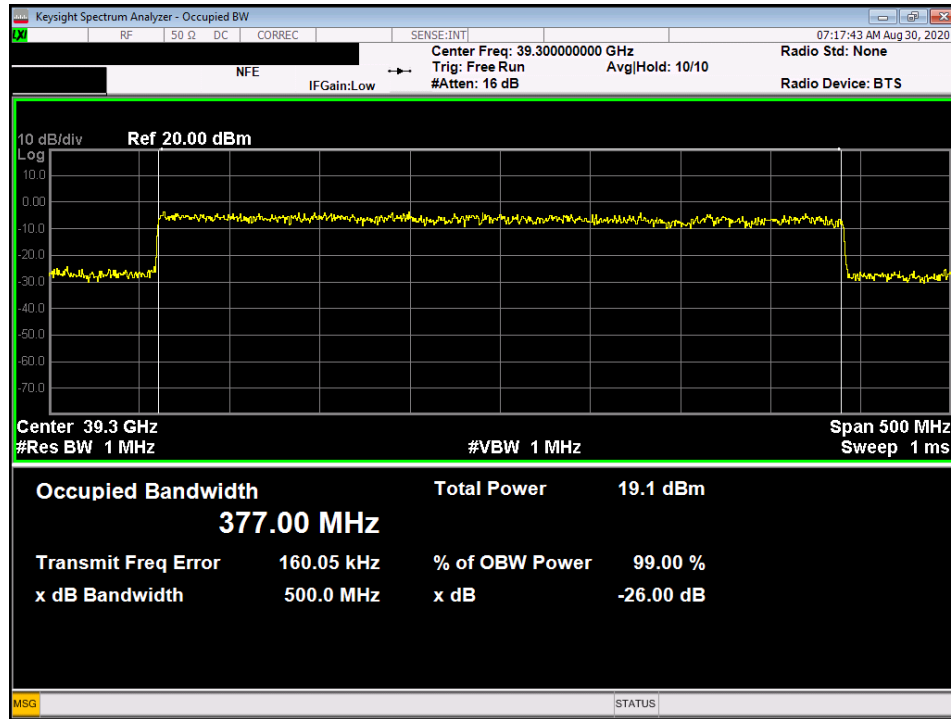


OCC BW_SISO DL In - SISO DL Out_100 MHz BW_p2070006_Output Signal_Pin + 3 dB

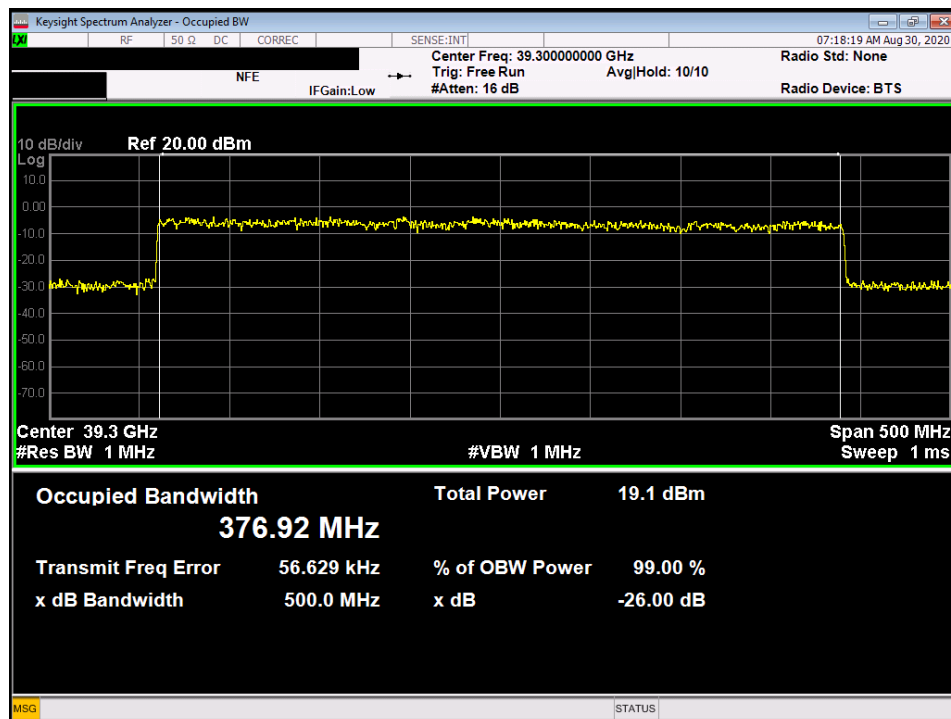




OCC BW_SISO DL In - SISO DL Out_400 MHz BW_p2070006_Output Signal

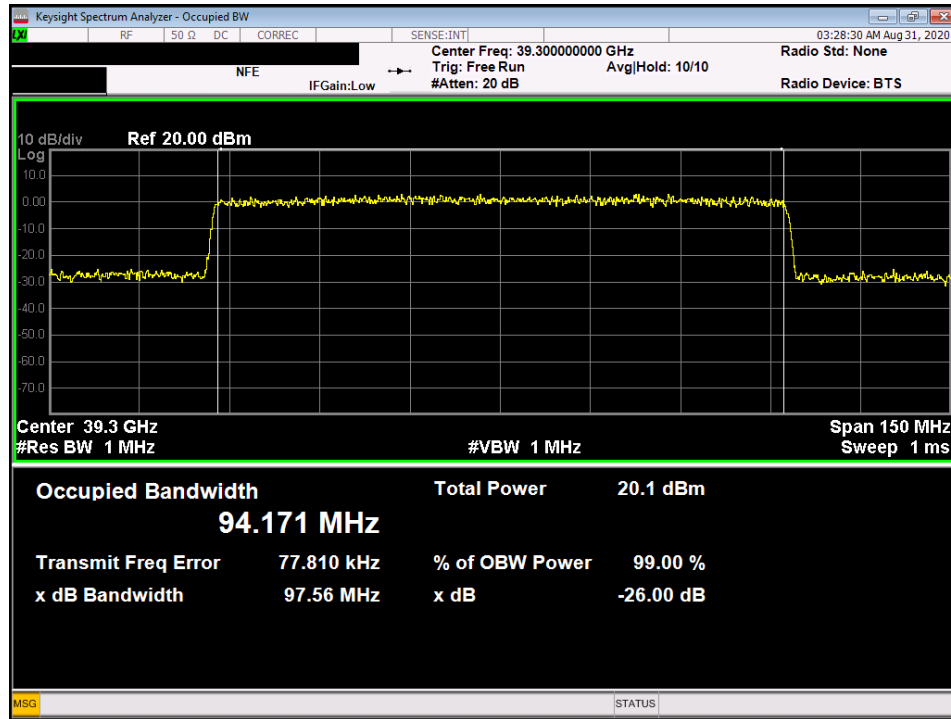


OCC BW_SISO DL In - SISO DL Out_400 MHz BW_p2070006_Output Signal_Pin + 3 dB

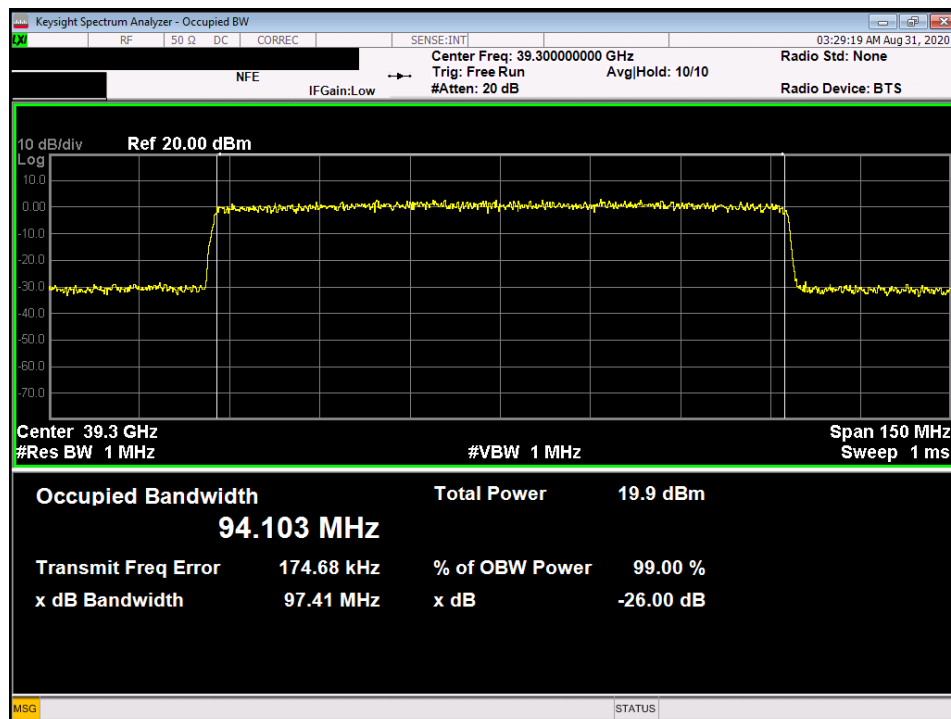




OCC BW_SISO UL In - SISO UL Out_100 MHz BW_p2070006_Output Signal

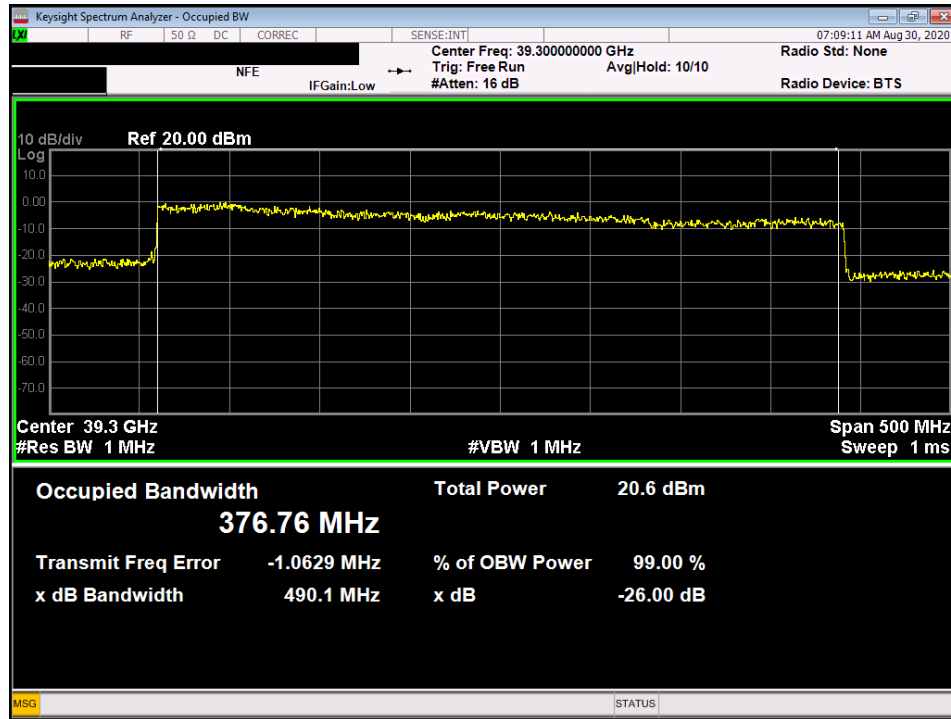


OCC BW_SISO UL In - SISO UL Out_100 MHz BW_p2070006_Output Signal_Pin + 3 dB





OCC BW_SISO UL In - SISO UL Out_400 MHz BW_p2070006_Output Signal



OCC BW_SISO UL In - SISO UL Out_400 MHz BW_p2070006_Output Signal_Pin + 3 dB

