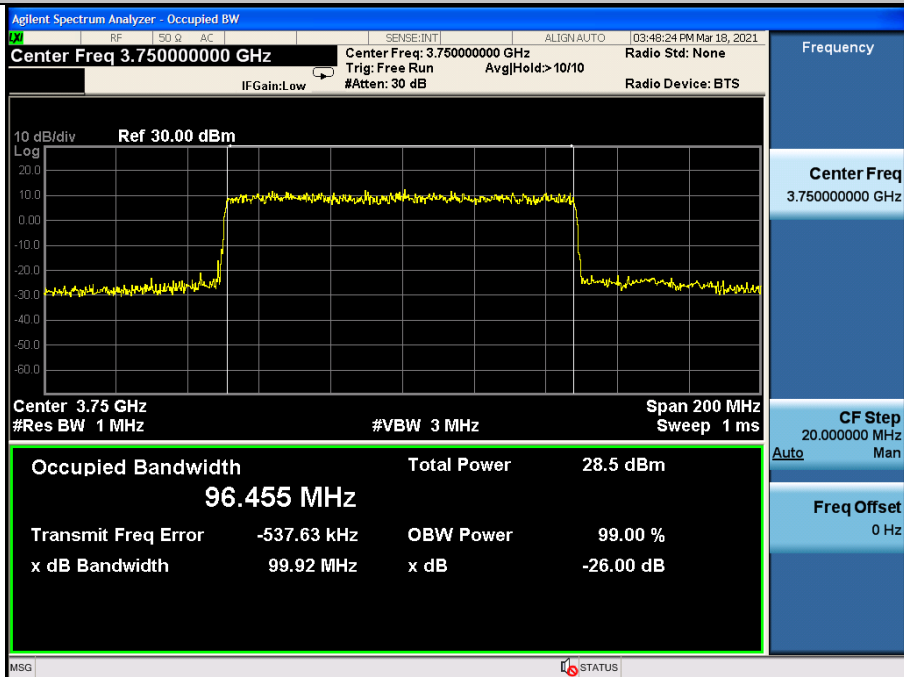
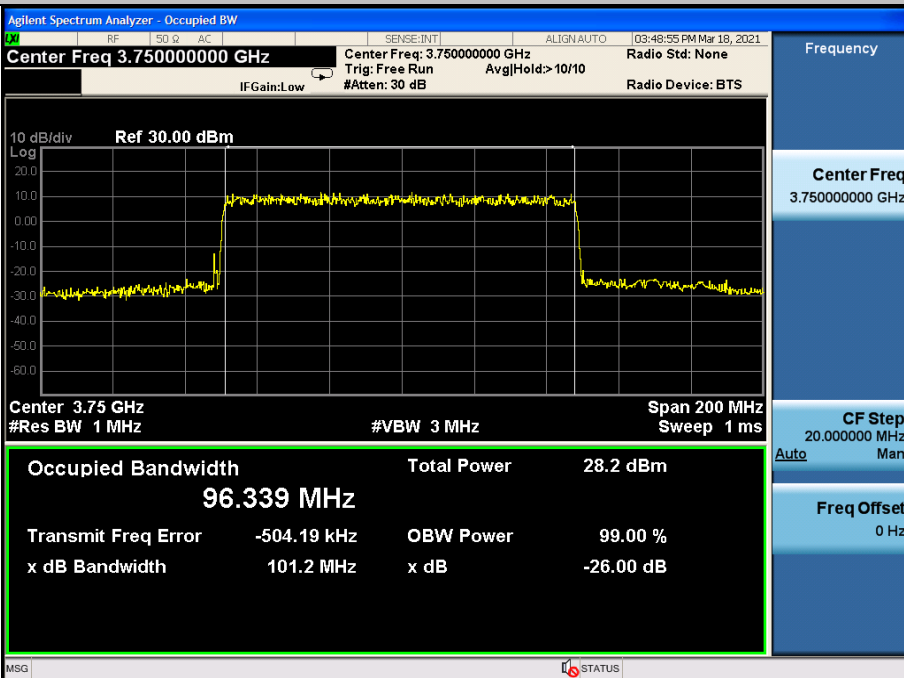


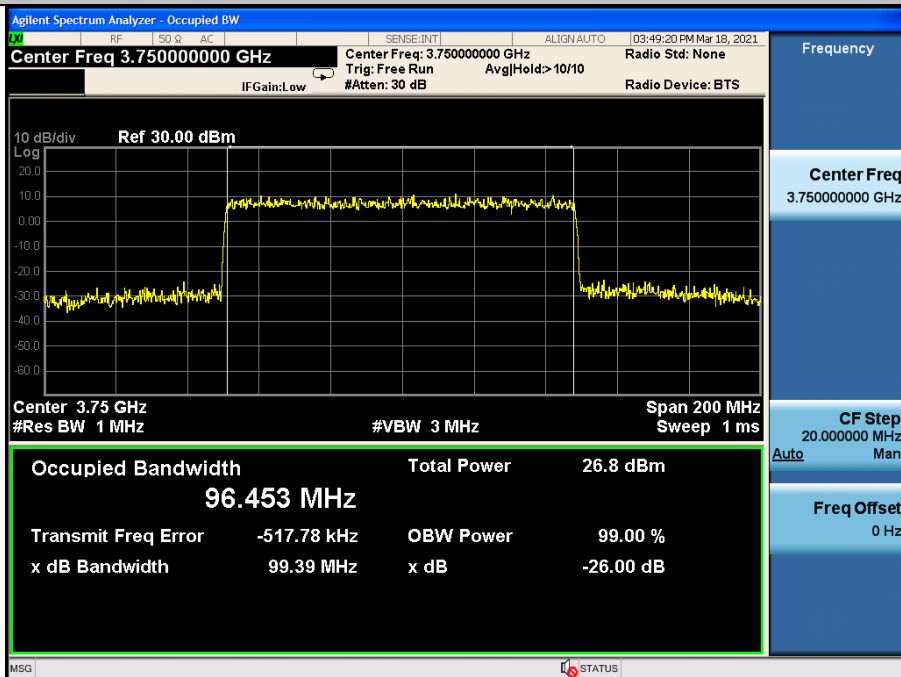
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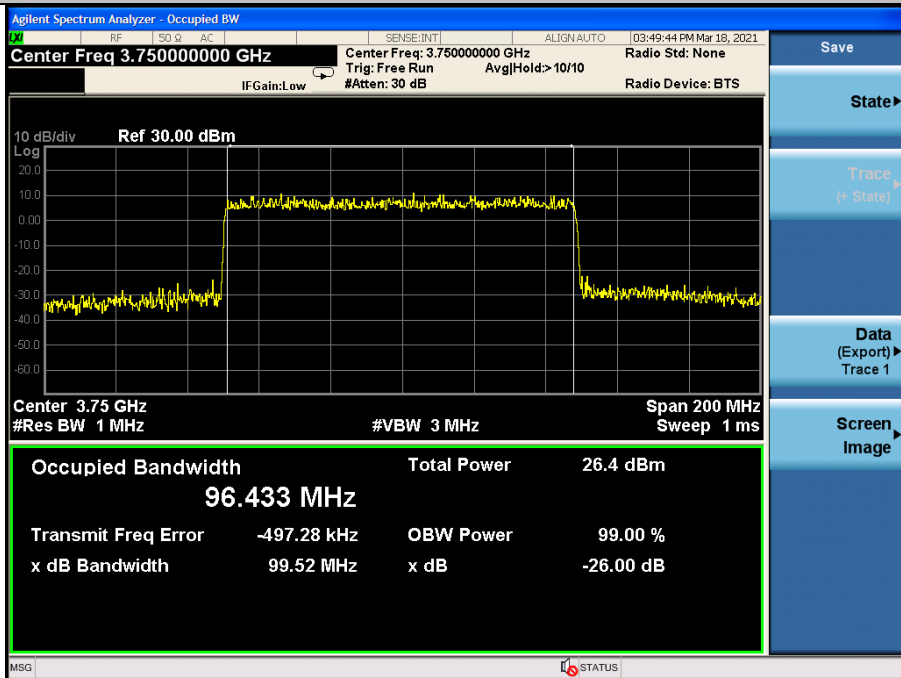
OBW&EBW N78 30KHz TM2 100MHz 650000 Outer Full



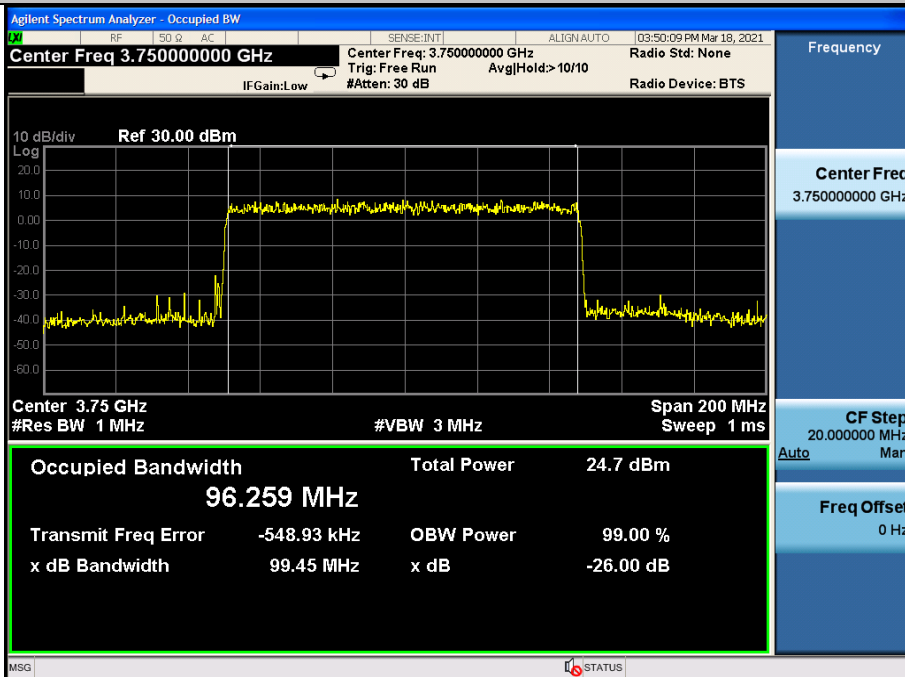
OBW&EBW N78 30KHz TM3 100MHz 650000 Outer Full



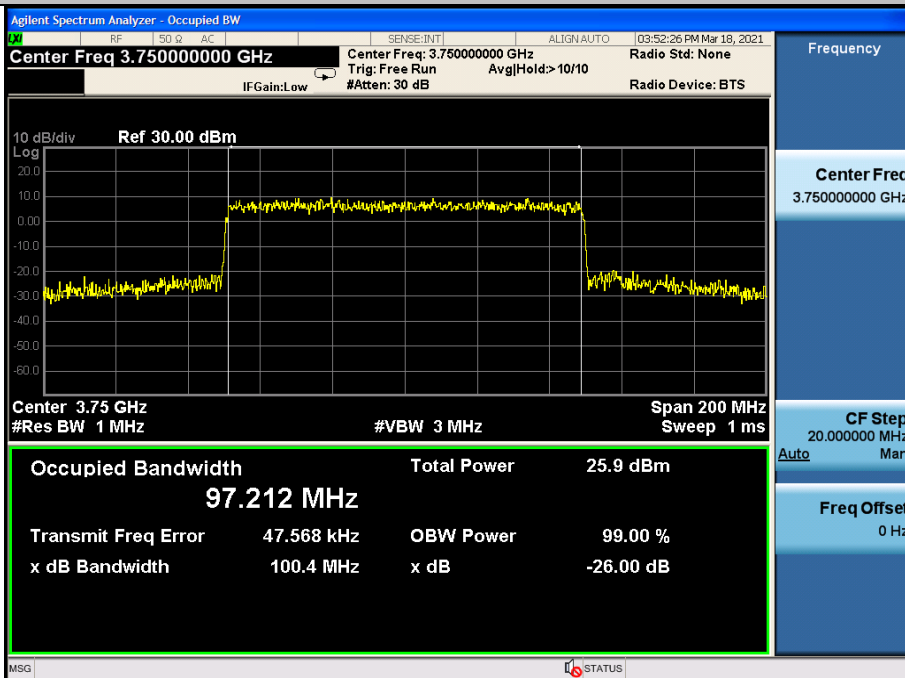
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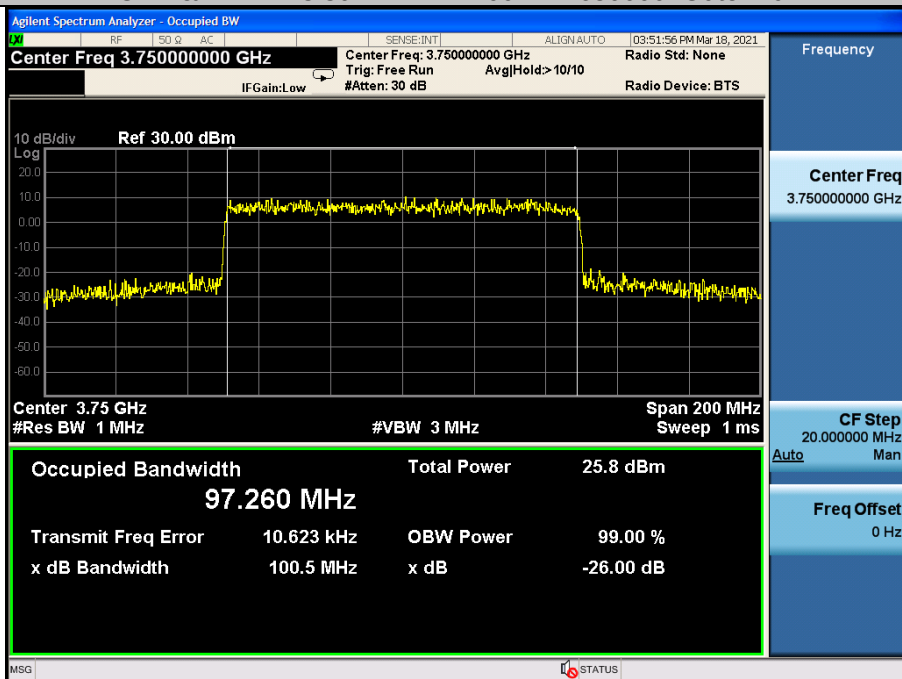
OBW&EBW N78 30KHz TM5 100MHz 650000 Outer Full



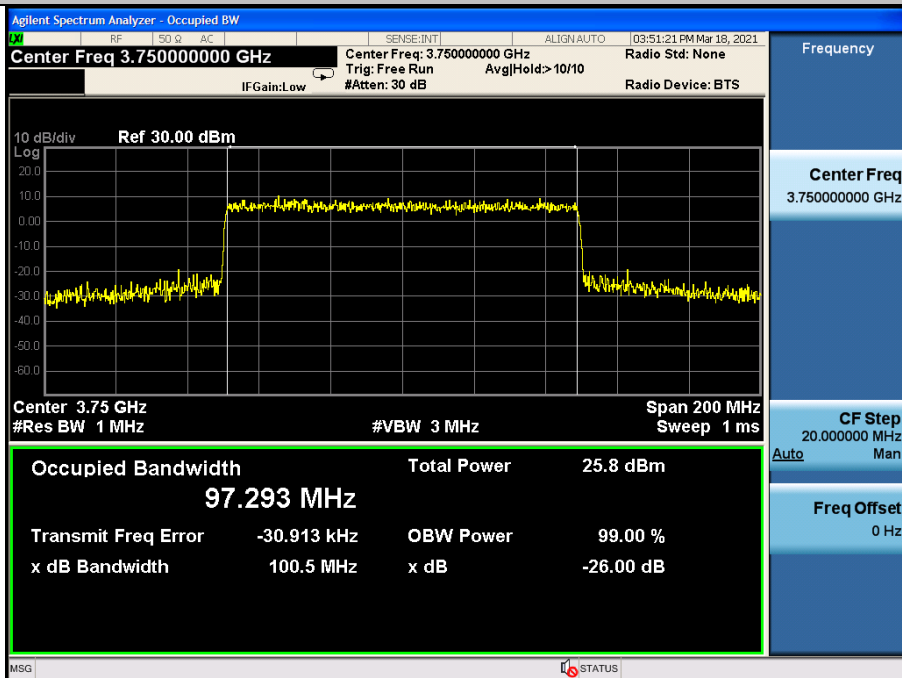
OBW&EBW N78 30KHz TM6 100MHz 650000 Outer Full

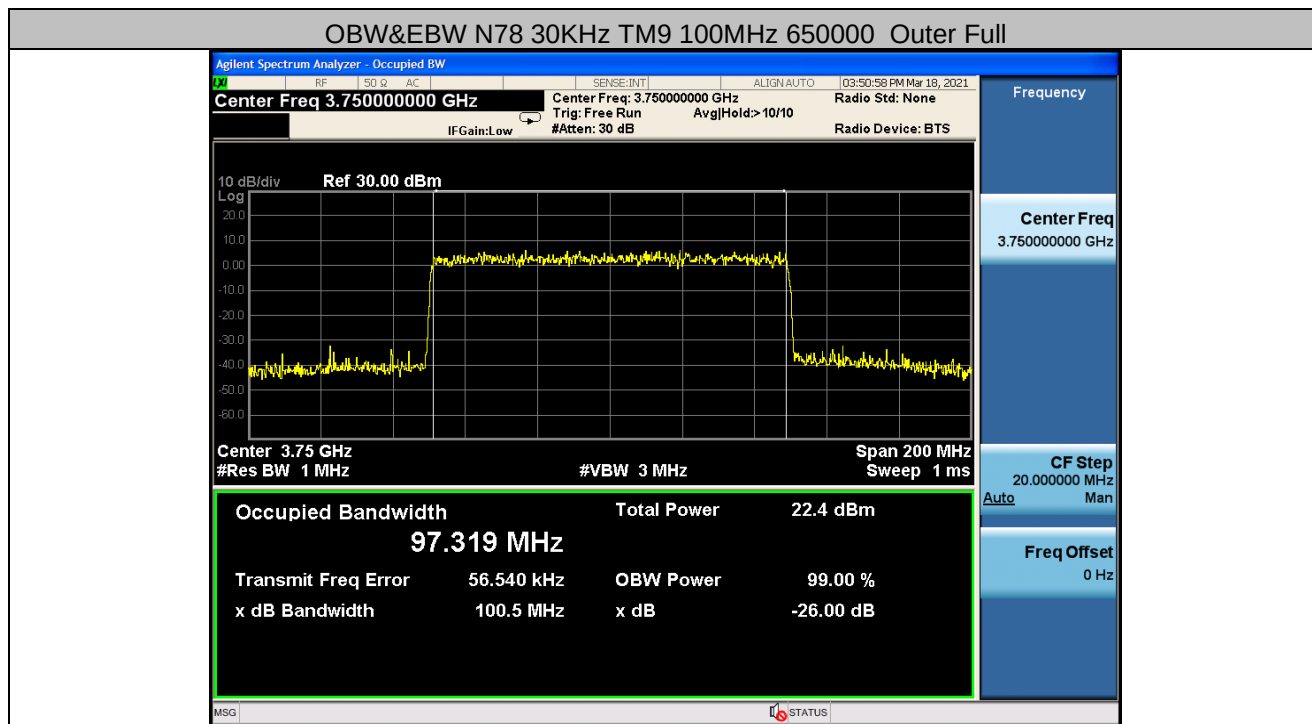


OBW&EBW N78 30KHz TM7 100MHz 650000 Outer Full



OBW&EBW N78 30KHz TM8 100MHz 650000 Outer Full





REMARK:

All antenna and all modulation had been tested, but only the worst case data displayed in this report.



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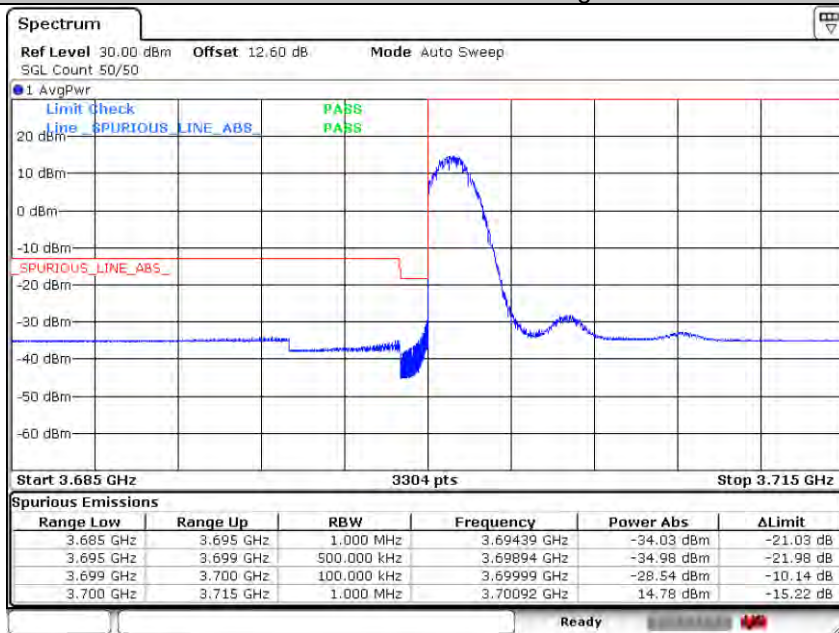
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5 Band Edges Compliance

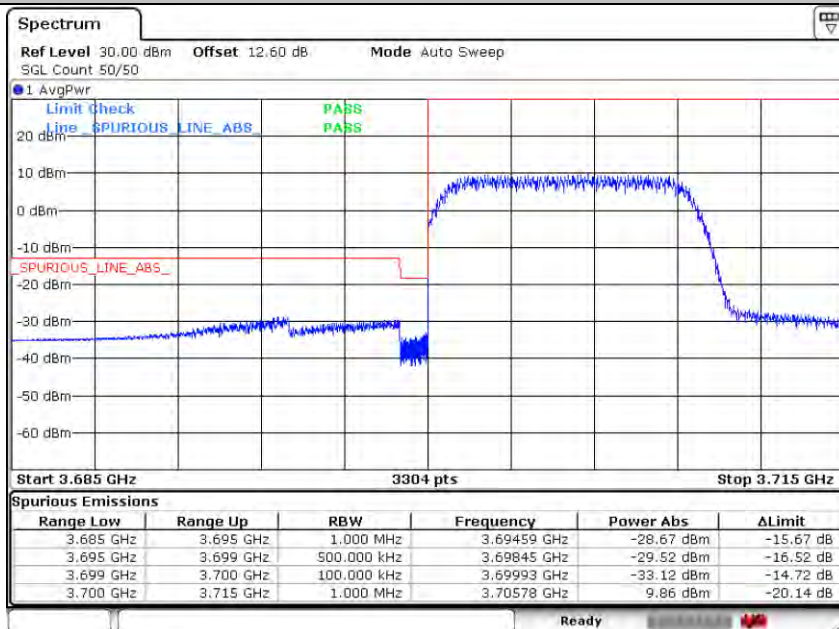
5.1 Test Plots

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Date: 9 MAR 2021 08:49:50

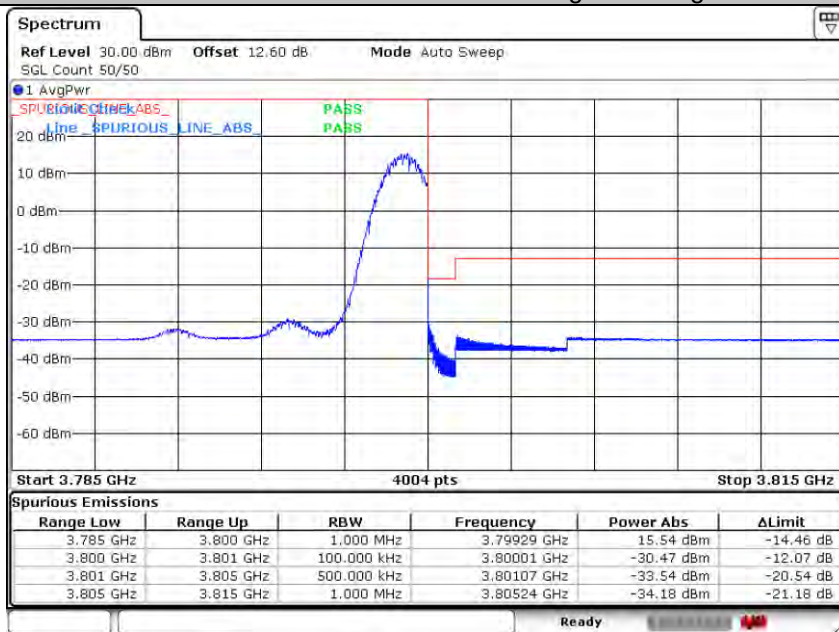
N78 30KHz TM1 10MHz 647000 Outer Full



Date: 9 MAR 2021 08:47:06

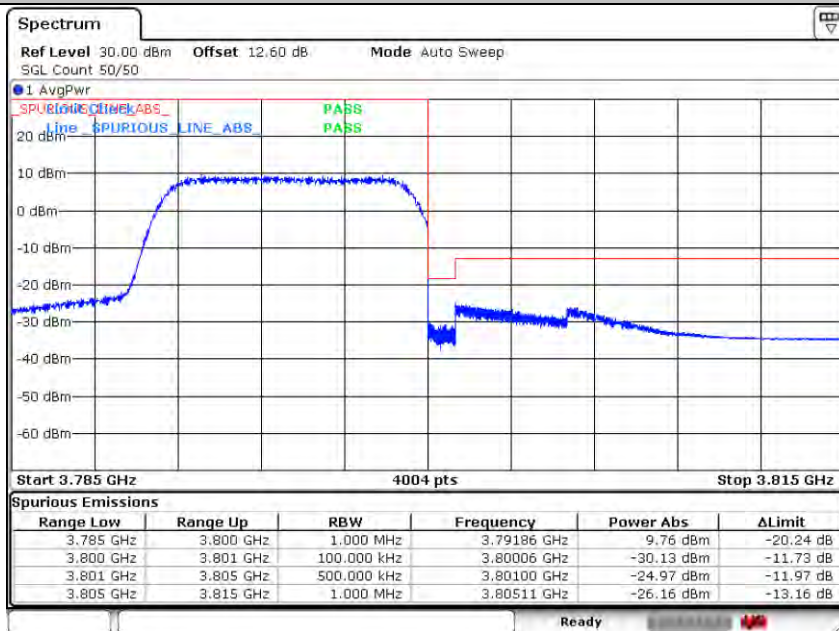


N78 30KHz TM1 10MHz 653000 Edge 1RB Right



Date: 9 MAR 2021 09:12:46

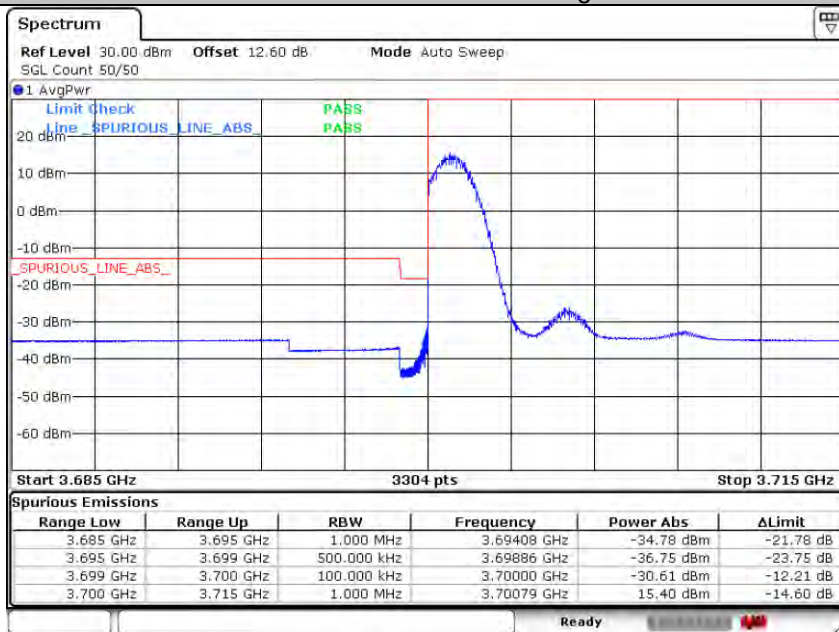
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Date: 9 MAR 2021 09:11:09

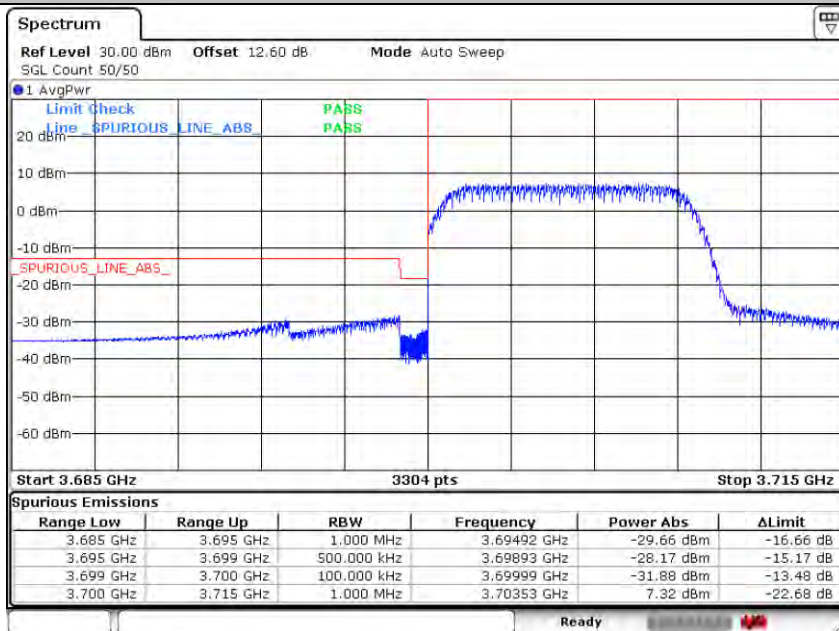


N78 30KHz TM6 10MHz 647000 Edge 1RB Left



Date: 9 MAR 2021 09:01:23

N78 30KHz TM6 10MHz 647000 Outer Full



Date: 9 MAR 2021 08:59:34



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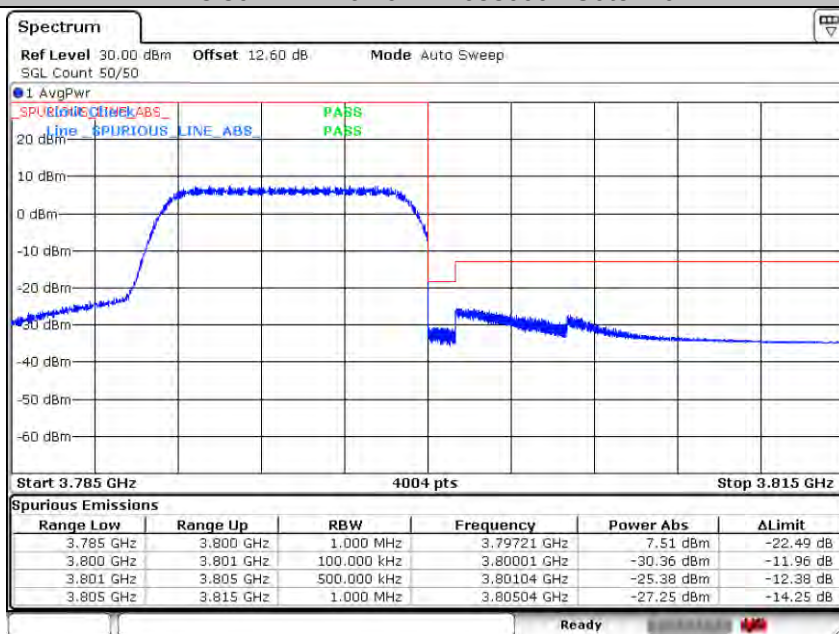
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N78 30KHz TM6 10MHz 653000 Edge 1RB Right



Date: 9 MAR 2021 09:07:09

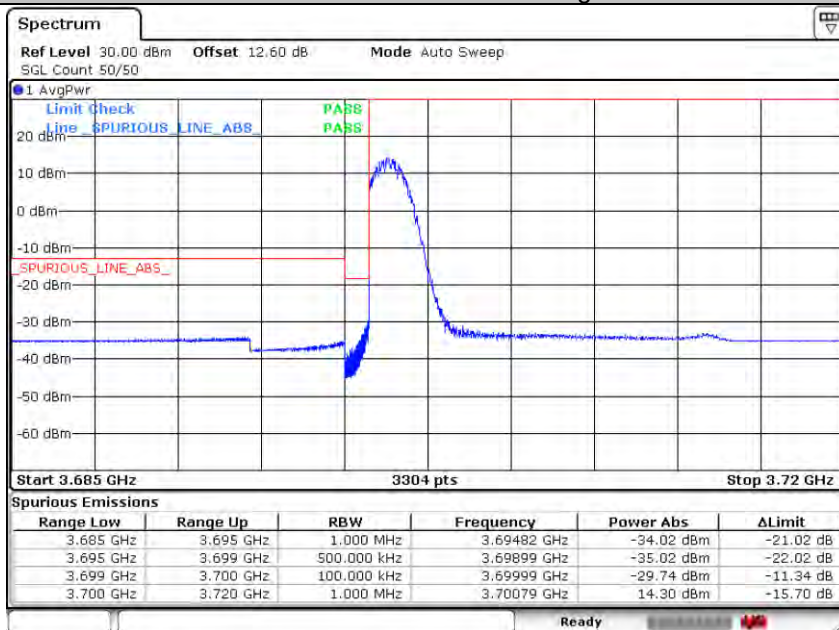
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Date: 9 MAR 2021 09:08:54

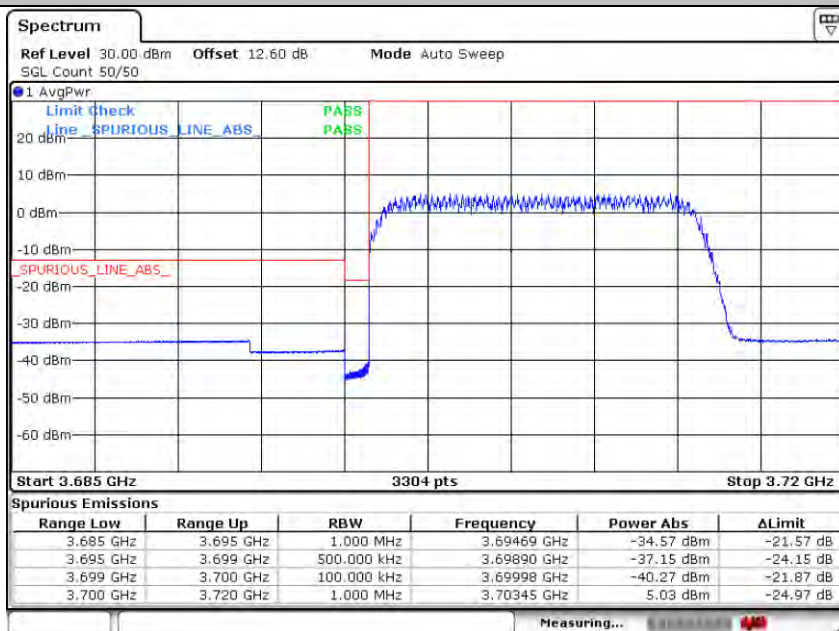


N78 30KHz TM1 15MHz 647168 Edge 1RB Left



Date: 7.MAR.2021 20:04:09

N78 30KHz TM1 15MHz 647168 Outer Full



Date: 7.MAR.2021 20:04:47



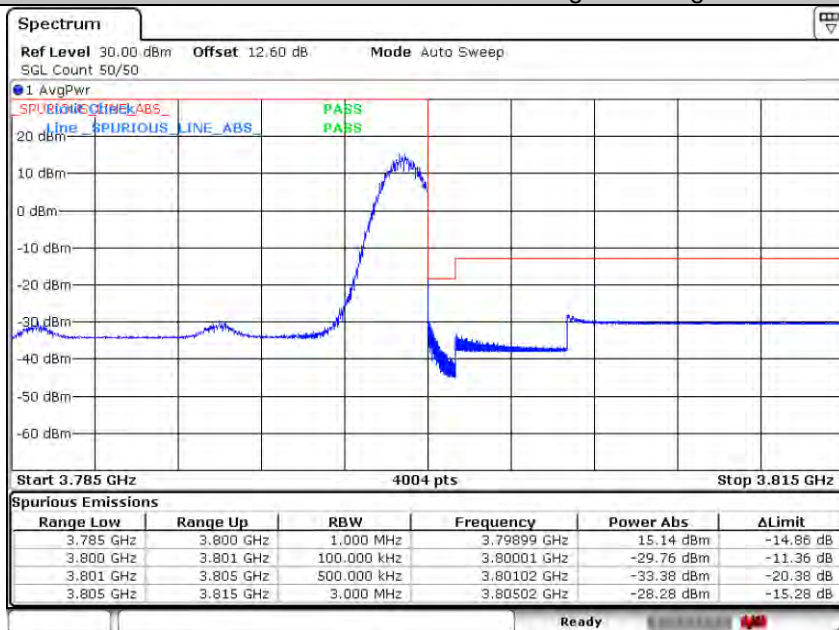
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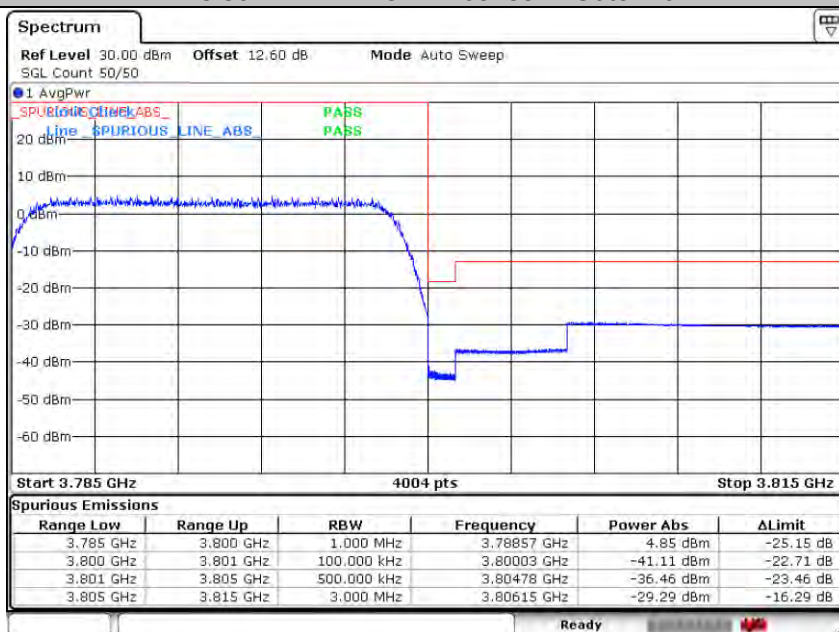
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N78 30KHz TM1 15MHz 652832 Edge 1RB Right



Date: 7.MAR.2021 16:41:55

N78 30KHz TM1 15MHz 652832 Outer Full



Date: 7.MAR.2021 16:40:25



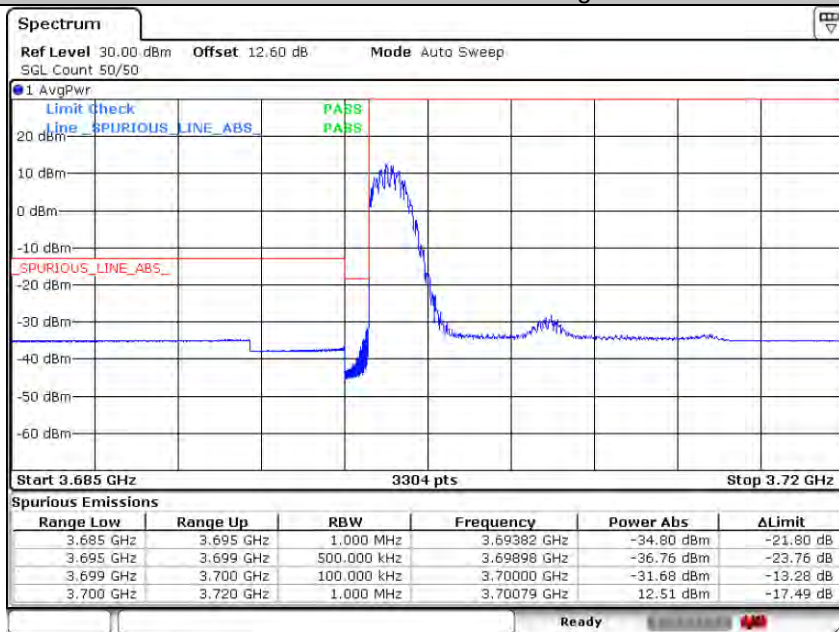
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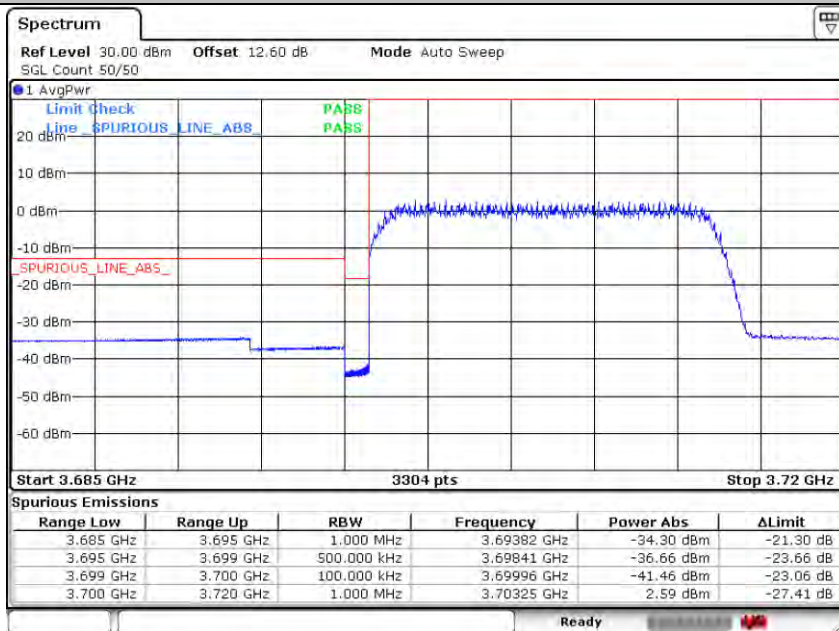
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N78 30KHz TM6 15MHz 647168 Edge 1RB Left



Date: 7.MAR.2021 20:02:14

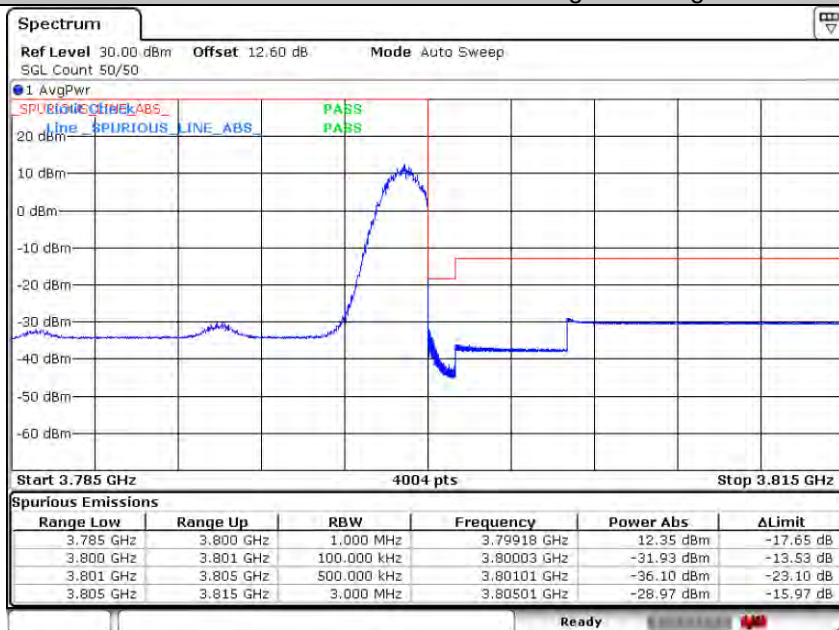
N78 30KHz TM6 15MHz 647168 Outer Full



Date: 7.MAR.2021 20:01:33

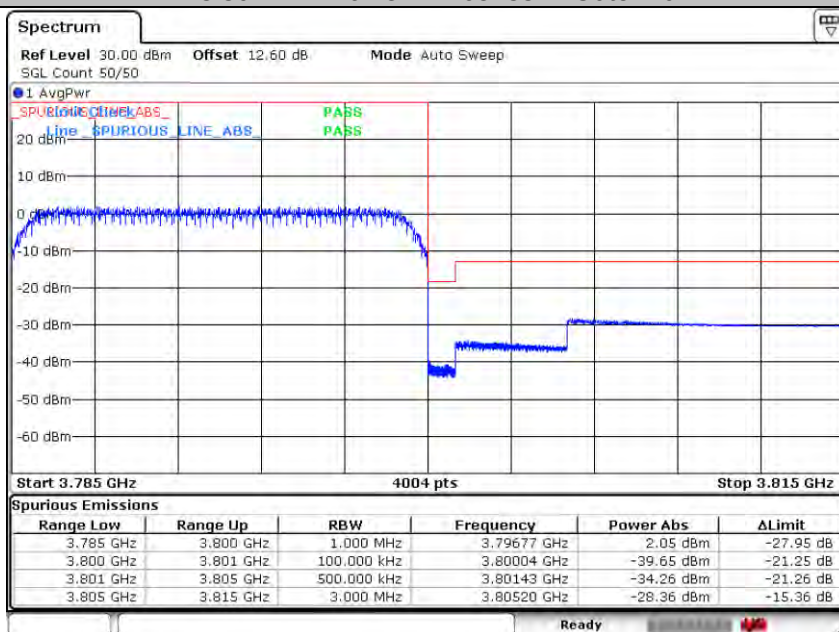


N78 30KHz TM6 15MHz 652832 Edge 1RB Right



Date: 7.MAR.2021 16:43:34

N78 30KHz TM6 15MHz 652832 Outer Full



Date: 7.MAR.2021 16:44:15



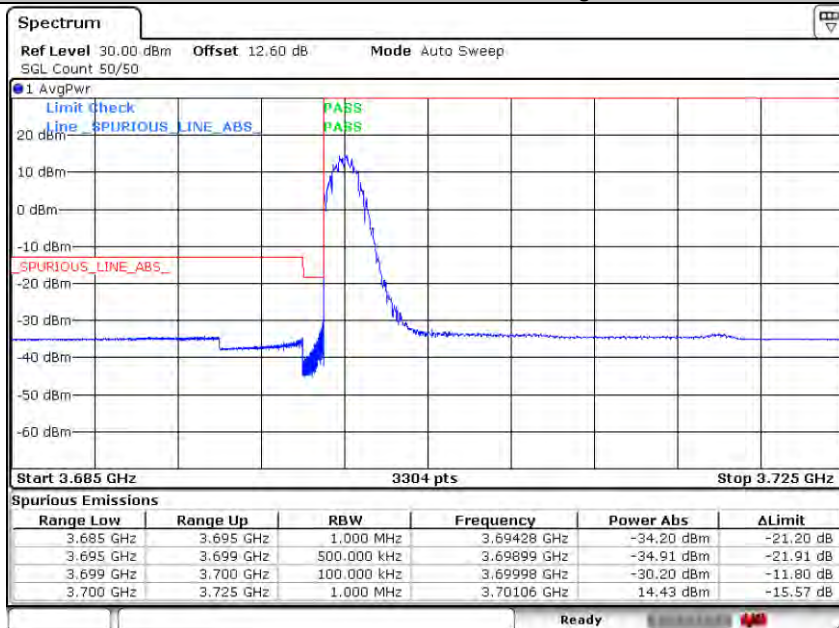
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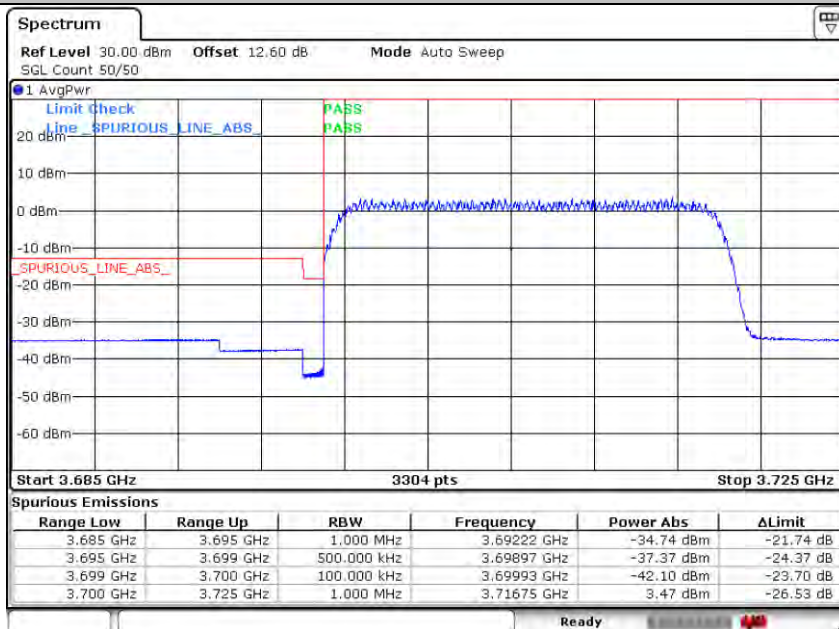
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N78 30KHz TM1 20MHz 647334 Edge 1RB Left



Date: 7.MAR.2021 19:57:00

N78 30KHz TM1 20MHz 647334 Outer Full



Date: 7.MAR.2021 19:56:17



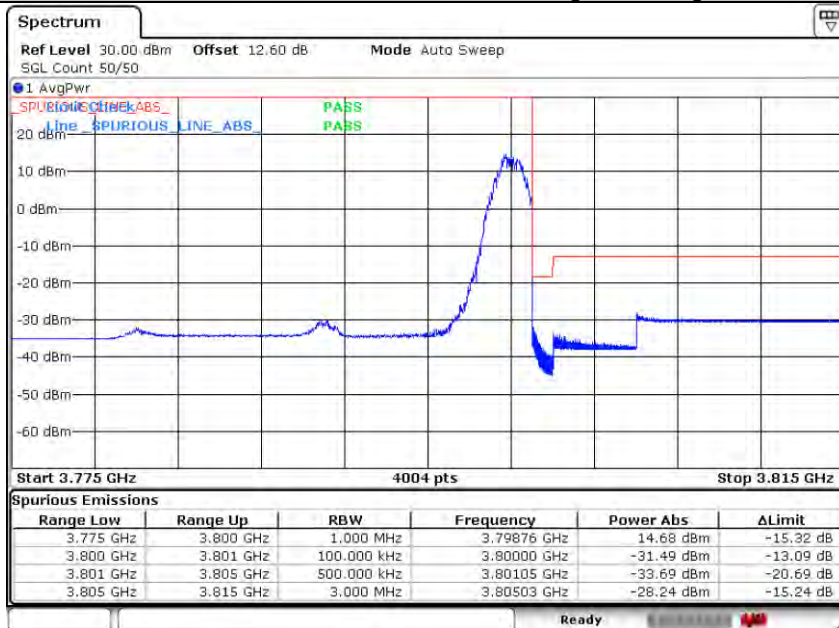
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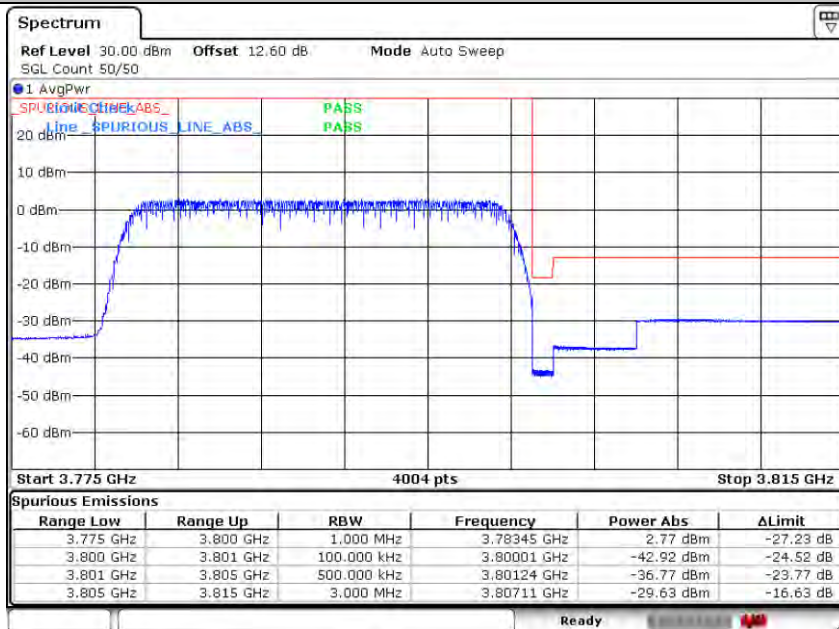
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N78 30KHz TM1 20MHz 652666 Edge 1RB Right



Date: 7.MAR.2021 16:49:08

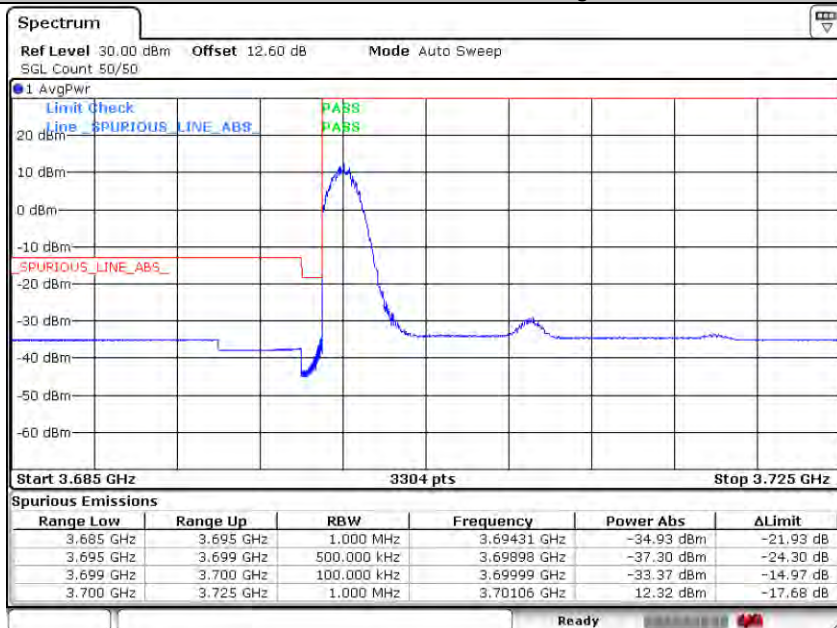
N78 30KHz TM1 20MHz 652666 Outer Full



Date: 7.MAR.2021 16:49:48

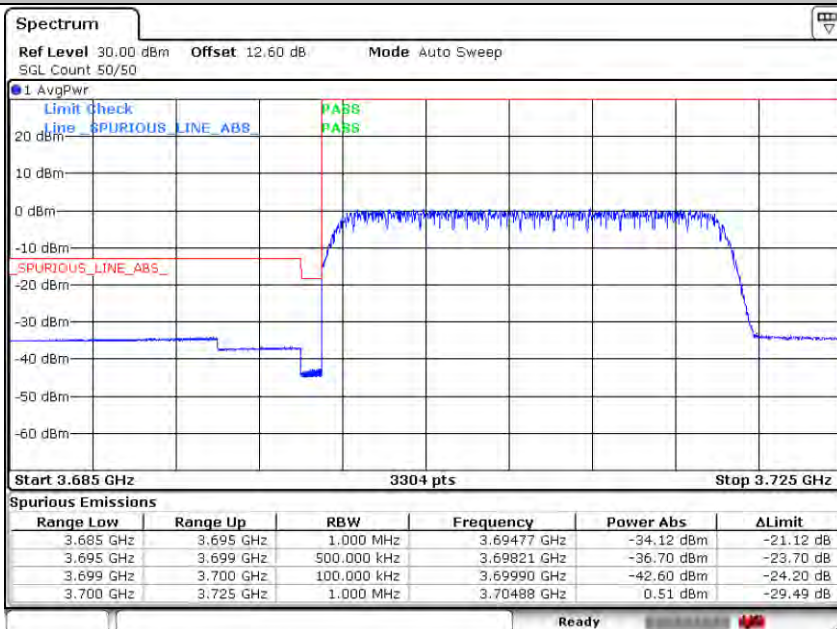


N78 30KHz TM6 20MHz 647334 Edge 1RB Left



Date: 7 MAR 2021 19:58:03

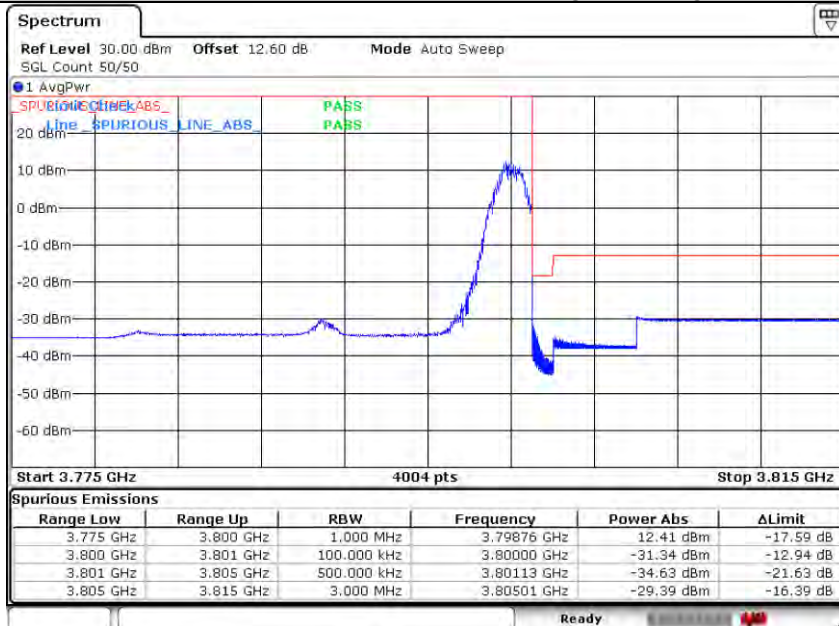
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Date: 7 MAR 2021 19:59:20

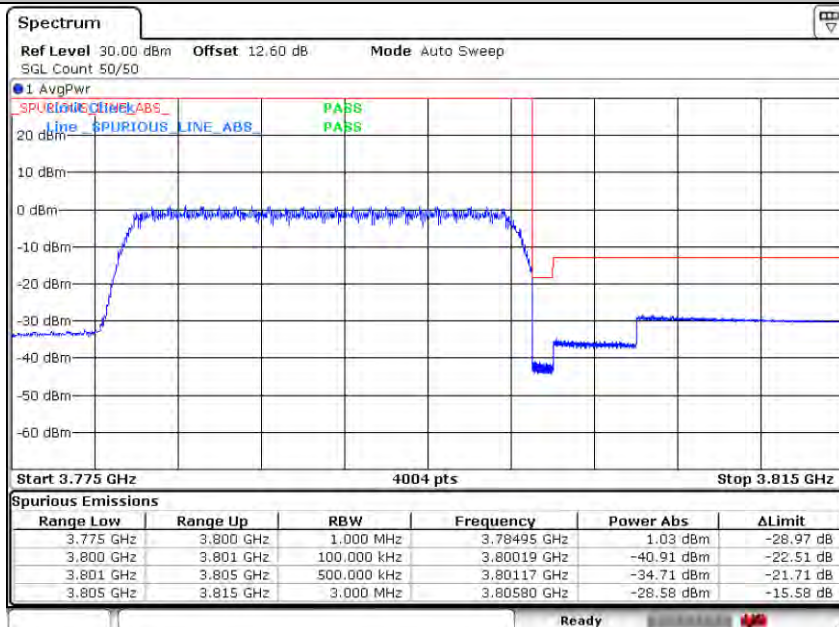


N78 30KHz TM6 20MHz 652666 Edge 1RB Right



Date: 7.MAR.2021 16:48:00

N78 30KHz TM6 20MHz 652666 Outer Full



Date: 7.MAR.2021 16:47:10



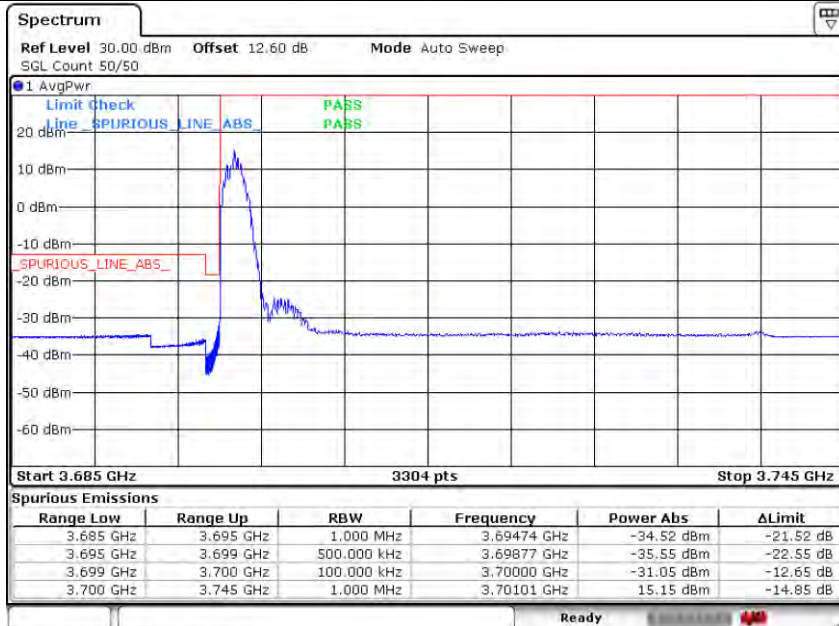
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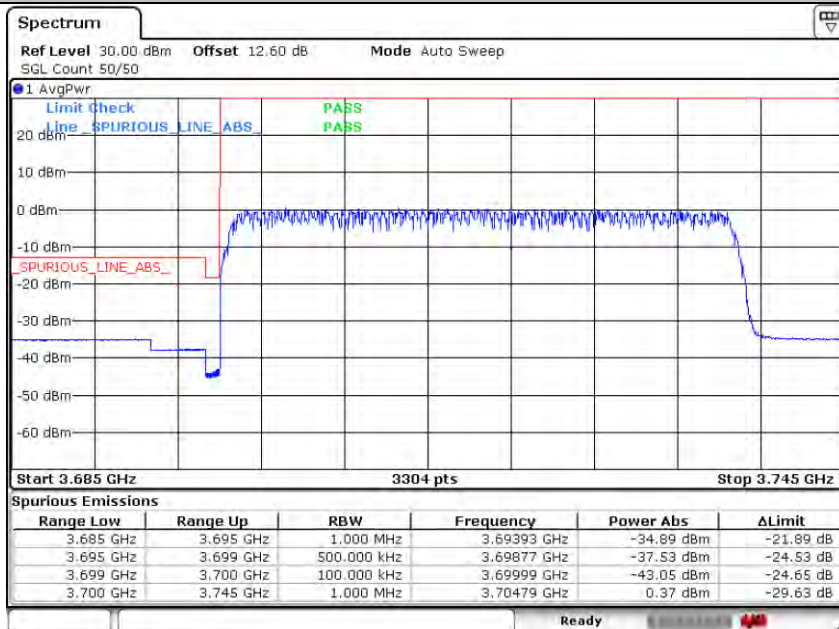
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N78 30KHz TM1 40MHz 648000 Edge 1RB Left



Date: 7.MAR.2021 19:53:46

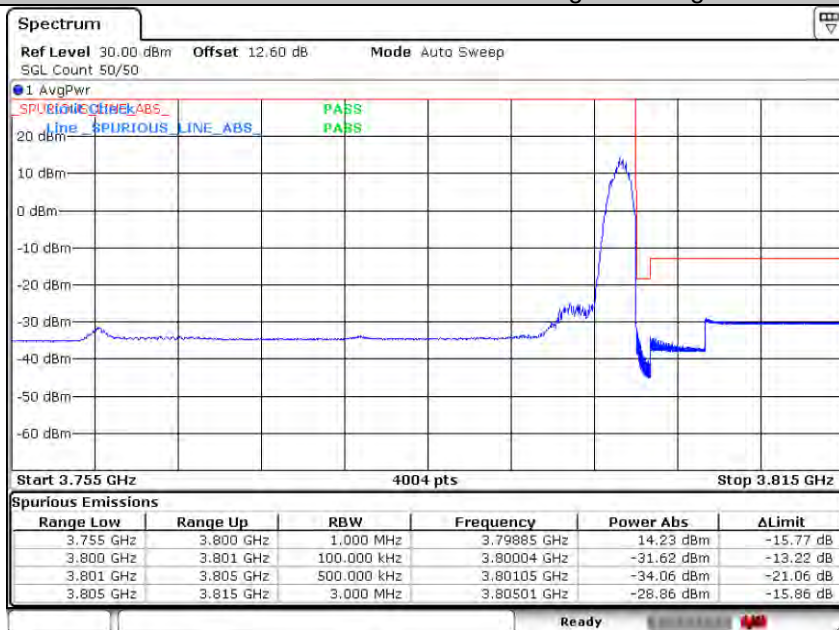
N78 30KHz TM1 40MHz 648000 Outer Full



Date: 7.MAR.2021 19:54:24

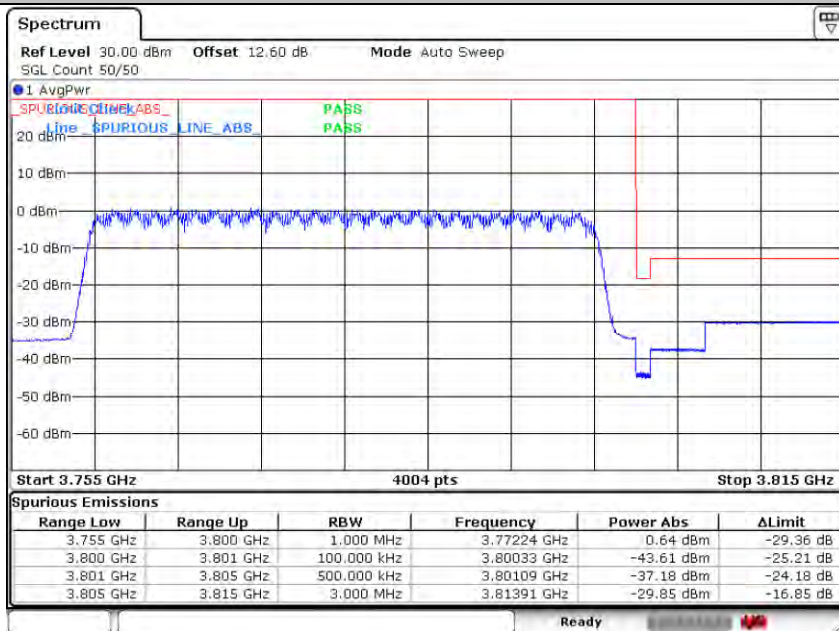


N78 30KHz TM1 40MHz 652000 Edge 1RB Right



Date: 7.MAR.2021 16:52:49

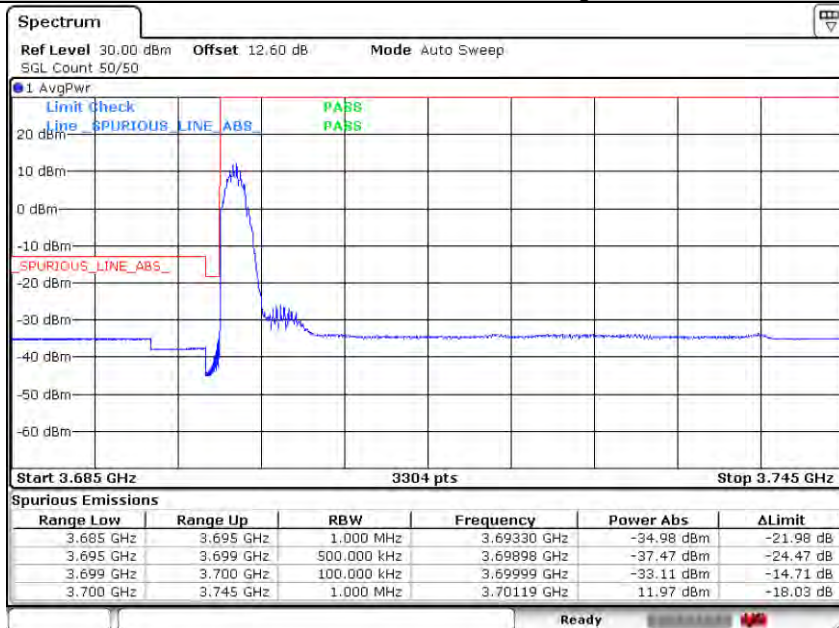
N78 30KHz TM1 40MHz 652000 Outer Full



Date: 7.MAR.2021 16:52:10

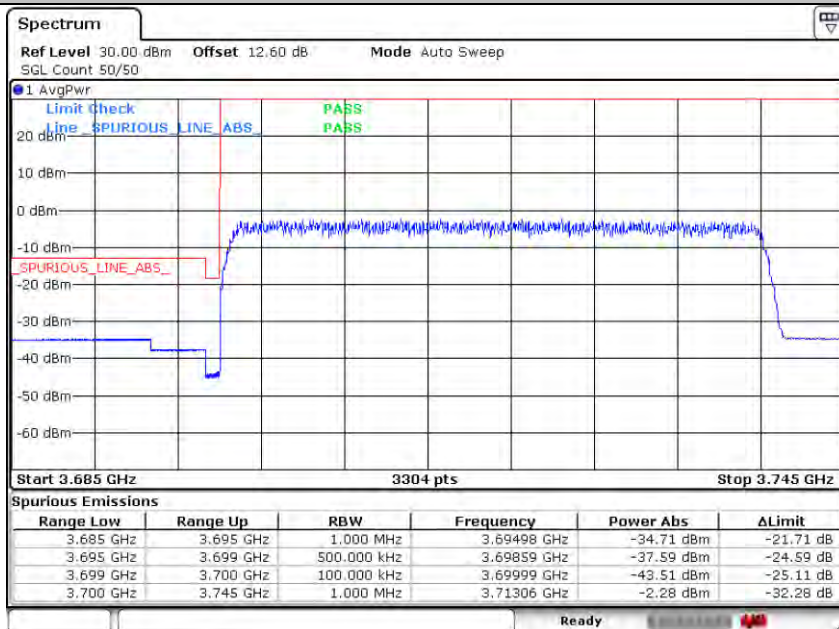


N78 30KHz TM6 40MHz 648000 Edge 1RB Left



Date: 7.MAR.2021 19:52:33

N78 30KHz TM6 40MHz 648000 Outer Full



Date: 7.MAR.2021 19:42:34



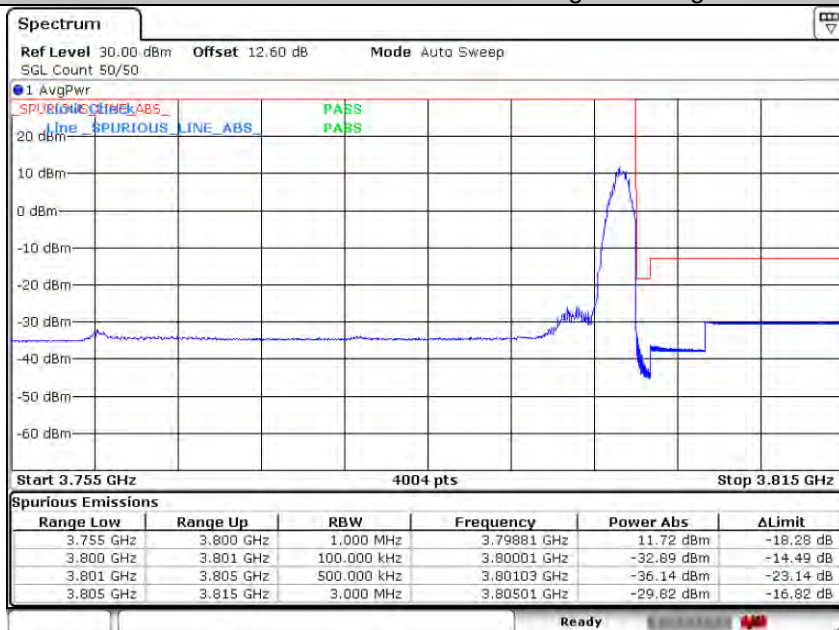
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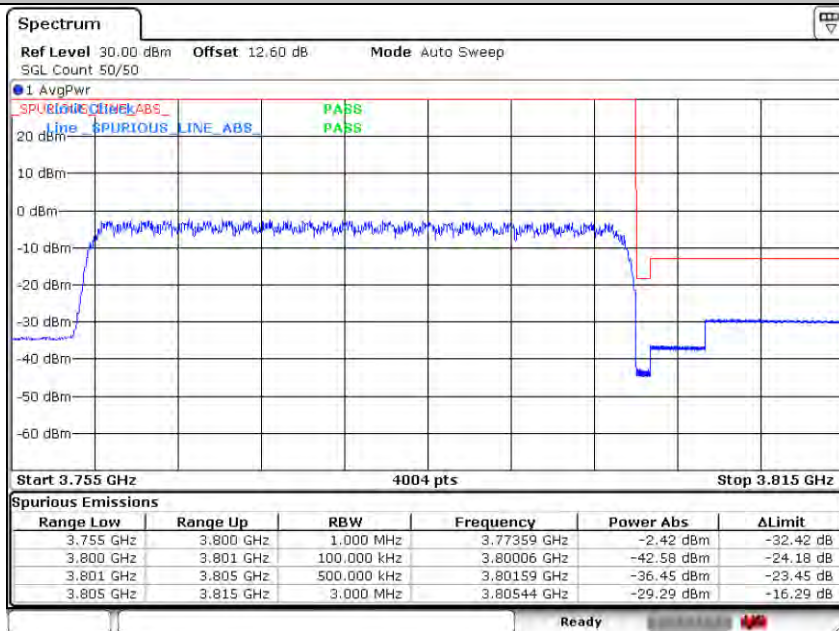
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N78 30KHz TM6 40MHz 652000 Edge 1RB Right



Date: 7.MAR.2021 16:54:42

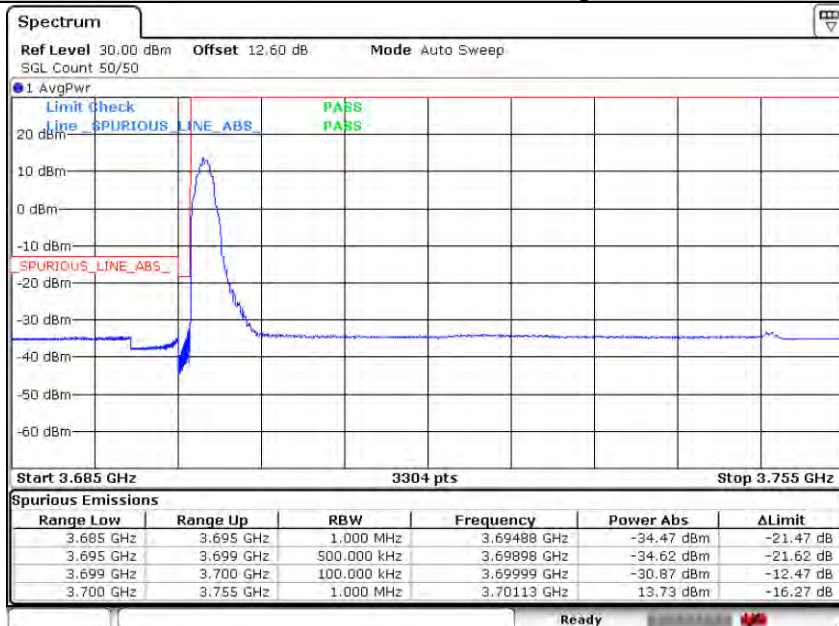
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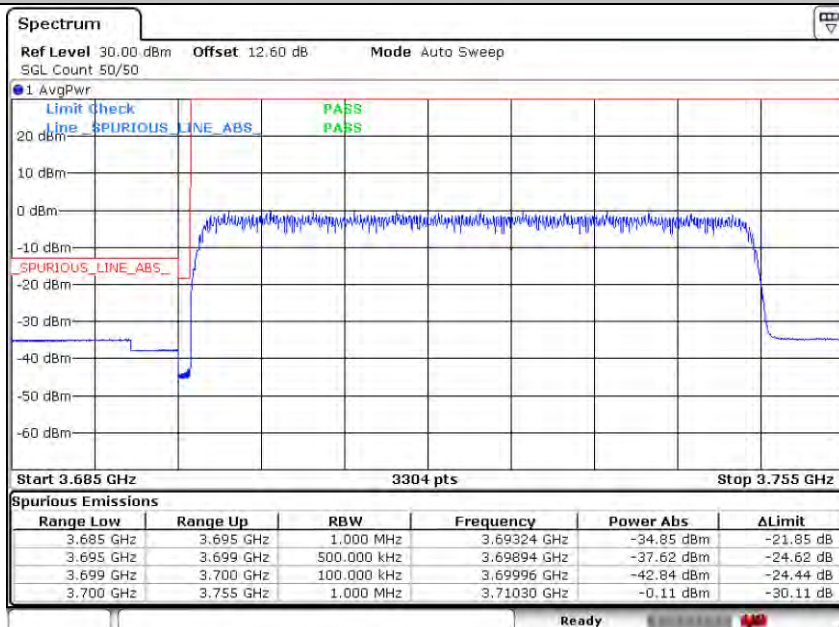


N78 30KHz TM1 50MHz 648334 Edge 1RB Left



Date: 7.MAR.2021 19:37:54

N78 30KHz TM1 50MHz 648334 Outer Full



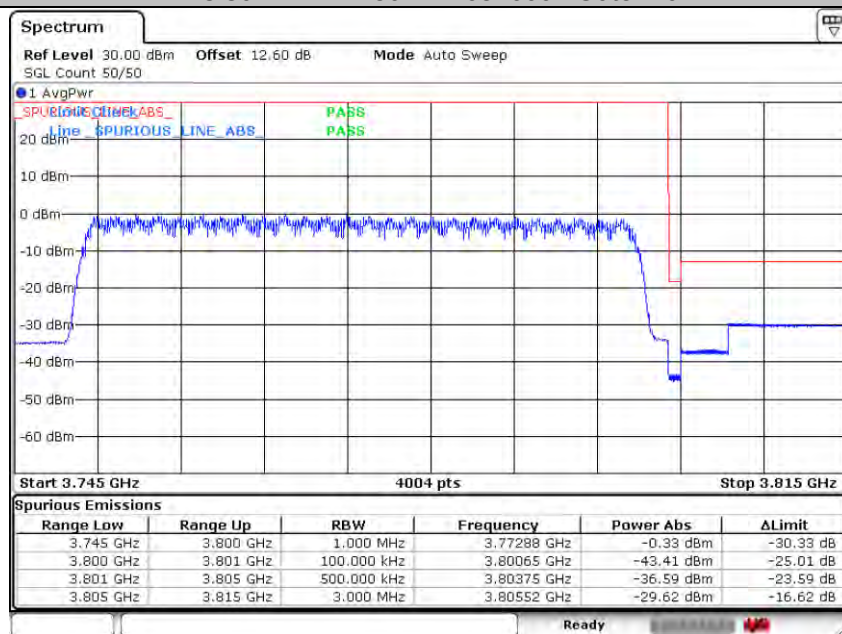
Date: 7.MAR.2021 19:37:21

N78 30KHz TM1 50MHz 651666 Edge 1RB Right



Date: 7.MAR.2021 17:01:49

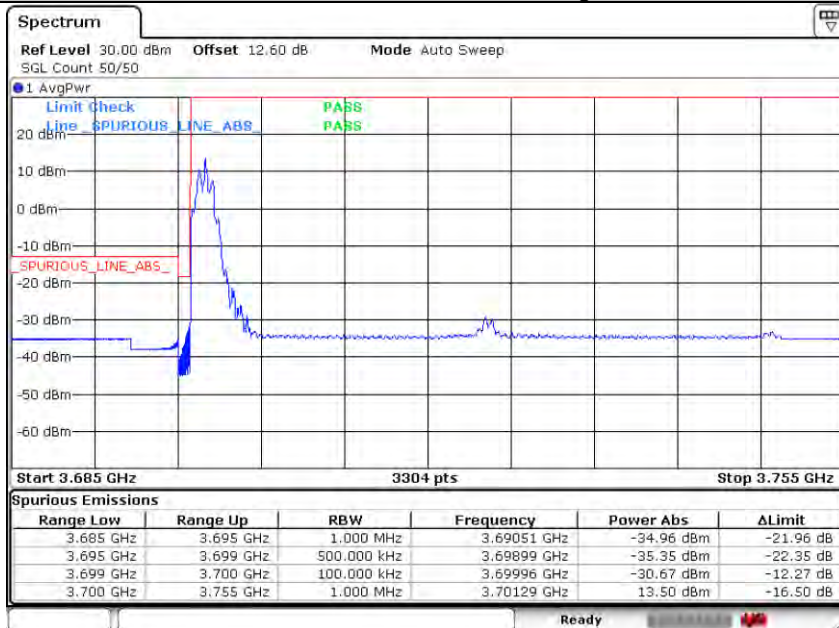
N78 30KHz TM1 50MHz 651666 Outer Full



Date: 7.MAR.2021 17:02:51

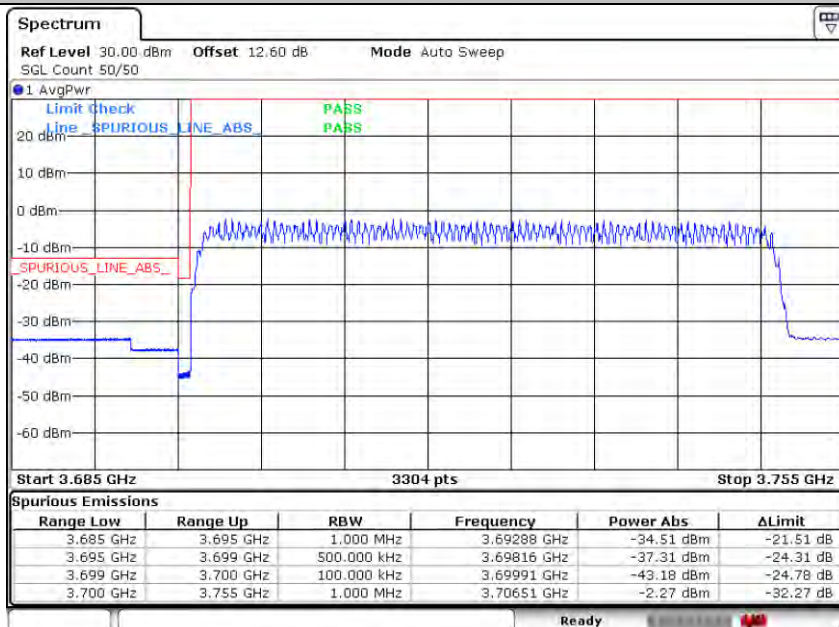


N78 30KHz TM6 50MHz 648334 Edge 1RB Left



Date: 7.MAR.2021 19:39:02

N78 30KHz TM6 50MHz 648334 Outer Full



Date: 7.MAR.2021 19:39:46



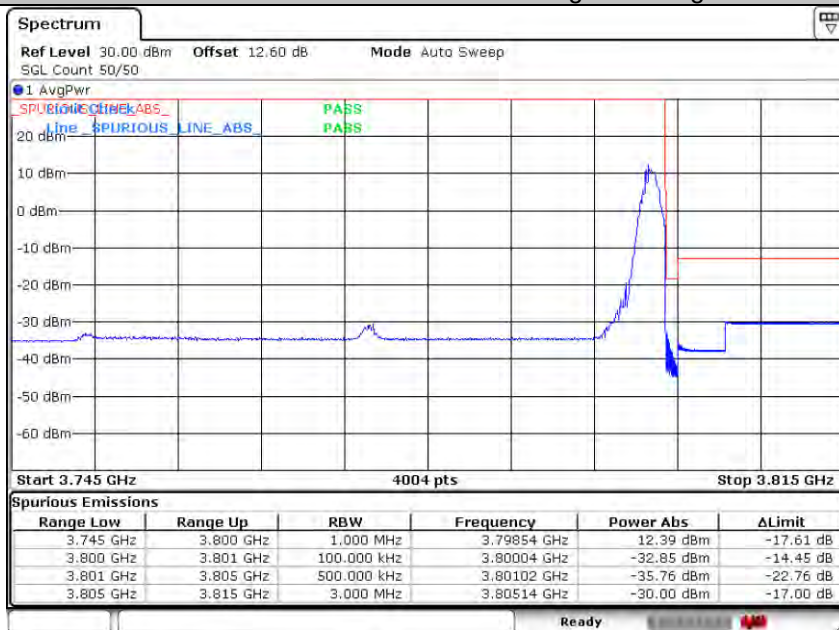
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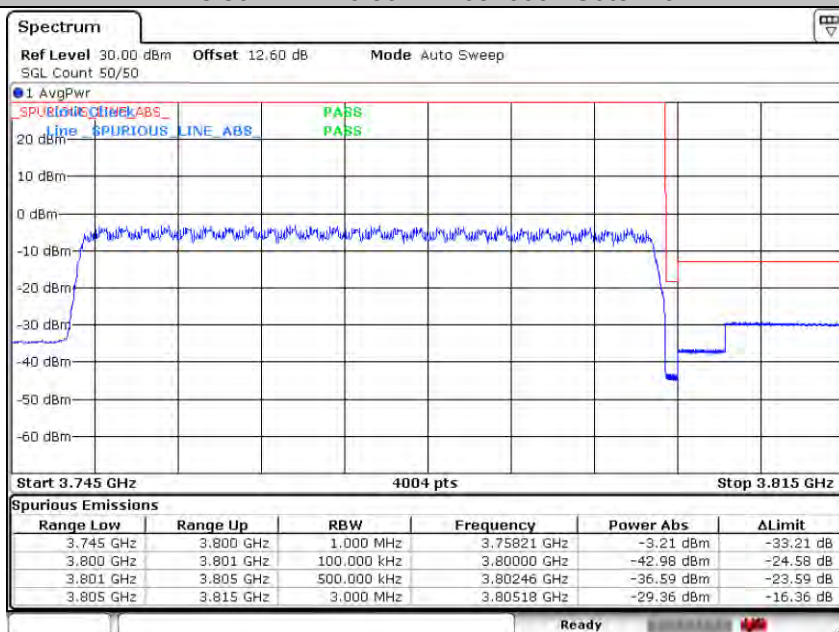
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N78 30KHz TM6 50MHz 651666 Edge 1RB Right



Date: 7.MAR.2021 16:59:32

N78 30KHz TM6 50MHz 651666 Outer Full



Date: 7.MAR.2021 16:58:37

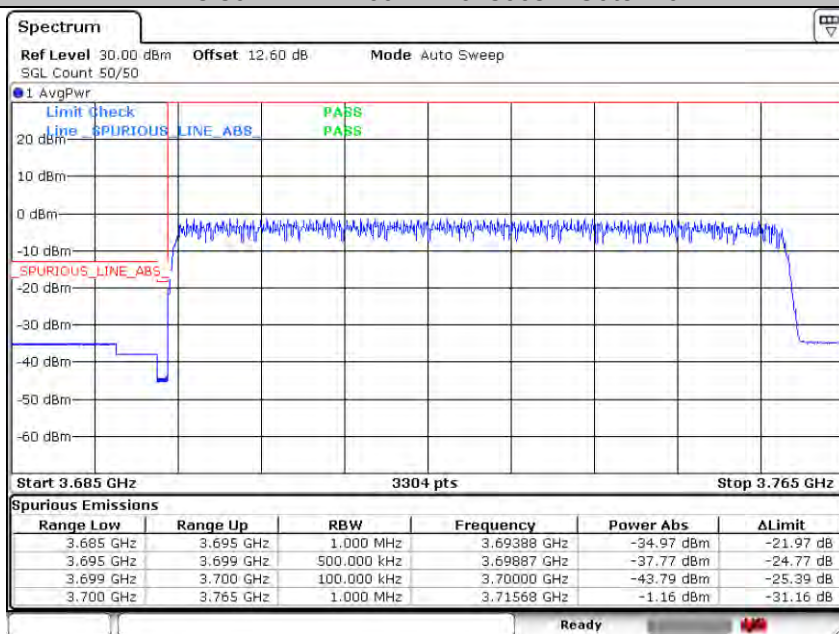


N78 30KHz TM1 60MHz 648668 Edge 1RB Left



Date: 7.MAR.2021 19:34:19

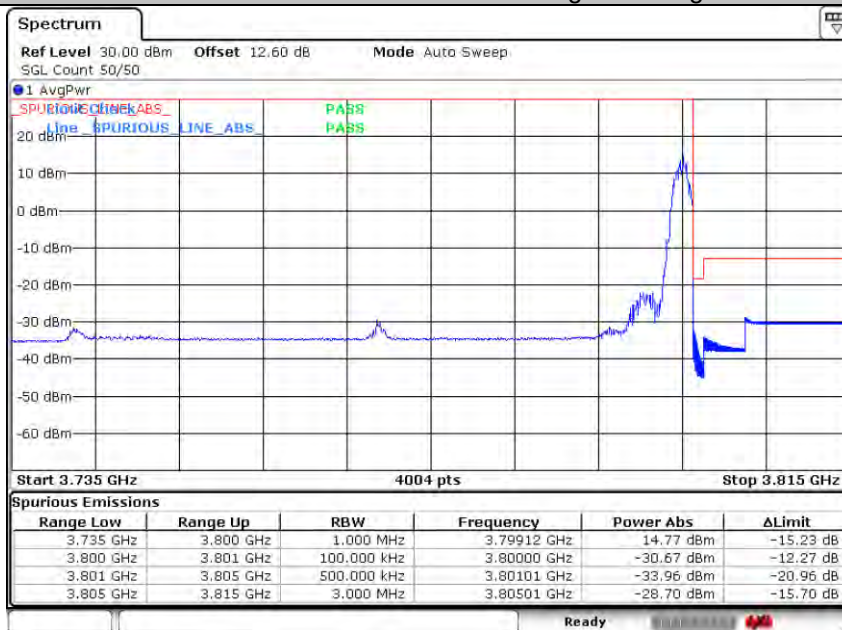
N78 30KHz TM1 60MHz 648668 Outer Full



Date: 7.MAR.2021 19:35:17

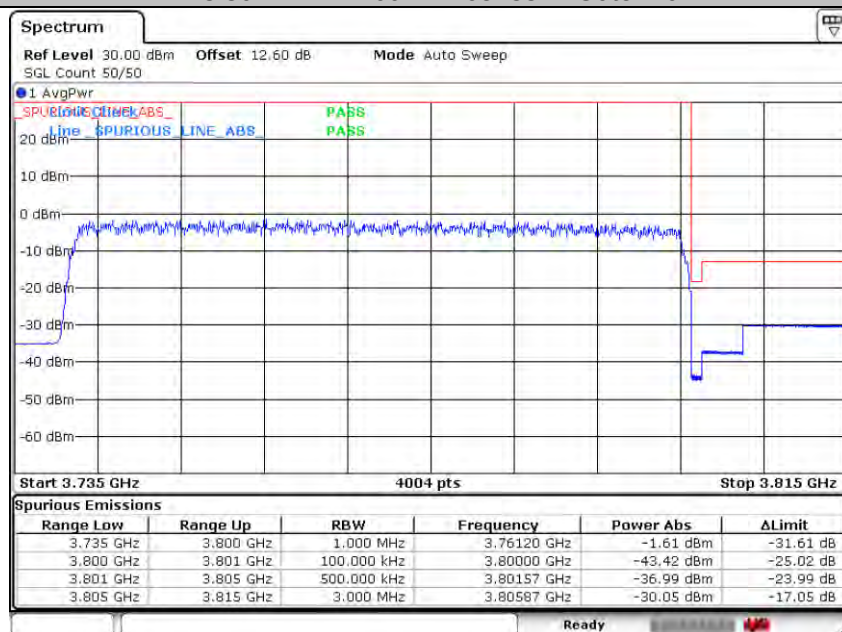


N78 30KHz TM1 60MHz 651332 Edge 1RB Right



Date: 7.MAR.2021 17:05:09

N78 30KHz TM1 60MHz 651332 Outer Full



Date: 7.MAR.2021 17:04:19

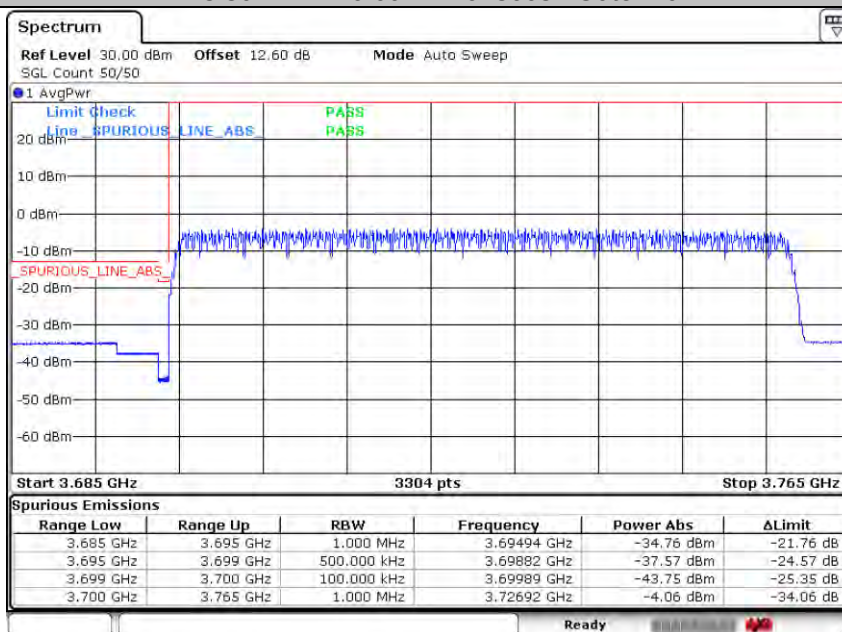


N78 30KHz TM6 60MHz 648668 Edge 1RB Left



Date: 7.MAR.2021 19:33:00

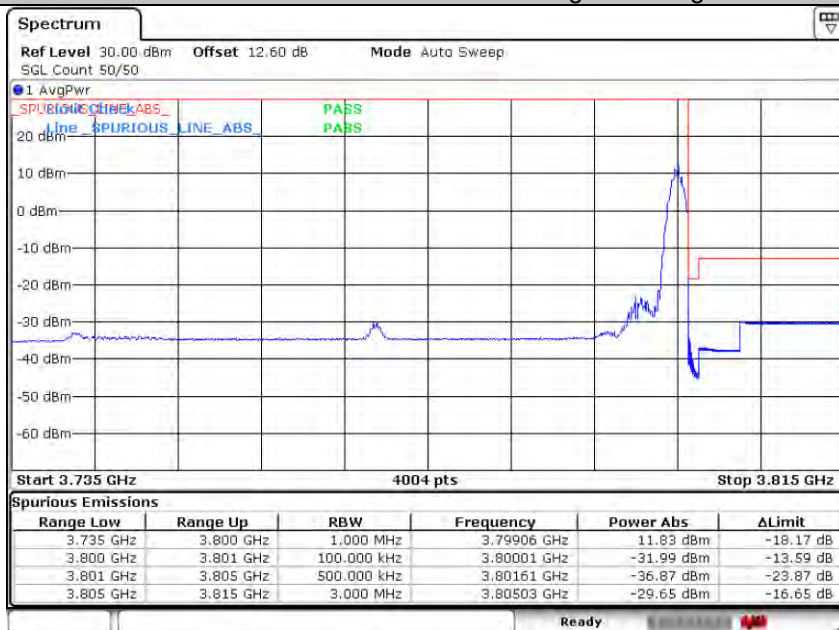
N78 30KHz TM6 60MHz 648668 Outer Full



Date: 7.MAR.2021 19:32:25

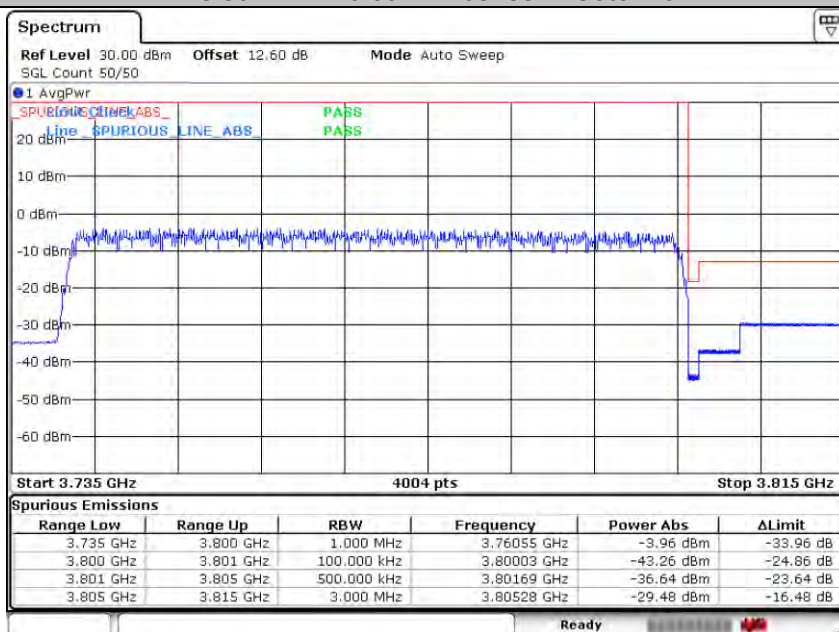


N78 30KHz TM6 60MHz 651332 Edge 1RB Right



Date: 7.MAR.2021 17:06:24

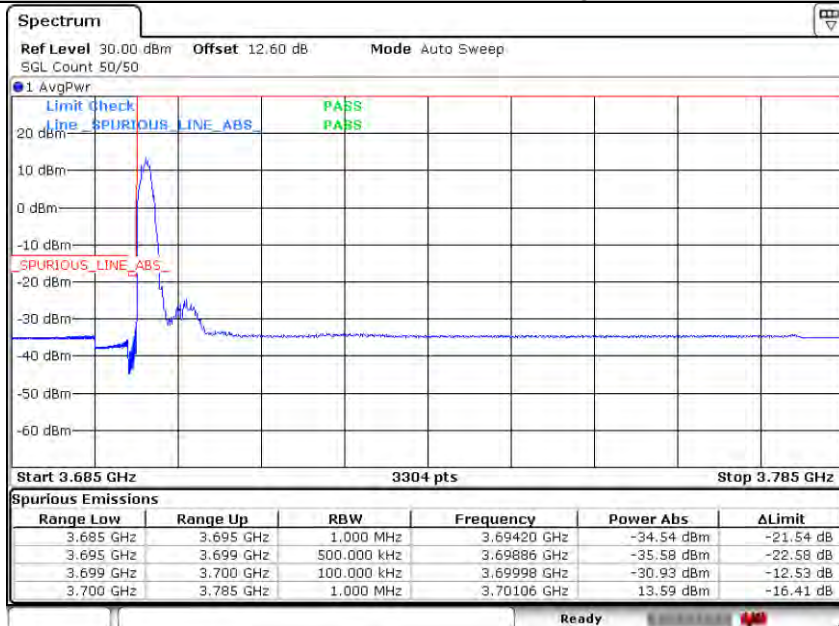
N78 30KHz TM6 60MHz 651332 Outer Full



Date: 7.MAR.2021 17:07:10

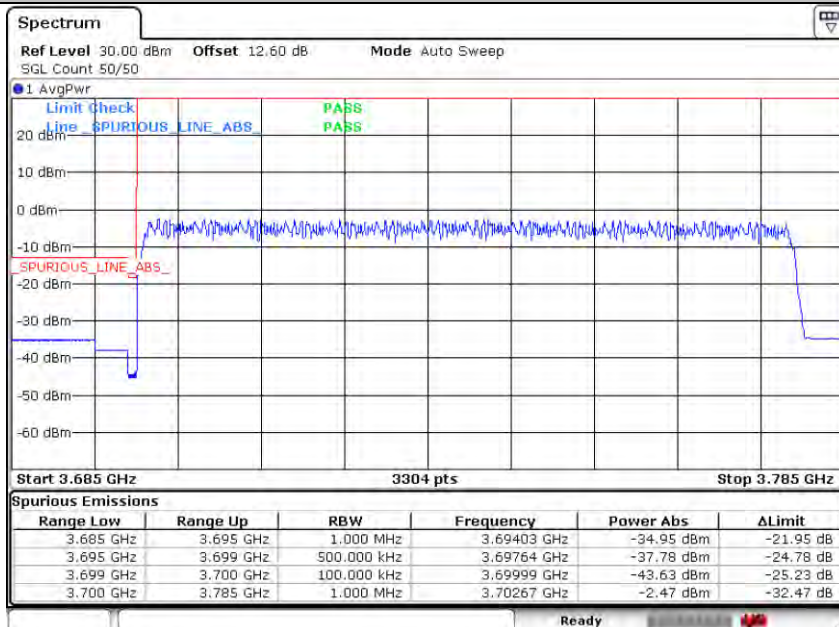


N78 30KHz TM1 80MHz 649334 Edge 1RB Left



Date: 7.MAR.2021 19:28:55

N78 30KHz TM1 80MHz 649334 Outer Full



Date: 7.MAR.2021 19:28:24



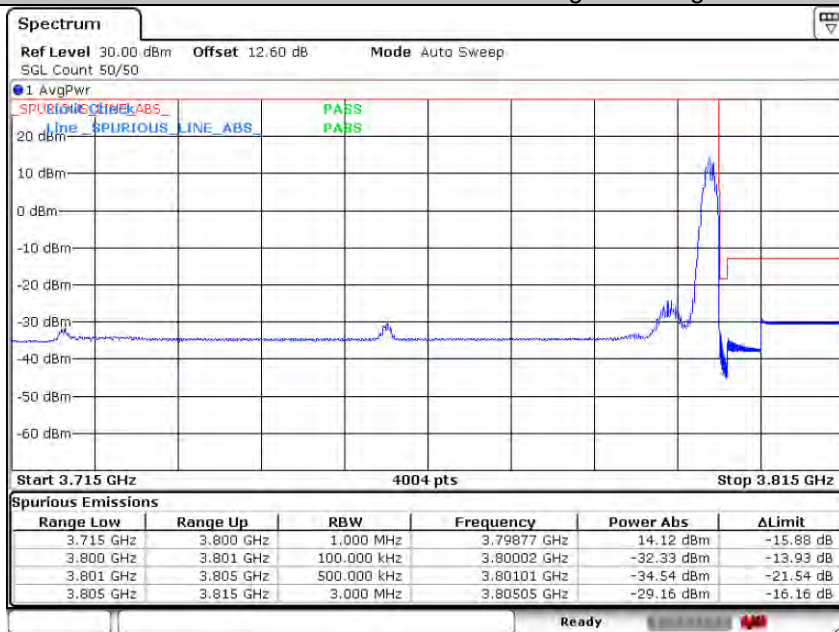
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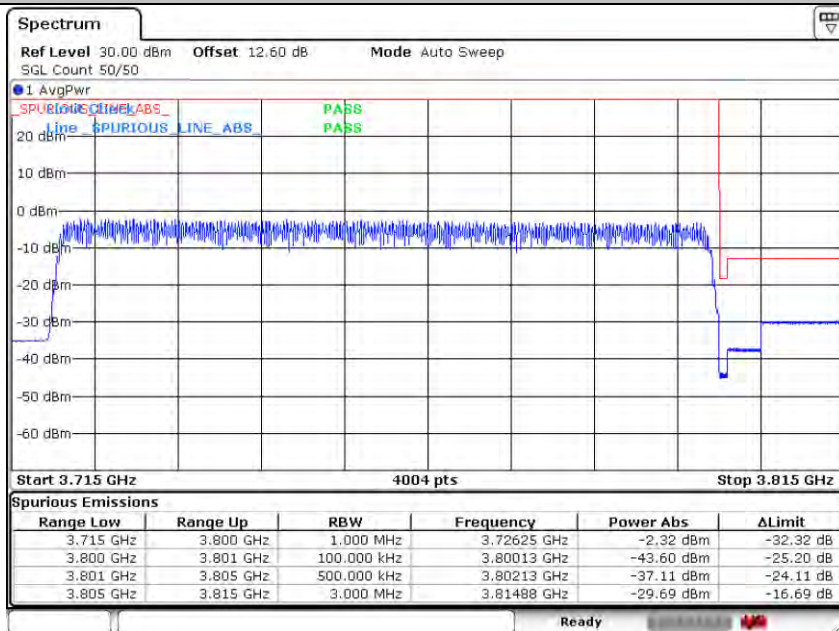
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N78 30KHz TM1 80MHz 650666 Edge 1RB Right



Date: 7.MAR.2021 17:13:56

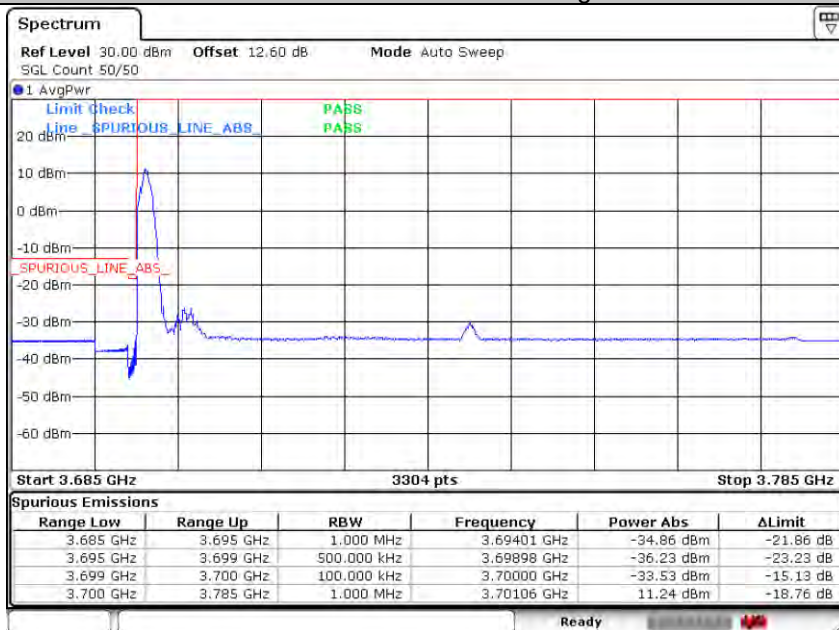
N78 30KHz TM1 80MHz 650666 Outer Full



Date: 7.MAR.2021 17:13:13

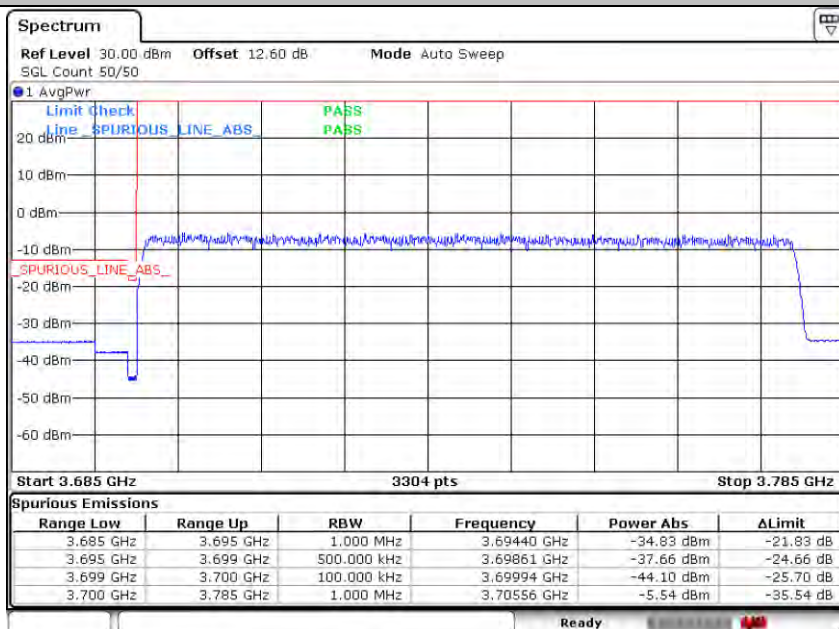


N78 30KHz TM6 80MHz 649334 Edge 1RB Left



Date: 7.MAR.2021 19:30:02

N78 30KHz TM6 80MHz 649334 Outer Full



Date: 7.MAR.2021 19:30:32



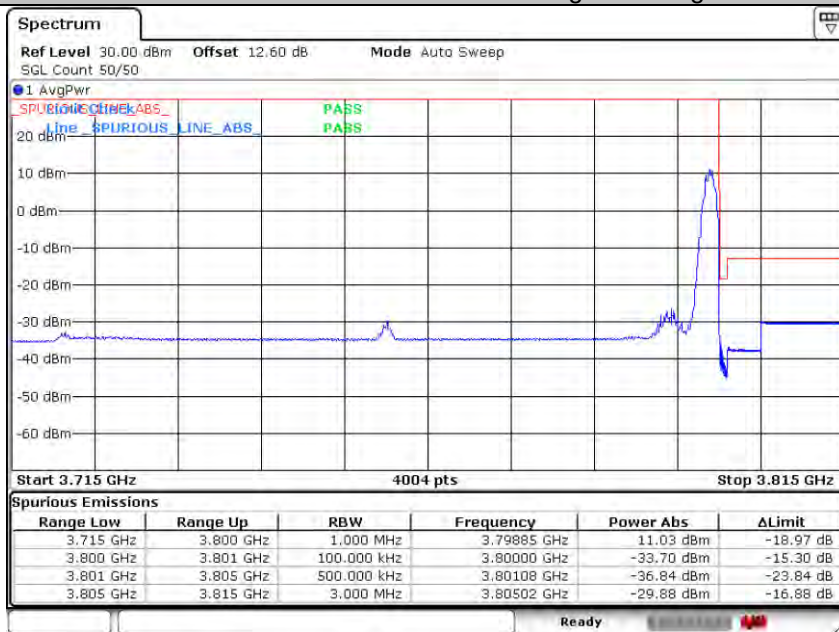
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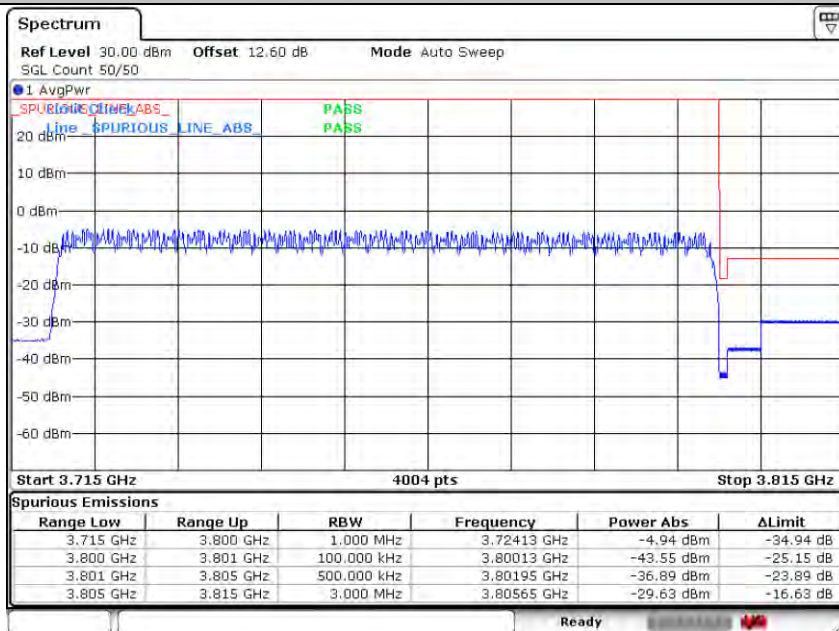
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N78 30KHz TM6 80MHz 650666 Edge 1RB Right



Date: 7.MAR.2021 17:11:02

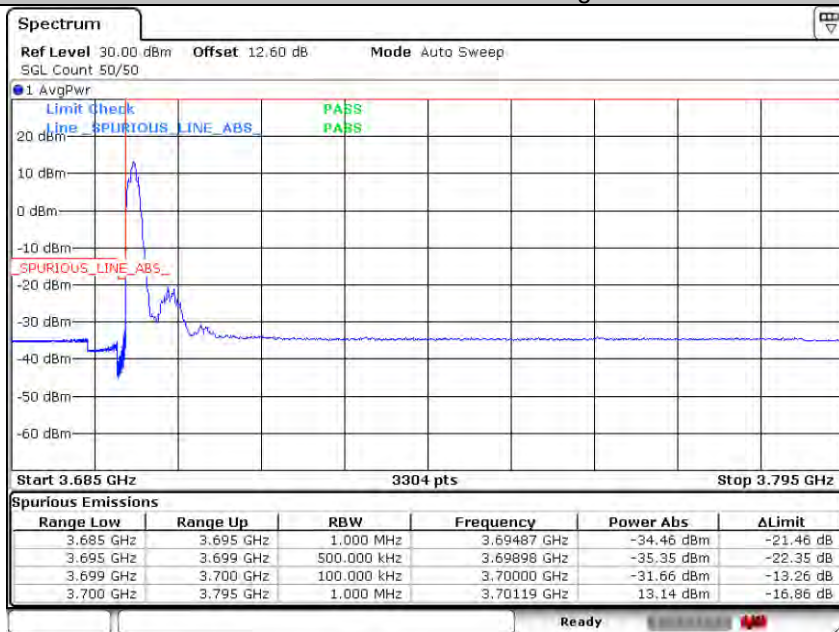
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Date: 7.MAR.2021 17:09:10

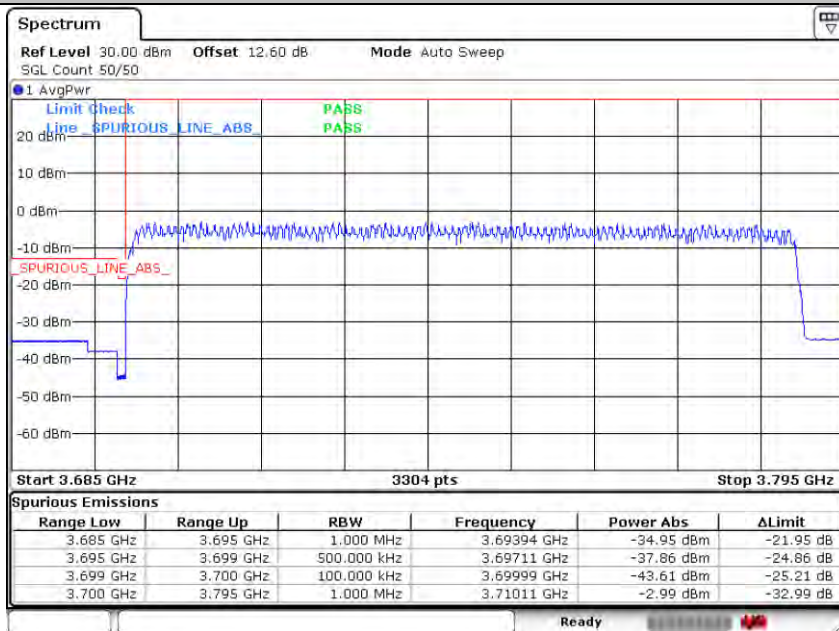


N78 30KHz TM1 90MHz 649668 Edge 1RB Left



Date: 7.MAR.2021 19:24:30

N78 30KHz TM1 90MHz 649668 Outer Full



Date: 7.MAR.2021 19:25:09

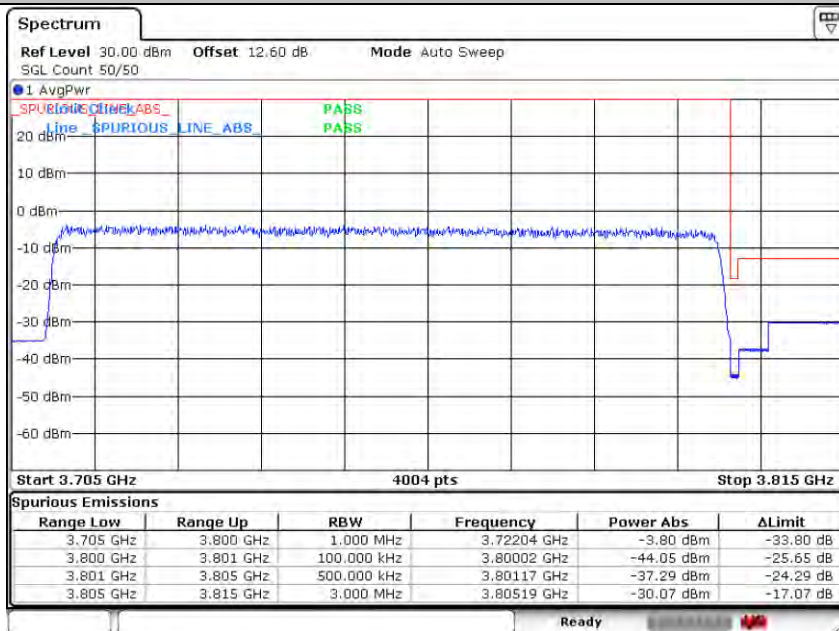


N78 30KHz TM1 90MHz 650332 Edge 1RB Right



Date: 7.MAR.2021 17:15:36

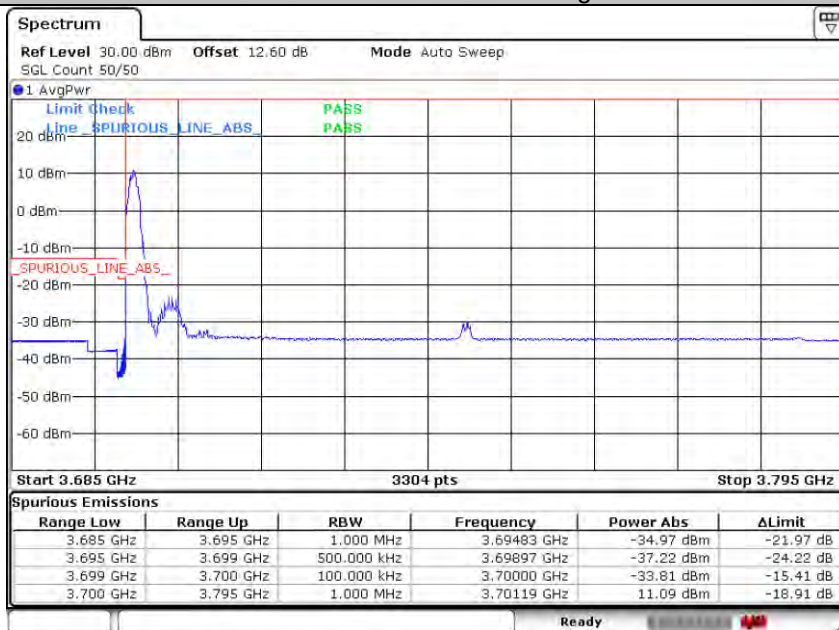
N78 30KHz TM1 90MHz 650332 Outer Full



Date: 7.MAR.2021 17:16:49

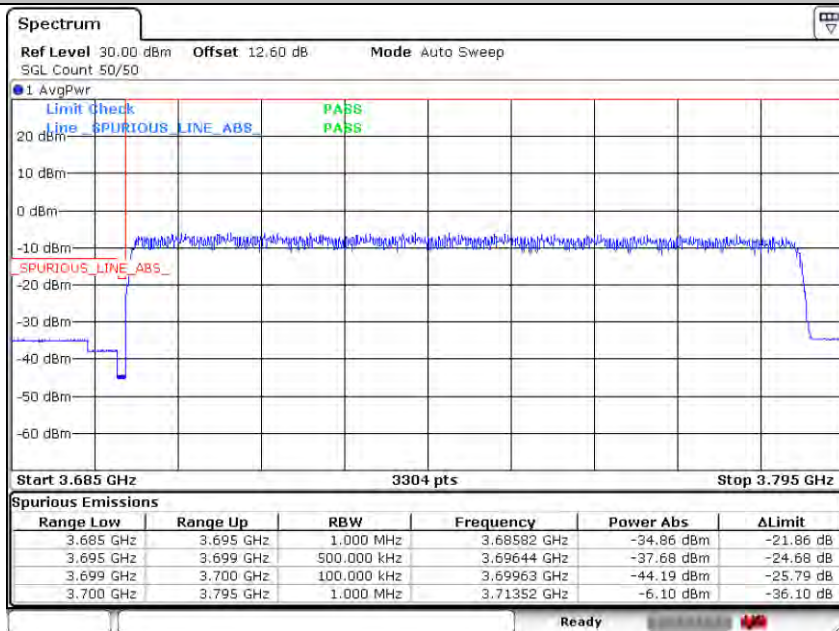


N78 30KHz TM6 90MHz 649668 Edge 1RB Left



Date: 7.MAR.2021 19:22:33

N78 30KHz TM6 90MHz 649668 Outer Full



Date: 7.MAR.2021 17:52:50

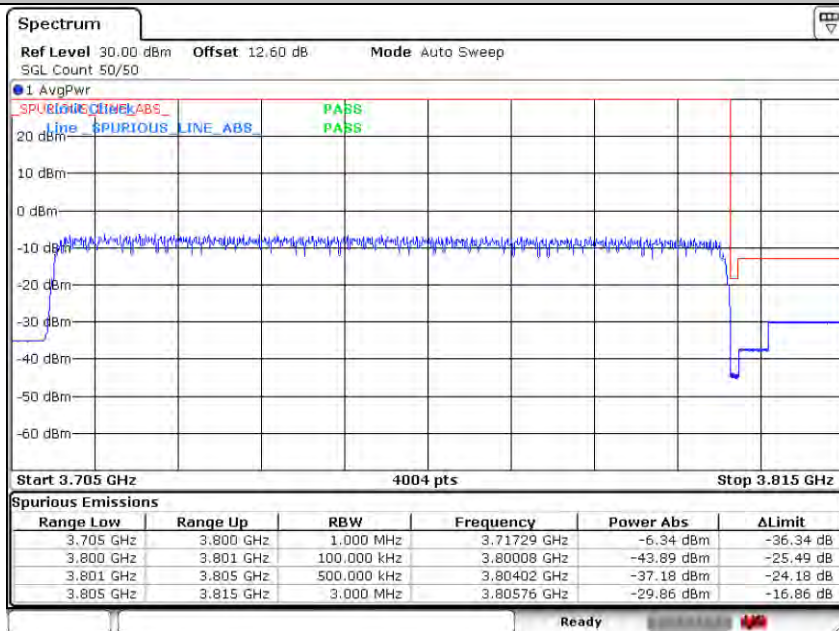


N78 30KHz TM6 90MHz 650332 Edge 1RB Right



Date: 7.MAR.2021 17:34:45

N78 30KHz TM6 90MHz 650332 Outer Full



Date: 7.MAR.2021 17:33:57

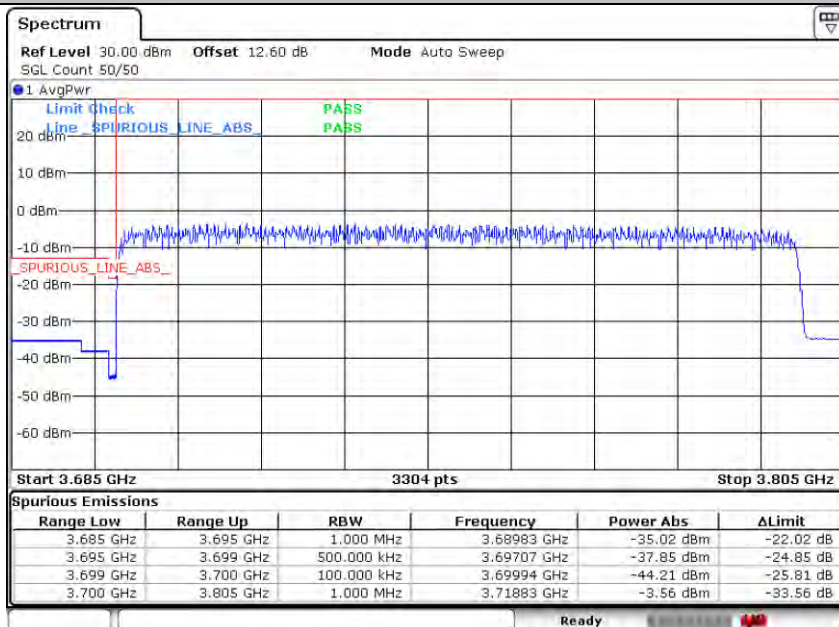


N78 30KHz TM1 100MHz 650000 Edge 1RB Left



Date: 7.MAR.2021 17:43:09

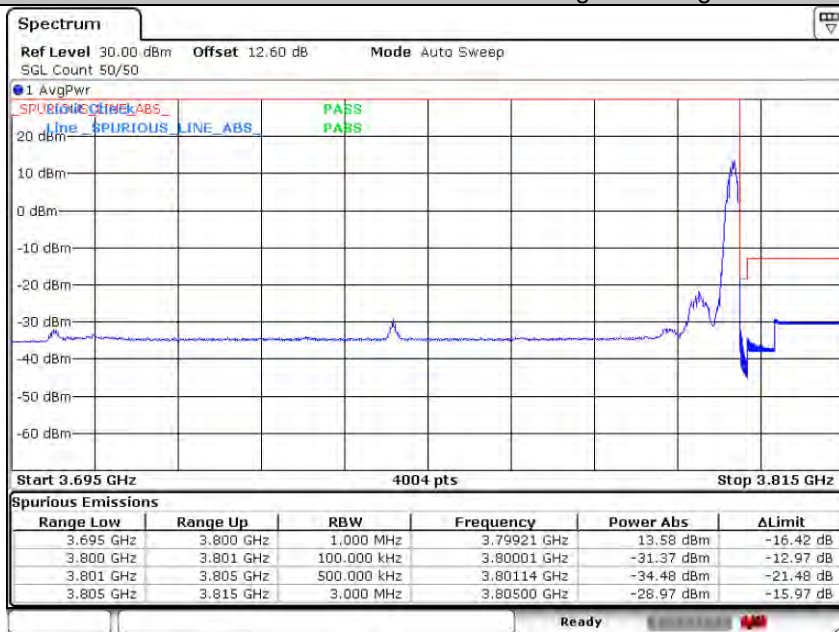
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Date: 7.MAR.2021 17:42:17

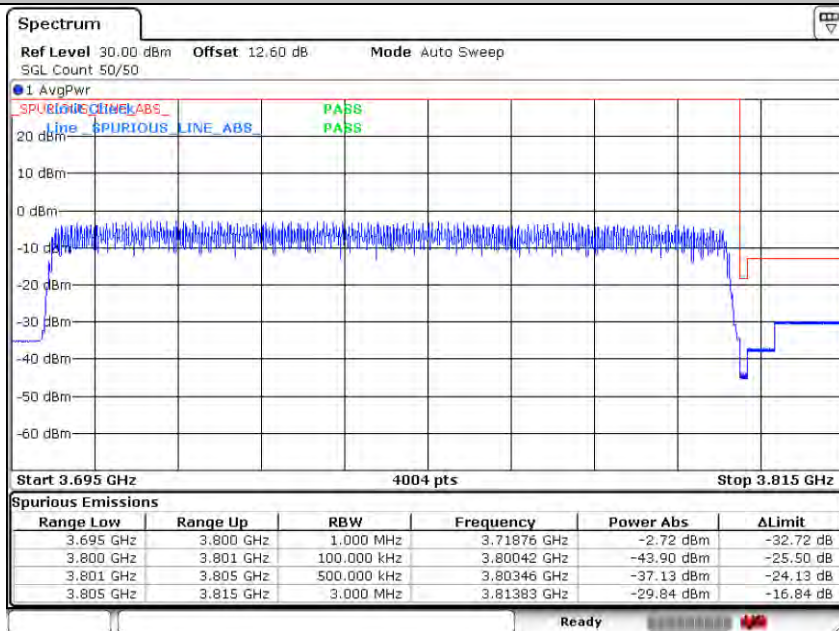


N78 30KHz TM1 100MHz 650000 Edge 1RB Right



Date: 7.MAR.2021 17:38:28

N78 30KHz TM1 100MHz 650000 Outer Full



Date: 7.MAR.2021 17:39:25

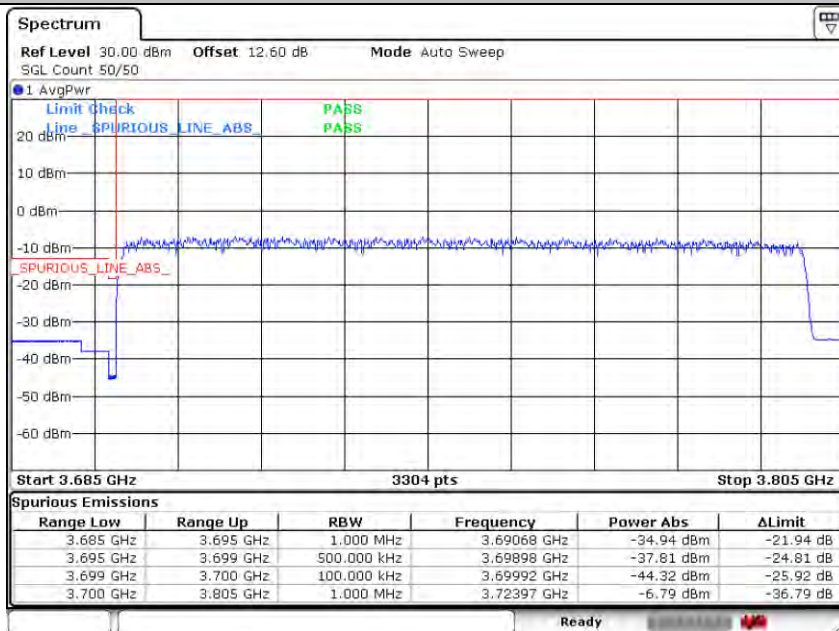


N78 30KHz TM6 100MHz 650000 Edge 1RB Left



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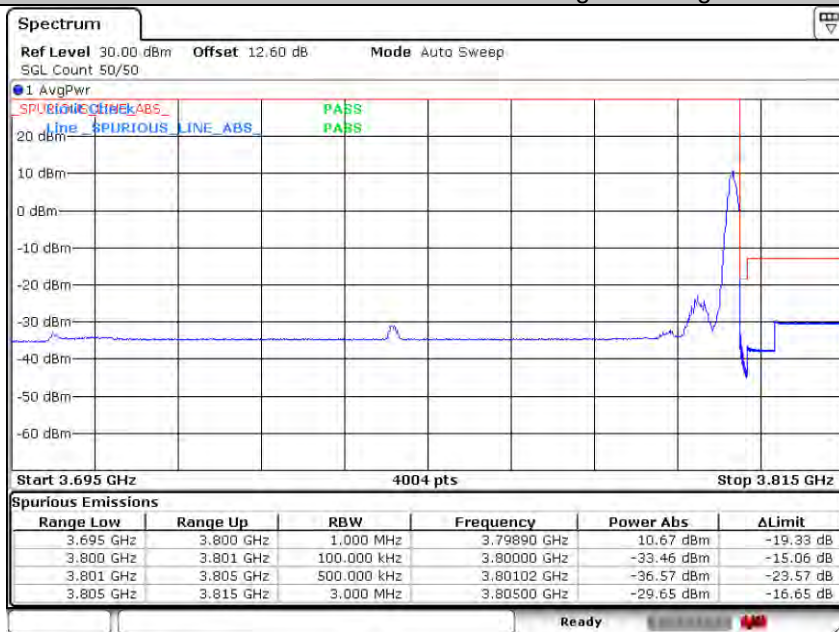
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Date: 7.MAR.2021 17:45:07

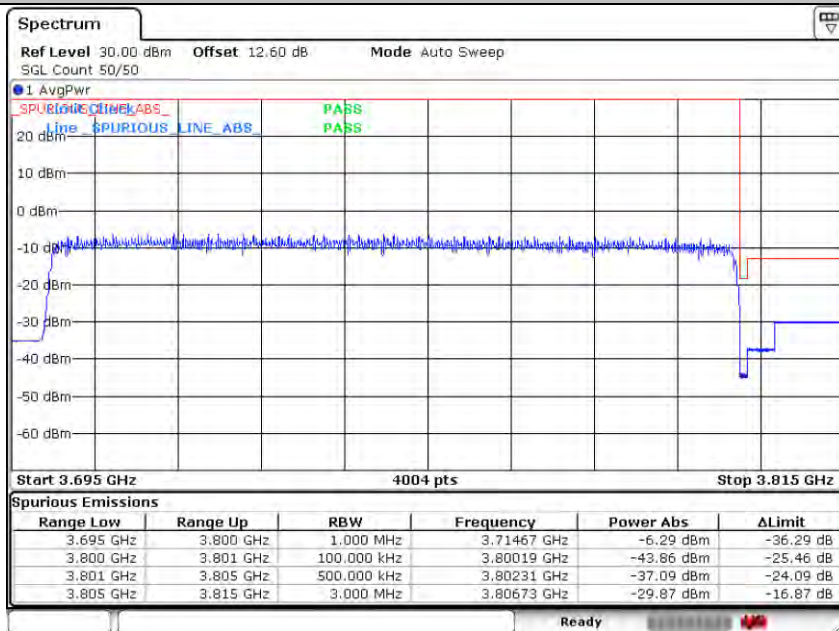


N78 30KHz TM6 100MHz 650000 Edge 1RB Right



Date: 7.MAR.2021 17:37:13

N78 30KHz TM6 100MHz 650000 Outer Full



Date: 7.MAR.2021 17:36:18

REMARK:

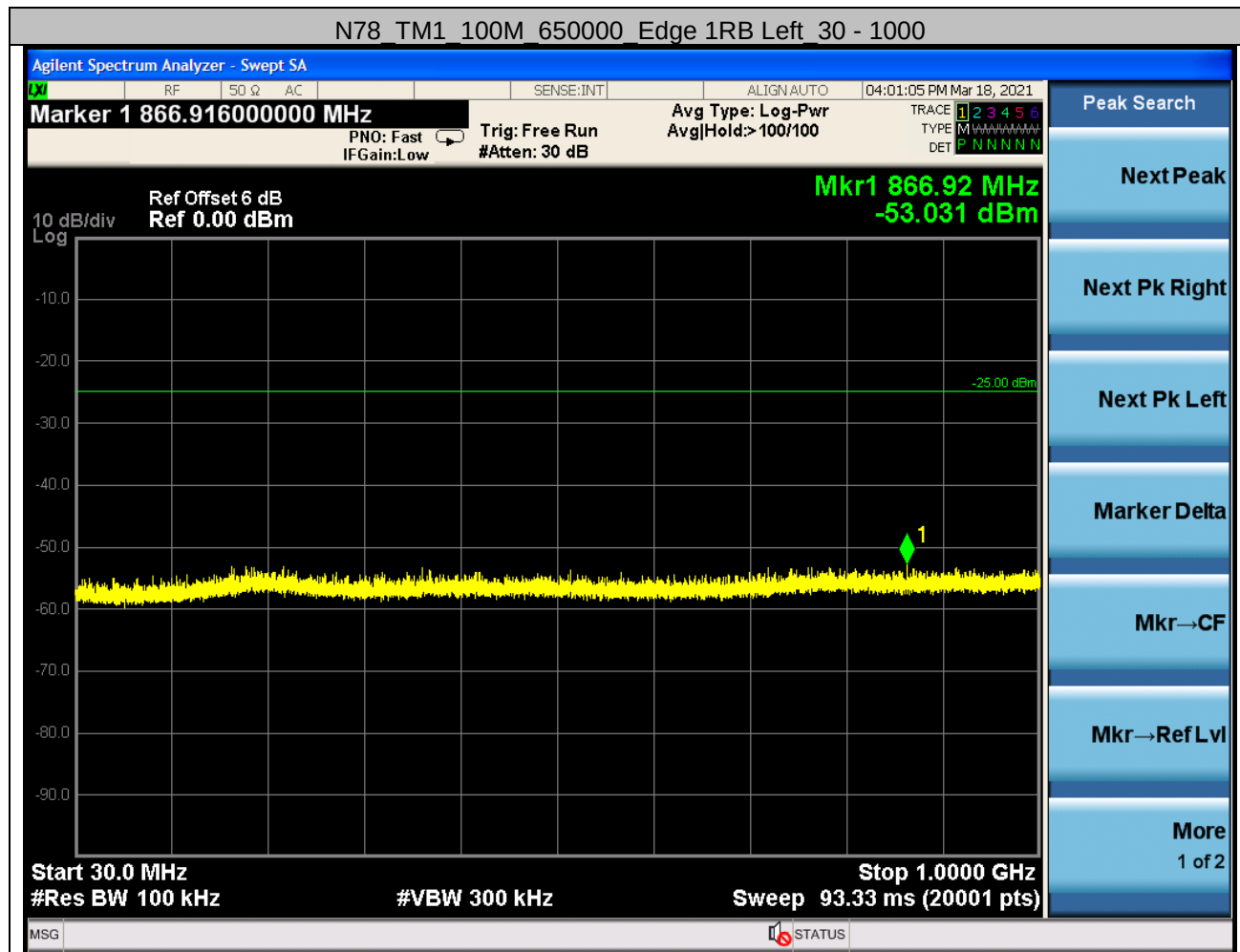
All antenna and all modulation had been tested, but only the worst case data displayed in this report



6 Spurious Emission at Antenna Terminal

REMARK: For the averaged unwanted emissions measurements, the measurement points in each sweep is greater than twice the Span/RBW in order to ensure bin-to-bin spacing of $< RBW/2$ so that narrow Band signals are not lost between frequency bins. As to the present test item, the "Measurement Points = $k * (Span / RBW)$ " with k between 4 and 5, which results in an acceptable level error of less than 0.5 dB.

6.1 Test Plots

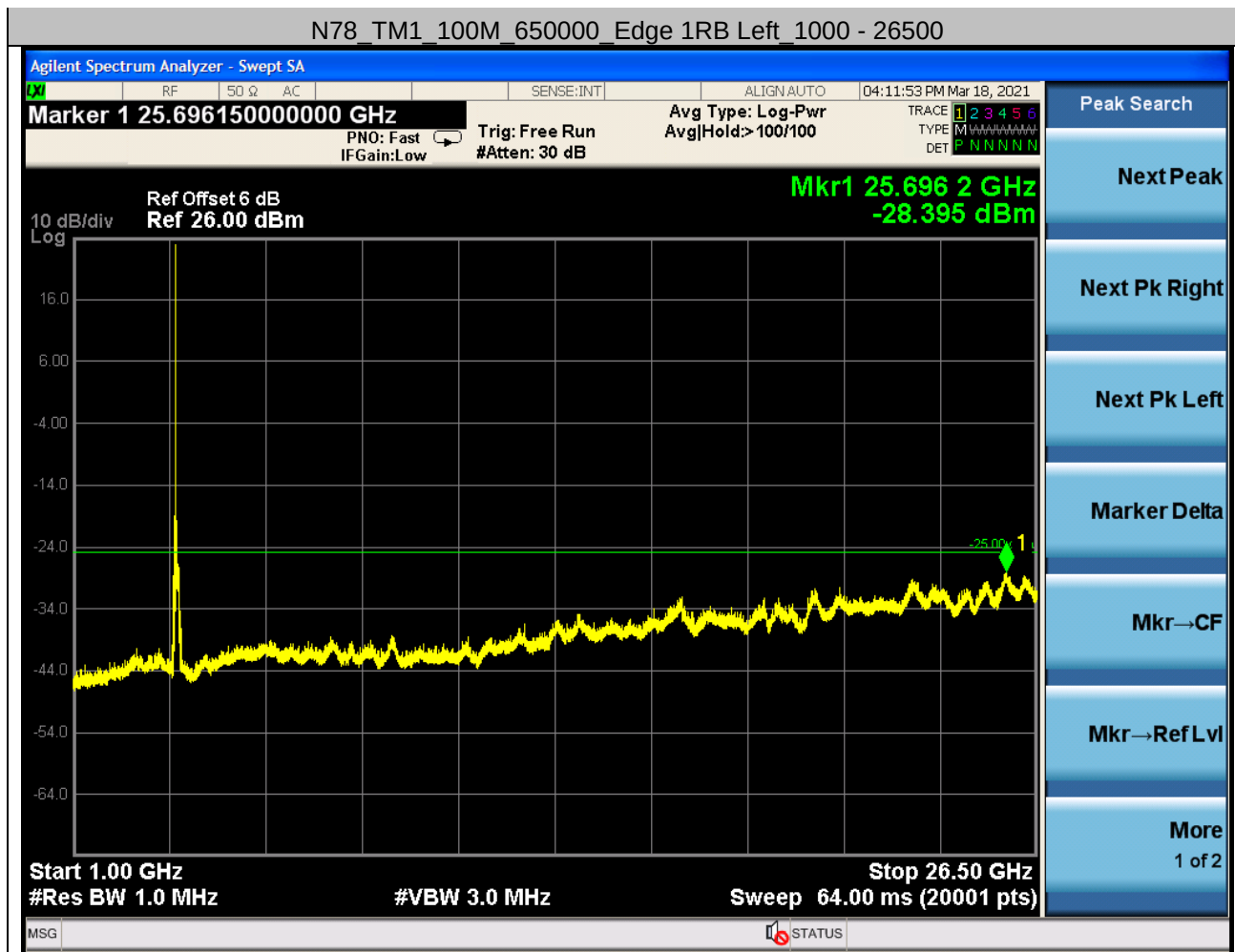


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REMARK:

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7 Frequency Stability

7.1 Frequency Error VS. Voltage

NR Band	SCS	Bandwidth h	Modulation	Channel	RB Config	Voltage [Vdc]	Temperature(°C)	Deviation (Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N78	30KHz	100MHz	TM1	650000	Outer Full	VL	NT	-6.08	-0.00162	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	NT	-3.84	-0.00102	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VH	NT	0.81	0.00022	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VL	NT	10.97	0.00293	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	NT	-6.00	-0.00160	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VH	NT	-0.03	-0.00001	±2.5	PASS

7.2 Frequency Error VS. Temperature

NR Band	SCS	Bandwidth h	Modulation	Channel	RB Config	Voltage [Vdc]	Temperature(°C)	Deviation (Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	-30	14.58	0.00389	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	-20	-7.31	-0.00195	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	-10	-2.81	-0.00075	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	0	-10.96	-0.00292	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	10	-4.49	-0.00120	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	20	2.02	0.00054	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	30	12.43	0.00331	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	40	0.08	0.00002	±2.5	PASS
N78	30KHz	100MHz	TM1	650000	Outer Full	VN	50	-6.05	-0.00161	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	-30	6.31	0.00168	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	-20	6.01	0.00160	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	-10	13.48	0.00359	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	0	8.84	0.00236	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	10	-2.66	-0.00071	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	20	14.08	0.00375	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	30	-10.18	-0.00271	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	40	1.81	0.00048	±2.5	PASS
N78	30KHz	100MHz	TM6	650000	Outer Full	VN	50	4.91	0.00131	±2.5	PASS

REMARK:

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The End



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