



11AX80SISO_Ant2_5775



11A-CDD_Ant1_5745



11A-CDD_Ant2_5745



11A-CDD_Ant1_5785



11A-CDD_Ant2_5785



11A-CDD_Ant1_5825



11A-CDD_Ant2_5825



11N20MIMO_Ant1_5745



11N20MIMO_Ant2_5745



11N20MIMO_Ant1_5785



11N20MIMO_Ant2_5785



11N20MIMO_Ant1_5825



11N20MIMO_Ant2_5825



11N40MIMO_Ant1_5755



11N40MIMO_Ant2_5755



11N40MIMO_Ant1_5795



11N40MIMO_Ant2_5795



11AC20MIMO_Ant1_5745



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11AC20MIMO_Ant1_5785



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11AC20MIMO_Ant1_5825



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11AC40MIMO_Ant1_5755



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11AC40MIMO_Ant1_5795



11AC40MIMO_Ant2_5795



11AC80MIMO_Ant1_5775



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11AX20MIMO_Ant1_5745



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11AX20MIMO_Ant1_5785



11AX20MIMO_Ant2_5785



11AX20MIMO_Ant1_5825



11AX20MIMO_Ant2_5825



11AX40MIMO_Ant1_5755



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11AX40MIMO_Ant1_5795



11AX40MIMO_Ant2_5795



11AX80MIMO_Ant1_5775



11AX80MIMO_Ant2_5775

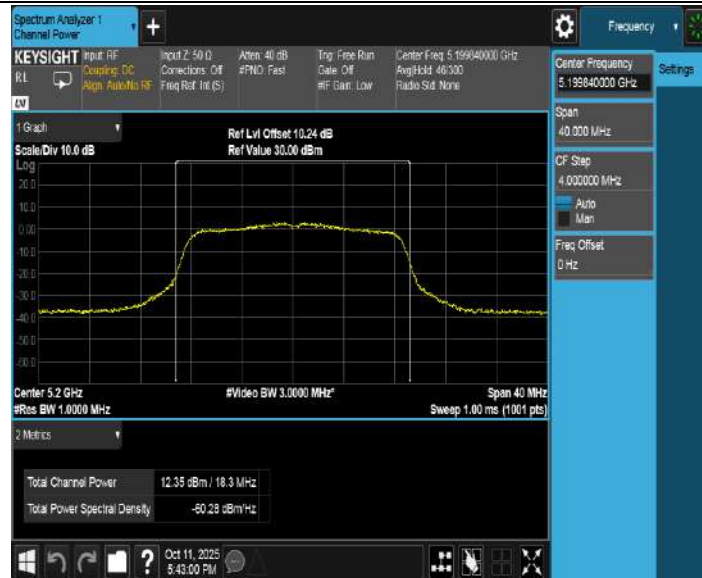




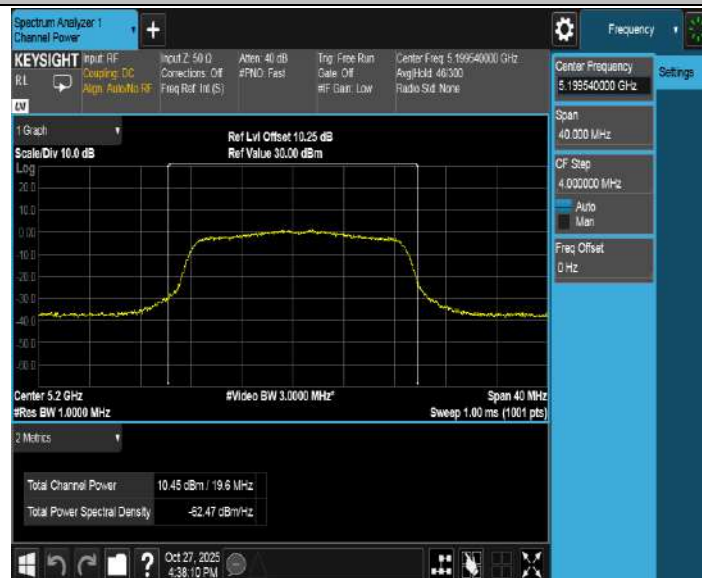
Appendix D: Maximum conducted output power

Test Graphs Channel Power

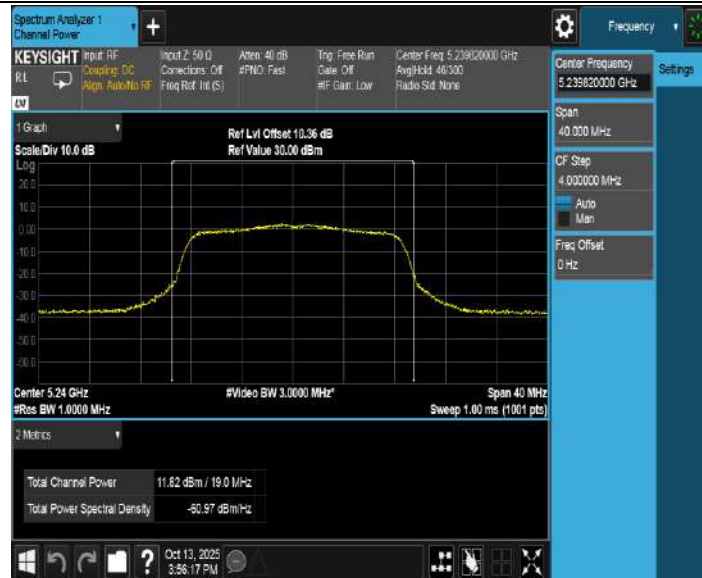




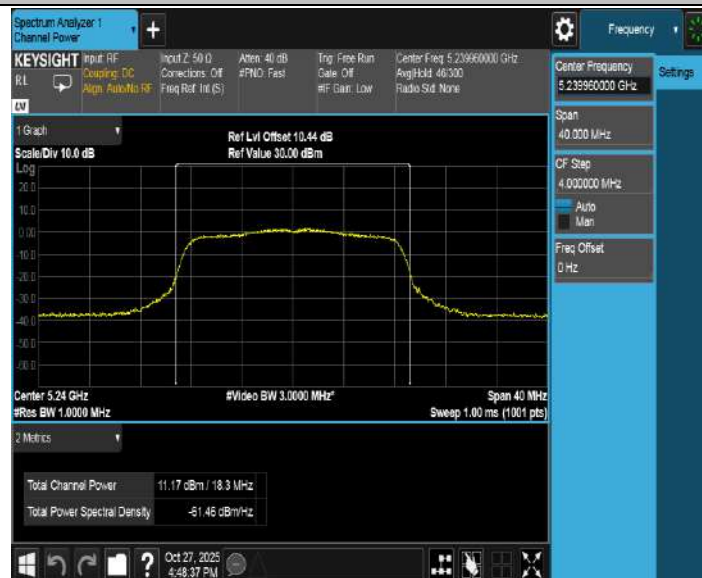
11A_Ant2_5200



11A_Ant1_5240



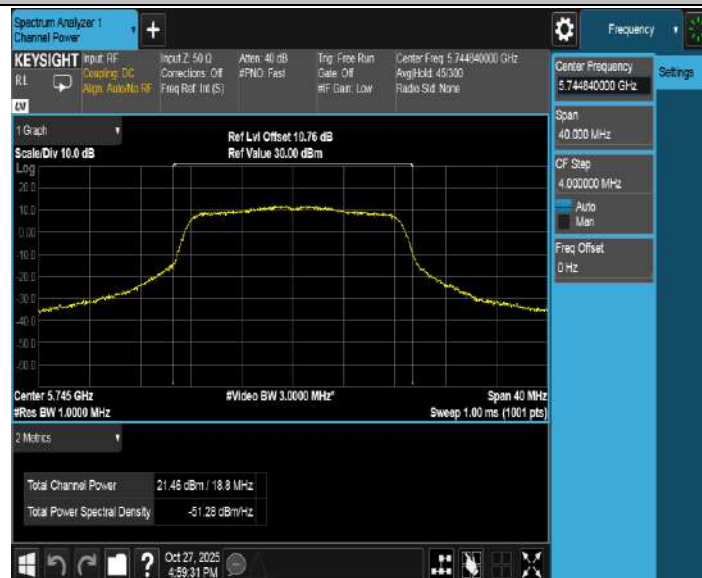
11A_Ant2_5240



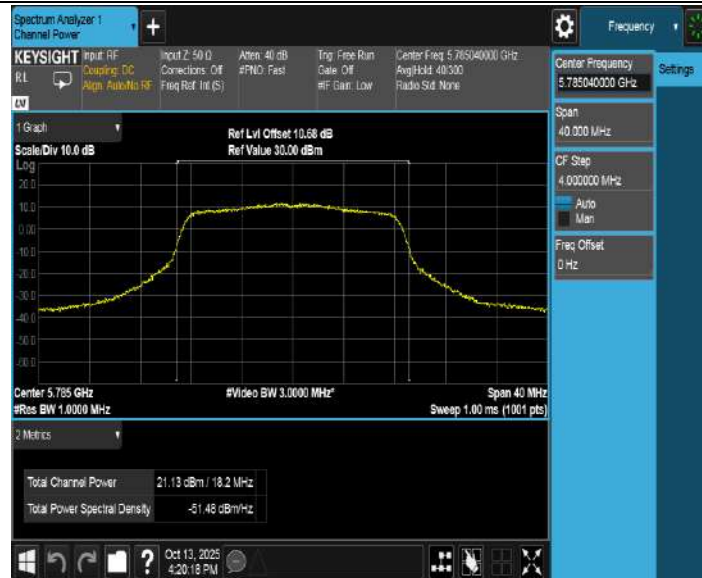
11A_Ant1_5745



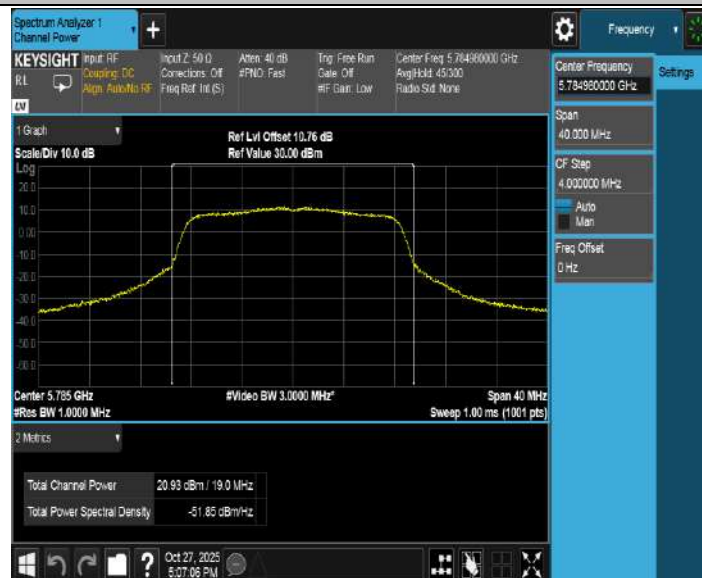
11A_Ant2_5745



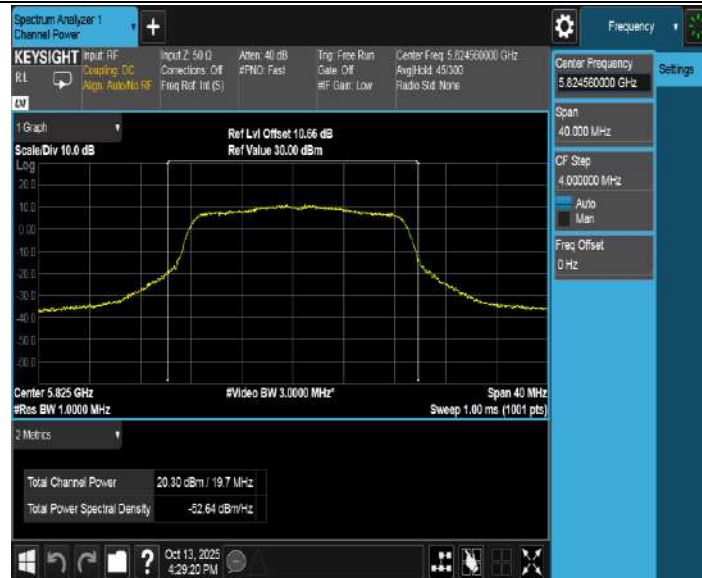
11A_Ant1_5785



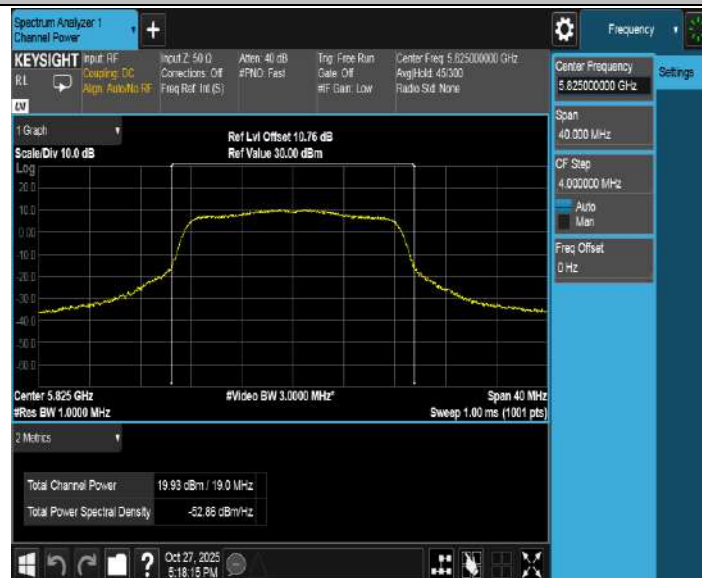
11A_Ant2_5785



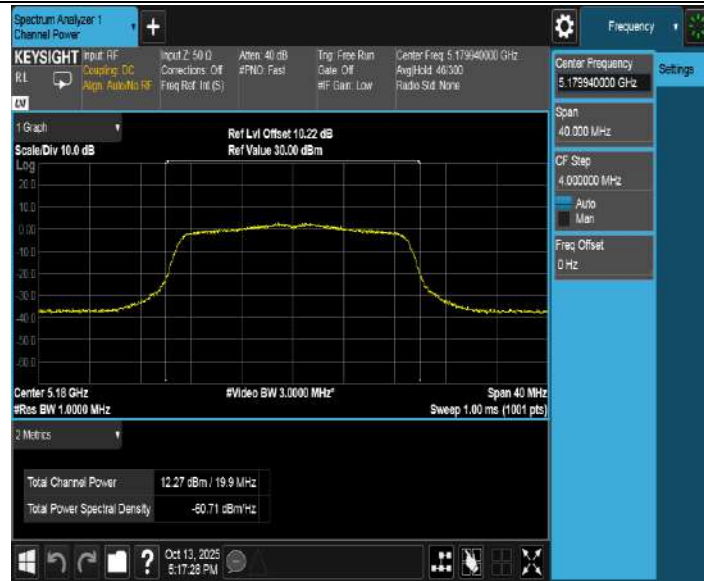
11A_Ant1_5825



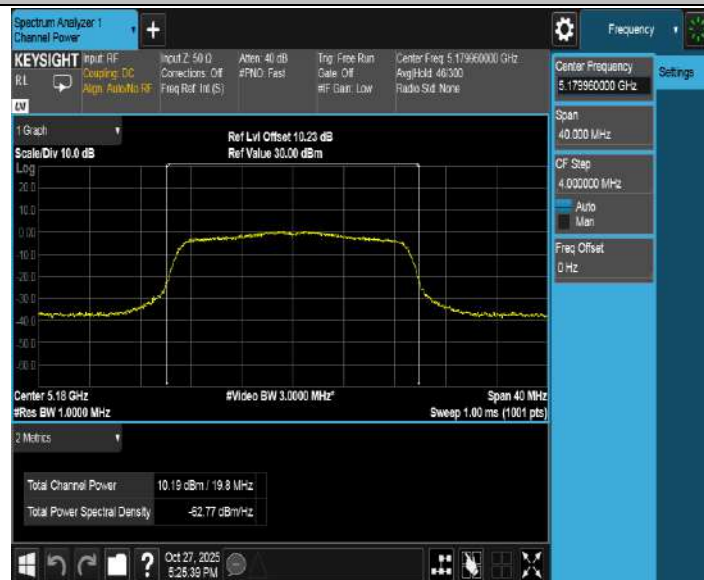
11A_Ant2_5825



11N20SISO_Ant1_5180



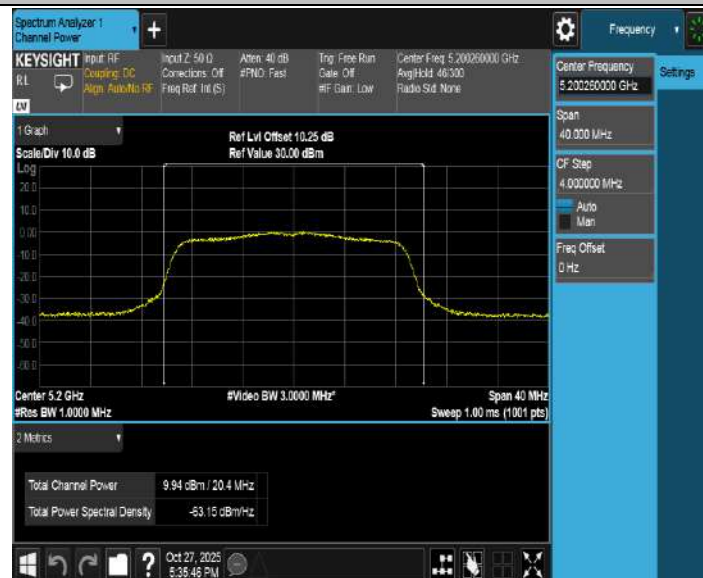
11N20SISO_Ant2_5180



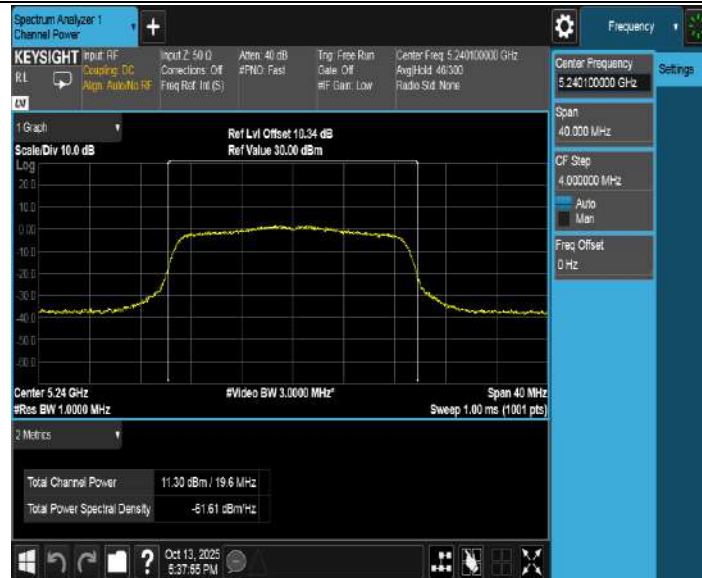
11N20SISO_Ant1_5200



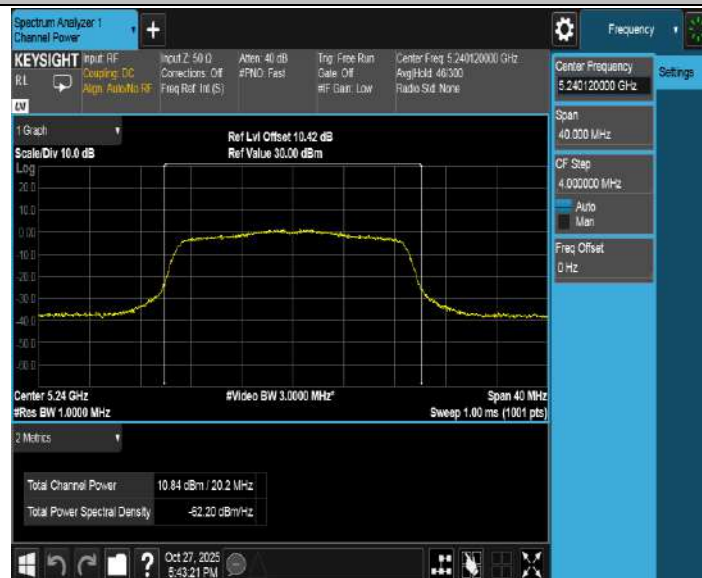
11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



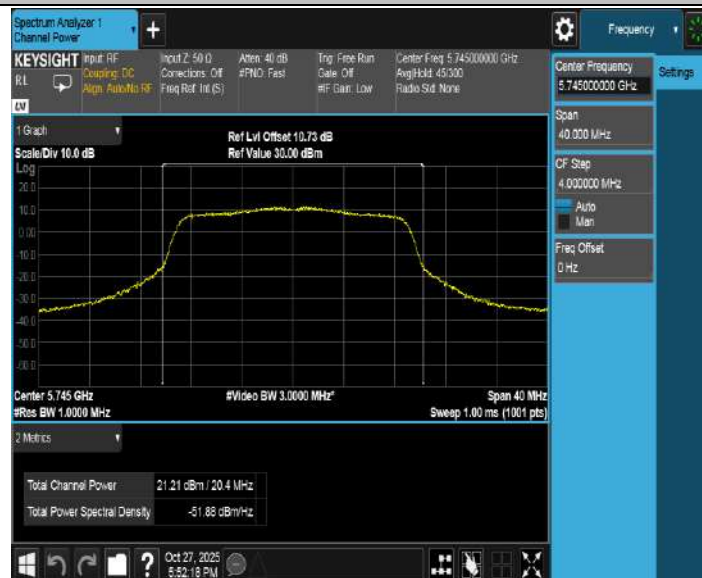
11N20SISO_Ant2_5240



11N20SISO_Ant1_5745



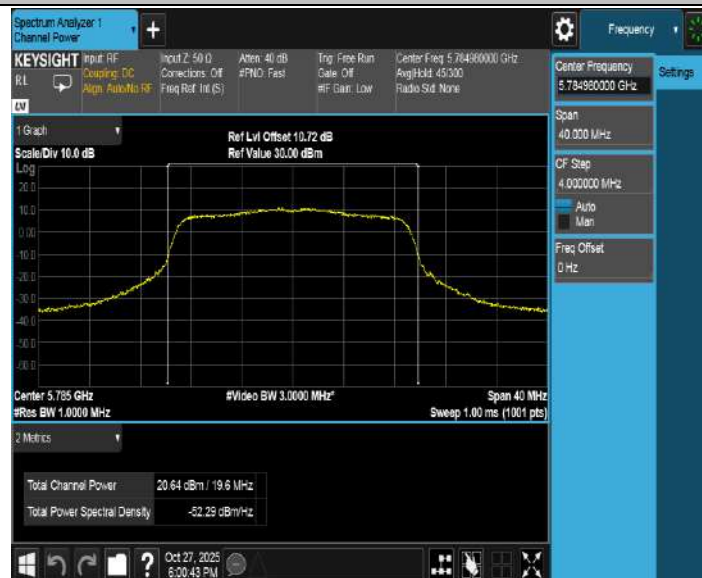
11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



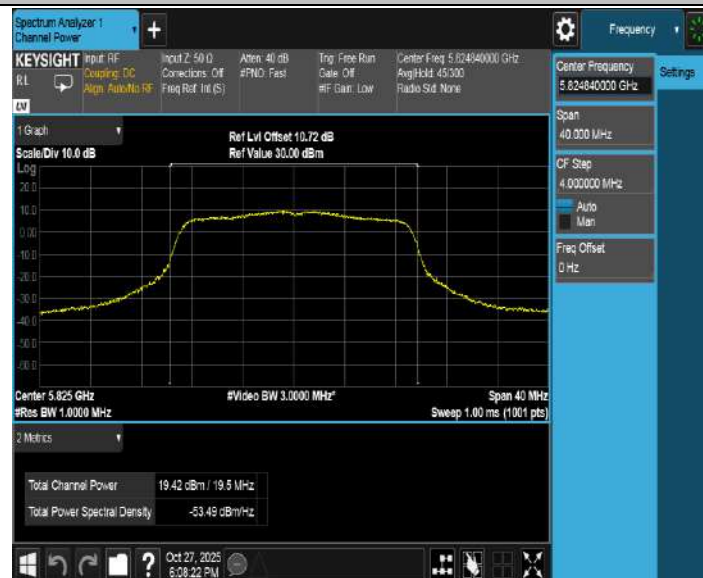
11N20SISO_Ant2_5785



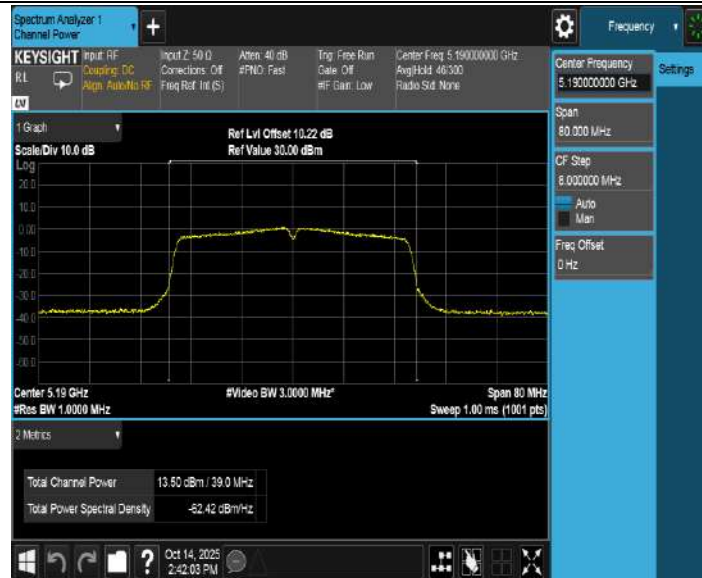
11N20SISO_Ant1_5825



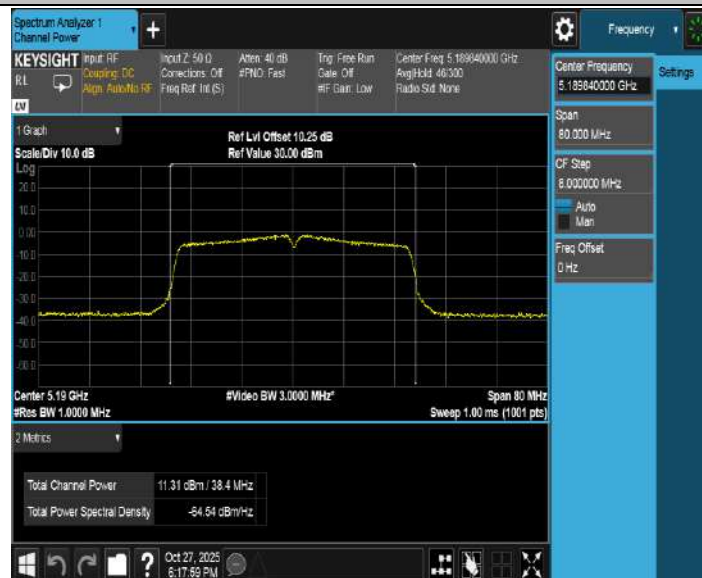
11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



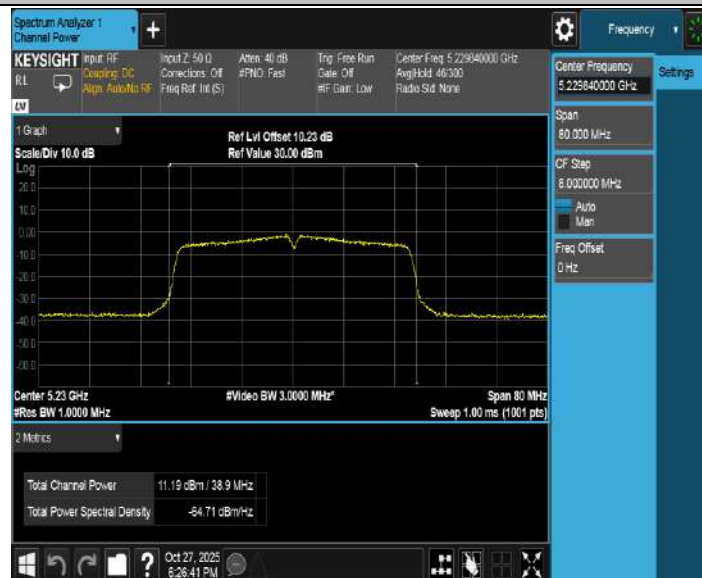
11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



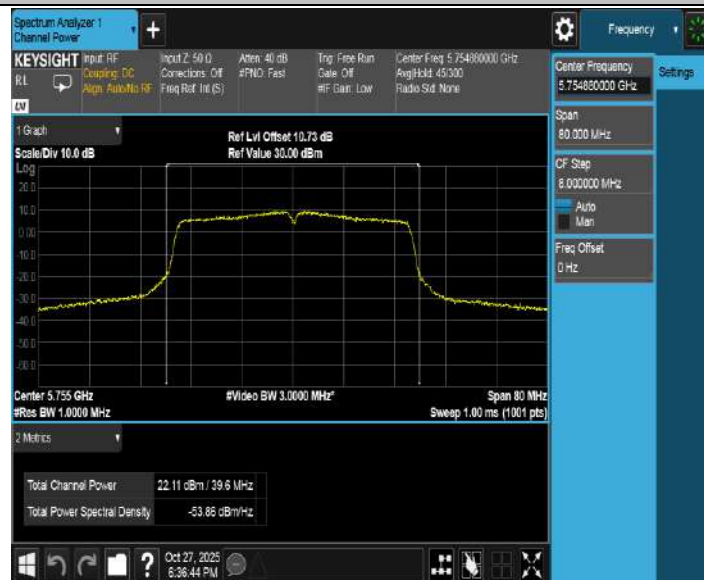
11N40SISO_Ant2_5230



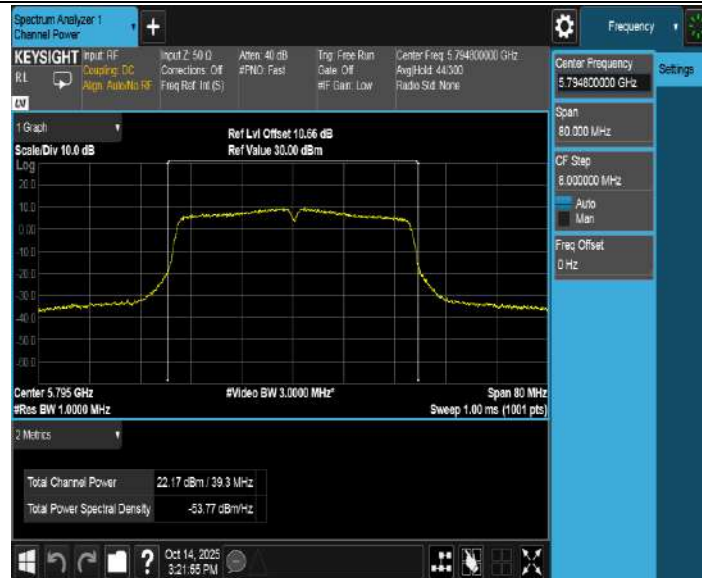
11N40SISO_Ant1_5755



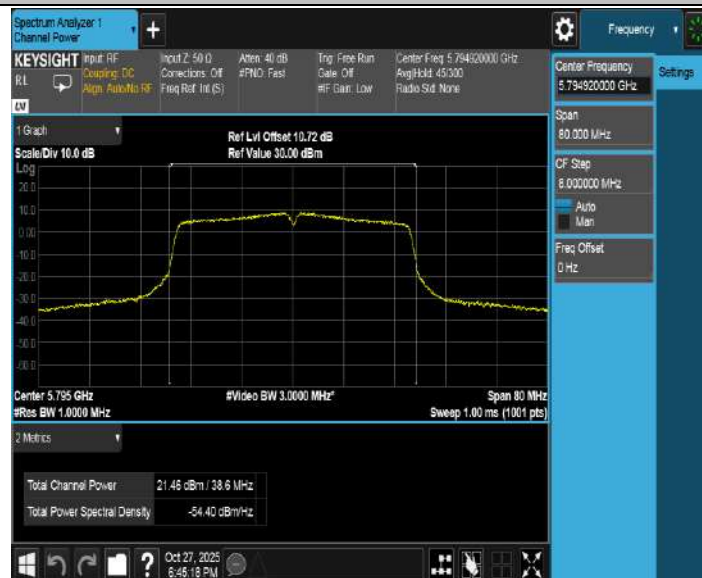
11N40SISO_Ant2_5755



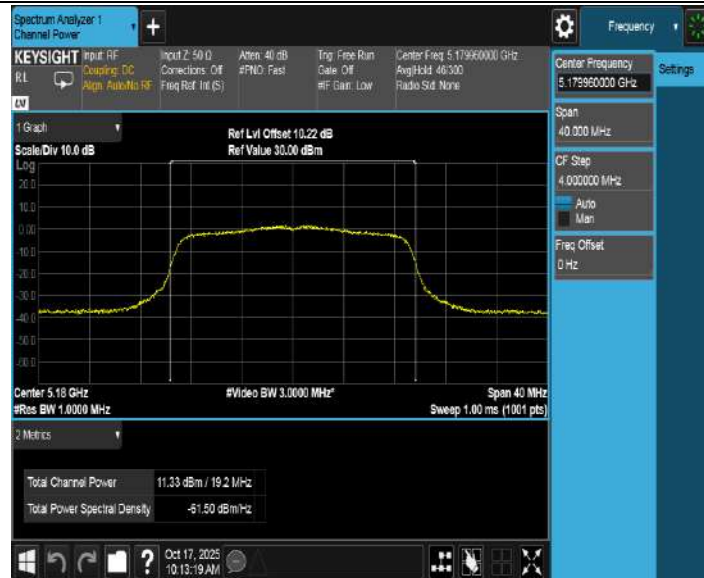
11N40SISO_Ant1_5795



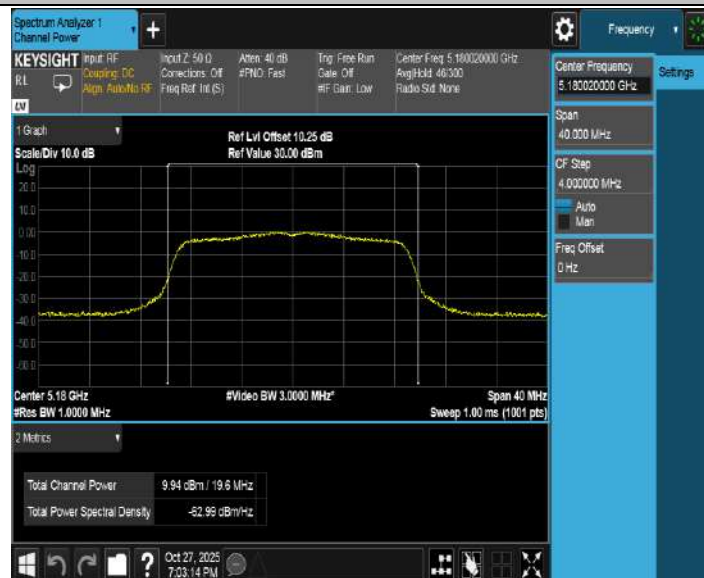
11N40SISO_Ant2_5795



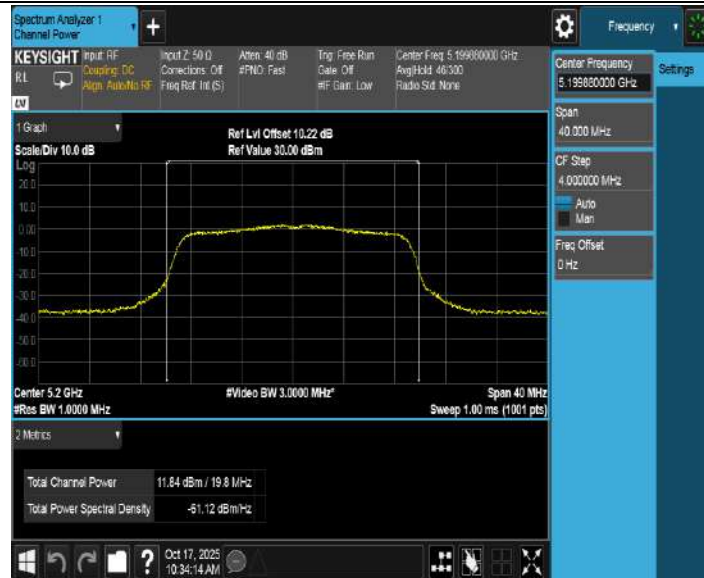
11AC20SISO_Ant1_5180



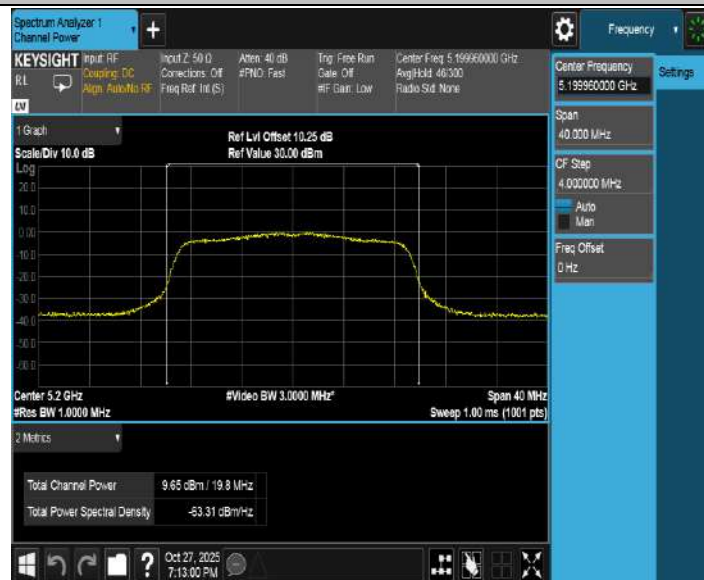
11AC20SISO_Ant2_5180



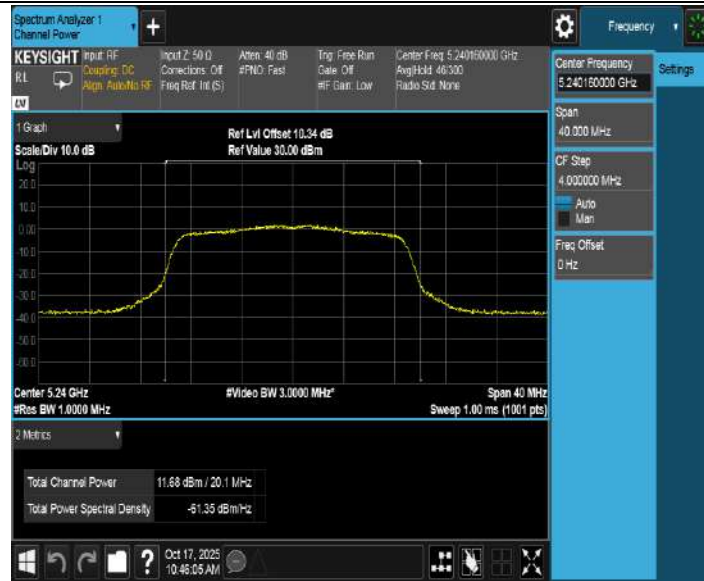
11AC20SISO_Ant1_5200



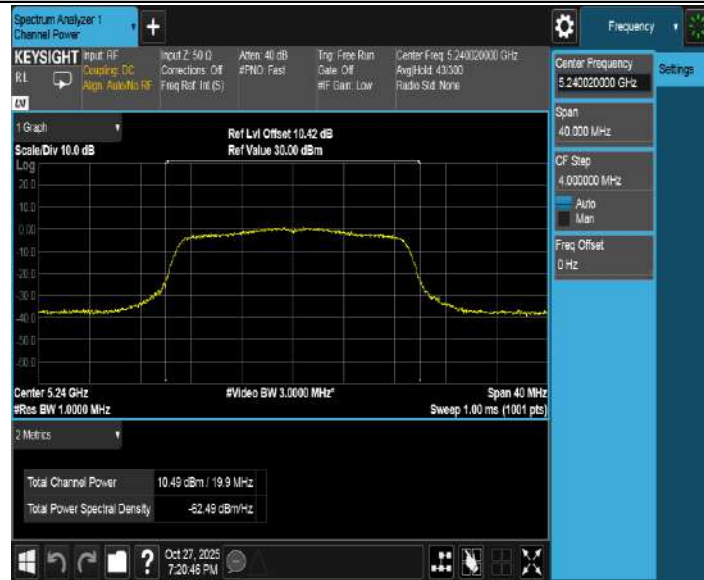
11AC20SISO_Ant2_5200



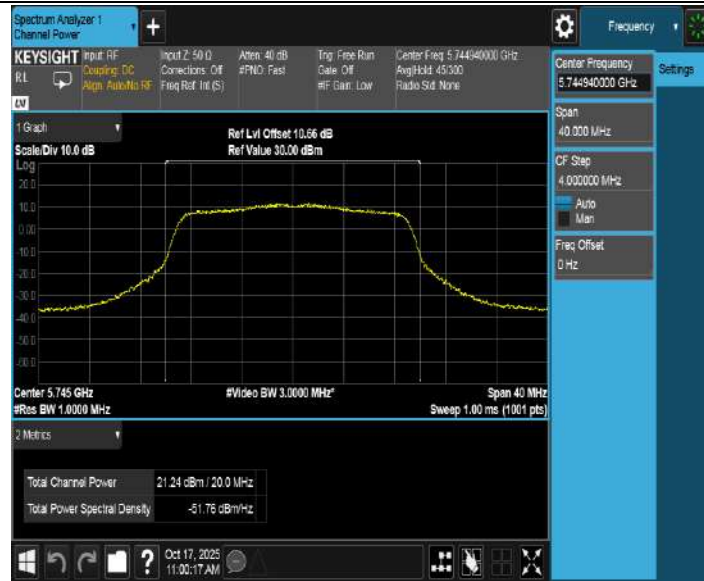
11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



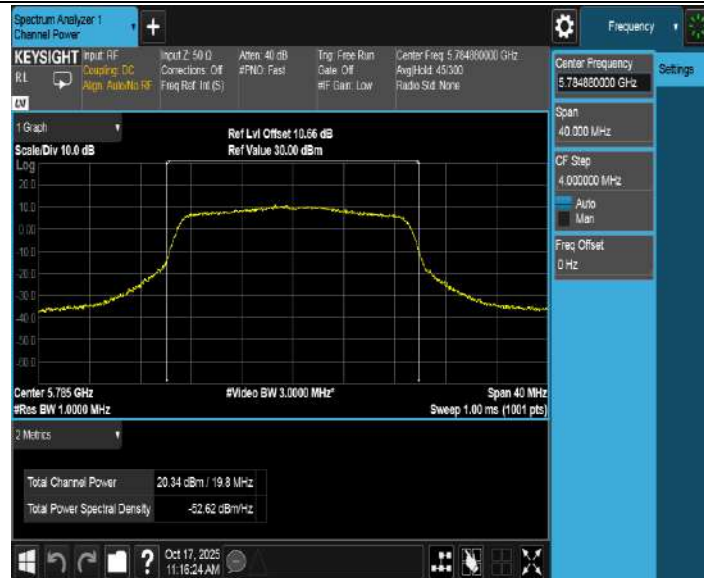
11AC20SISO_Ant1_5745



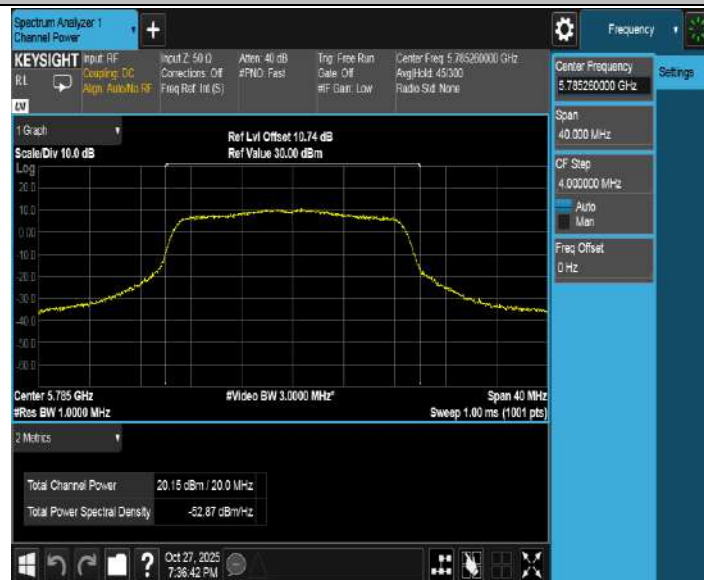
11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



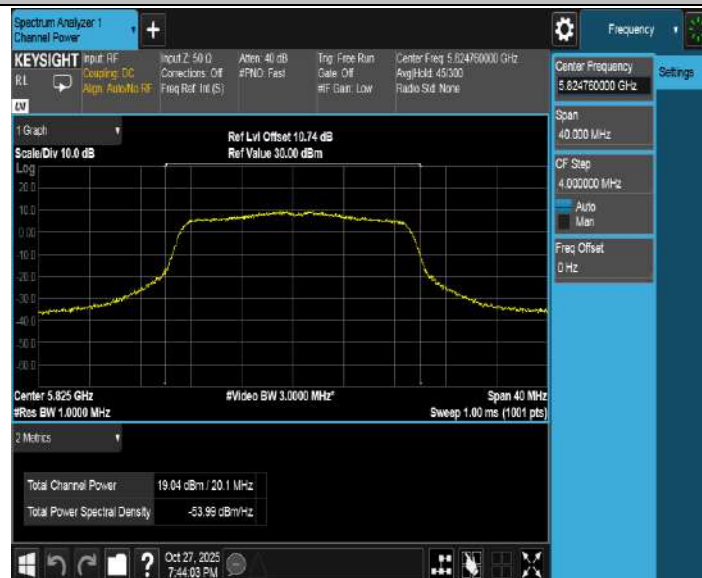
11AC20SiSO_Ant2_5785



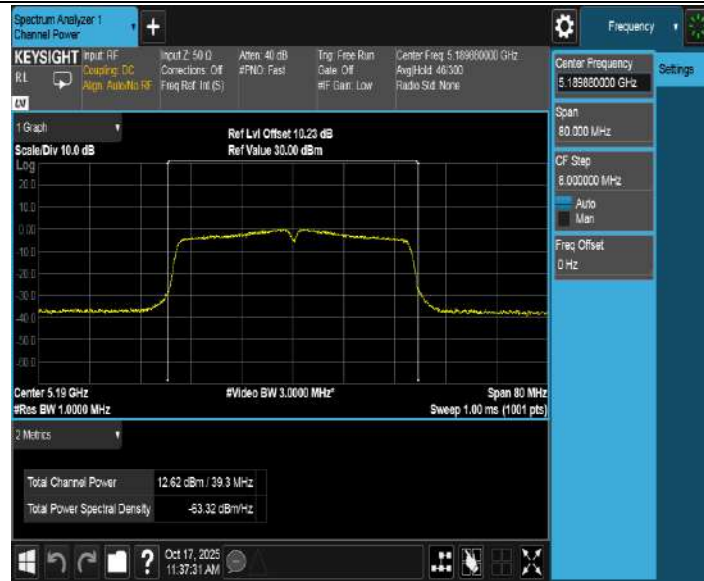
11AC20SiSO_Ant1_5825



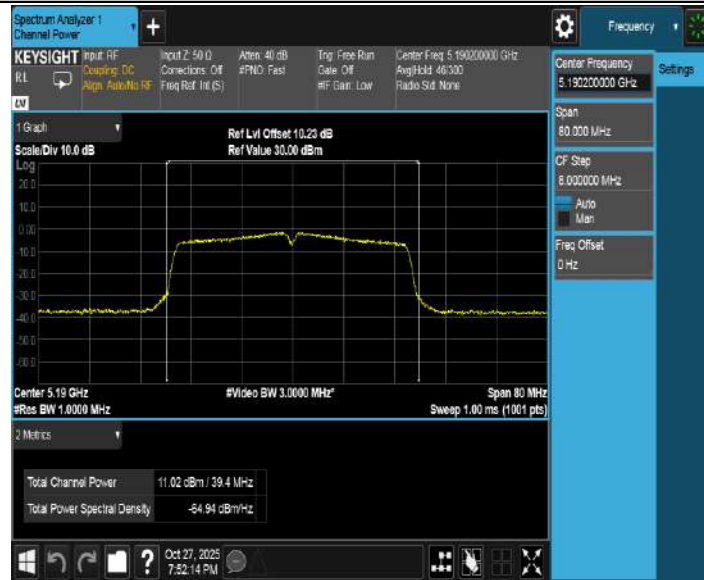
11AC20SiSO_Ant2_5825



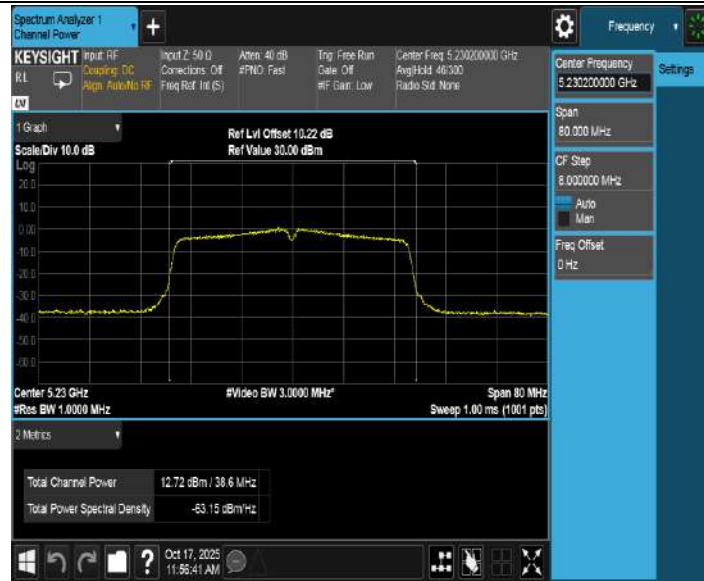
11AC40SiSO_Ant1_5190



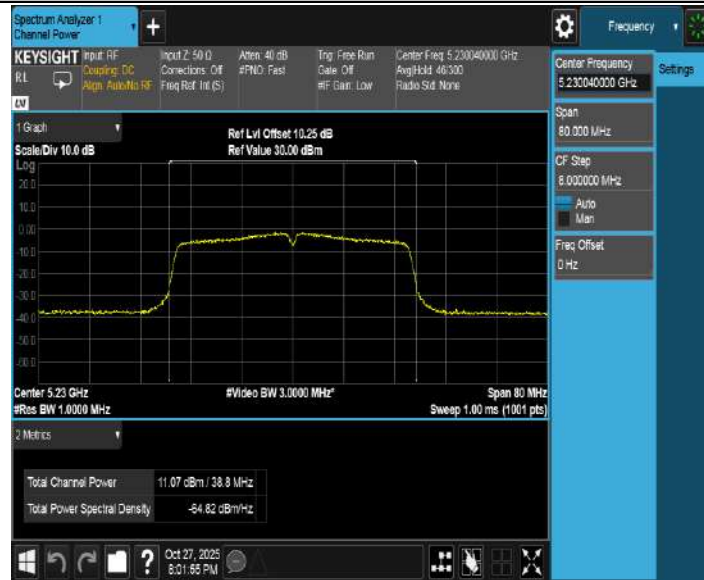
11AC40SiSO_Ant2_5190



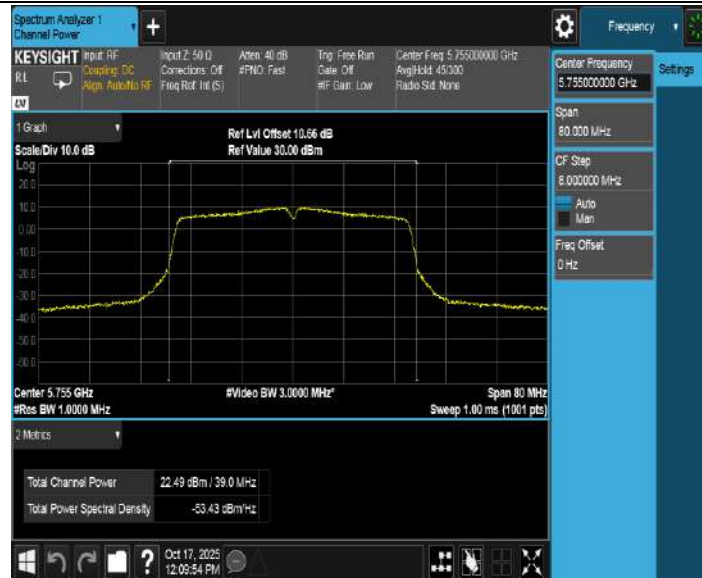
11AC40SiSO_Ant1_5230



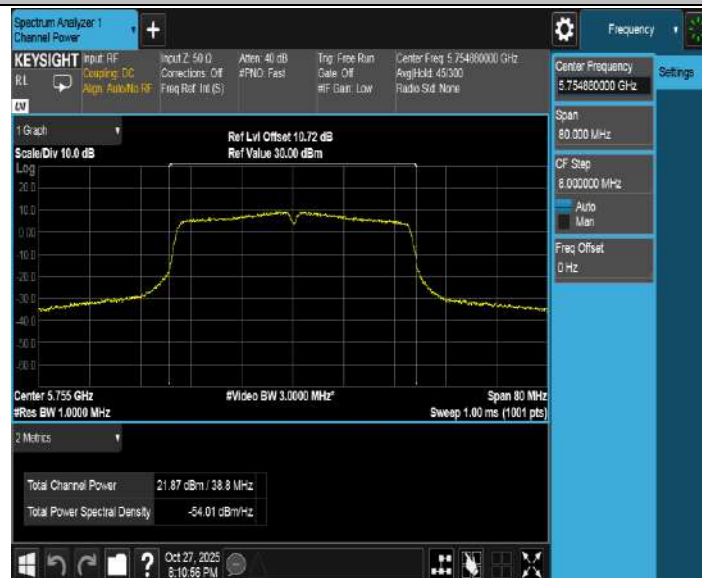
11AC40SiSO_Ant2_5230



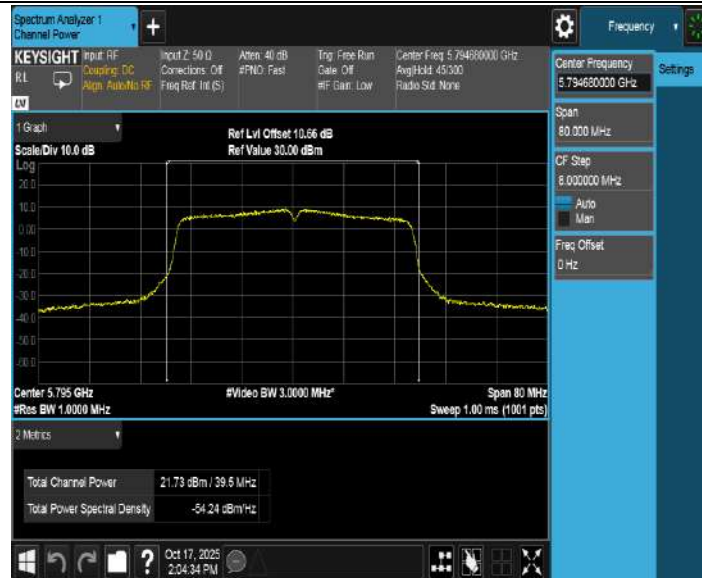
11AC40SiSO_Ant1_5755



11AC40SiSO_Ant2_5755



11AC40SiSO_Ant1_5795



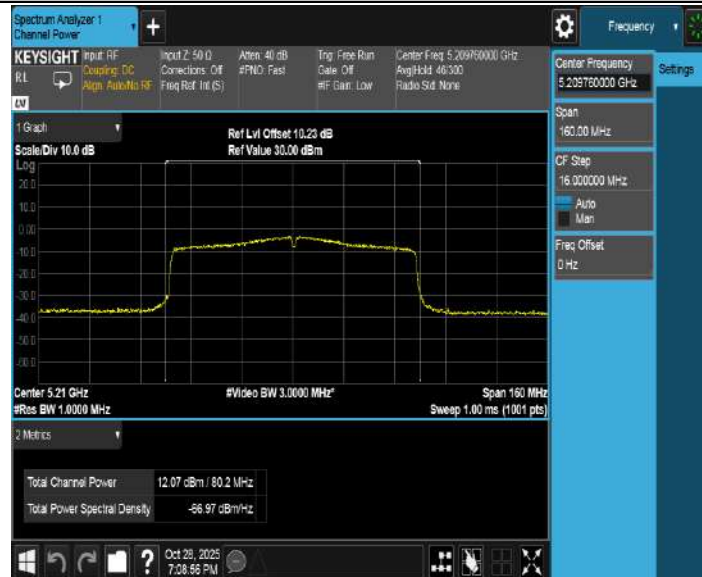
11AC40SiSO_Ant2_5795



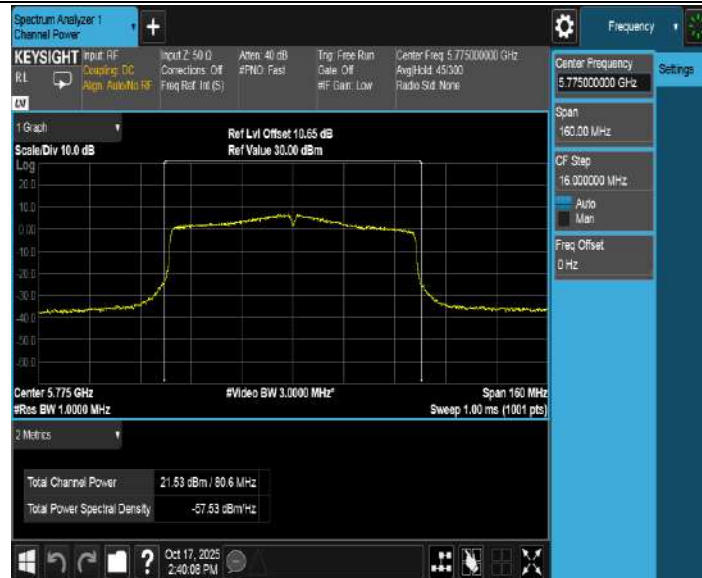
11AC80SiSO_Ant1_5210



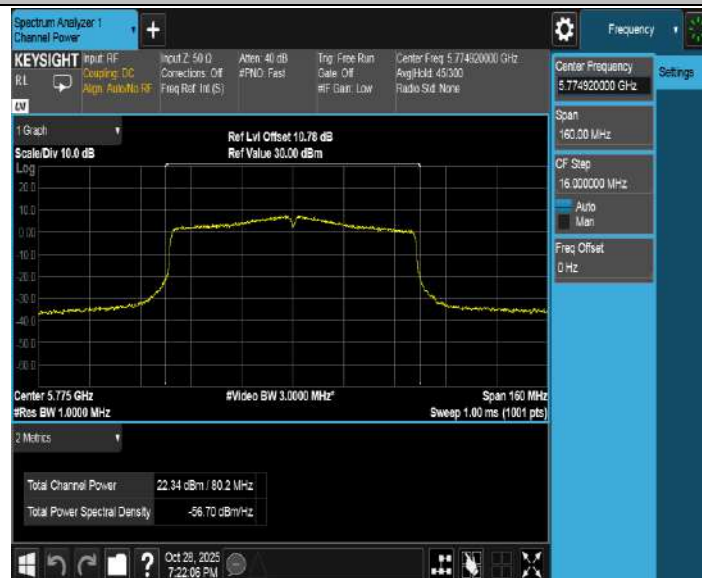
11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775



11AX20SISO_Ant1_5180



11AX20SISO_Ant2_5180



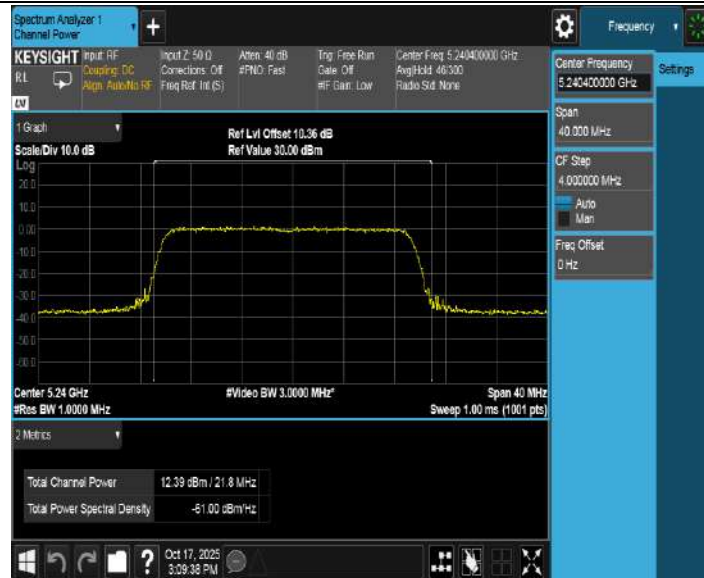
11AX20SISO_Ant1_5200



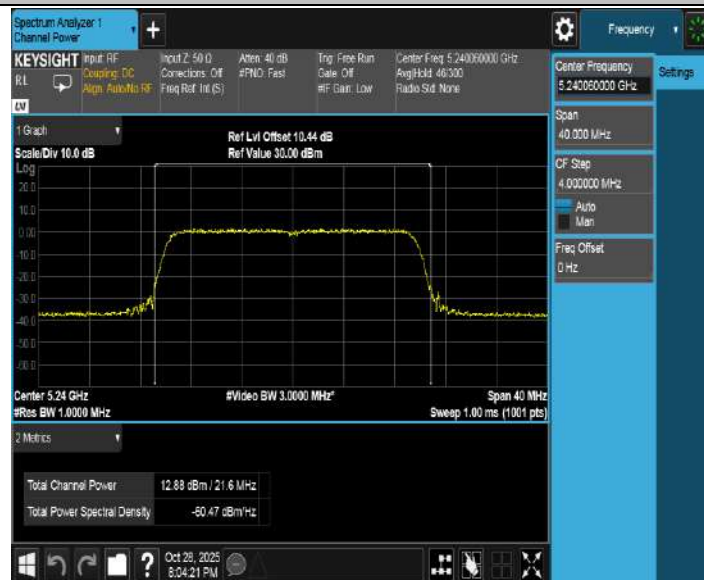
11AX20SISO_Ant2_5200



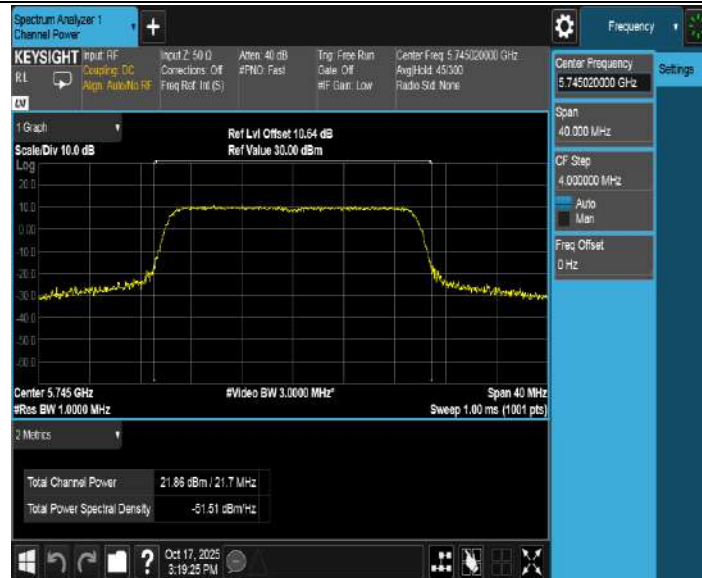
11AX20SISO_Ant1_5240



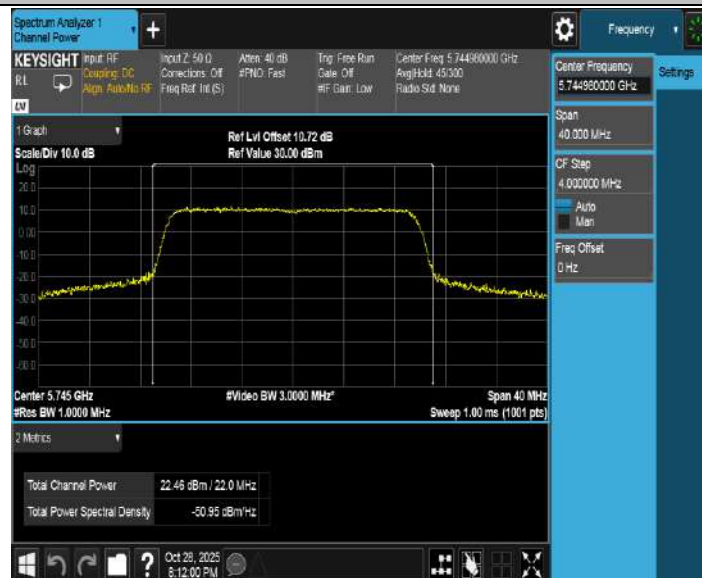
11AX20SISO_Ant2_5240



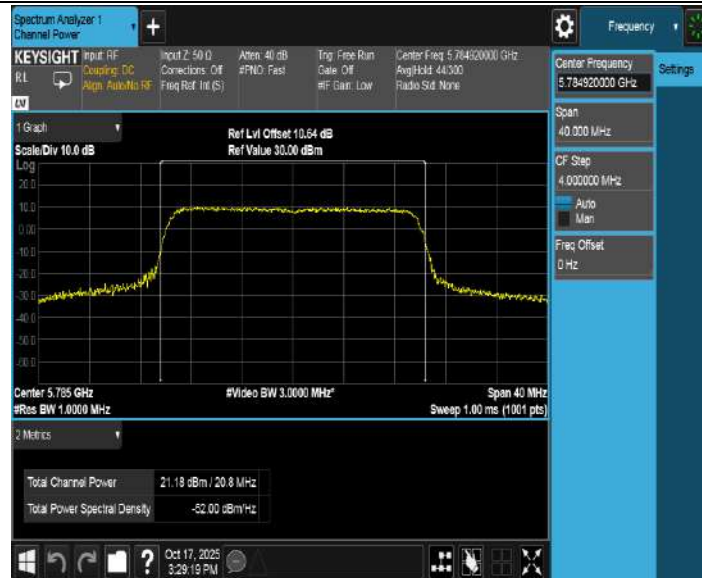
11AX20SISO_Ant1_5745



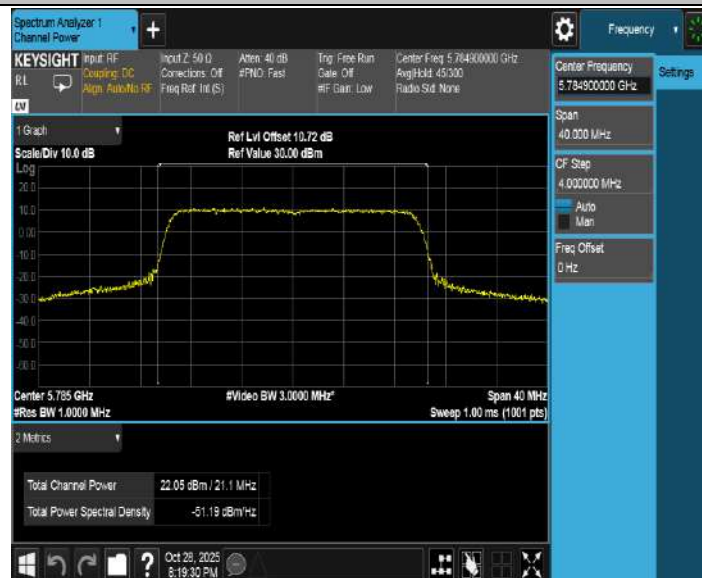
11AX20SISO_Ant2_5745



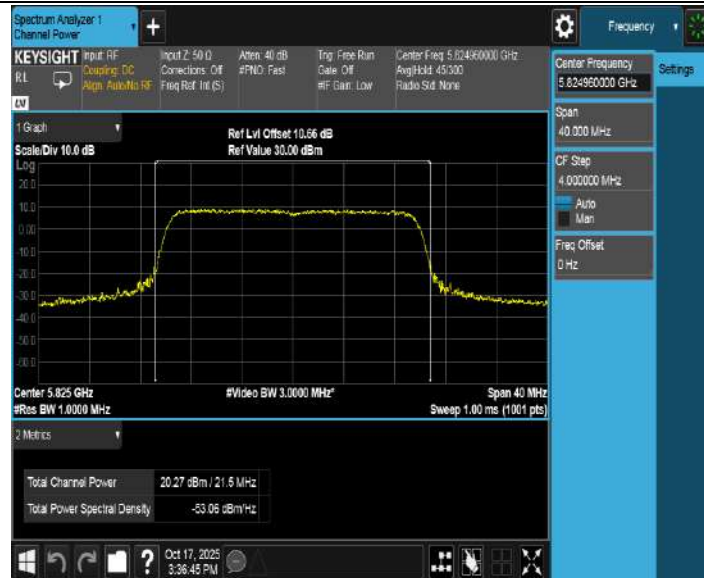
11AX20SISO_Ant1_5785



11AX20SISO_Ant2_5785



11AX20SISO_Ant1_5825



11AX20SISO_Ant2_5825



11AX40SISO_Ant1_5190



11AX40SISO_Ant2_5190



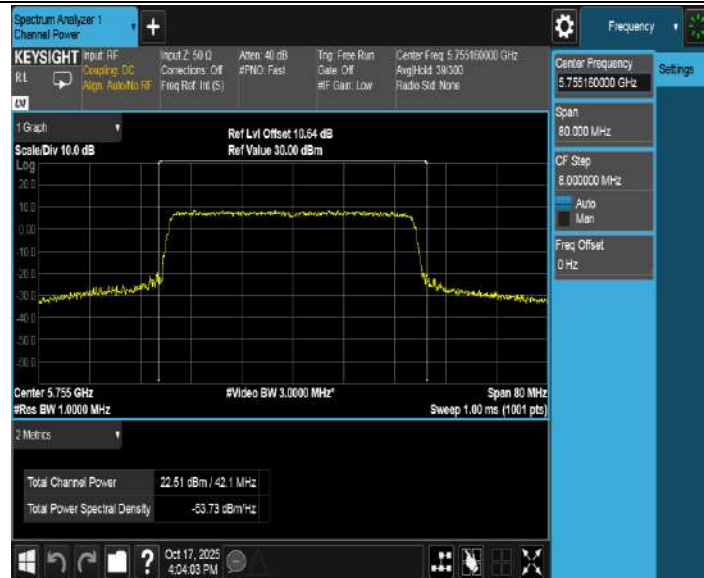
11AX40SISO_Ant1_5230



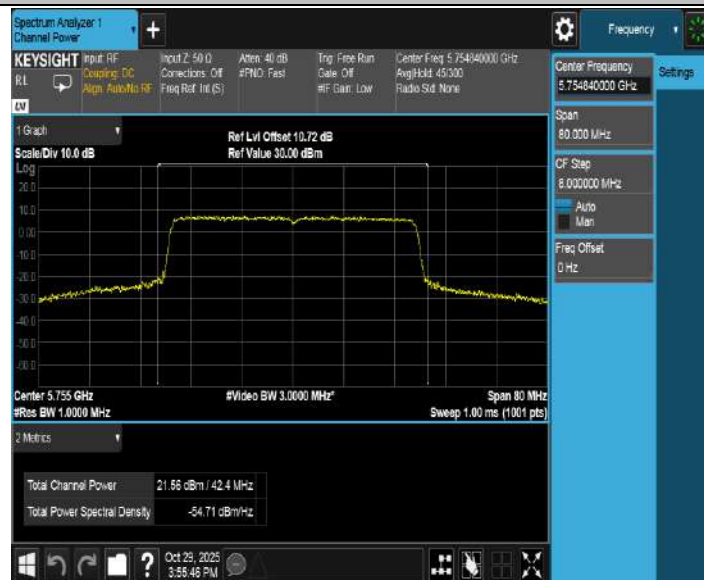
11AX40SISO_Ant2_5230



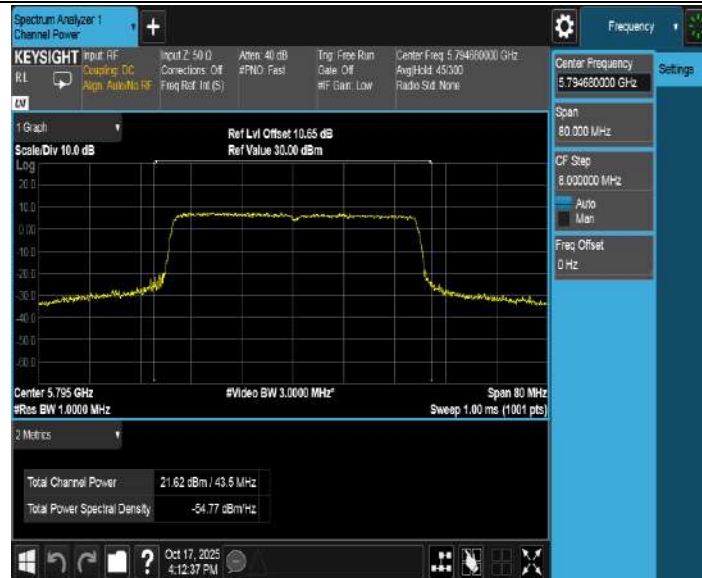
11AX40SISO_Ant1_5755



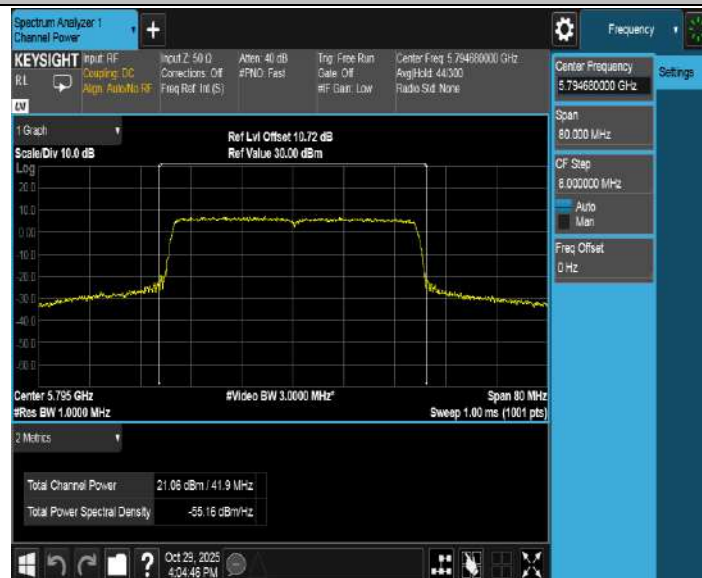
11AX40SISO_Ant2_5755



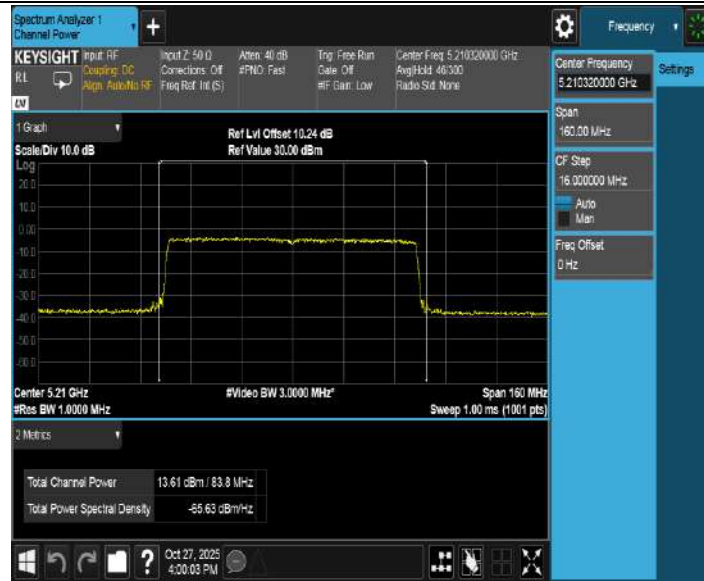
11AX40SISO_Ant1_5795



11AX40SISO_Ant2_5795



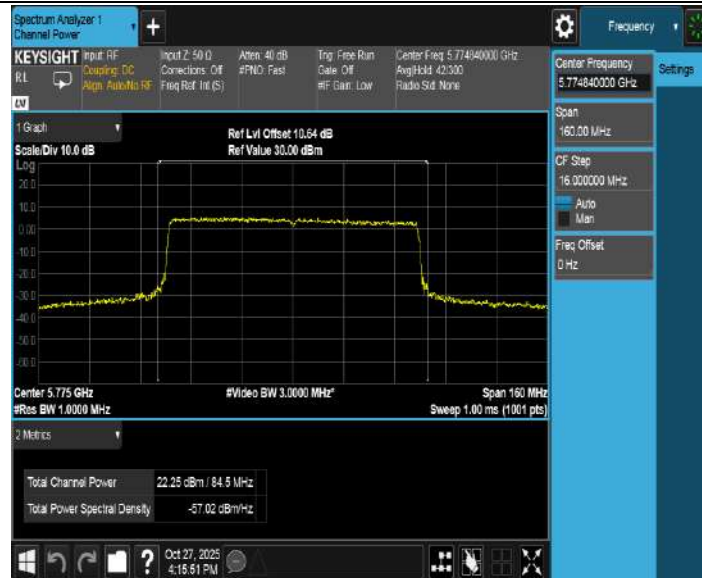
11AX80SISO_Ant1_5210



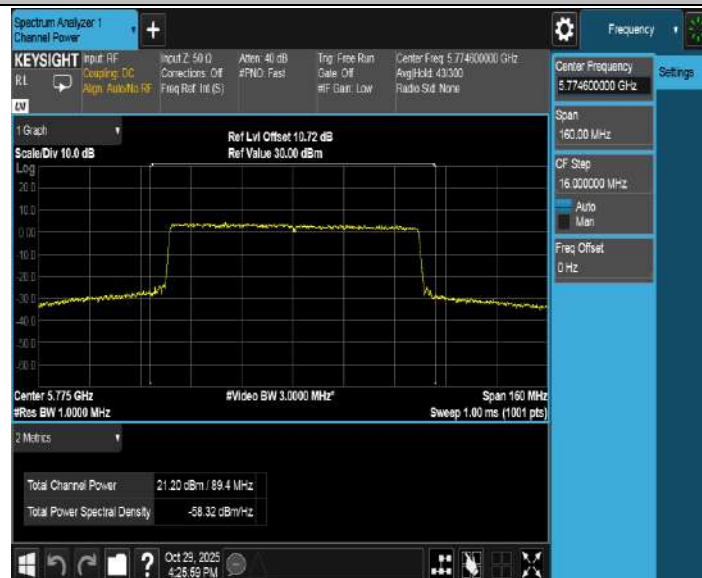
11AX80SISO_Ant2_5210



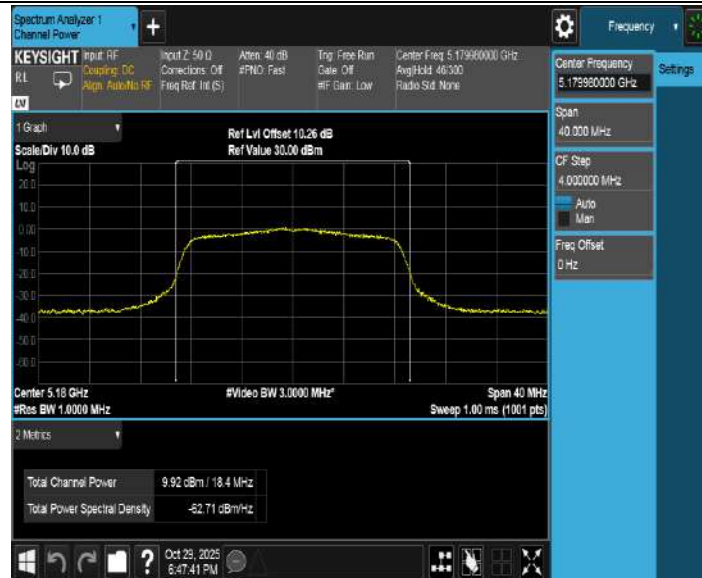
11AX80SISO_Ant1_5775



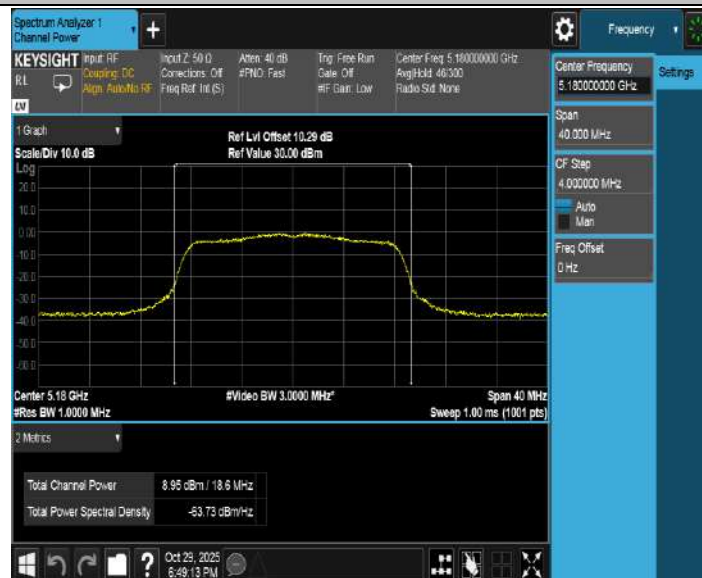
11AX80ISO_Ant2_5775



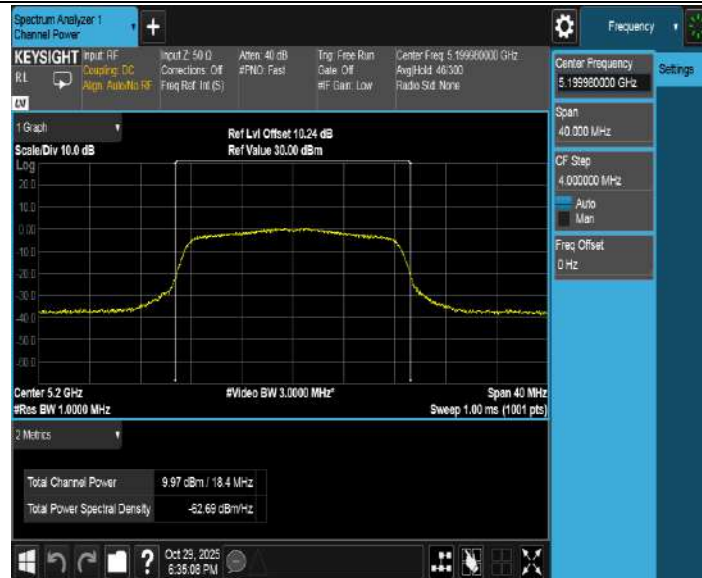
11A-CDD_Ant1_5180



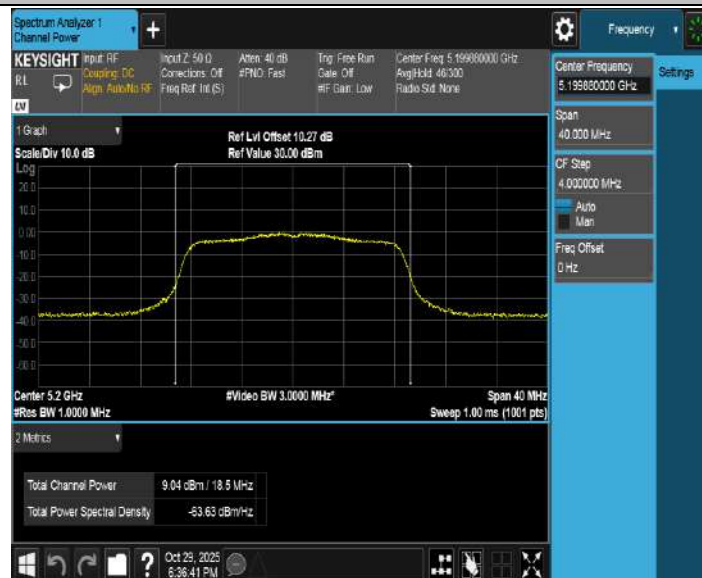
11A-CDD_Ant2_5180



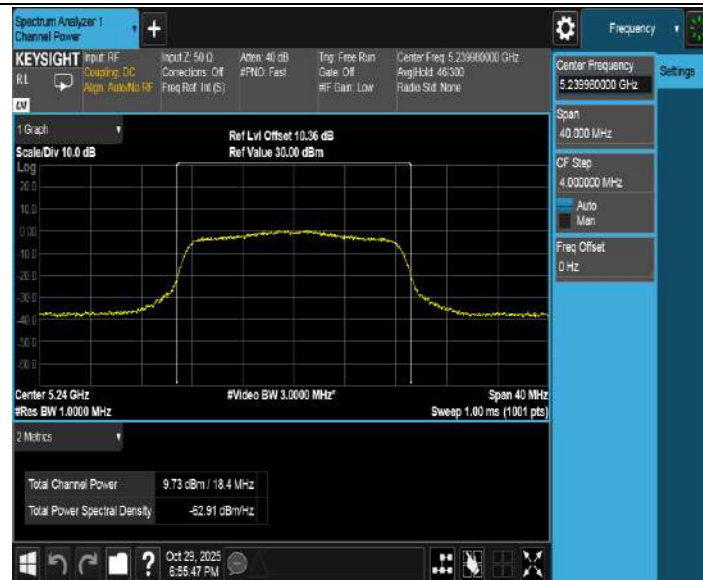
11A-CDD_Ant1_5200



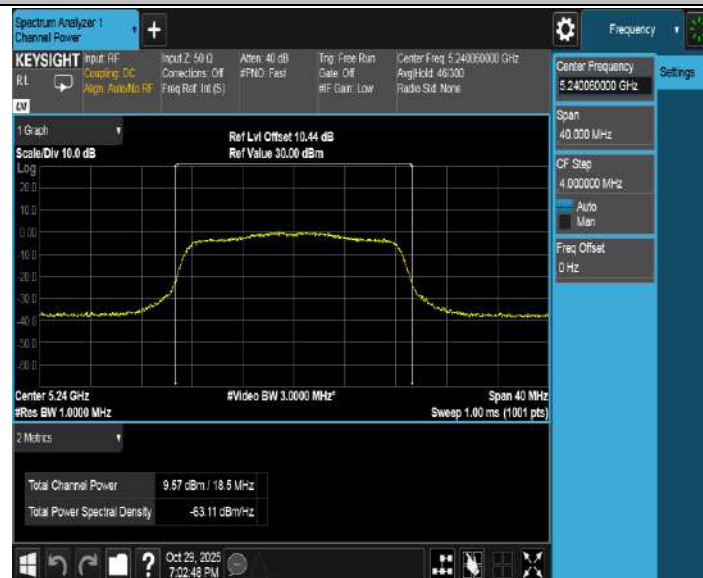
11A-CDD_Ant2_5200



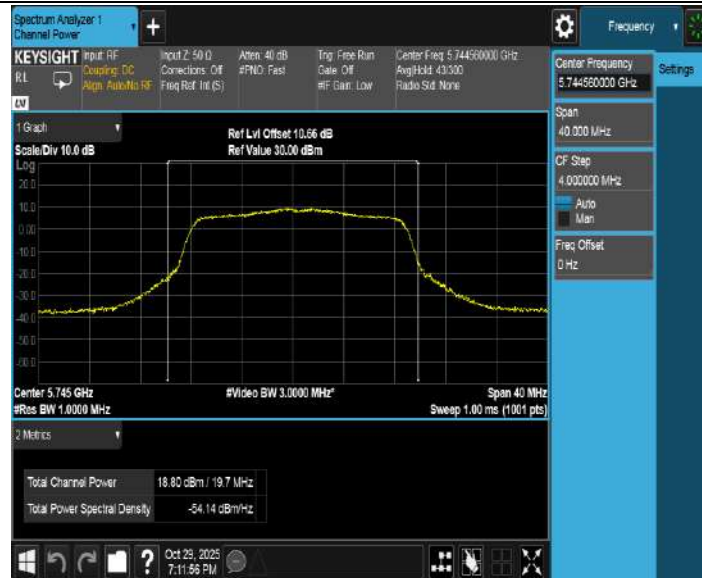
11A-CDD_Ant1_5240



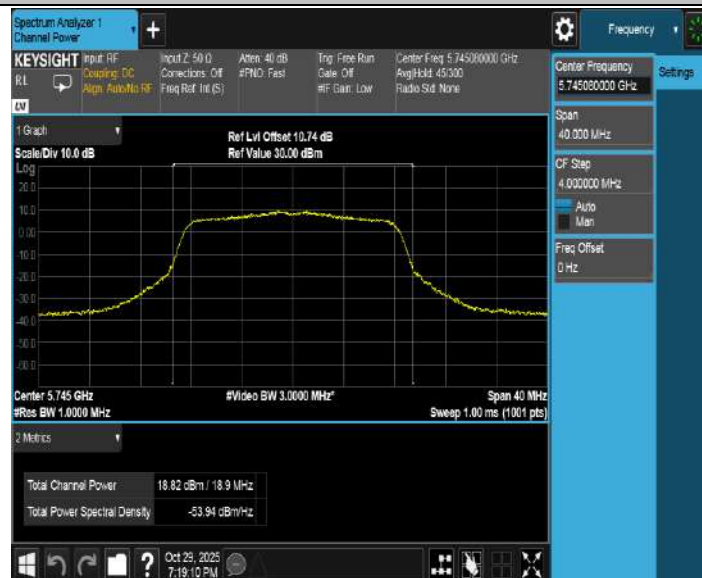
11A-CDD_Ant2_5240



11A-CDD_Ant1_5745



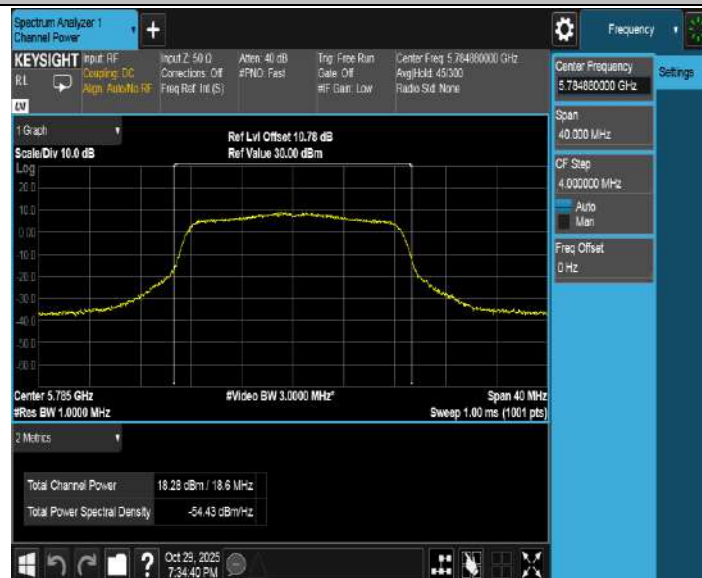
11A-CDD_Ant2_5745



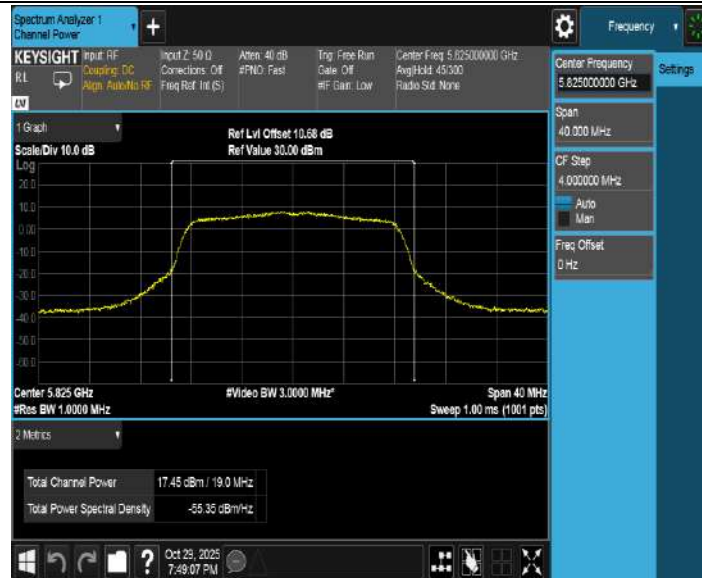
11A-CDD_Ant1_5785



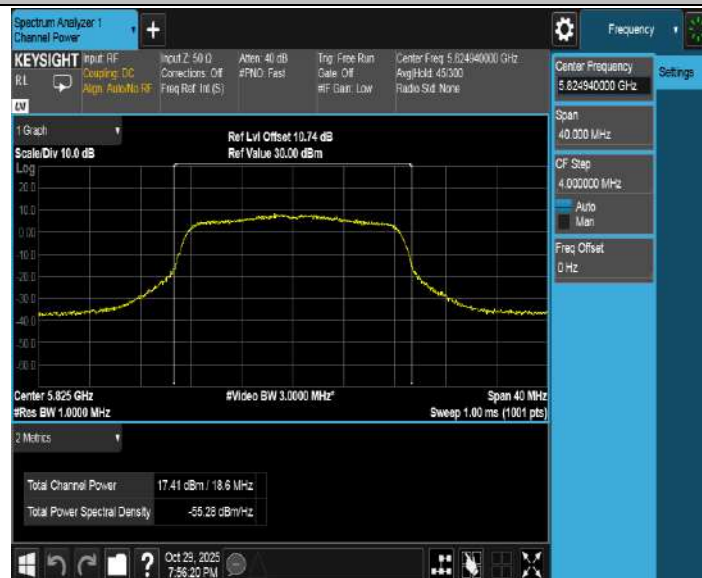
11A-CDD_Ant2_5785



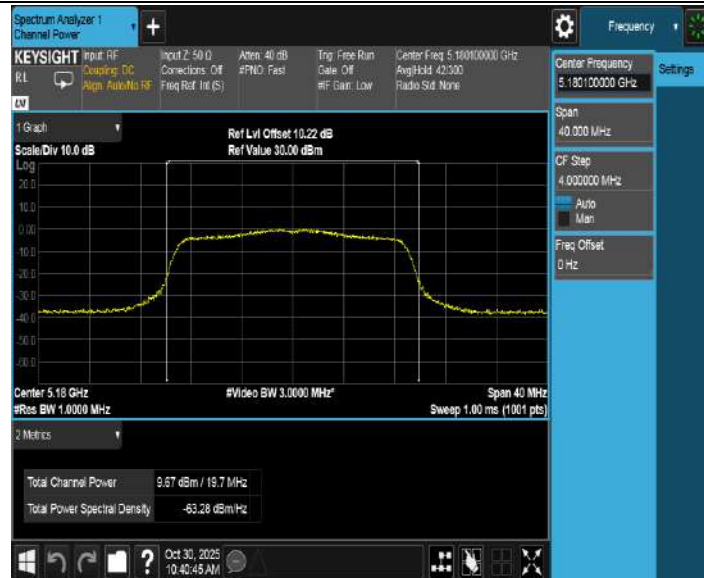
11A-CDD_Ant1_5825



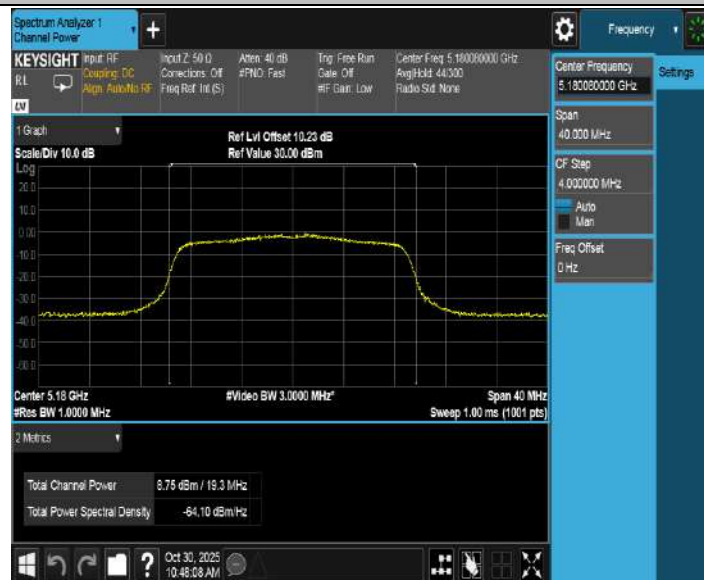
11A-CDD_Ant2_5825



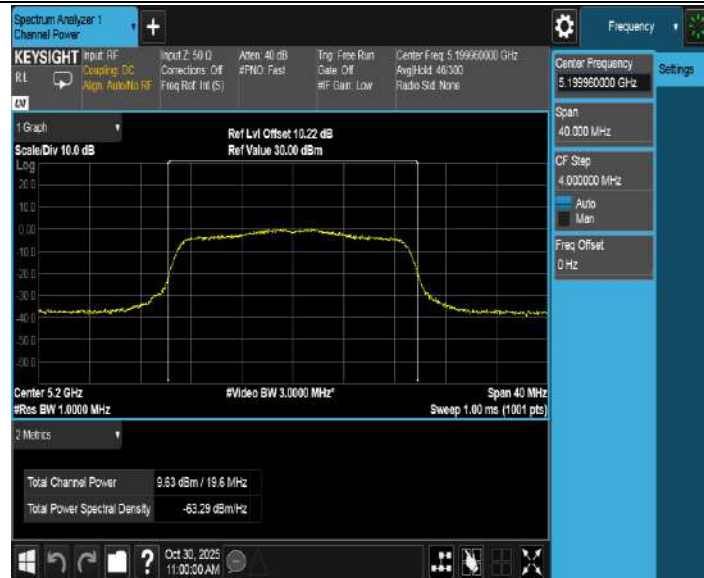
11N20MIMO_Ant1_5180



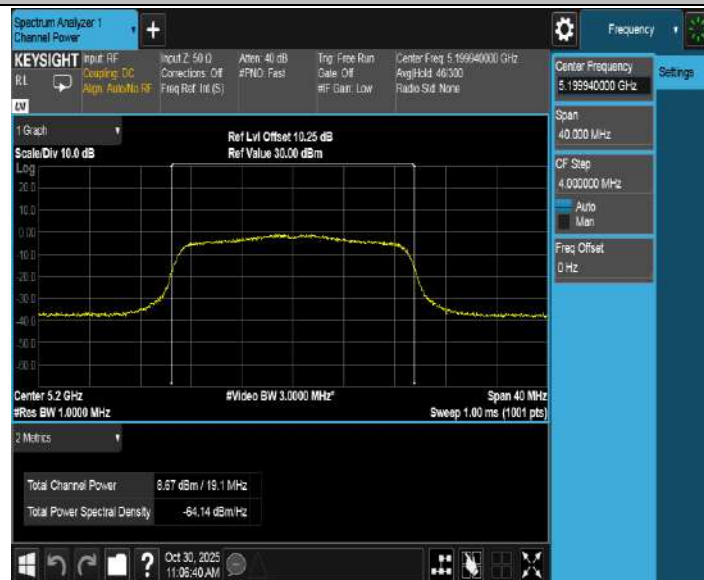
11N20MIMO_Ant2_5180



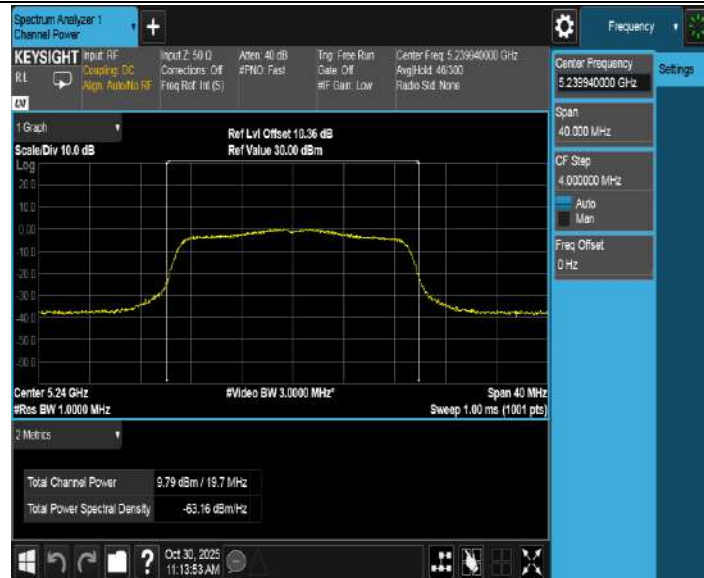
11N20MIMO_Ant1_5200



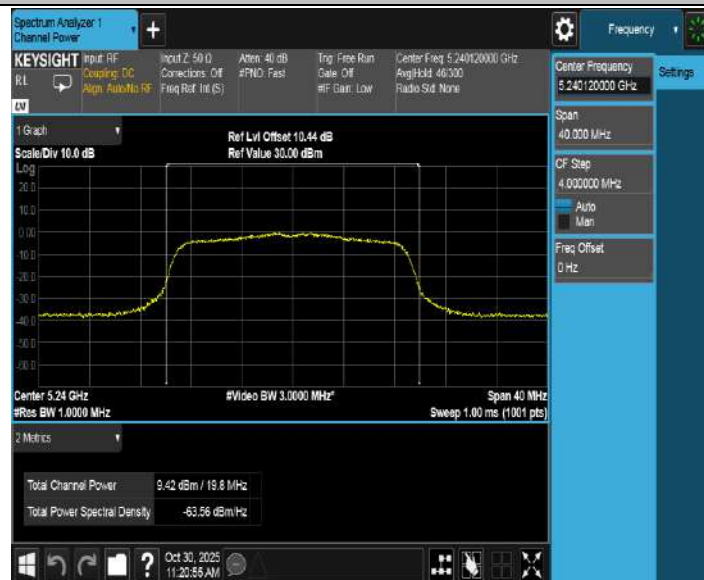
11N20MIMO_Ant2_5200



11N20MIMO_Ant1_5240



11N20MIMO_Ant2_5240



11N20MIMO_Ant1_5745



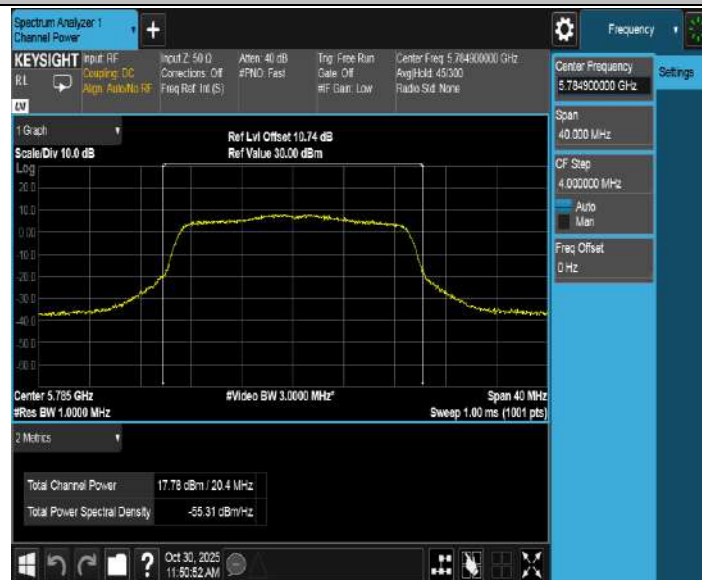
11N20MIMO_Ant2_5745



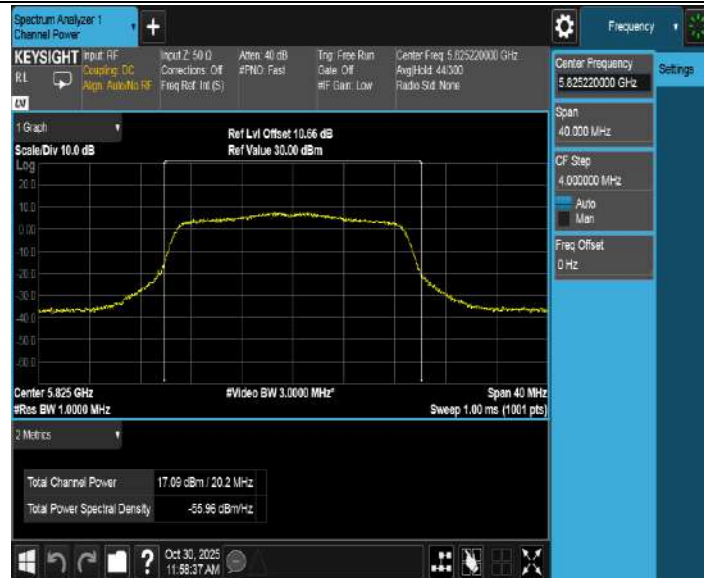
11N20MIMO_Ant1_5785



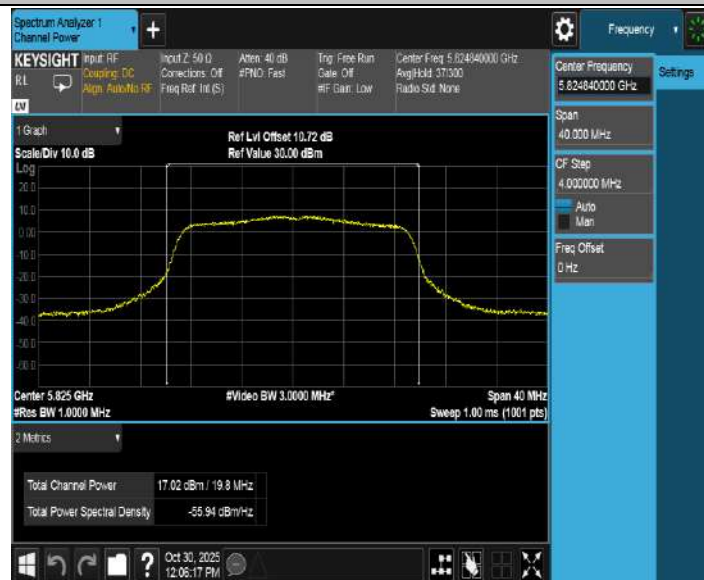
11N20MIMO_Ant2_5785



11N20MIMO_Ant1_5825



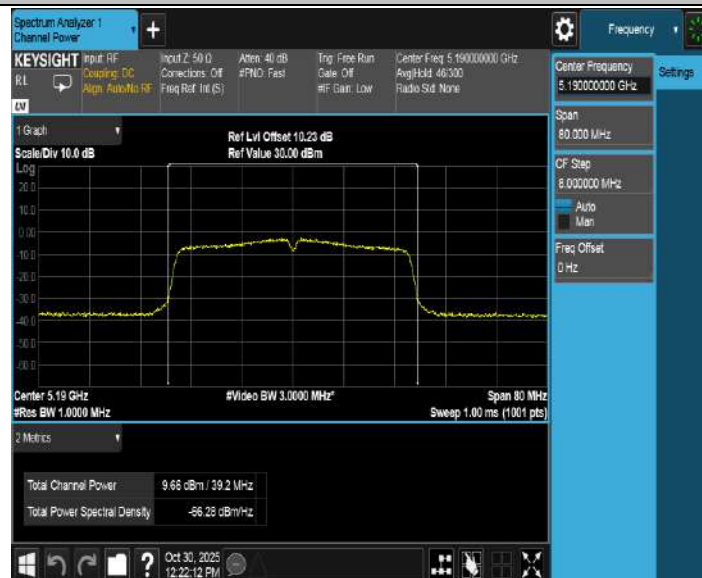
11N20MIMO_Ant2_5825



11N40MIMO_Ant1_5190



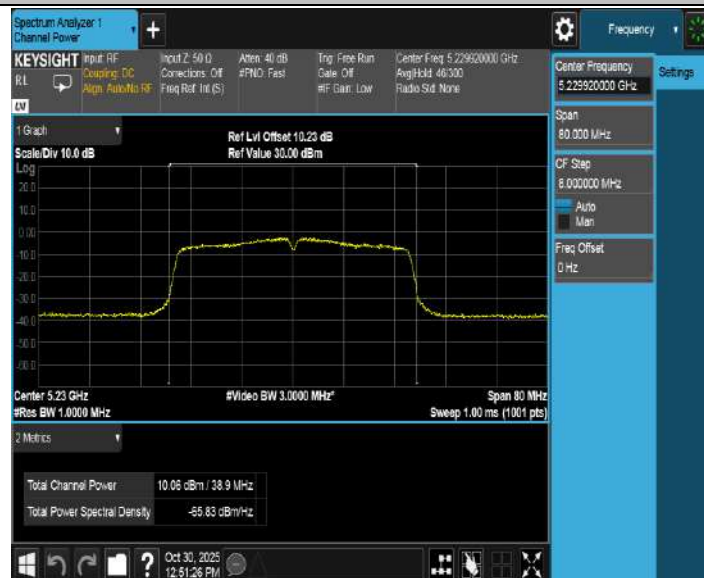
11N40MIMO_Ant2_5190



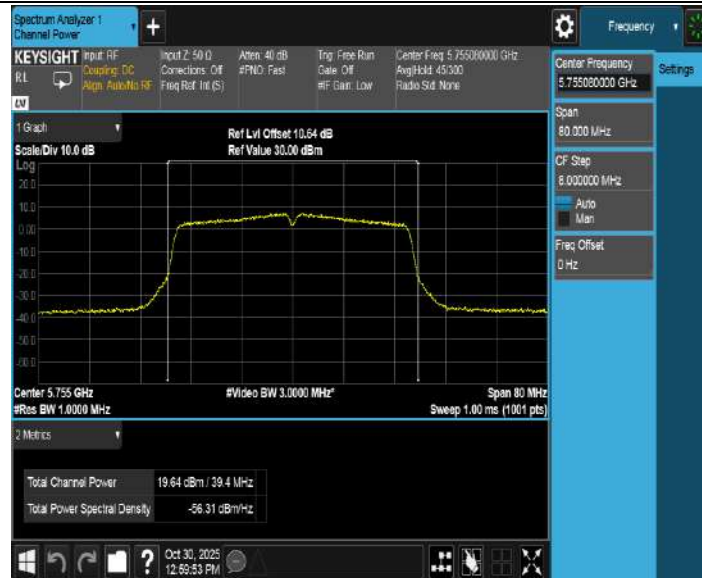
11N40MIMO_Ant1_5230



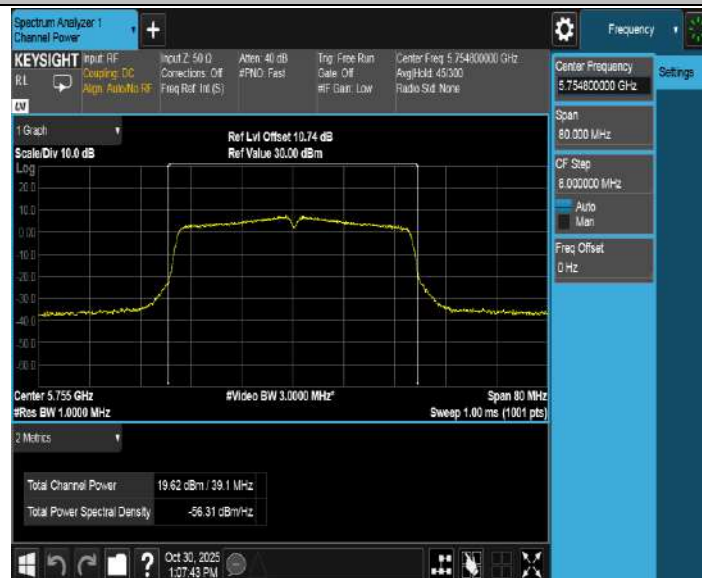
11N40MIMO_Ant2_5230



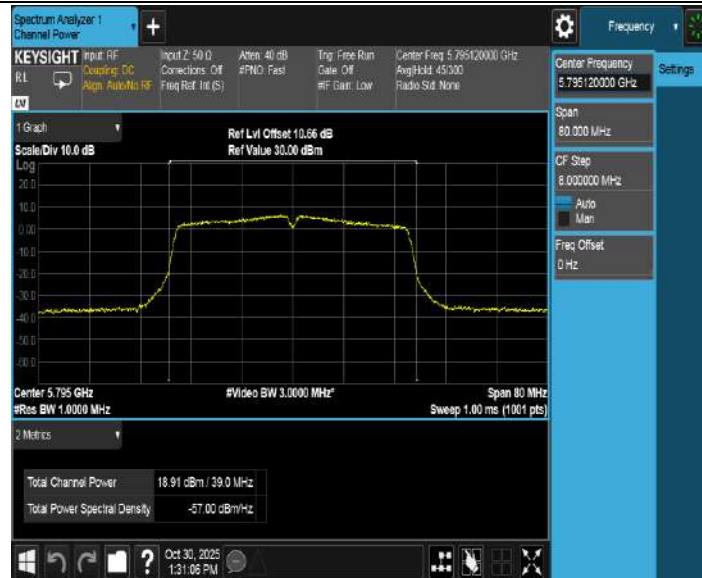
11N40MIMO_Ant1_5755



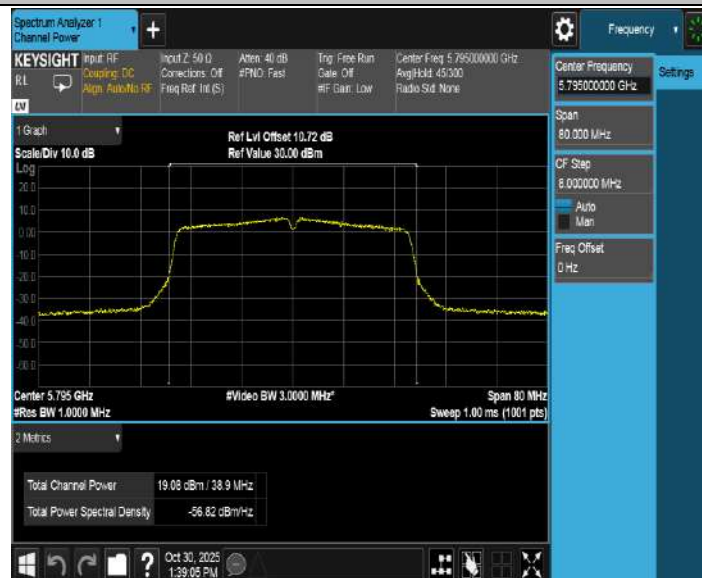
11N40MIMO_Ant2_5755



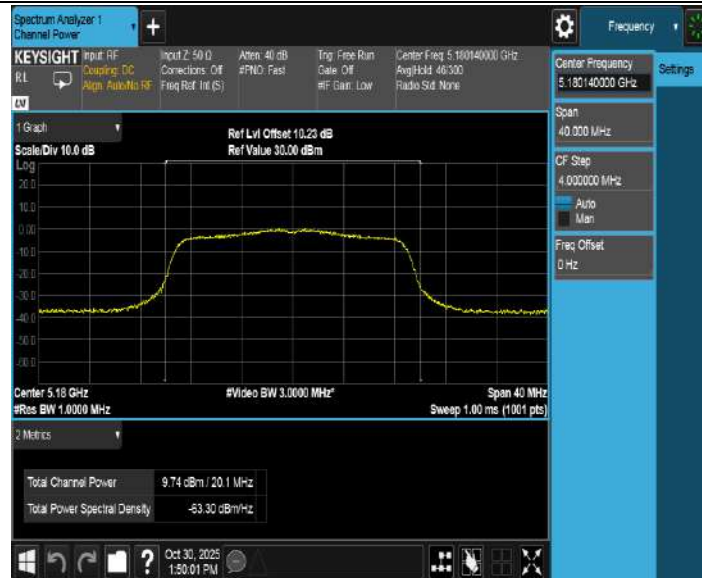
11N40MIMO_Ant1_5795



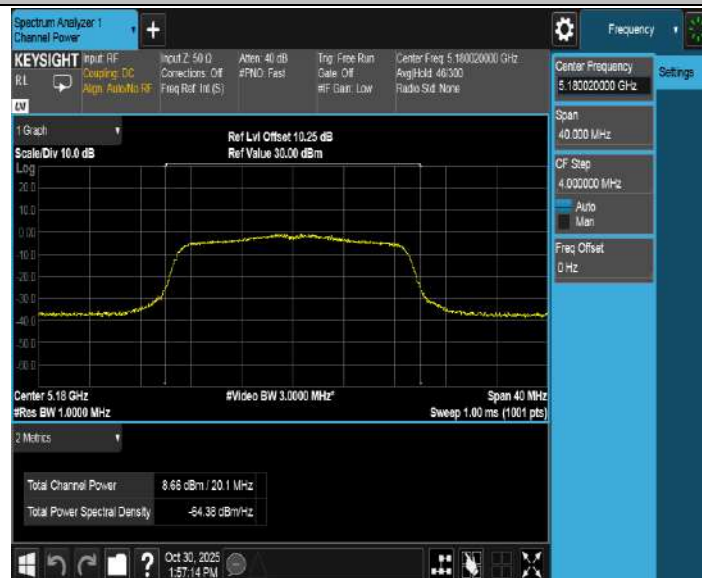
11N40MIMO_Ant2_5795



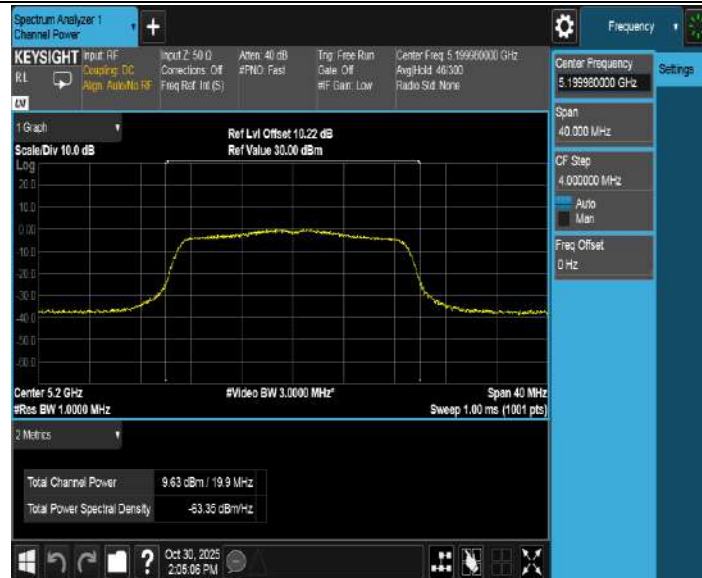
11AC20MIMO_Ant1_5180



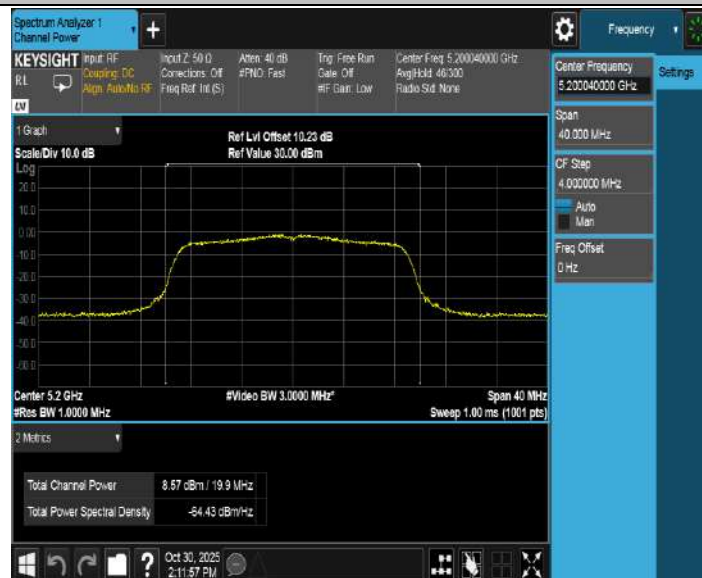
11AC20MIMO_Ant2_5180



11AC20MIMO_Ant1_5200



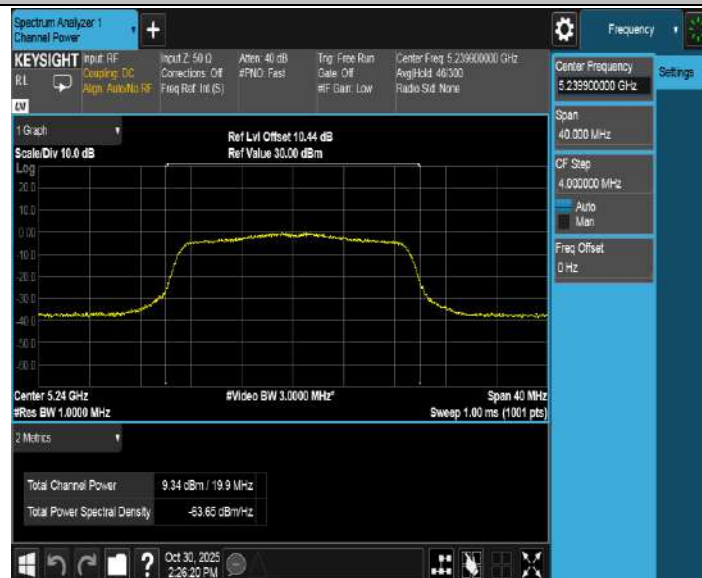
11AC20MIMO_Ant2_5200



11AC20MIMO_Ant1_5240



11AC20MIMO_Ant2_5240



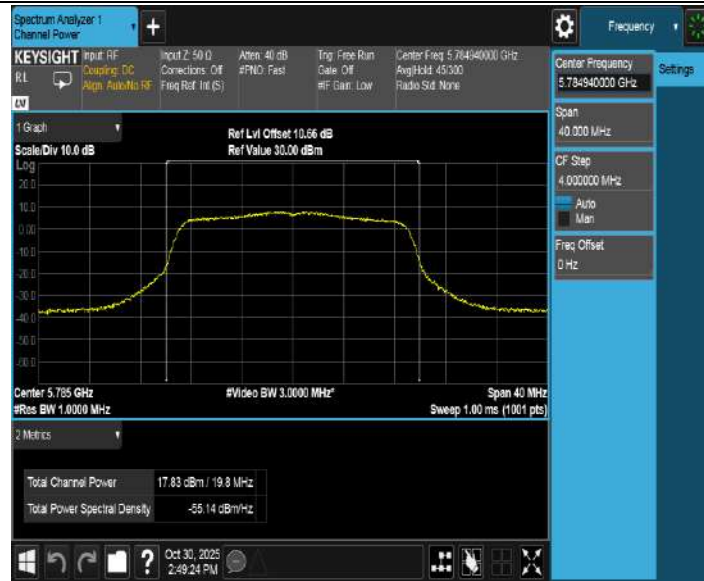
11AC20MIMO_Ant1_5745



11AC20MIMO_Ant2_5745



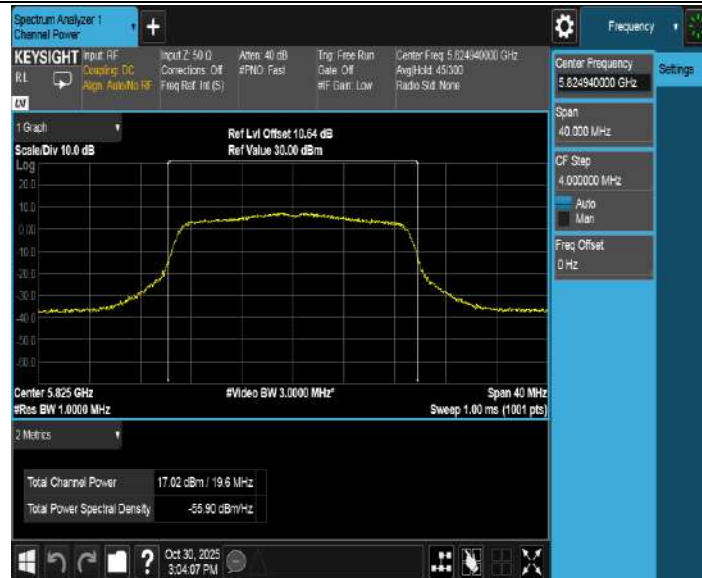
11AC20MIMO_Ant1_5785



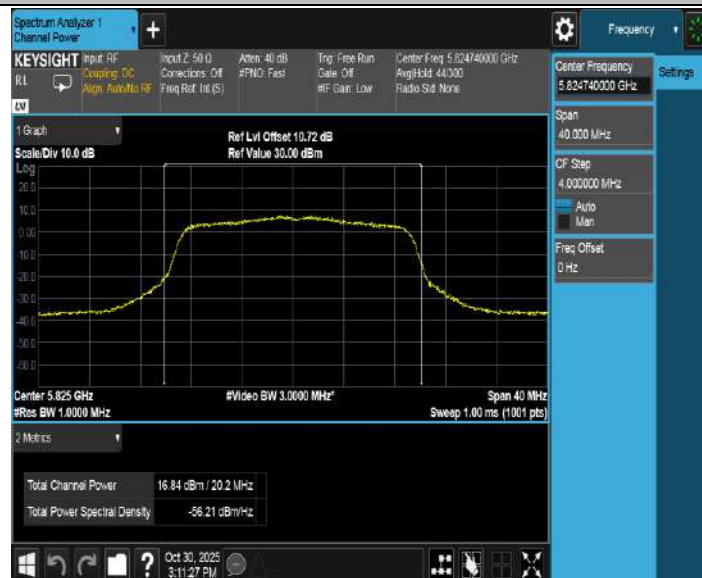
11AC20MIMO_Ant2_5785



11AC20MIMO_Ant1_5825



11AC20MIMO_Ant2_5825



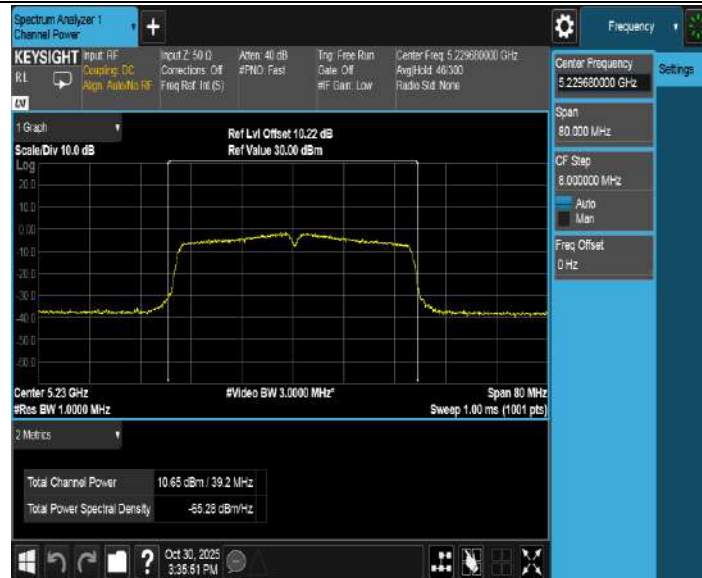
11AC40MIMO_Ant1_5190



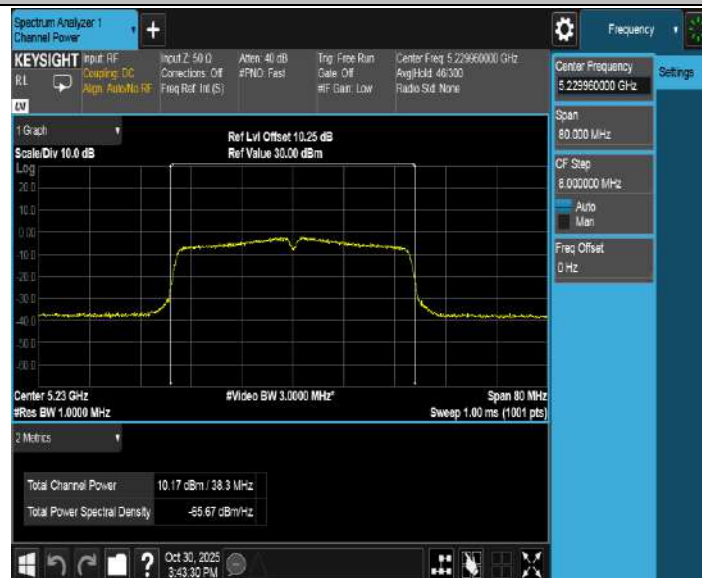
11AC40MIMO_Ant2_5190



11AC40MIMO_Ant1_5230



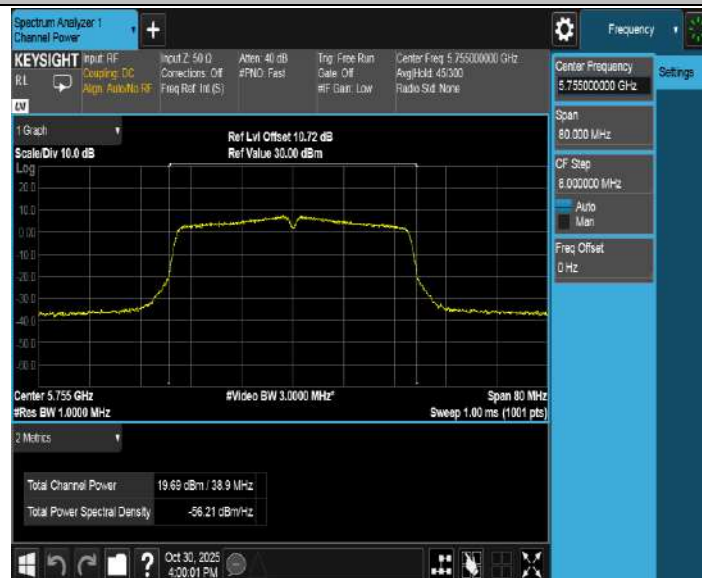
11AC40MIMO_Ant2_5230



11AC40MIMO_Ant1_5755



11AC40MIMO_Ant2_5755



11AC40MIMO_Ant1_5795