

Appendix for n41

Product Name: Tablet

Model No: MDT871

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	23.69	PC2	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	24.73	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	21.40	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	21.57	PC2	PASS
N41	30	10	DFT-16QAM	L	Outer_Full	22.54	PC2	PASS
N41	30	10	DFT-16QAM	L	Inner_Full	23.61	PC2	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Left	21.37	PC2	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Right	21.56	PC2	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	24.23	PC2	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	25.31	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	21.94	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	21.81	PC2	PASS
N41	30	10	DFT-16QAM	M	Outer_Full	23.24	PC2	PASS
N41	30	10	DFT-16QAM	M	Inner_Full	24.40	PC2	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Left	22.25	PC2	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Right	22.18	PC2	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	24.34	PC2	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	25.18	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	22.21	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	21.99	PC2	PASS
N41	30	10	DFT-16QAM	H	Outer_Full	23.31	PC2	PASS
N41	30	10	DFT-16QAM	H	Inner_Full	24.23	PC2	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Left	22.21	PC2	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Right	21.98	PC2	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	23.68	PC2	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	24.58	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	21.25	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	21.31	PC2	PASS
N41	30	15	DFT-16QAM	L	Outer_Full	22.62	PC2	PASS
N41	30	15	DFT-16QAM	L	Inner_Full	23.76	PC2	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Left	21.29	PC2	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Right	21.39	PC2	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	24.36	PC2	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	25.22	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	21.90	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	21.83	PC2	PASS

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N41	30	15	DFT-16QAM	M	Outer_Full	23.30	PC2	PASS
N41	30	15	DFT-16QAM	M	Inner_Full	24.31	PC2	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Left	22.39	PC2	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Right	22.28	PC2	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	24.38	PC2	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	25.29	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	22.39	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	22.11	PC2	PASS
N41	30	15	DFT-16QAM	H	Outer_Full	23.58	PC2	PASS
N41	30	15	DFT-16QAM	H	Inner_Full	24.30	PC2	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Left	22.27	PC2	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Right	21.94	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	23.63	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	24.77	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	21.32	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	21.25	PC2	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	22.88	PC2	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	23.81	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	21.27	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	21.23	PC2	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	24.34	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	25.37	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	21.99	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	21.95	PC2	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	23.38	PC2	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	24.35	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Left	21.81	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	21.73	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	24.46	PC2	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	25.43	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	22.06	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	21.83	PC2	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	23.43	PC2	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	24.45	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	22.23	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	21.94	PC2	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	23.59	PC2	PASS
N41	30	30	DFT-QPSK	L	Inner_Full	24.50	PC2	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	21.21	PC2	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Right	20.80	PC2	PASS
N41	30	30	DFT-16QAM	L	Outer_Full	22.60	PC2	PASS
N41	30	30	DFT-16QAM	L	Inner_Full	23.51	PC2	PASS

N41	30	30	DFT-16QAM	L	Edge_1RB_Left	20.85	PC2	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Right	20.47	PC2	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	24.28	PC2	PASS
N41	30	30	DFT-QPSK	M	Inner_Full	25.23	PC2	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	21.88	PC2	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Right	21.93	PC2	PASS
N41	30	30	DFT-16QAM	M	Outer_Full	23.37	PC2	PASS
N41	30	30	DFT-16QAM	M	Inner_Full	24.25	PC2	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Left	21.74	PC2	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Right	21.80	PC2	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	24.31	PC2	PASS
N41	30	30	DFT-QPSK	H	Inner_Full	25.34	PC2	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	22.08	PC2	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Right	21.89	PC2	PASS
N41	30	30	DFT-16QAM	H	Outer_Full	23.43	PC2	PASS
N41	30	30	DFT-16QAM	H	Inner_Full	24.34	PC2	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Left	21.98	PC2	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Right	21.83	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	23.53	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	24.30	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	21.13	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	20.93	PC2	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	22.61	PC2	PASS
N41	30	40	DFT-16QAM	L	Inner_Full	23.40	PC2	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Left	21.44	PC2	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Right	21.15	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	24.36	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	25.27	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	21.97	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	22.24	PC2	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	23.48	PC2	PASS
N41	30	40	DFT-16QAM	M	Inner_Full	24.36	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Left	21.89	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Right	22.06	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	24.36	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	25.36	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	22.07	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	21.83	PC2	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	23.54	PC2	PASS
N41	30	40	DFT-16QAM	H	Inner_Full	24.56	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Left	22.02	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Right	21.79	PC2	PASS

N41	30	50	DFT-QPSK	L	Outer_Full	23.47	PC2	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	24.11	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	21.17	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	21.23	PC2	PASS
N41	30	50	DFT-16QAM	L	Outer_Full	22.62	PC2	PASS
N41	30	50	DFT-16QAM	L	Inner_Full	23.22	PC2	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Left	21.09	PC2	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Right	21.17	PC2	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	24.55	PC2	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	25.40	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	21.54	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	21.99	PC2	PASS
N41	30	50	DFT-16QAM	M	Outer_Full	23.41	PC2	PASS
N41	30	50	DFT-16QAM	M	Inner_Full	24.46	PC2	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Left	21.71	PC2	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Right	22.18	PC2	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	24.34	PC2	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	25.38	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	22.04	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	21.91	PC2	PASS
N41	30	50	DFT-16QAM	H	Outer_Full	23.34	PC2	PASS
N41	30	50	DFT-16QAM	H	Inner_Full	24.46	PC2	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Left	21.75	PC2	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Right	21.61	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	23.47	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	24.04	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	21.10	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	21.31	PC2	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	22.62	PC2	PASS
N41	30	60	DFT-16QAM	L	Inner_Full	23.16	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Left	21.05	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Right	21.29	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	24.32	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	25.37	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	21.44	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	21.92	PC2	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	23.45	PC2	PASS
N41	30	60	DFT-16QAM	M	Inner_Full	24.38	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Left	21.36	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Right	21.89	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	24.35	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	25.40	PC2	PASS

N41	30	60	DFT-QPSK	H	Edge_1RB_Left	21.74	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	21.78	PC2	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	23.40	PC2	PASS
N41	30	60	DFT-16QAM	H	Inner_Full	24.47	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Left	22.01	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Right	22.08	PC2	PASS
N41	30	70	DFT-QPSK	L	Outer_Full	23.65	PC2	PASS
N41	30	70	DFT-QPSK	L	Inner_Full	24.30	PC2	PASS
N41	30	70	DFT-QPSK	L	Edge_1RB_Left	21.38	PC2	PASS
N41	30	70	DFT-QPSK	L	Edge_1RB_Right	21.69	PC2	PASS
N41	30	70	DFT-16QAM	L	Outer_Full	22.78	PC2	PASS
N41	30	70	DFT-16QAM	L	Inner_Full	23.39	PC2	PASS
N41	30	70	DFT-16QAM	L	Edge_1RB_Left	21.40	PC2	PASS
N41	30	70	DFT-16QAM	L	Edge_1RB_Right	21.81	PC2	PASS
N41	30	70	DFT-QPSK	M	Outer_Full	24.43	PC2	PASS
N41	30	70	DFT-QPSK	M	Inner_Full	25.50	PC2	PASS
N41	30	70	DFT-QPSK	M	Edge_1RB_Left	21.43	PC2	PASS
N41	30	70	DFT-QPSK	M	Edge_1RB_Right	21.93	PC2	PASS
N41	30	70	DFT-16QAM	M	Outer_Full	23.43	PC2	PASS
N41	30	70	DFT-16QAM	M	Inner_Full	24.49	PC2	PASS
N41	30	70	DFT-16QAM	M	Edge_1RB_Left	21.38	PC2	PASS
N41	30	70	DFT-16QAM	M	Edge_1RB_Right	21.84	PC2	PASS
N41	30	70	DFT-QPSK	H	Outer_Full	24.38	PC2	PASS
N41	30	70	DFT-QPSK	H	Inner_Full	25.43	PC2	PASS
N41	30	70	DFT-QPSK	H	Edge_1RB_Left	21.93	PC2	PASS
N41	30	70	DFT-QPSK	H	Edge_1RB_Right	21.89	PC2	PASS
N41	30	70	DFT-16QAM	H	Outer_Full	23.44	PC2	PASS
N41	30	70	DFT-16QAM	H	Inner_Full	24.47	PC2	PASS
N41	30	70	DFT-16QAM	H	Edge_1RB_Left	21.85	PC2	PASS
N41	30	70	DFT-16QAM	H	Edge_1RB_Right	21.78	PC2	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	23.95	PC2	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	24.32	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	21.28	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	21.86	PC2	PASS
N41	30	80	DFT-16QAM	L	Outer_Full	23.00	PC2	PASS
N41	30	80	DFT-16QAM	L	Inner_Full	23.49	PC2	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Left	21.40	PC2	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Right	21.98	PC2	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	24.37	PC2	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	25.41	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	21.41	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	21.89	PC2	PASS

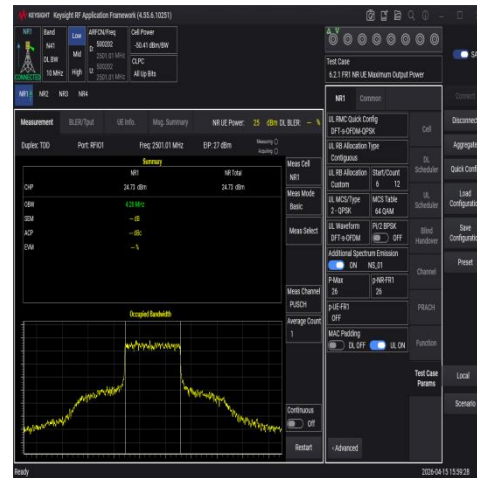
N41	30	80	DFT-16QAM	M	Outer_Full	23.30	PC2	PASS
N41	30	80	DFT-16QAM	M	Inner_Full	24.47	PC2	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Left	21.10	PC2	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Right	21.63	PC2	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	24.27	PC2	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	25.42	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	22.08	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	21.97	PC2	PASS
N41	30	80	DFT-16QAM	H	Outer_Full	23.42	PC2	PASS
N41	30	80	DFT-16QAM	H	Inner_Full	24.46	PC2	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Left	22.01	PC2	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Right	21.90	PC2	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	24.07	PC2	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	24.50	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	21.30	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	22.03	PC2	PASS
N41	30	90	DFT-16QAM	L	Outer_Full	23.06	PC2	PASS
N41	30	90	DFT-16QAM	L	Inner_Full	23.64	PC2	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Left	21.27	PC2	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Right	21.97	PC2	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	24.32	PC2	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	25.46	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	21.30	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	21.91	PC2	PASS
N41	30	90	DFT-16QAM	M	Outer_Full	23.30	PC2	PASS
N41	30	90	DFT-16QAM	M	Inner_Full	24.62	PC2	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Left	21.66	PC2	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Right	22.24	PC2	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	24.31	PC2	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	25.37	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	21.96	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	22.00	PC2	PASS
N41	30	90	DFT-16QAM	H	Outer_Full	23.49	PC2	PASS
N41	30	90	DFT-16QAM	H	Inner_Full	24.49	PC2	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Left	21.89	PC2	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Right	21.93	PC2	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	24.44	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	25.14	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	21.53	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	22.17	PC2	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	23.55	PC2	PASS
N41	30	100	DFT-16QAM	L	Inner_Full	24.12	PC2	PASS

N41	30	100	DFT-16QAM	L	Edge_1RB_Left	21.48	PC2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Right	22.13	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	24.19	PC2	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	25.51	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	21.10	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	21.87	PC2	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	23.27	PC2	PASS
N41	30	100	DFT-16QAM	M	Inner_Full	24.45	PC2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Left	21.02	PC2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Right	21.81	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	24.41	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	25.29	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	21.71	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	21.73	PC2	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	23.46	PC2	PASS
N41	30	100	DFT-16QAM	H	Inner_Full	24.36	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Left	21.89	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Right	21.88	PC2	PASS

Test Graphs



NTNV-N41-PC2-30-10-L-DFT-QPSK-Outer_Full-P
ASS



NTNV-N41-PC2-30-10-L-DFT-QPSK-Inner_Full-P
ASS



NTNV-N41-PC2-30-10-L-DFT-QPSK-Edge_1RB_
Left-PASS



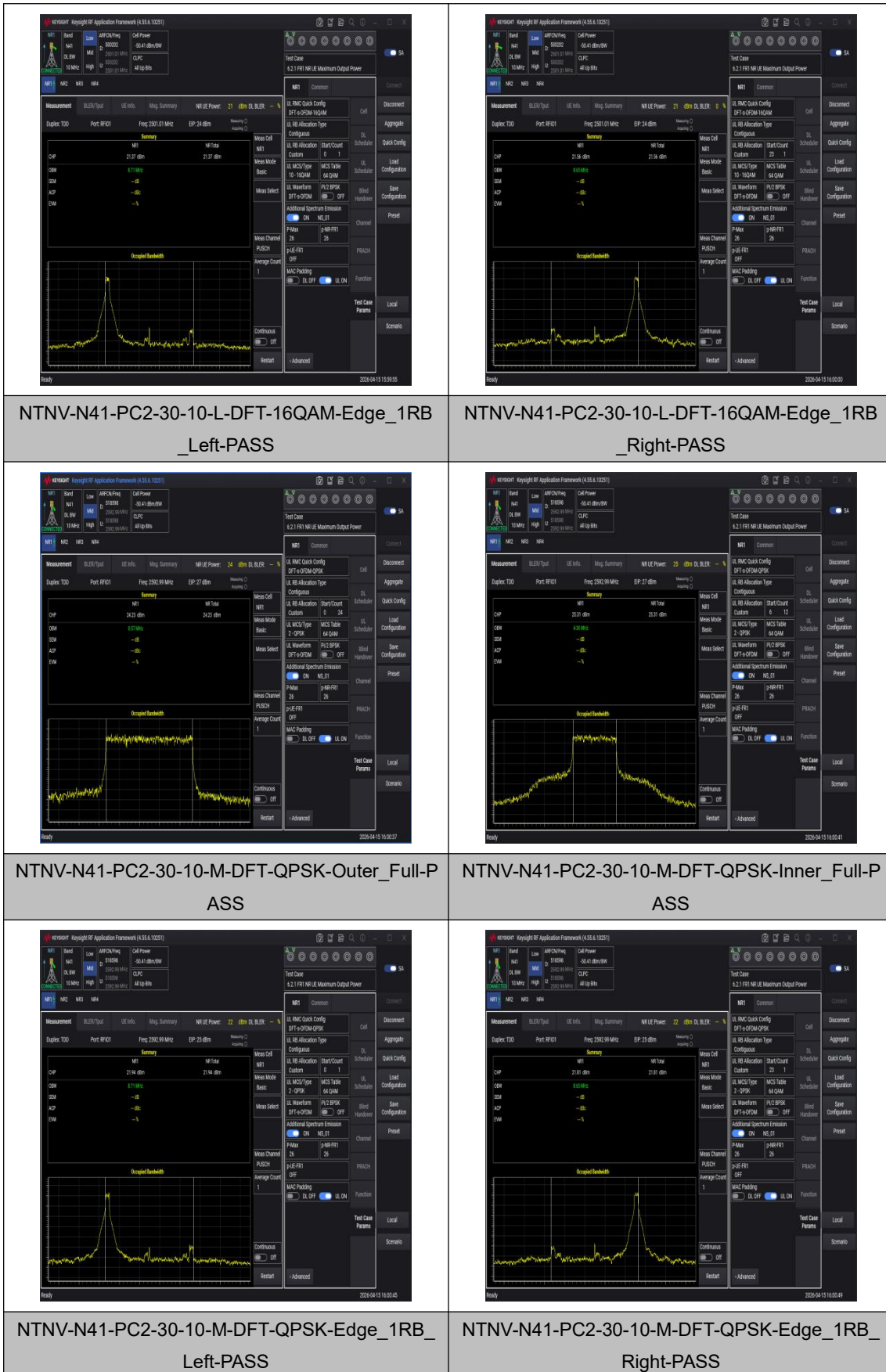
NTNV-N41-PC2-30-10-L-DFT-QPSK-Edge_1RB_
Right-PASS

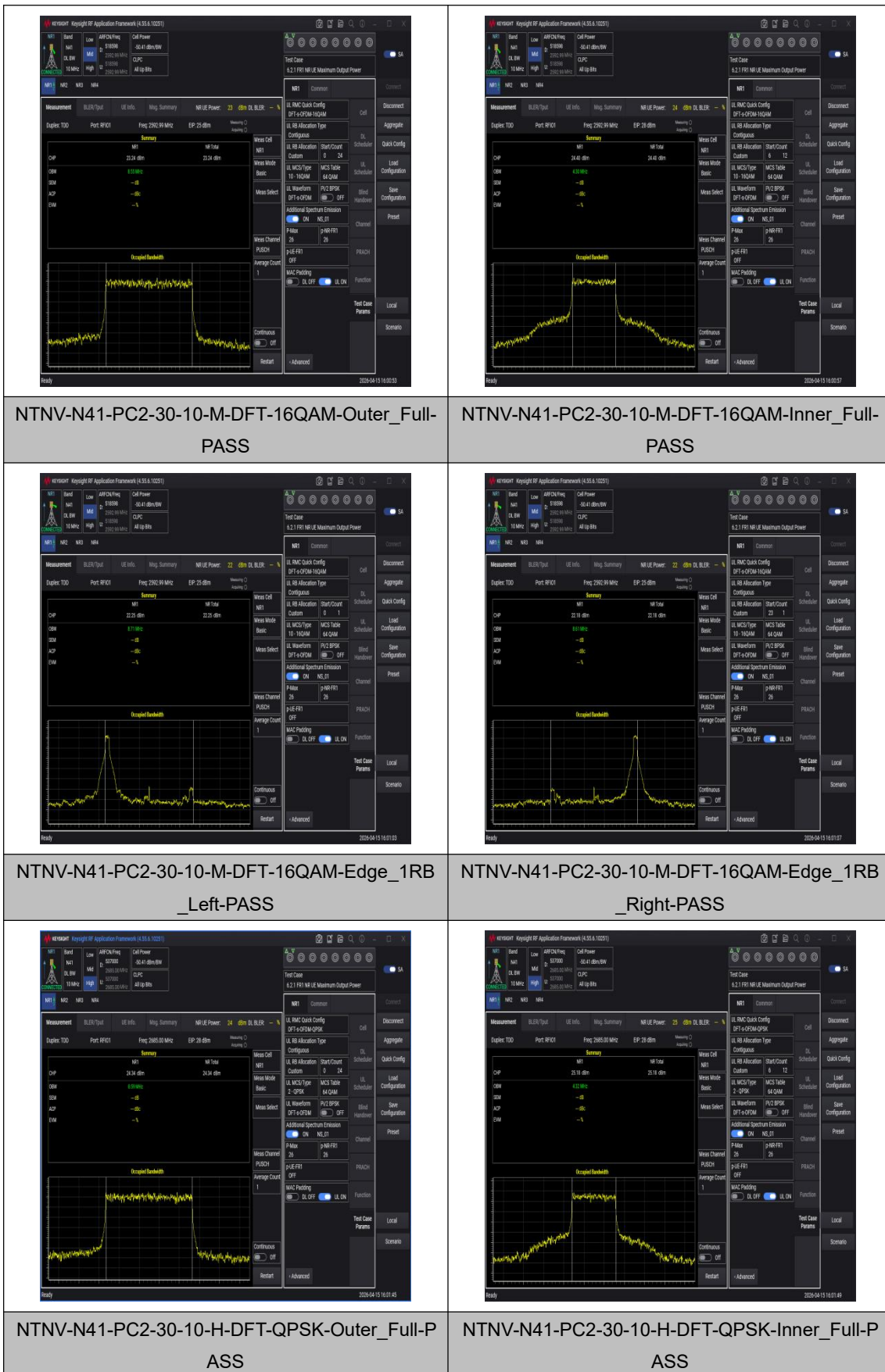


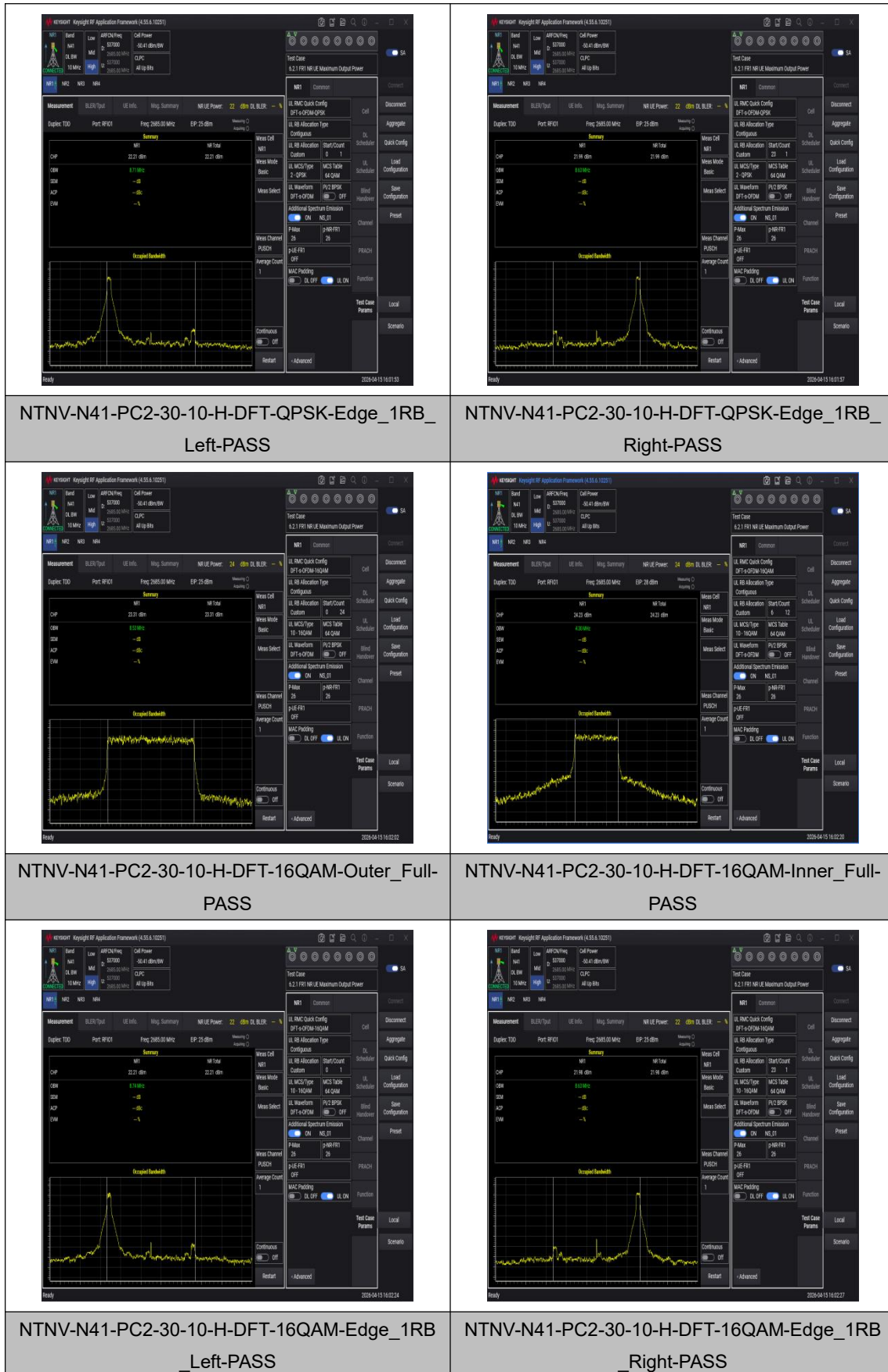
NTNV-N41-PC2-30-10-L-DFT-16QAM-Outer_Full-
PASS

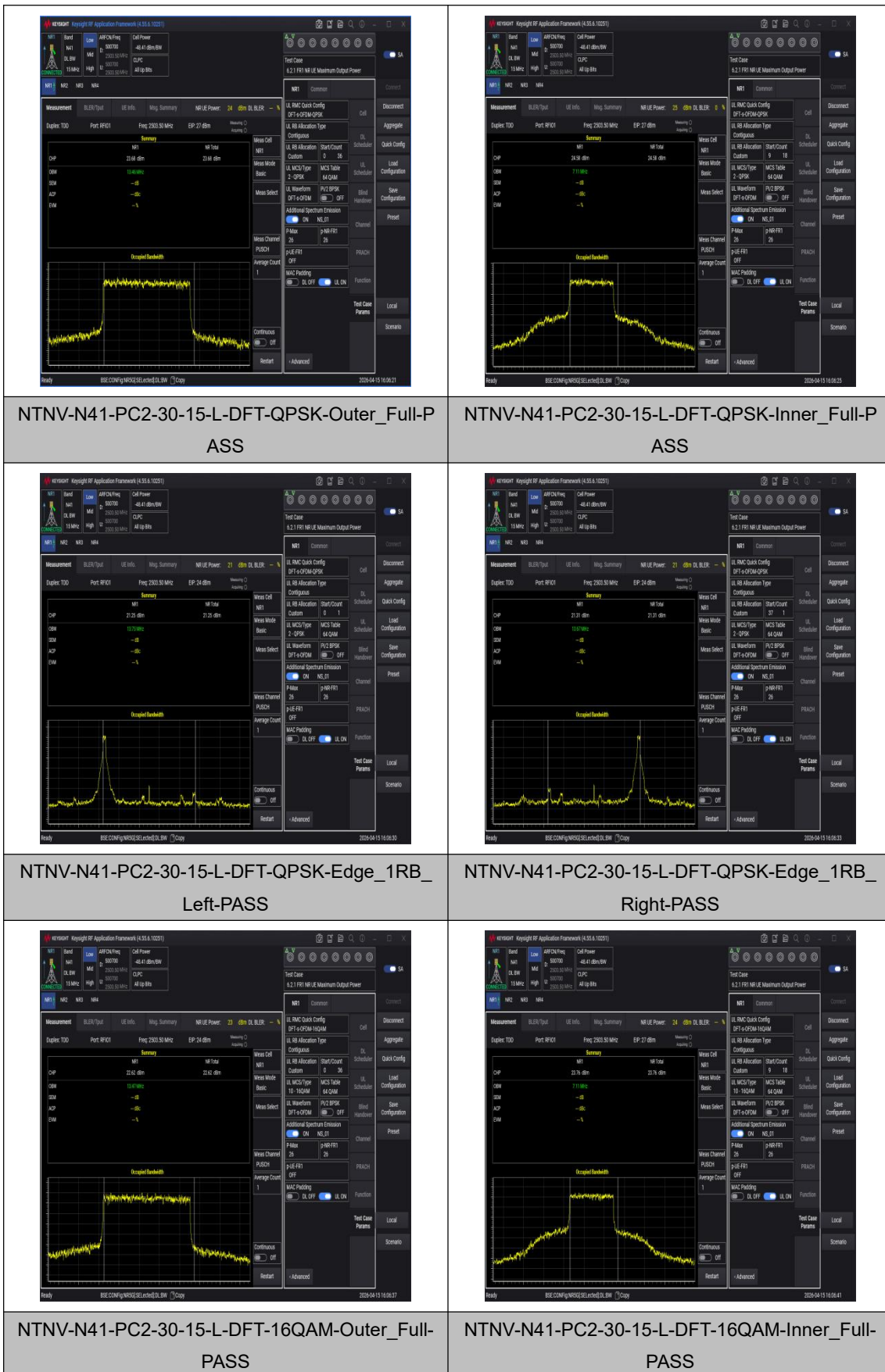


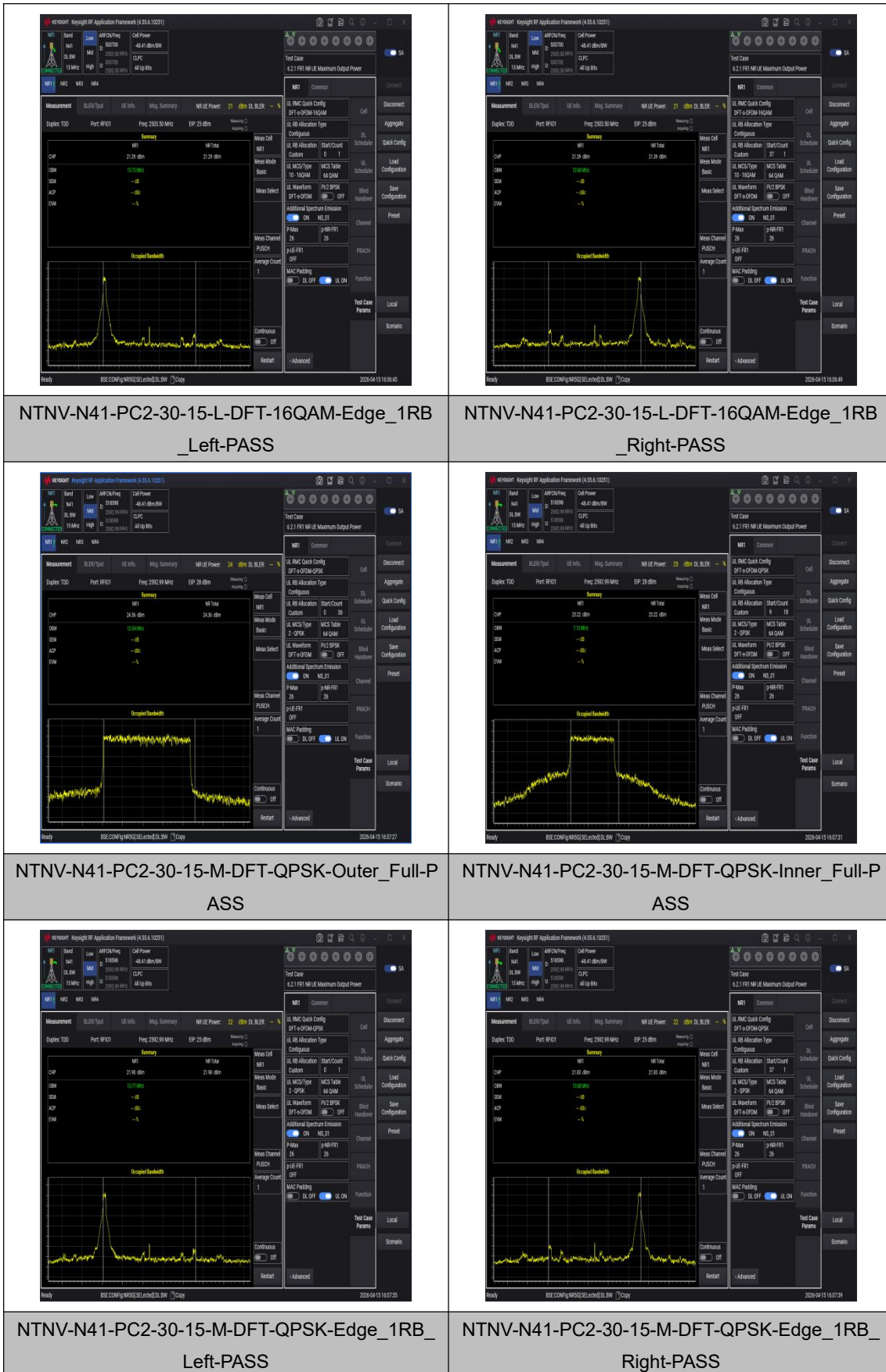
NTNV-N41-PC2-30-10-L-DFT-16QAM-Inner_Full-
PASS

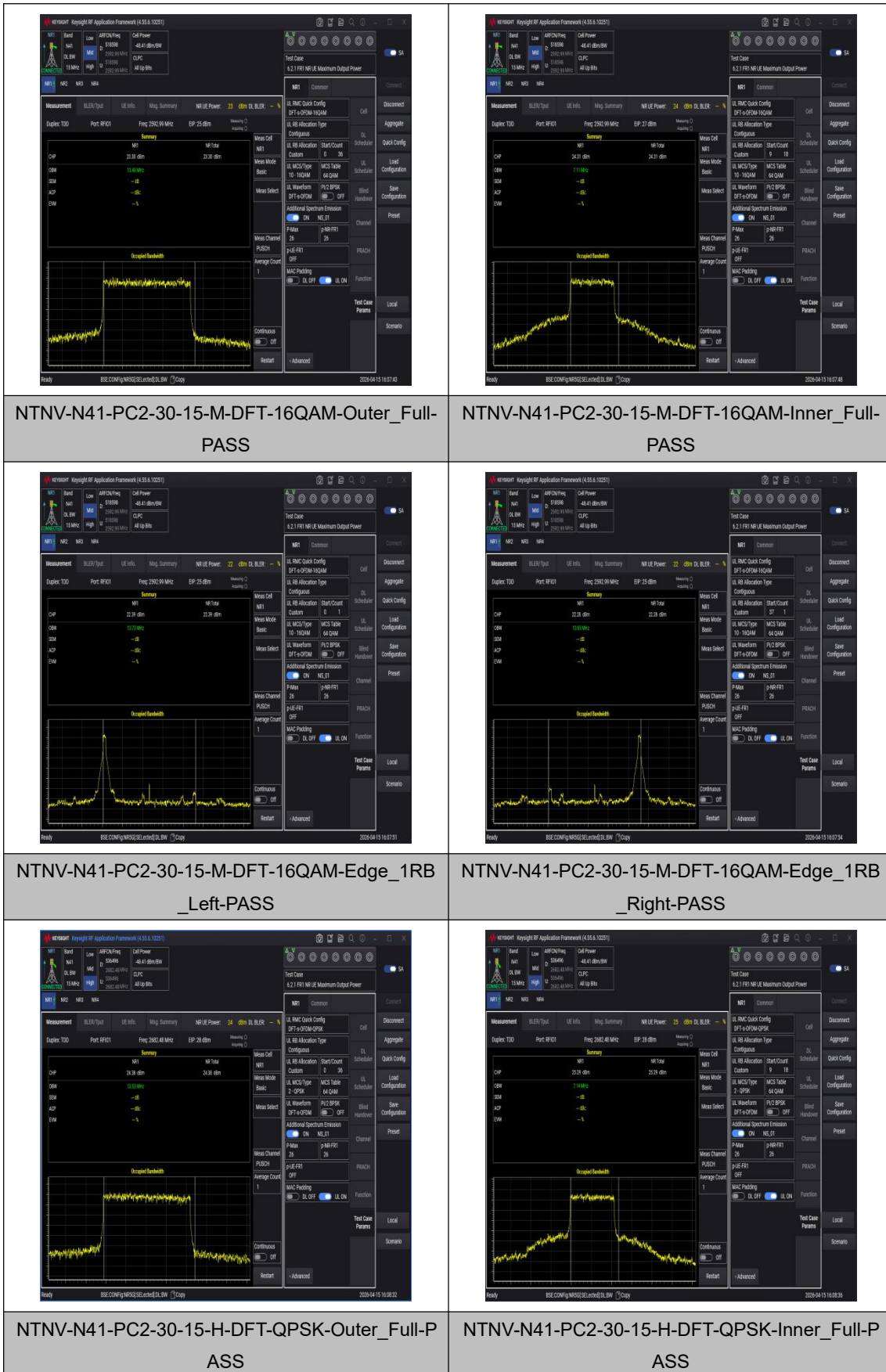


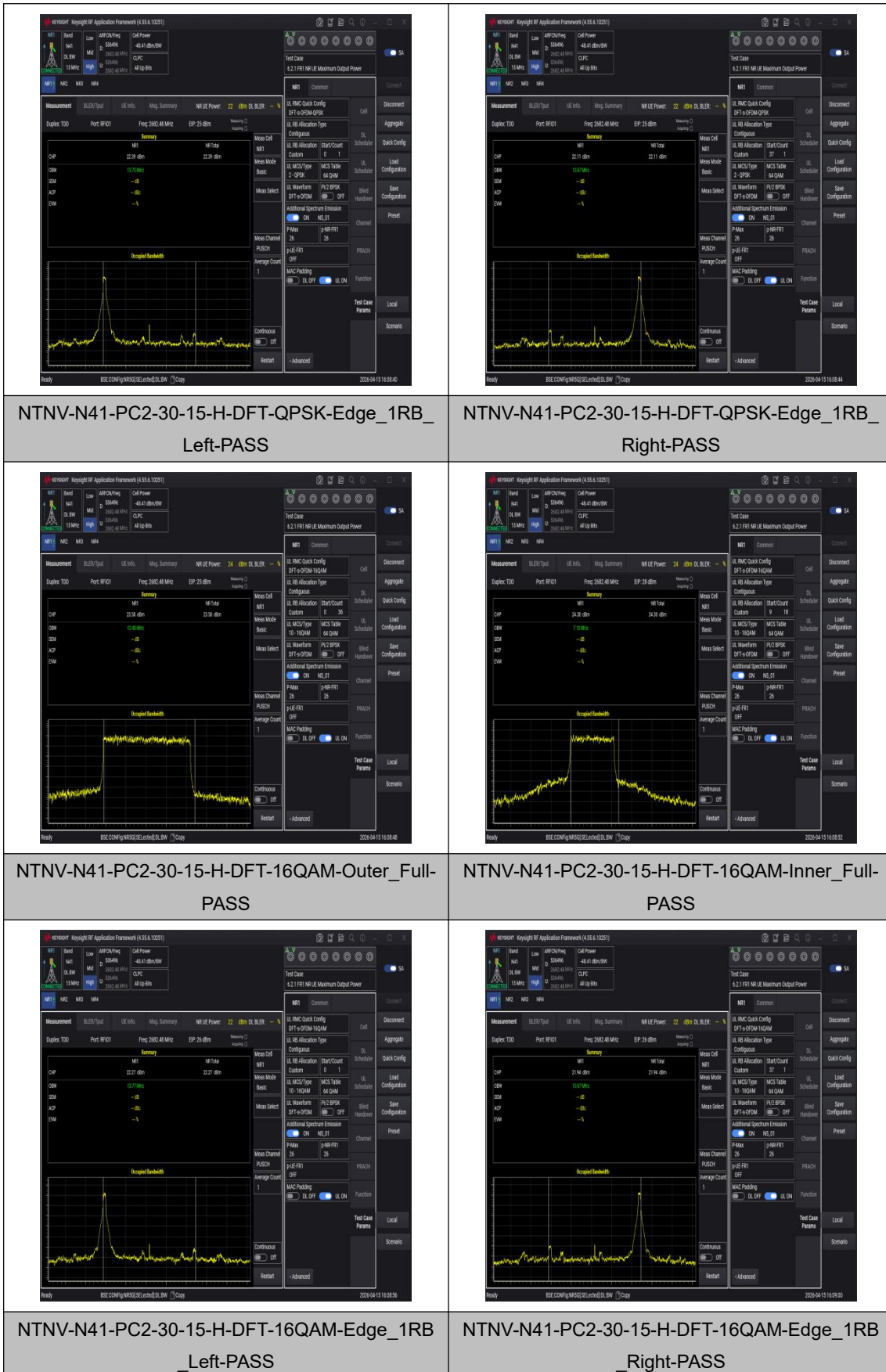


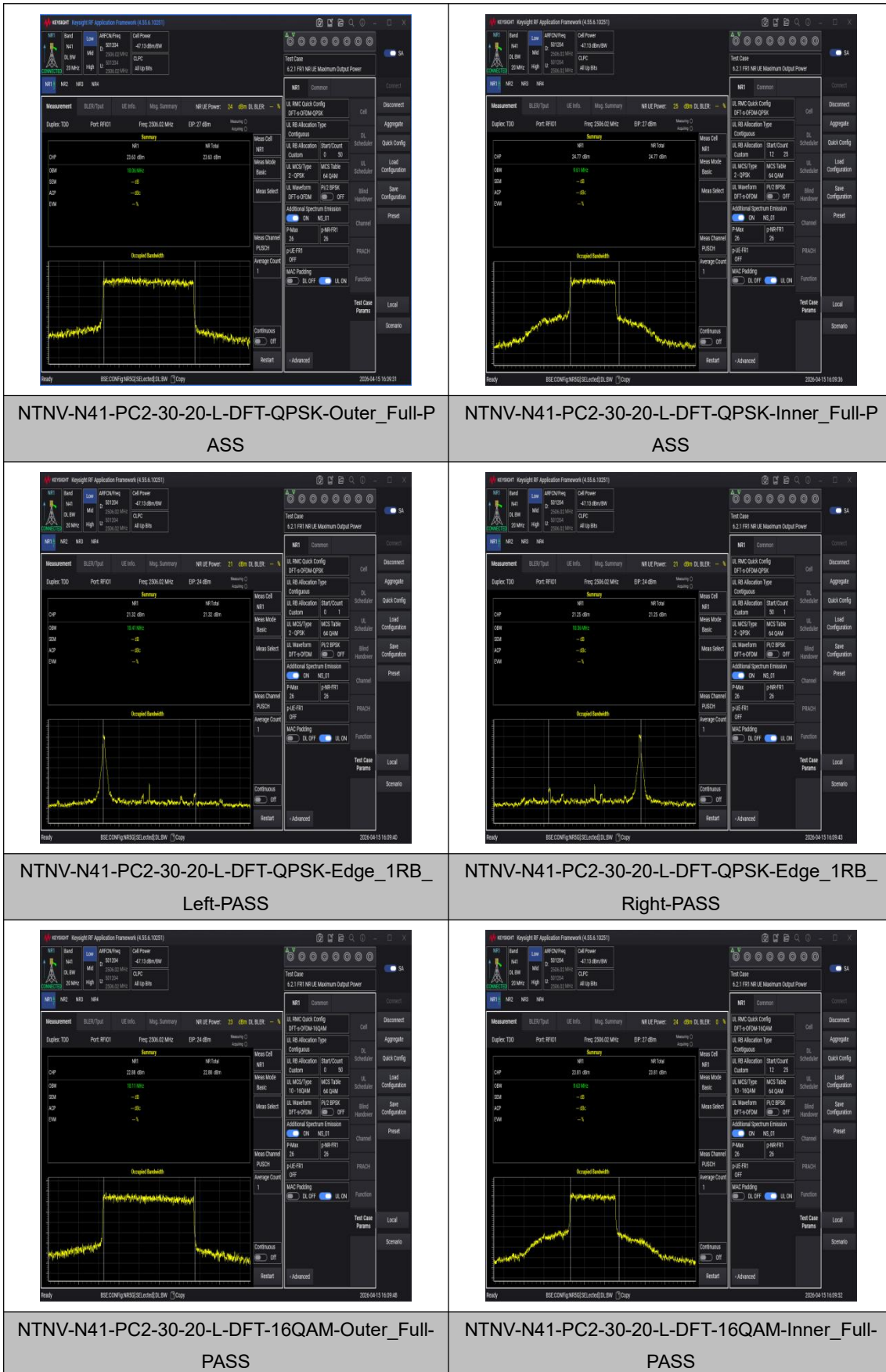












NTNV-N41-PC2-30-20-L-DFT-16QAM-Edge_1RB
_Left-PASSNTNV-N41-PC2-30-20-L-DFT-16QAM-Edge_1RB
_Right-PASSNTNV-N41-PC2-30-20-M-DFT-QPSK-Outer_Full-P
ASSNTNV-N41-PC2-30-20-M-DFT-QPSK-Inner_Full-P
ASSNTNV-N41-PC2-30-20-M-DFT-QPSK-Edge_1RB_
Left-PASSNTNV-N41-PC2-30-20-M-DFT-QPSK-Edge_1RB_
Right-PASS



NTNV-N41-PC2-30-20-M-DFT-16QAM-Outer_Full-PASS



NTNV-N41-PC2-30-20-M-DFT-16QAM-Inner_Full-PASS



NTNV-N41-PC2-30-20-M-DFT-16QAM-Edge_1RB_Left-PASS



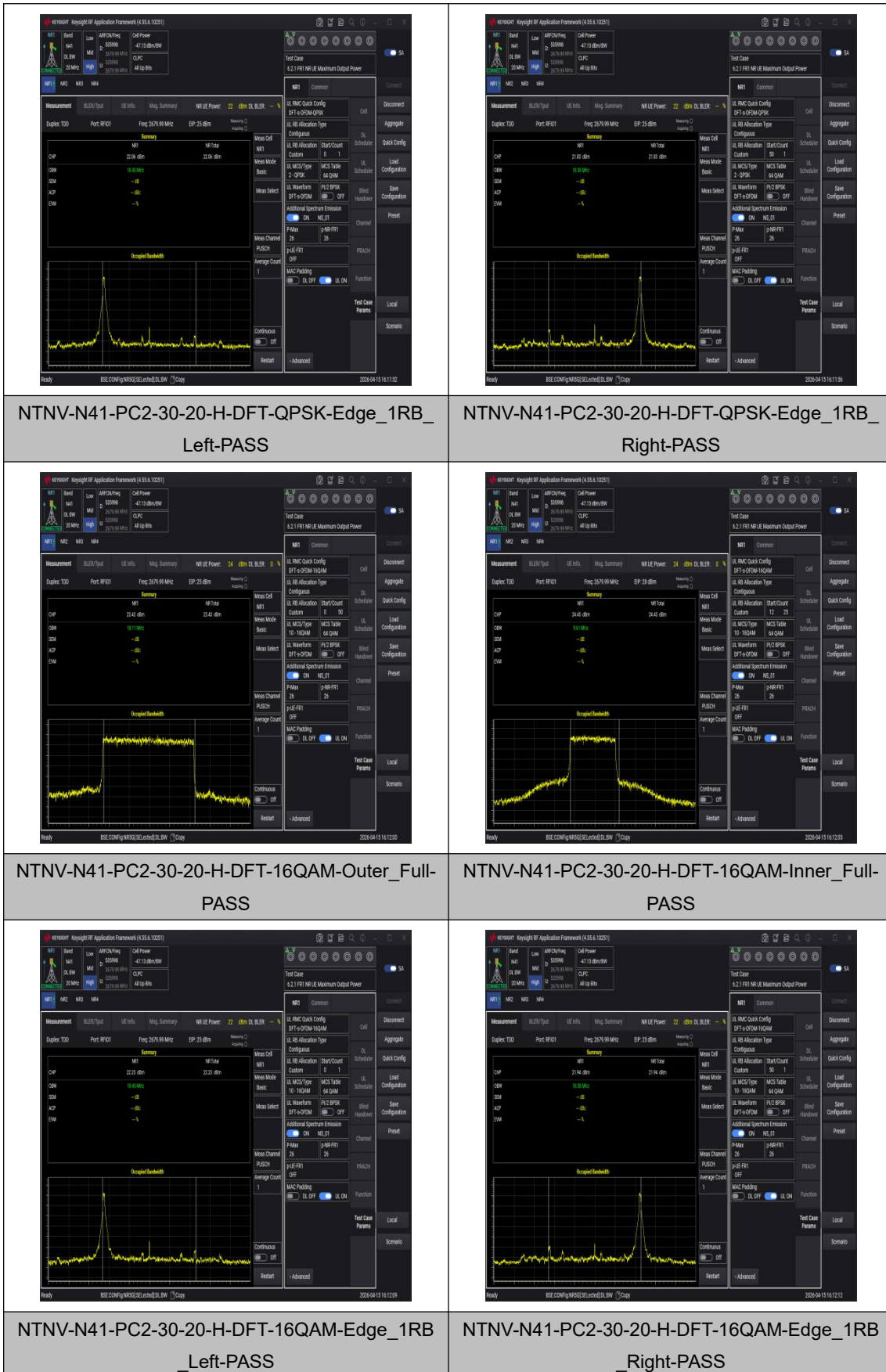
NTNV-N41-PC2-30-20-M-DFT-16QAM-Edge_1RB_Right-PASS

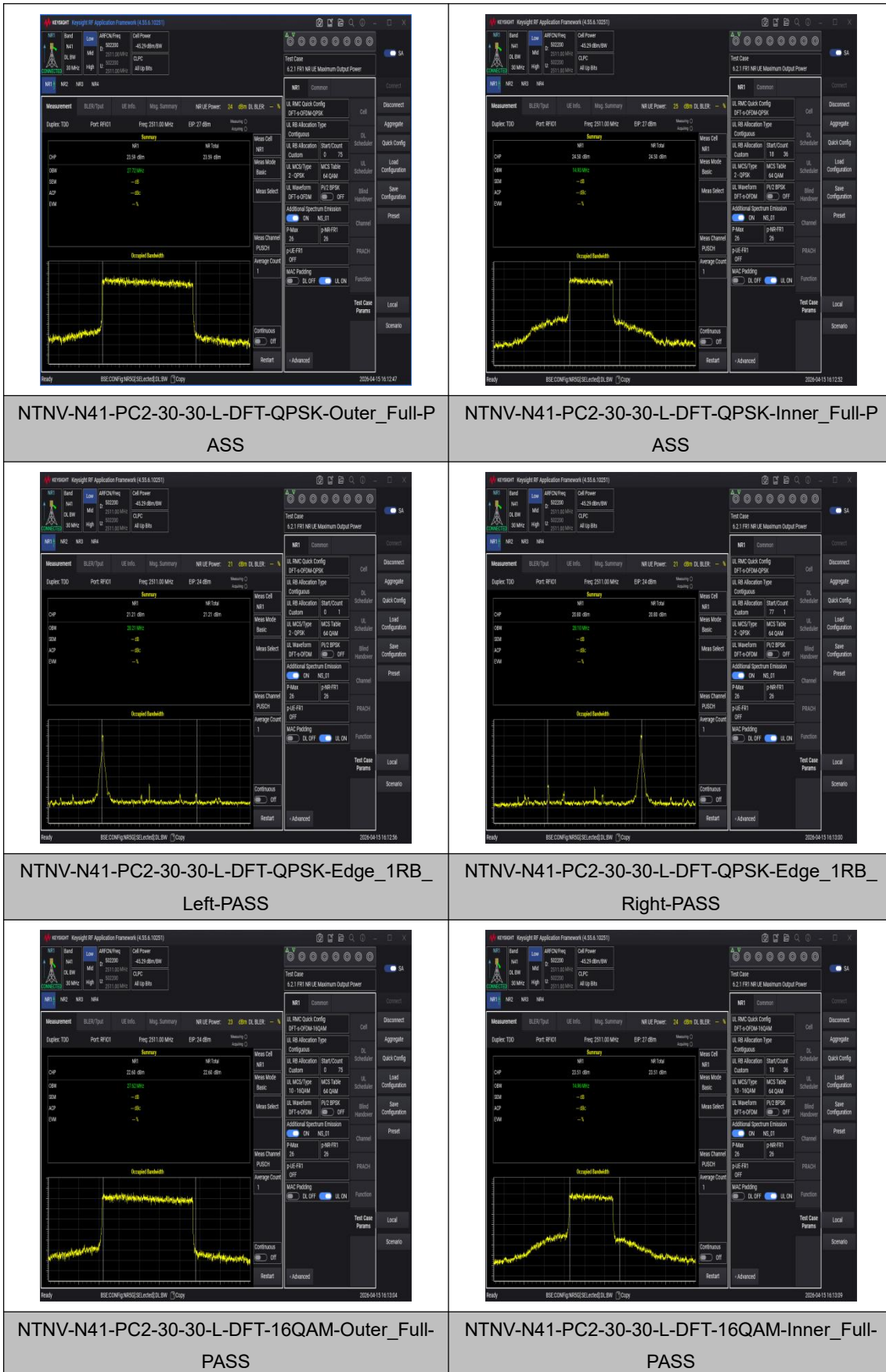


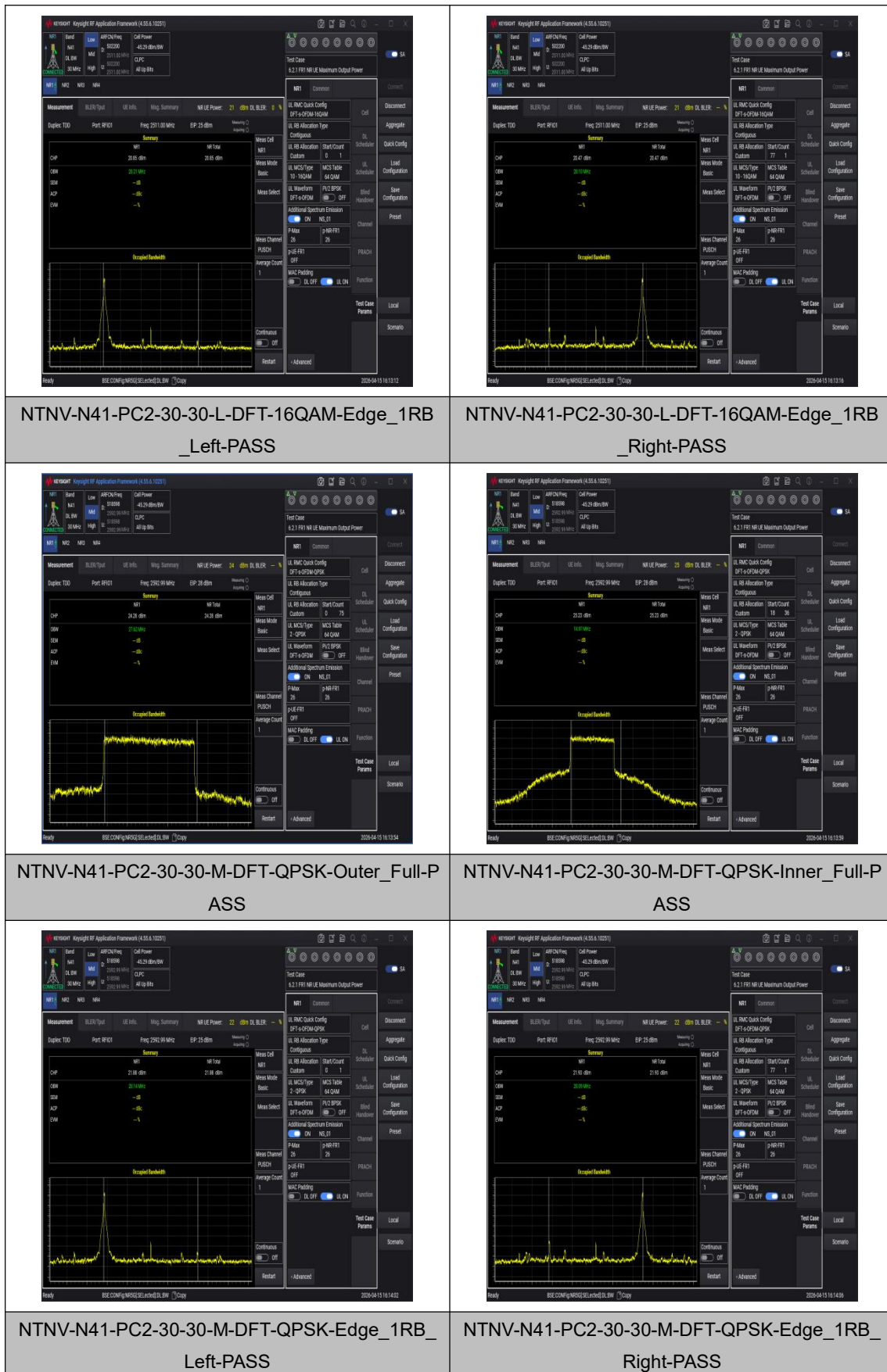
NTNV-N41-PC2-30-20-H-DFT-QPSK-Outer_Full-PASS



NTNV-N41-PC2-30-20-H-DFT-QPSK-Inner_Full-PASS







NTNV-N41-PC2-30-30-L-DFT-16QAM-Edge_1RB
_Left-PASS

NTNV-N41-PC2-30-30-L-DFT-16QAM-Edge_1RB
_Right-PASS

NTNV-N41-PC2-30-30-M-DFT-QPSK-Outer_Full-P
ASS

NTNV-N41-PC2-30-30-M-DFT-QPSK-Inner_Full-P
ASS

NTNV-N41-PC2-30-30-M-DFT-QPSK-Edge_1RB_
Left-PASS

NTNV-N41-PC2-30-30-M-DFT-QPSK-Edge_1RB_
Right-PASS



NTNV-N41-PC2-30-30-M-DFT-16QAM-Outer_Full-PASS



NTNV-N41-PC2-30-30-M-DFT-16QAM-Inner_Full-PASS



NTNV-N41-PC2-30-30-M-DFT-16QAM-Edge_1RB_Left-PASS



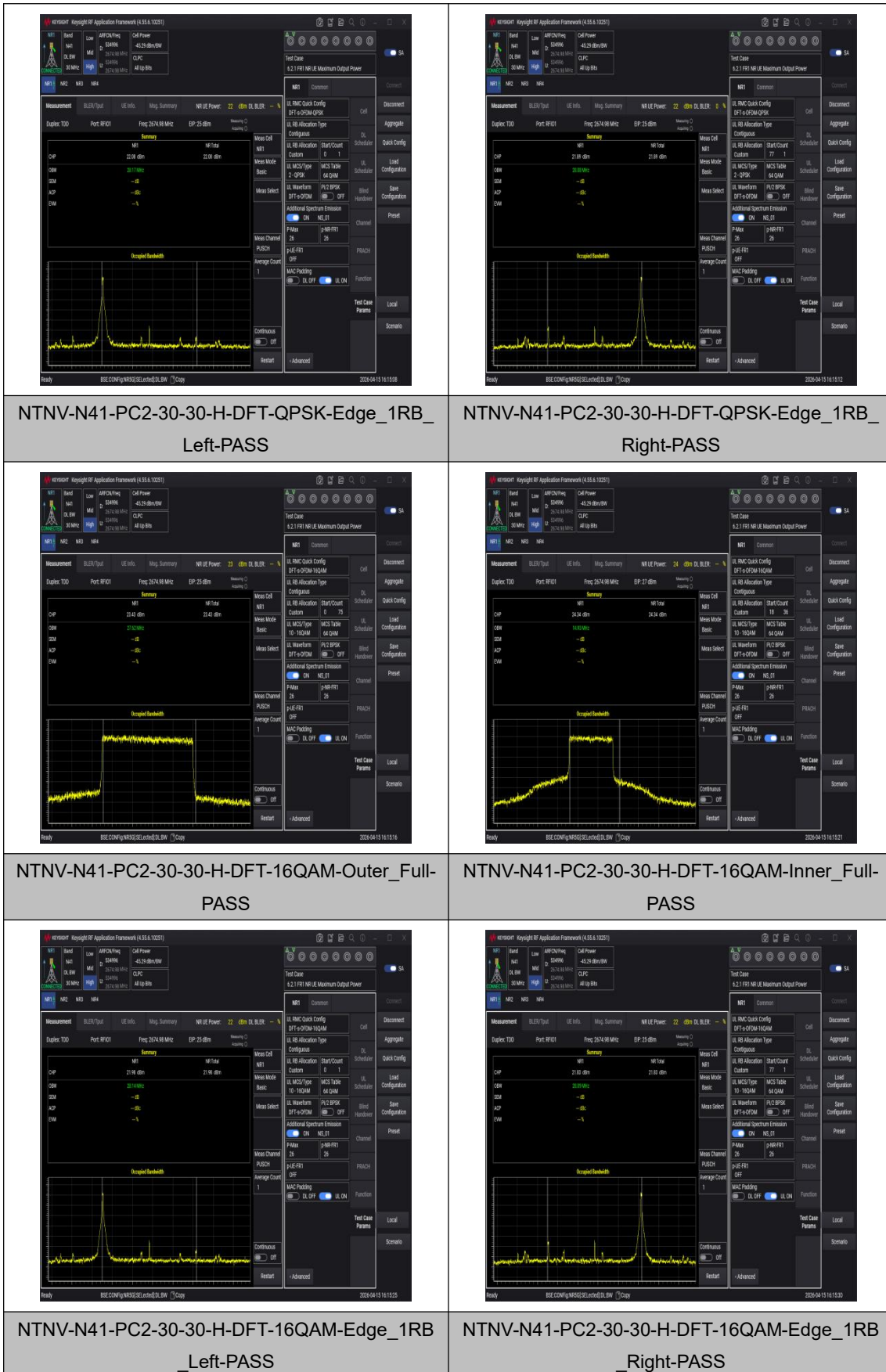
NTNV-N41-PC2-30-30-M-DFT-16QAM-Edge_1RB_Right-PASS



NTNV-N41-PC2-30-30-H-DFT-QPSK-Outer_Full-PASS



NTNV-N41-PC2-30-30-H-DFT-QPSK-Inner_Full-PASS





NTNV-N41-PC2-30-40-L-DFT-QPSK-Outer_Full-P
ASS



NTNV-N41-PC2-30-40-L-DFT-QPSK-Inner_Full-P
ASS



NTNV-N41-PC2-30-40-L-DFT-QPSK-Edge_1RB_
Left-PASS



NTNV-N41-PC2-30-40-L-DFT-QPSK-Edge_1RB_
Right-PASS



NTNV-N41-PC2-30-40-L-DFT-16QAM-Outer_Full-
PASS



NTNV-N41-PC2-30-40-L-DFT-16QAM-Inner_Full-
PASS

