

Appendix for n41

Product Name: Tablet

Model No: MDT880

Appendix A: Average Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N41	30	10	DFT-QPSK	L	Outer_Full	24.99	PC2	PASS
N41	30	10	DFT-QPSK	L	Inner_Full	25.96	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Left	22.69	PC2	PASS
N41	30	10	DFT-QPSK	L	Edge_1RB_Right	22.77	PC2	PASS
N41	30	10	DFT-16QAM	L	Outer_Full	23.83	PC2	PASS
N41	30	10	DFT-16QAM	L	Inner_Full	25.00	PC2	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Left	22.66	PC2	PASS
N41	30	10	DFT-16QAM	L	Edge_1RB_Right	22.79	PC2	PASS
N41	30	10	DFT-QPSK	M	Outer_Full	25.45	PC2	PASS
N41	30	10	DFT-QPSK	M	Inner_Full	26.34	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Left	22.89	PC2	PASS
N41	30	10	DFT-QPSK	M	Edge_1RB_Right	22.79	PC2	PASS
N41	30	10	DFT-16QAM	M	Outer_Full	24.21	PC2	PASS
N41	30	10	DFT-16QAM	M	Inner_Full	25.47	PC2	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Left	23.20	PC2	PASS
N41	30	10	DFT-16QAM	M	Edge_1RB_Right	23.11	PC2	PASS
N41	30	10	DFT-QPSK	H	Outer_Full	24.96	PC2	PASS
N41	30	10	DFT-QPSK	H	Inner_Full	25.82	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Left	22.69	PC2	PASS
N41	30	10	DFT-QPSK	H	Edge_1RB_Right	22.61	PC2	PASS
N41	30	10	DFT-16QAM	H	Outer_Full	23.92	PC2	PASS
N41	30	10	DFT-16QAM	H	Inner_Full	24.84	PC2	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Left	22.71	PC2	PASS
N41	30	10	DFT-16QAM	H	Edge_1RB_Right	22.58	PC2	PASS
N41	30	15	DFT-QPSK	L	Outer_Full	25.04	PC2	PASS
N41	30	15	DFT-QPSK	L	Inner_Full	26.04	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Left	22.32	PC2	PASS
N41	30	15	DFT-QPSK	L	Edge_1RB_Right	22.41	PC2	PASS
N41	30	15	DFT-16QAM	L	Outer_Full	24.04	PC2	PASS
N41	30	15	DFT-16QAM	L	Inner_Full	24.91	PC2	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Left	22.49	PC2	PASS
N41	30	15	DFT-16QAM	L	Edge_1RB_Right	22.62	PC2	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	25.44	PC2	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	26.29	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	22.80	PC2	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	22.67	PC2	PASS

N41	30	15	DFT-16QAM	M	Outer_Full	24.33	PC2	PASS
N41	30	15	DFT-16QAM	M	Inner_Full	25.49	PC2	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Left	22.66	PC2	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Right	22.53	PC2	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	25.08	PC2	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	25.97	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	22.39	PC2	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	22.33	PC2	PASS
N41	30	15	DFT-16QAM	H	Outer_Full	24.06	PC2	PASS
N41	30	15	DFT-16QAM	H	Inner_Full	24.99	PC2	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Left	22.65	PC2	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Right	22.56	PC2	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	25.05	PC2	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	25.98	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	22.44	PC2	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	22.43	PC2	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	23.99	PC2	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	24.98	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	22.20	PC2	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	22.28	PC2	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	25.39	PC2	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	26.46	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	22.89	PC2	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	22.75	PC2	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	24.21	PC2	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	25.39	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Left	22.73	PC2	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	22.62	PC2	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	25.03	PC2	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	26.01	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	22.57	PC2	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	22.56	PC2	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	24.02	PC2	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	24.88	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	22.54	PC2	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	22.52	PC2	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	24.87	PC2	PASS
N41	30	30	DFT-QPSK	L	Inner_Full	25.83	PC2	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	22.44	PC2	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Right	22.13	PC2	PASS
N41	30	30	DFT-16QAM	L	Outer_Full	23.87	PC2	PASS
N41	30	30	DFT-16QAM	L	Inner_Full	24.92	PC2	PASS

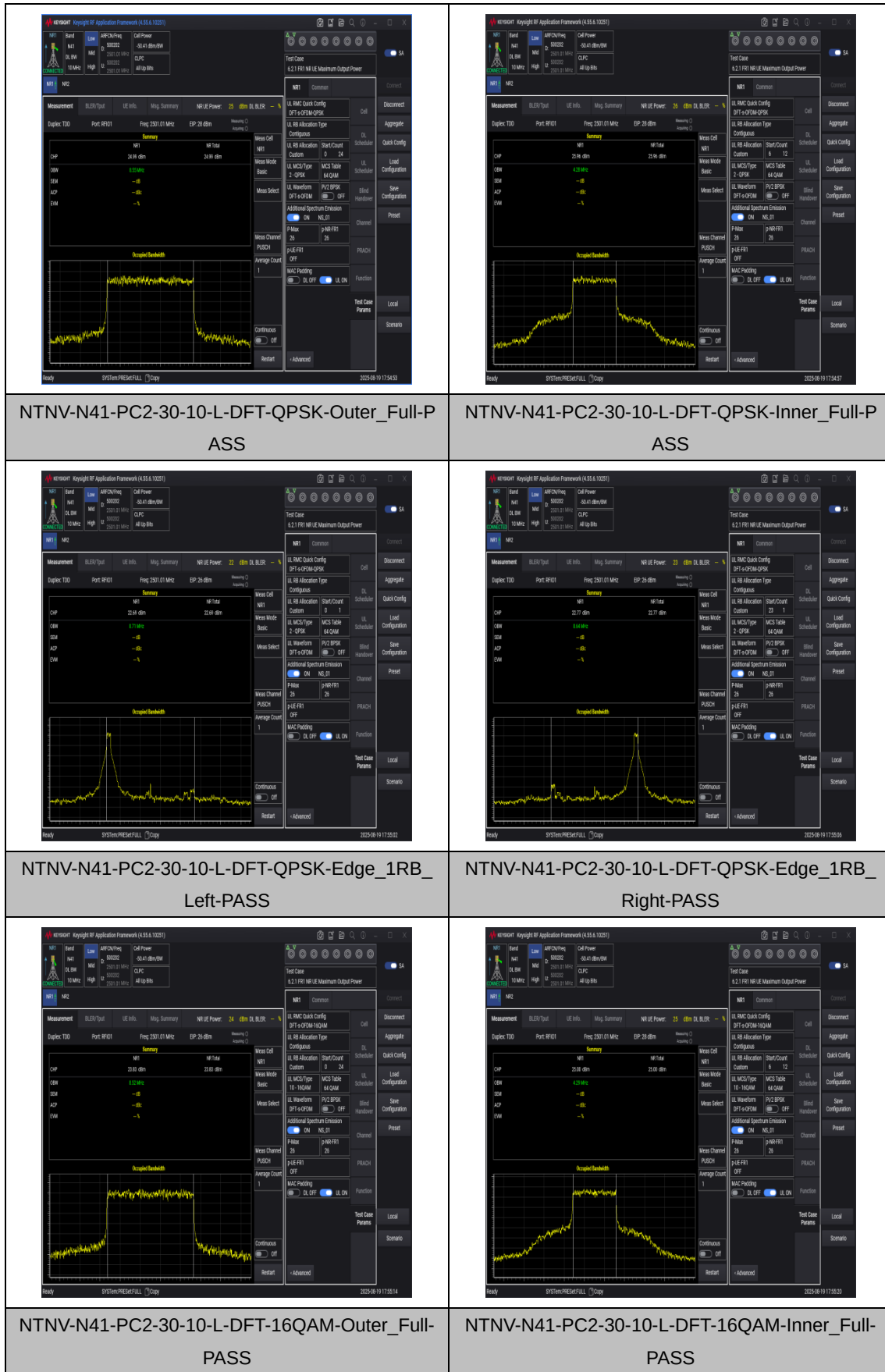
N41	30	30	DFT-16QAM	L	Edge_1RB_Left	22.74	PC2	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Right	22.38	PC2	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	25.44	PC2	PASS
N41	30	30	DFT-QPSK	M	Inner_Full	26.41	PC2	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	22.88	PC2	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Right	22.86	PC2	PASS
N41	30	30	DFT-16QAM	M	Outer_Full	24.16	PC2	PASS
N41	30	30	DFT-16QAM	M	Inner_Full	25.38	PC2	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Left	22.83	PC2	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Right	22.74	PC2	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	24.96	PC2	PASS
N41	30	30	DFT-QPSK	H	Inner_Full	26.00	PC2	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	22.36	PC2	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Right	22.59	PC2	PASS
N41	30	30	DFT-16QAM	H	Outer_Full	24.02	PC2	PASS
N41	30	30	DFT-16QAM	H	Inner_Full	25.15	PC2	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Left	22.34	PC2	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Right	22.53	PC2	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	24.81	PC2	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	25.65	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	22.34	PC2	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	22.03	PC2	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	23.69	PC2	PASS
N41	30	40	DFT-16QAM	L	Inner_Full	24.75	PC2	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Left	22.40	PC2	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Right	22.04	PC2	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	25.35	PC2	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	26.31	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	22.61	PC2	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	22.68	PC2	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	24.16	PC2	PASS
N41	30	40	DFT-16QAM	M	Inner_Full	25.34	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Left	22.80	PC2	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Right	22.88	PC2	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	24.89	PC2	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	25.88	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	22.45	PC2	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	22.60	PC2	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	23.83	PC2	PASS
N41	30	40	DFT-16QAM	H	Inner_Full	24.96	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Left	21.99	PC2	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Right	22.15	PC2	PASS

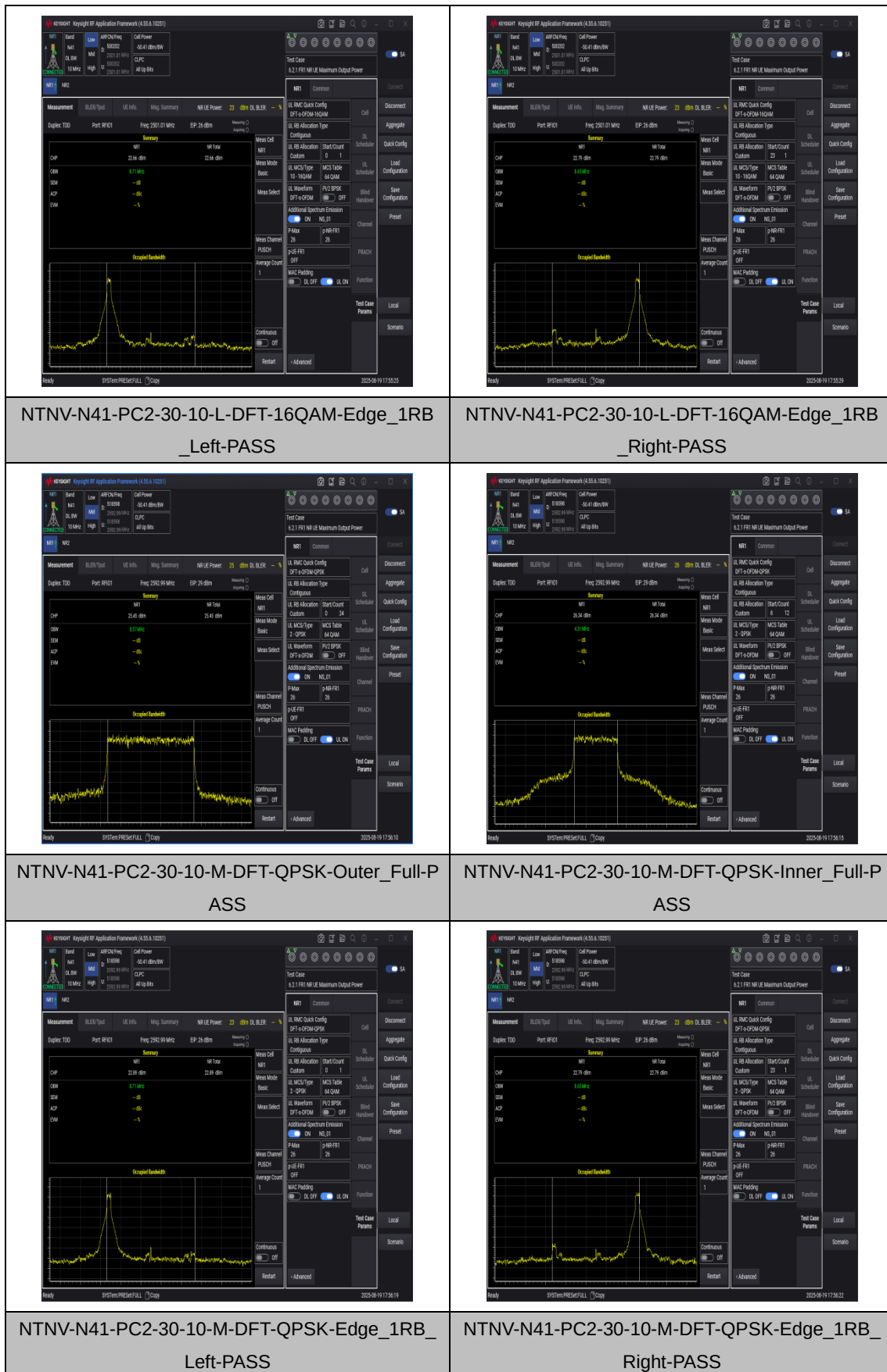
N41	30	50	DFT-QPSK	L	Outer_Full	24.85	PC2	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	25.56	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	22.40	PC2	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	22.34	PC2	PASS
N41	30	50	DFT-16QAM	L	Outer_Full	23.73	PC2	PASS
N41	30	50	DFT-16QAM	L	Inner_Full	24.66	PC2	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Left	22.35	PC2	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Right	22.33	PC2	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	25.38	PC2	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	26.38	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	22.54	PC2	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	22.67	PC2	PASS
N41	30	50	DFT-16QAM	M	Outer_Full	24.19	PC2	PASS
N41	30	50	DFT-16QAM	M	Inner_Full	25.37	PC2	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Left	22.52	PC2	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Right	22.60	PC2	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	24.87	PC2	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	25.91	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	22.41	PC2	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	22.50	PC2	PASS
N41	30	50	DFT-16QAM	H	Outer_Full	23.80	PC2	PASS
N41	30	50	DFT-16QAM	H	Inner_Full	25.03	PC2	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Left	22.65	PC2	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Right	22.79	PC2	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	24.68	PC2	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	25.38	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	22.36	PC2	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	22.42	PC2	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	23.68	PC2	PASS
N41	30	60	DFT-16QAM	L	Inner_Full	24.46	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Left	22.39	PC2	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Right	22.41	PC2	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	25.35	PC2	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	26.28	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	22.38	PC2	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	22.43	PC2	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	24.12	PC2	PASS
N41	30	60	DFT-16QAM	M	Inner_Full	25.37	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Left	22.33	PC2	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Right	22.38	PC2	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	24.89	PC2	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	25.81	PC2	PASS

N41	30	60	DFT-QPSK	H	Edge_1RB_Left	22.18	PC2	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	22.39	PC2	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	23.78	PC2	PASS
N41	30	60	DFT-16QAM	H	Inner_Full	24.89	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Left	22.07	PC2	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Right	22.30	PC2	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	25.06	PC2	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	25.49	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	22.37	PC2	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	22.64	PC2	PASS
N41	30	80	DFT-16QAM	L	Outer_Full	23.99	PC2	PASS
N41	30	80	DFT-16QAM	L	Inner_Full	24.62	PC2	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Left	22.49	PC2	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Right	22.77	PC2	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	25.21	PC2	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	26.34	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	22.39	PC2	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	22.35	PC2	PASS
N41	30	80	DFT-16QAM	M	Outer_Full	24.11	PC2	PASS
N41	30	80	DFT-16QAM	M	Inner_Full	25.31	PC2	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Left	22.12	PC2	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Right	22.12	PC2	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	24.87	PC2	PASS
N41	30	80	DFT-QPSK	H	Inner_Full	25.83	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Left	22.69	PC2	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	22.49	PC2	PASS
N41	30	80	DFT-16QAM	H	Outer_Full	23.89	PC2	PASS
N41	30	80	DFT-16QAM	H	Inner_Full	24.97	PC2	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Left	22.62	PC2	PASS
N41	30	80	DFT-16QAM	H	Edge_1RB_Right	22.43	PC2	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	25.17	PC2	PASS
N41	30	90	DFT-QPSK	L	Inner_Full	25.68	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	22.40	PC2	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Right	22.75	PC2	PASS
N41	30	90	DFT-16QAM	L	Outer_Full	24.05	PC2	PASS
N41	30	90	DFT-16QAM	L	Inner_Full	24.86	PC2	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Left	22.34	PC2	PASS
N41	30	90	DFT-16QAM	L	Edge_1RB_Right	22.71	PC2	PASS
N41	30	90	DFT-QPSK	M	Outer_Full	25.26	PC2	PASS
N41	30	90	DFT-QPSK	M	Inner_Full	26.39	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Left	22.29	PC2	PASS
N41	30	90	DFT-QPSK	M	Edge_1RB_Right	22.32	PC2	PASS

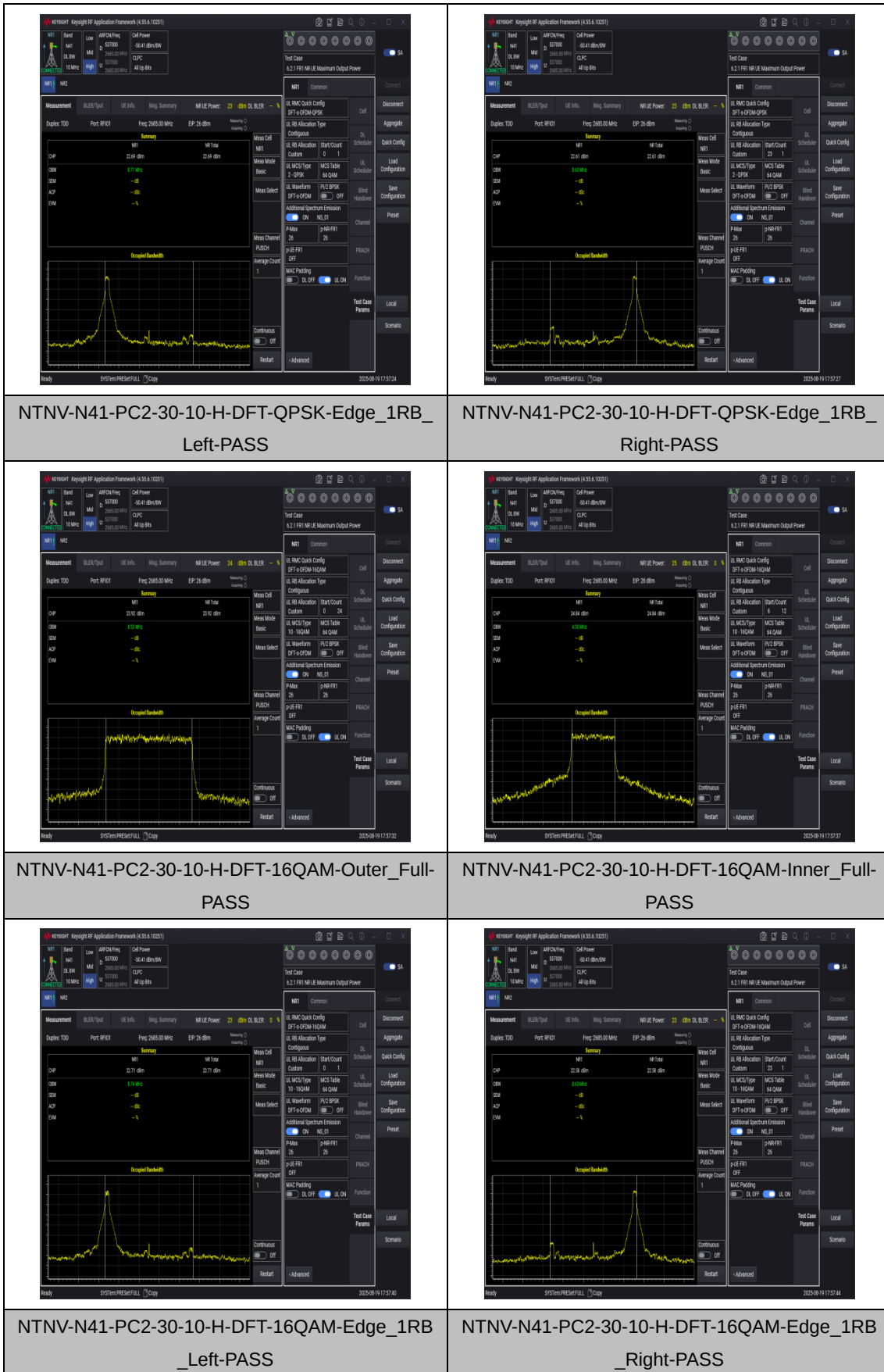
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N41	30	90	DFT-16QAM	M	Inner_Full	25.52	PC2	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Left	22.59	PC2	PASS
N41	30	90	DFT-16QAM	M	Edge_1RB_Right	22.63	PC2	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	25.00	PC2	PASS
N41	30	90	DFT-QPSK	H	Inner_Full	25.75	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Left	22.68	PC2	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	22.50	PC2	PASS
N41	30	90	DFT-16QAM	H	Outer_Full	23.97	PC2	PASS
N41	30	90	DFT-16QAM	H	Inner_Full	24.88	PC2	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Left	22.62	PC2	PASS
N41	30	90	DFT-16QAM	H	Edge_1RB_Right	22.43	PC2	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	25.16	PC2	PASS
N41	30	100	DFT-QPSK	L	Inner_Full	25.82	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	22.21	PC2	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Right	22.57	PC2	PASS
N41	30	100	DFT-16QAM	L	Outer_Full	24.11	PC2	PASS
N41	30	100	DFT-16QAM	L	Inner_Full	24.81	PC2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Left	22.20	PC2	PASS
N41	30	100	DFT-16QAM	L	Edge_1RB_Right	22.54	PC2	PASS
N41	30	100	DFT-QPSK	M	Outer_Full	25.14	PC2	PASS
N41	30	100	DFT-QPSK	M	Inner_Full	26.58	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	22.05	PC2	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Right	22.27	PC2	PASS
N41	30	100	DFT-16QAM	M	Outer_Full	24.01	PC2	PASS
N41	30	100	DFT-16QAM	M	Inner_Full	25.41	PC2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Left	21.98	PC2	PASS
N41	30	100	DFT-16QAM	M	Edge_1RB_Right	22.16	PC2	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	25.08	PC2	PASS
N41	30	100	DFT-QPSK	H	Inner_Full	25.73	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	22.47	PC2	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	22.22	PC2	PASS
N41	30	100	DFT-16QAM	H	Outer_Full	24.01	PC2	PASS
N41	30	100	DFT-16QAM	H	Inner_Full	24.98	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Left	22.62	PC2	PASS
N41	30	100	DFT-16QAM	H	Edge_1RB_Right	22.39	PC2	PASS

Test Graphs

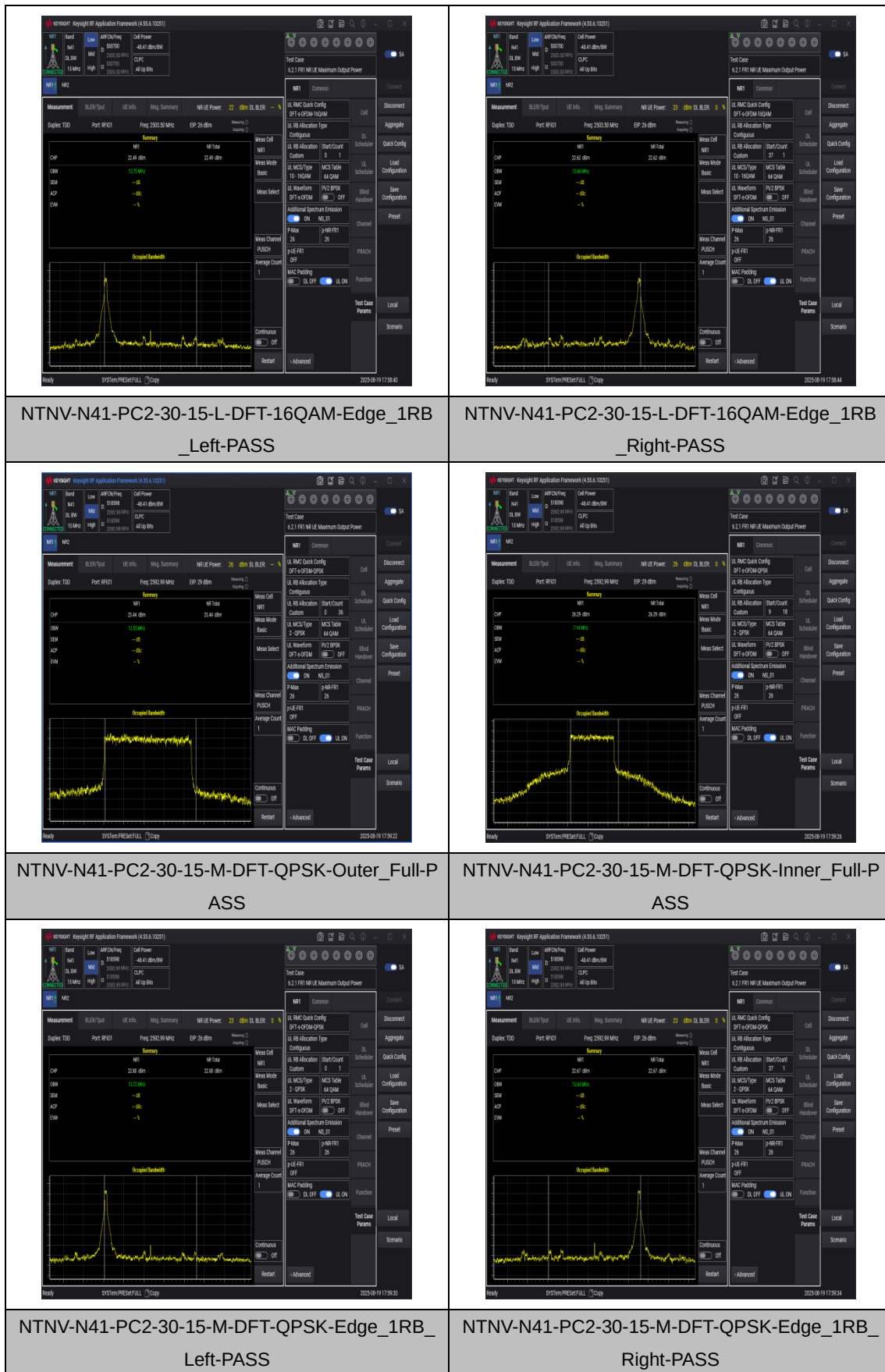


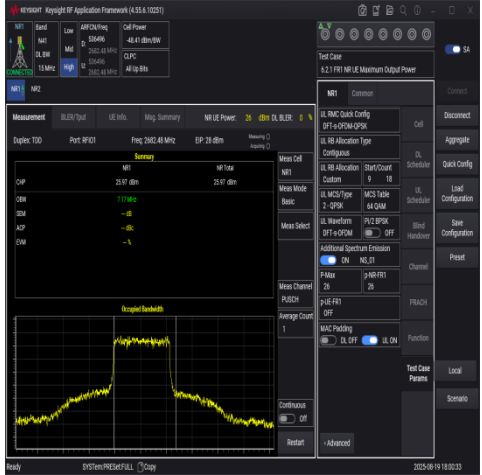


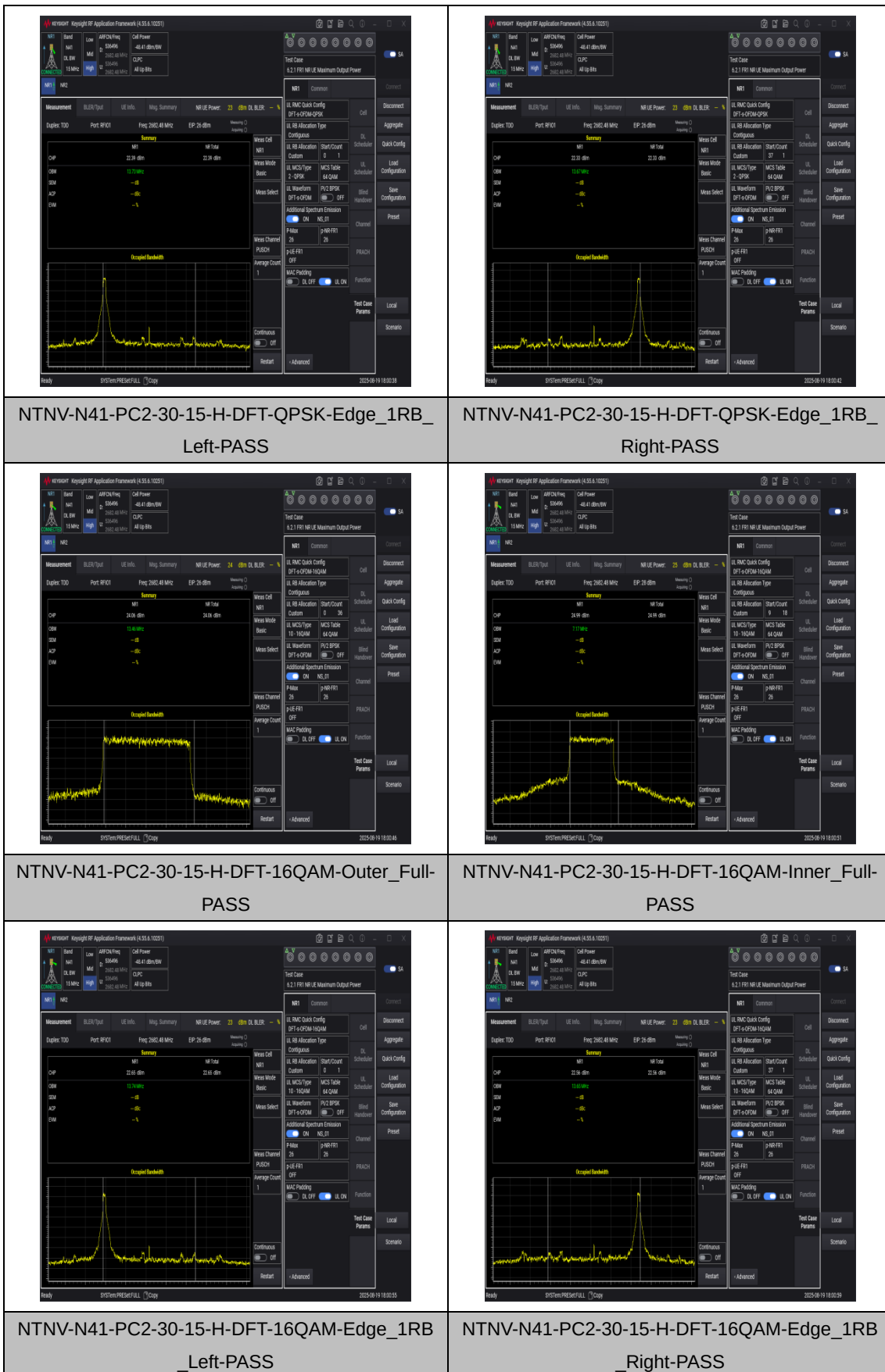
NTNV-N41-PC2-30-10-M-DFT-16QAM-Outer_Full-PASS	NTNV-N41-PC2-30-10-M-DFT-16QAM-Inner_Full-PASS
NTNV-N41-PC2-30-10-M-DFT-16QAM-Edge_1RB_Left-PASS	NTNV-N41-PC2-30-10-M-DFT-16QAM-Edge_1RB_Right-PASS
NTNV-N41-PC2-30-10-H-DFT-QPSK-Outer_Full-PASS	NTNV-N41-PC2-30-10-H-DFT-QPSK-Inner_Full-PASS

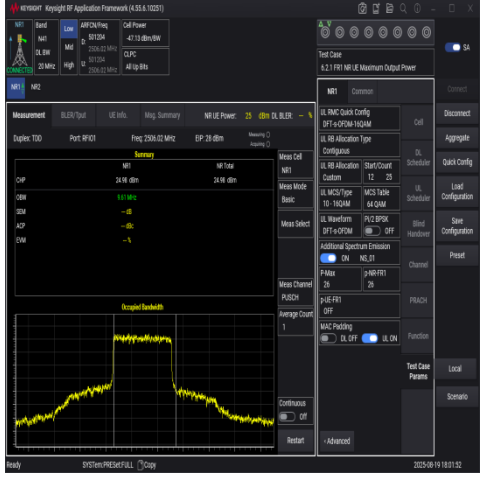


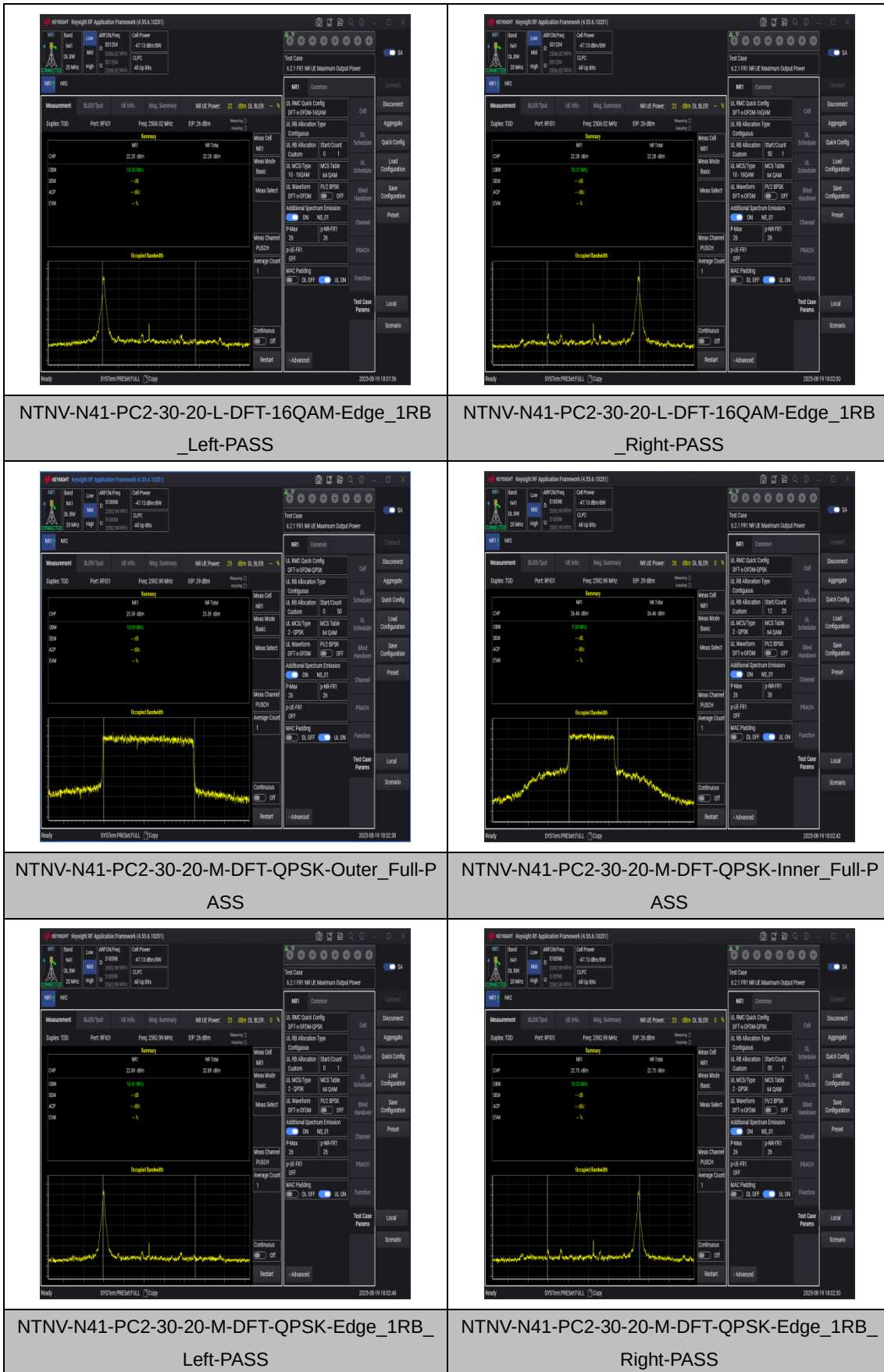
NTNV-N41-PC2-30-15-L-DFT-QPSK-Outer_Full-P ASS	NTNV-N41-PC2-30-15-L-DFT-QPSK-Inner_Full-P ASS
NTNV-N41-PC2-30-15-L-DFT-QPSK-Edge_1RB_ Left-PASS	NTNV-N41-PC2-30-15-L-DFT-QPSK-Edge_1RB_ Right-PASS
NTNV-N41-PC2-30-15-L-DFT-16QAM-Outer_Full- PASS	NTNV-N41-PC2-30-15-L-DFT-16QAM-Inner_Full- PASS



	
NTNV-N41-PC2-30-15-M-DFT-16QAM-Outer_Full-PASS	NTNV-N41-PC2-30-15-M-DFT-16QAM-Inner_Full-PASS
	
NTNV-N41-PC2-30-15-M-DFT-16QAM-Edge_1RB_Left-PASS	NTNV-N41-PC2-30-15-M-DFT-16QAM-Edge_1RB_Right-PASS
	
NTNV-N41-PC2-30-15-H-DFT-QPSK-Outer_Full-PASS	NTNV-N41-PC2-30-15-H-DFT-QPSK-Inner_Full-PASS



	
NTNV-N41-PC2-30-20-L-DFT-QPSK-Outter_Full-P ASS	NTNV-N41-PC2-30-20-L-DFT-QPSK-Inner_Full-P ASS
	
NTNV-N41-PC2-30-20-L-DFT-QPSK-Edge_1RB_ Left-PASS	NTNV-N41-PC2-30-20-L-DFT-QPSK-Edge_1RB_ Right-PASS
	
NTNV-N41-PC2-30-20-L-DFT-16QAM-Outter_Full- PASS	NTNV-N41-PC2-30-20-L-DFT-16QAM-Inner_Full- PASS



NTNV-N41-PC2-30-20-L-DFT-16QAM-Edge_1RB
_Left-PASS

NTNV-N41-PC2-30-20-L-DFT-16QAM-Edge_1RB
_Right-PASS

NTNV-N41-PC2-30-20-M-DFT-QPSK-Outer_Full-P
ASS

NTNV-N41-PC2-30-20-M-DFT-QPSK-Inner_Full-P
ASS

NTNV-N41-PC2-30-20-M-DFT-QPSK-Edge_1RB_
Left-PASS

NTNV-N41-PC2-30-20-M-DFT-QPSK-Edge_1RB_
Right-PASS