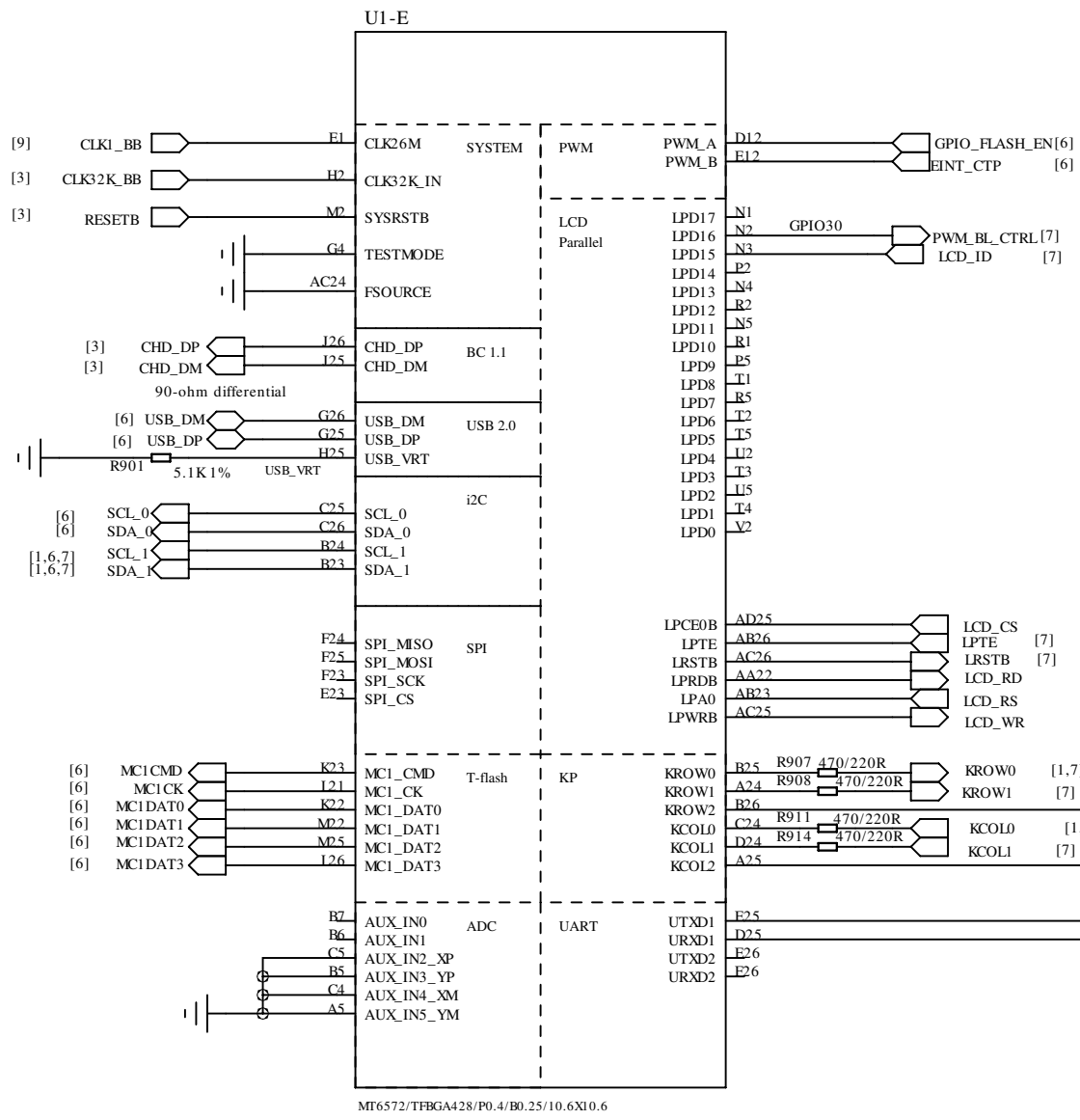
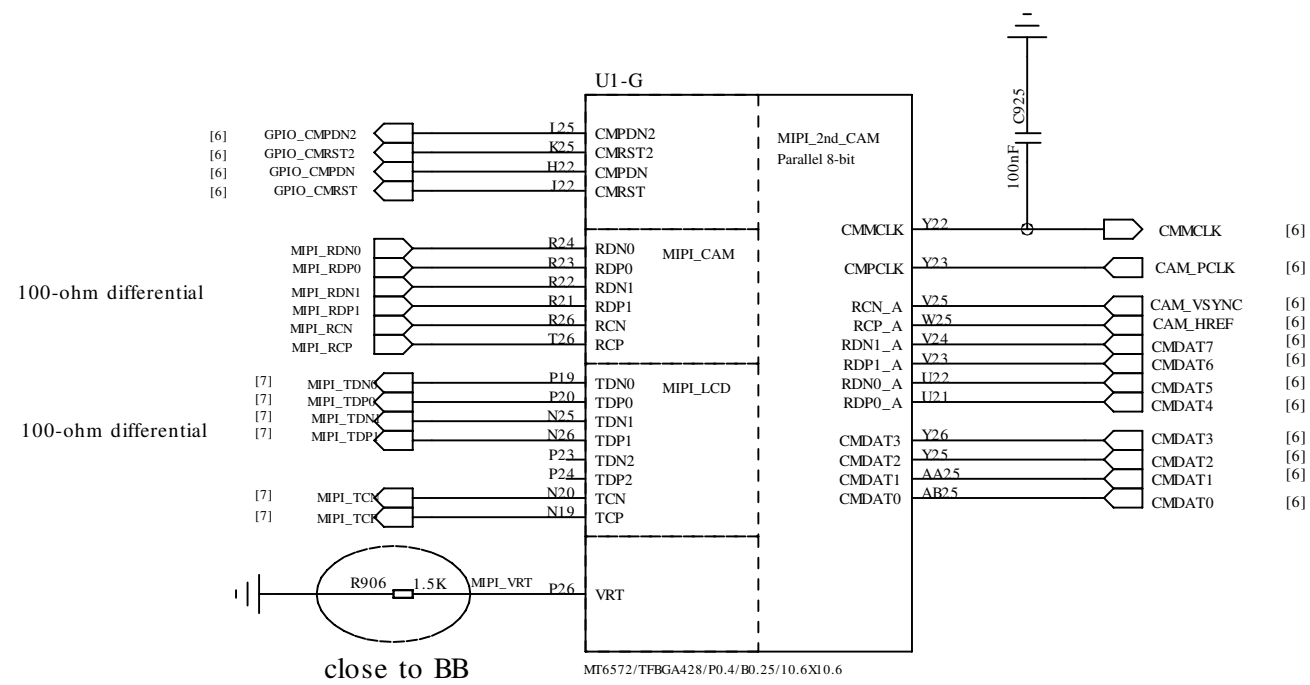
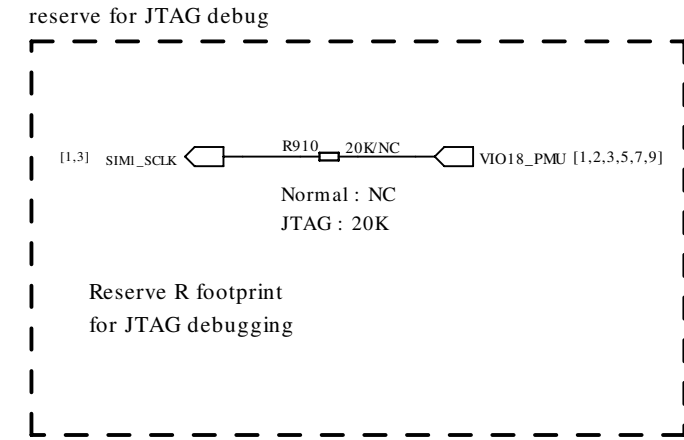
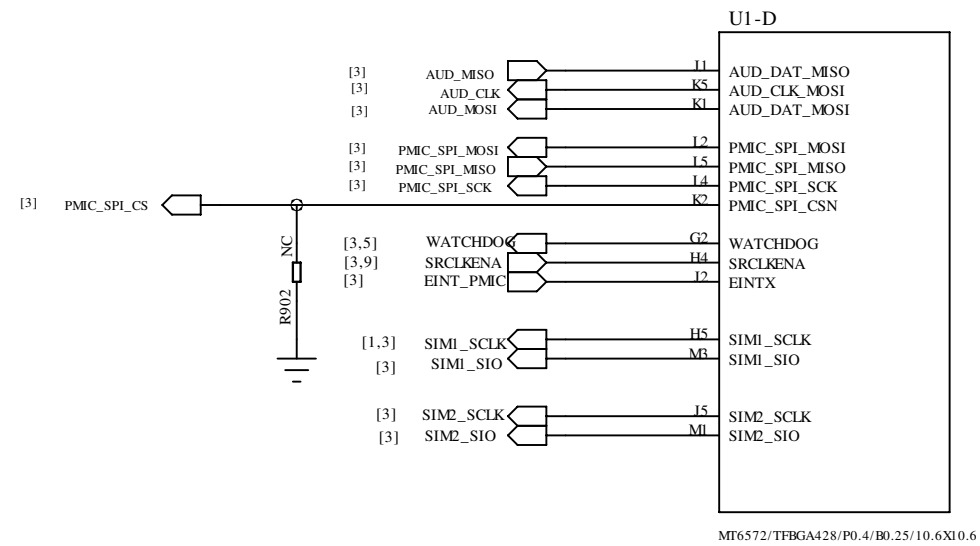
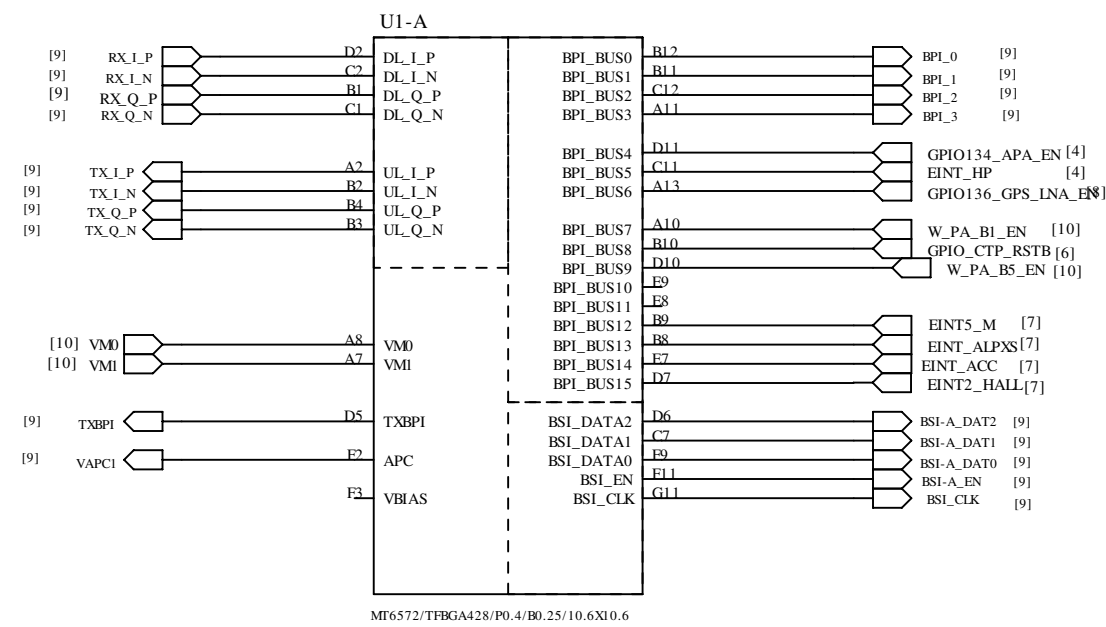
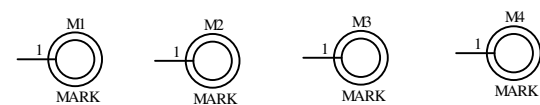
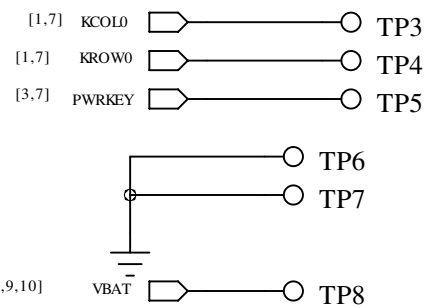
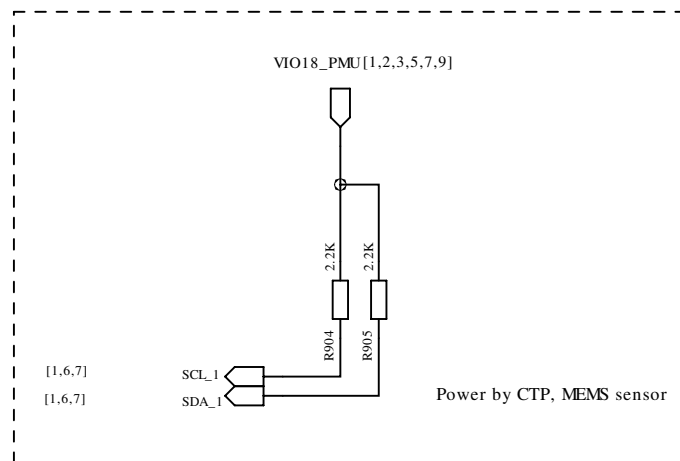




MT6572 support JTAG from below :

1. KP (recommand)
2. MC1
3. CAM

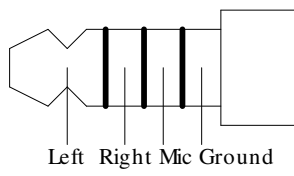
for JTAG pin out from MC1/CAM, refer to HW design notice



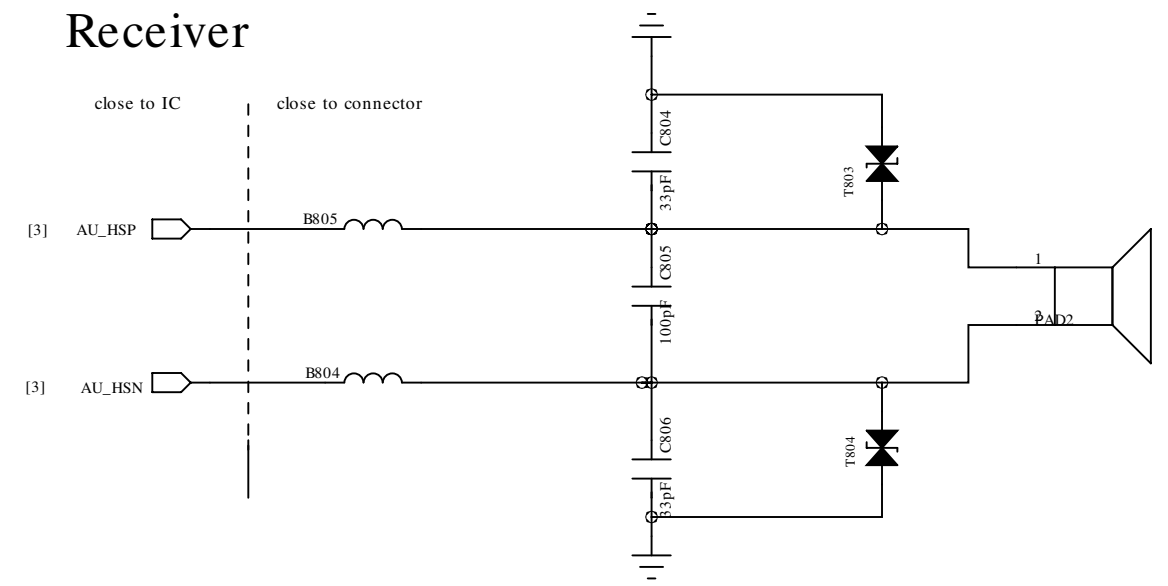
Charger



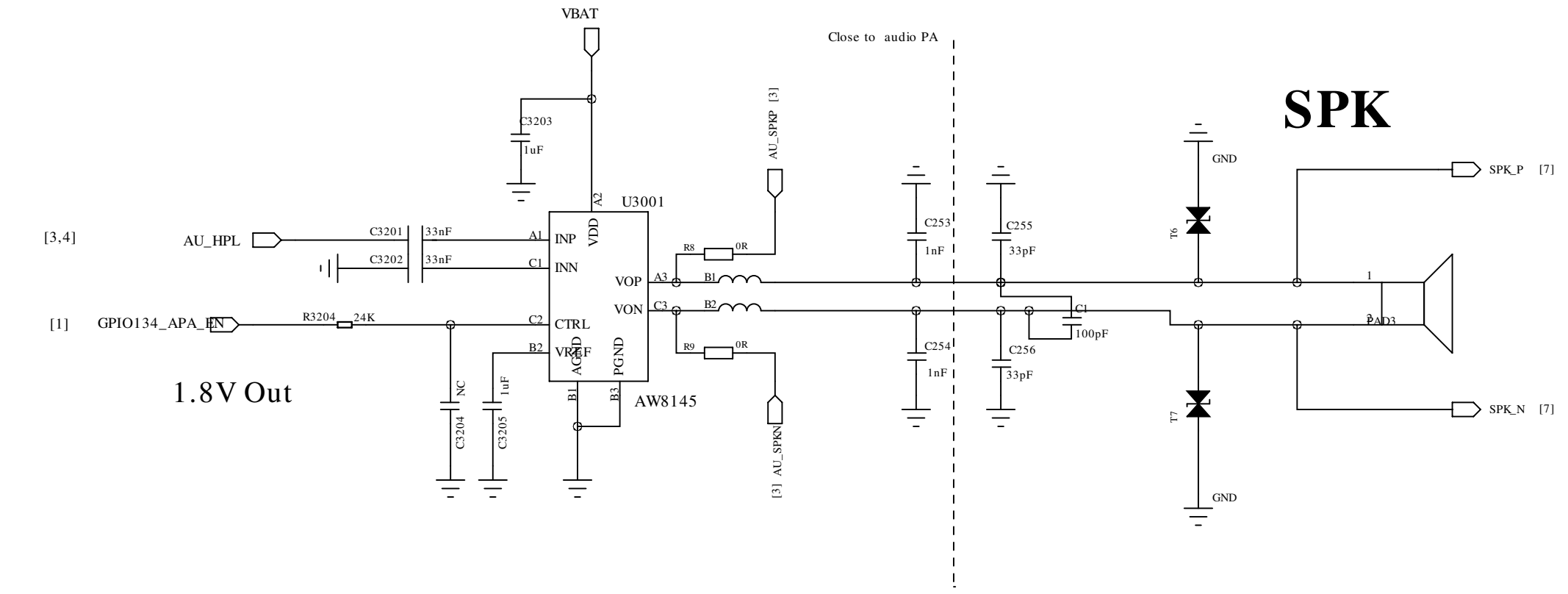
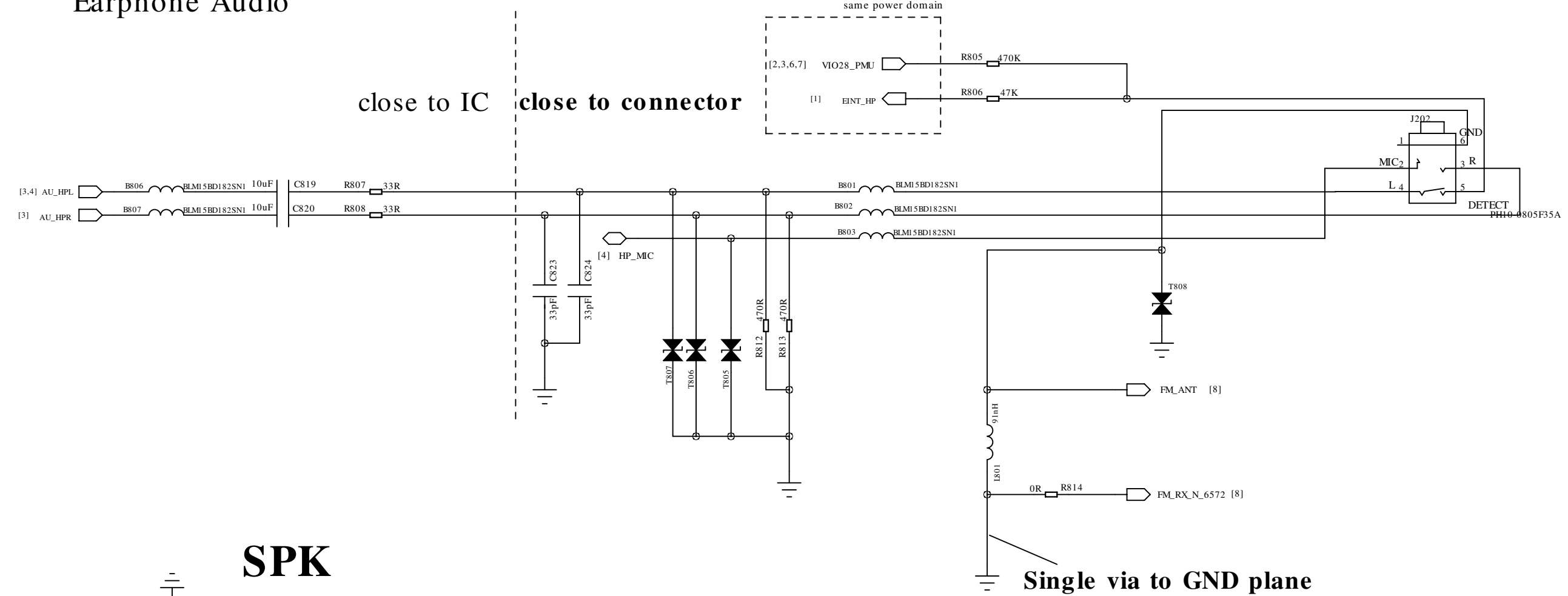
Vibrator



Receiver

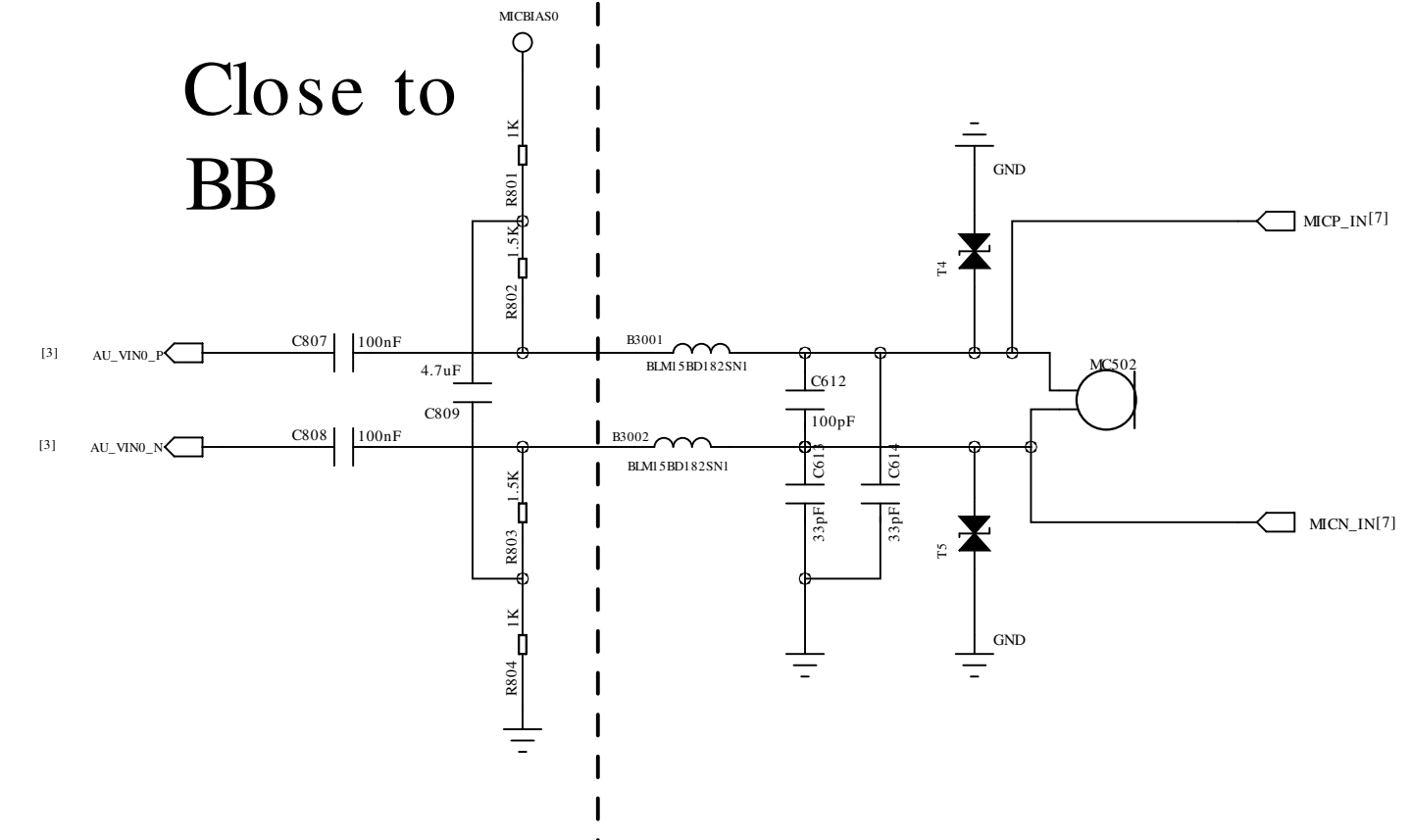


Earphone Audio



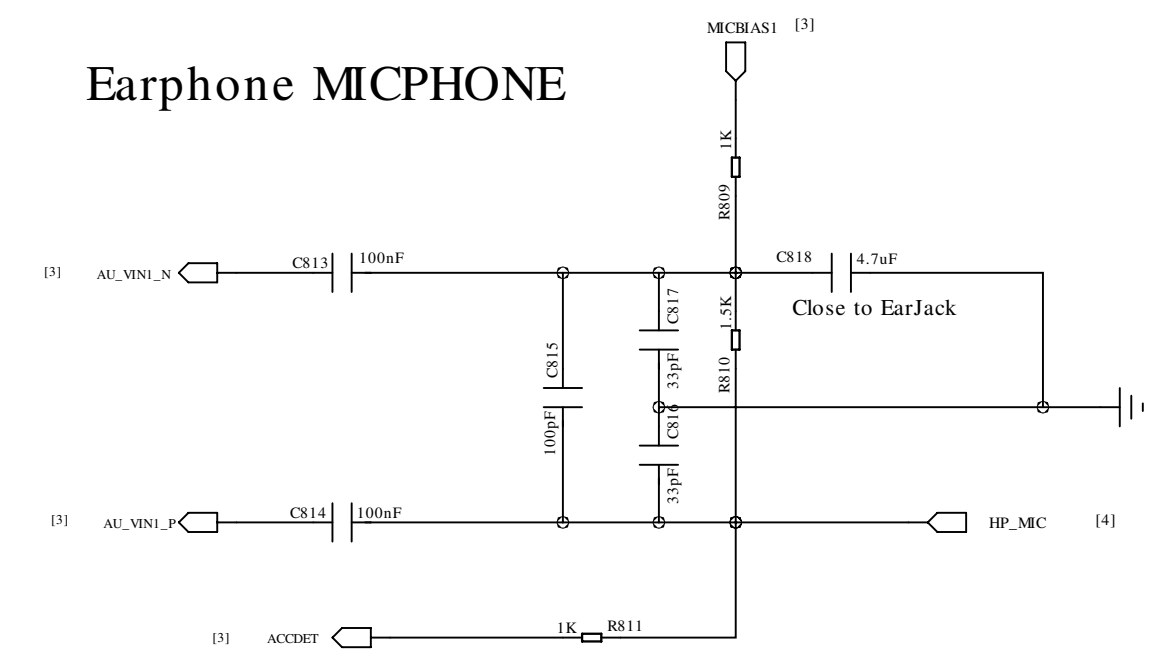
SPK

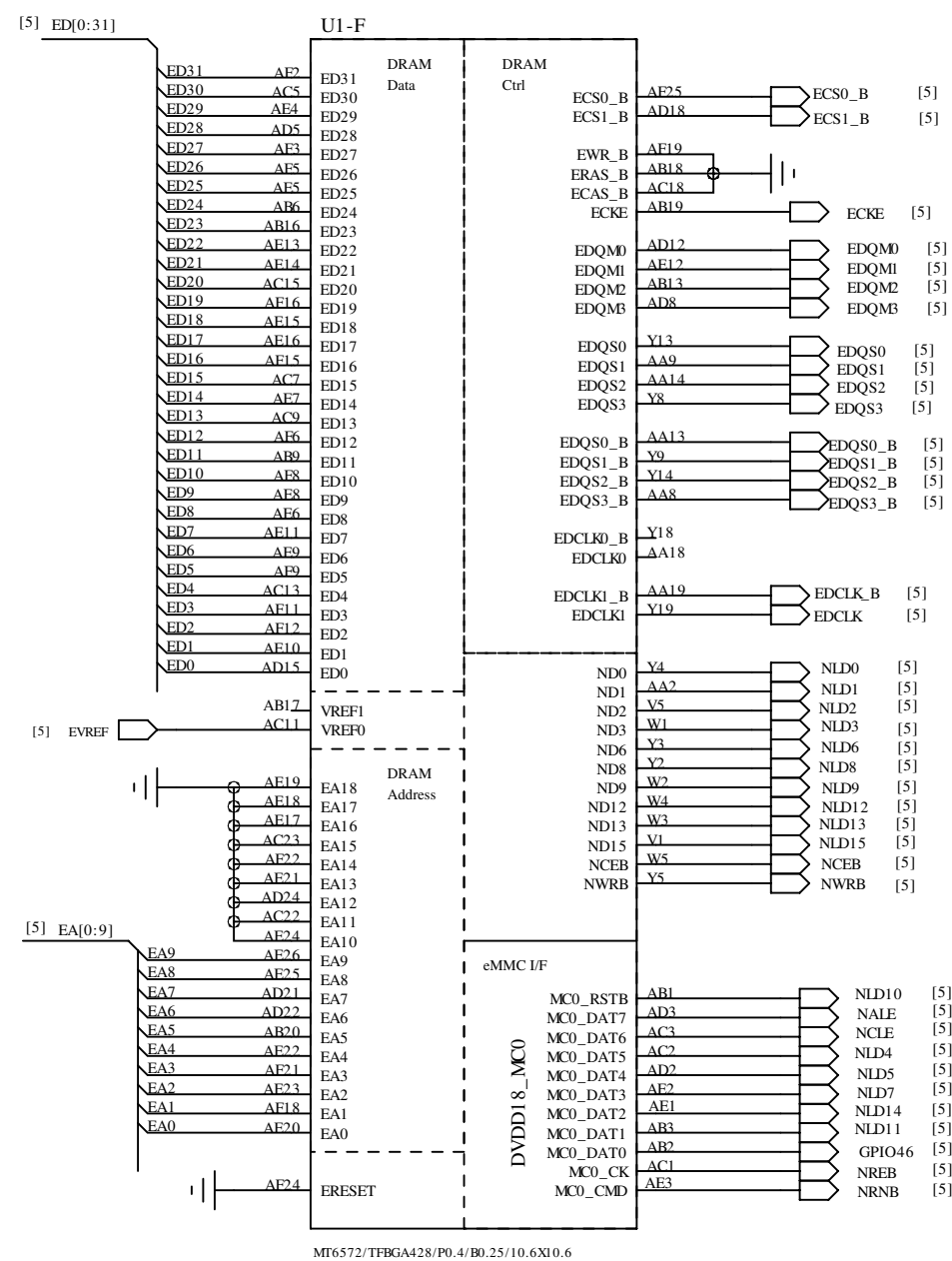
Handset Microphone 1



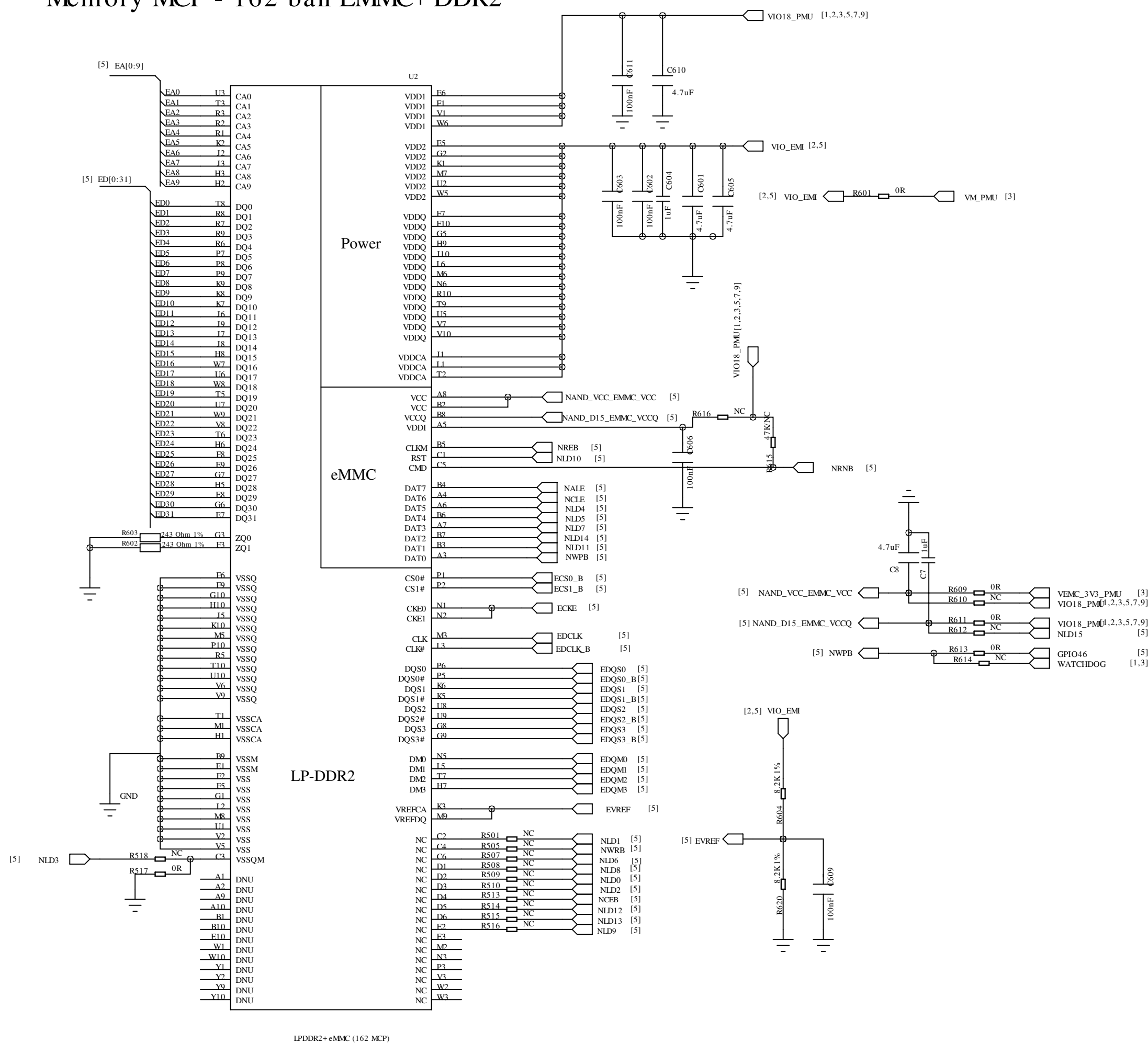
MIC

Earphone MICPHONE

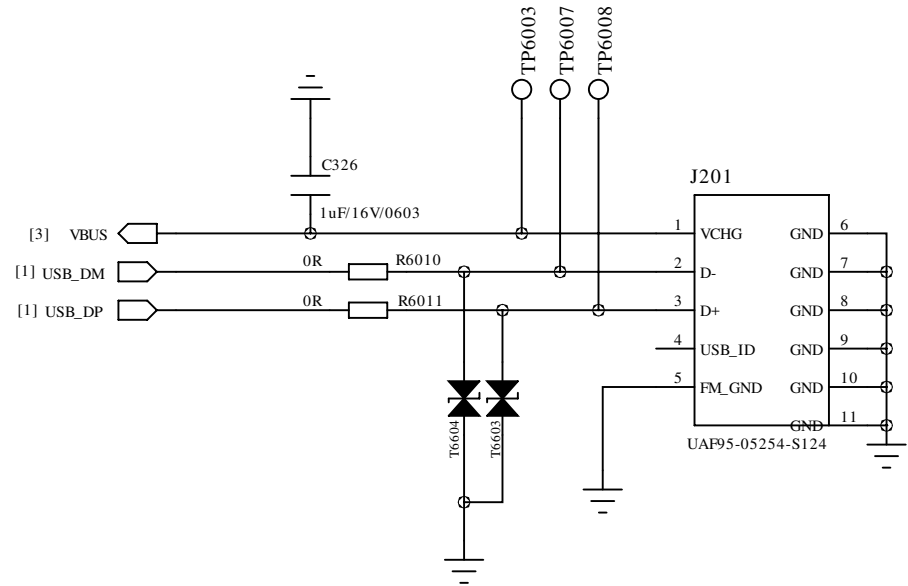




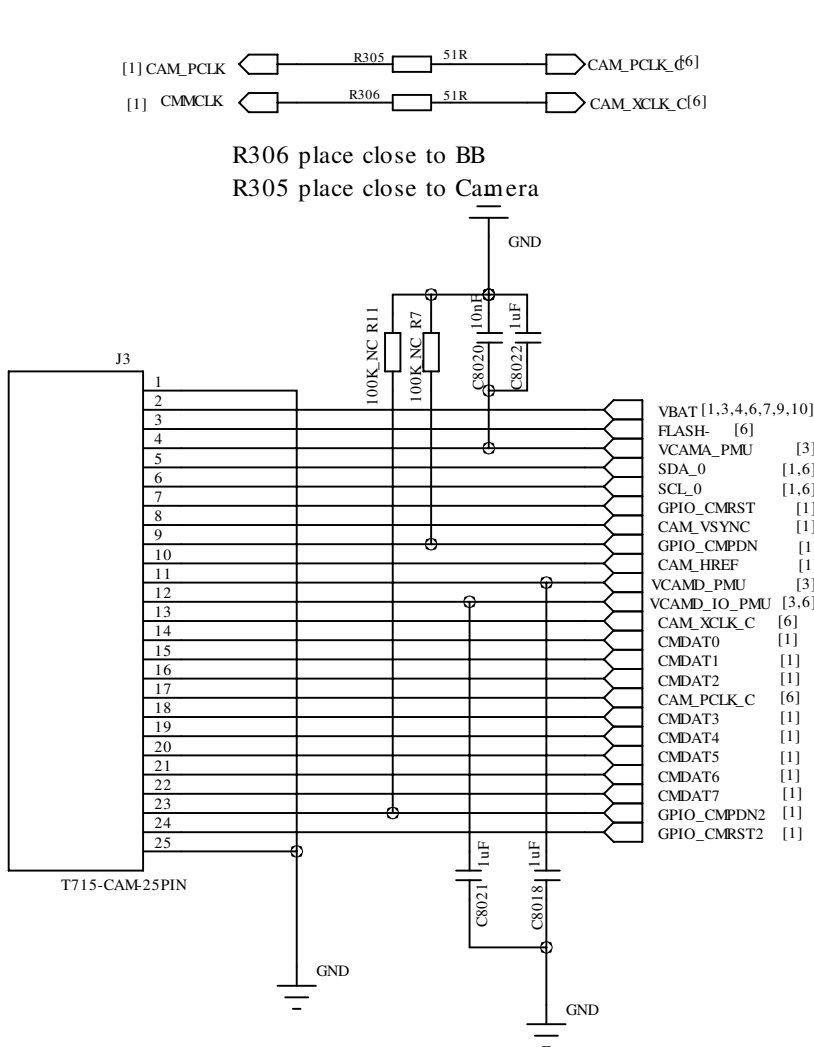
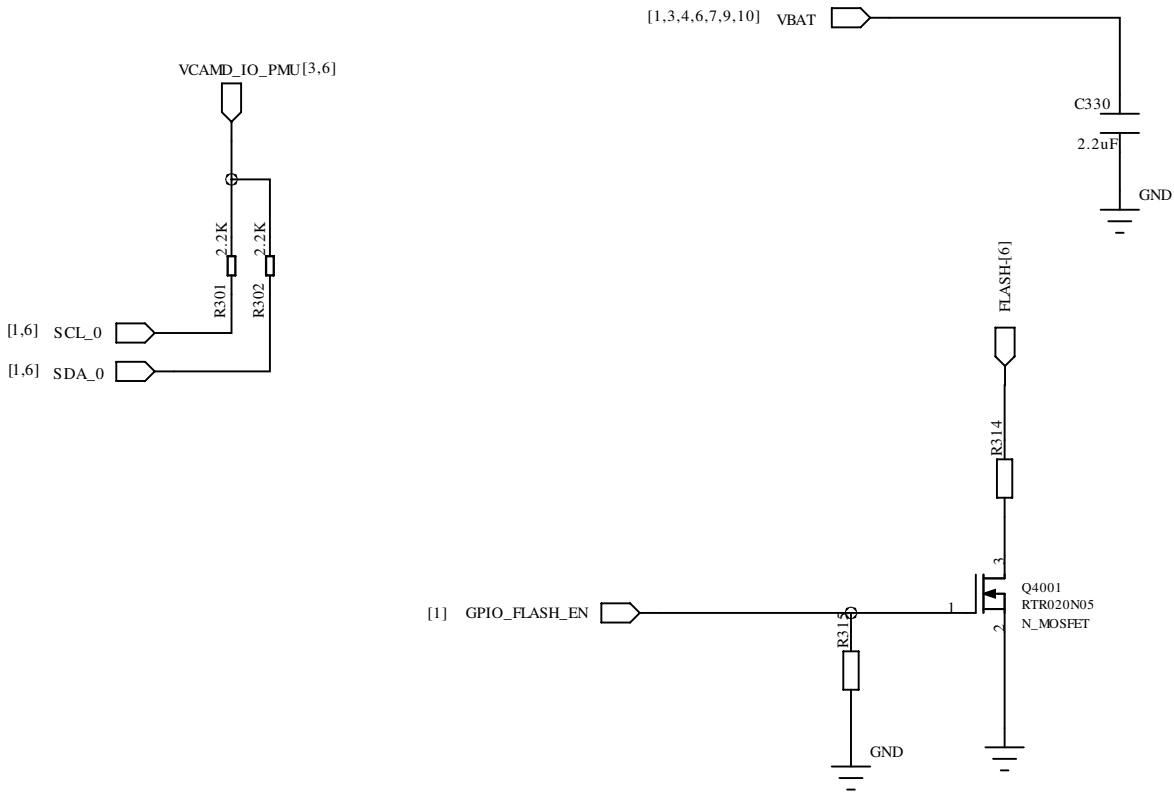
Memory MCP - 162 ball EMMC+ DDR2



USB



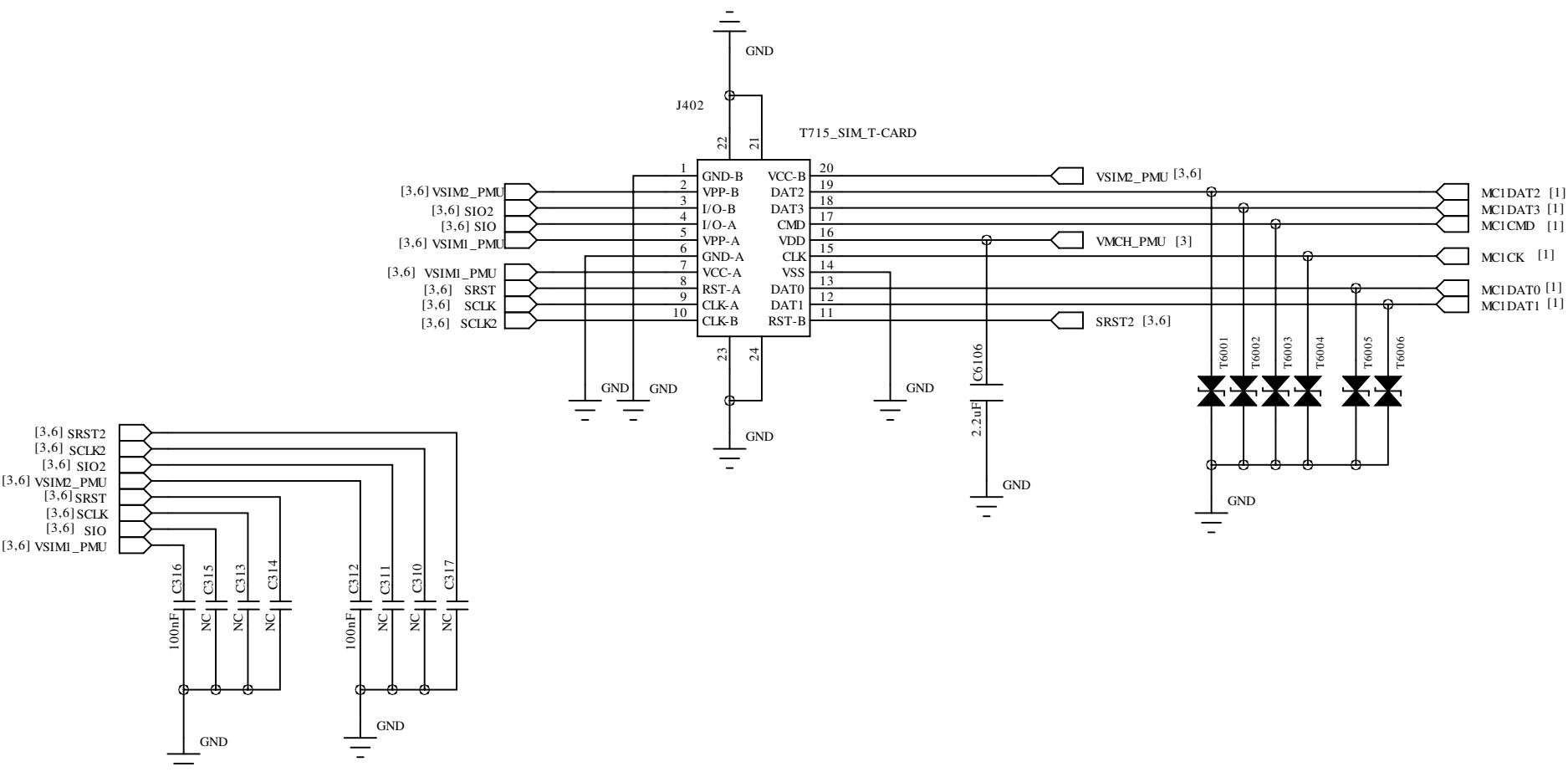
Camera



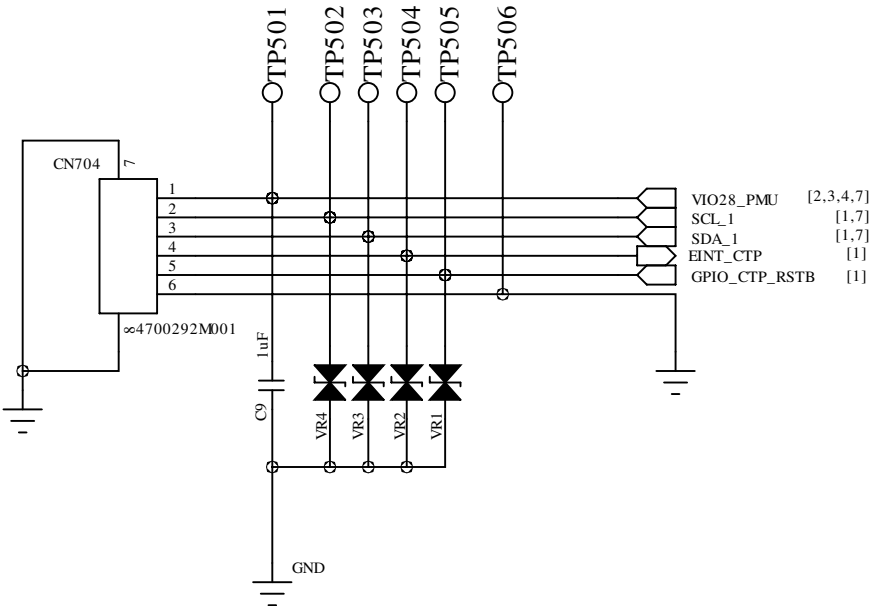
2014.03.20

VCAMA_PMU

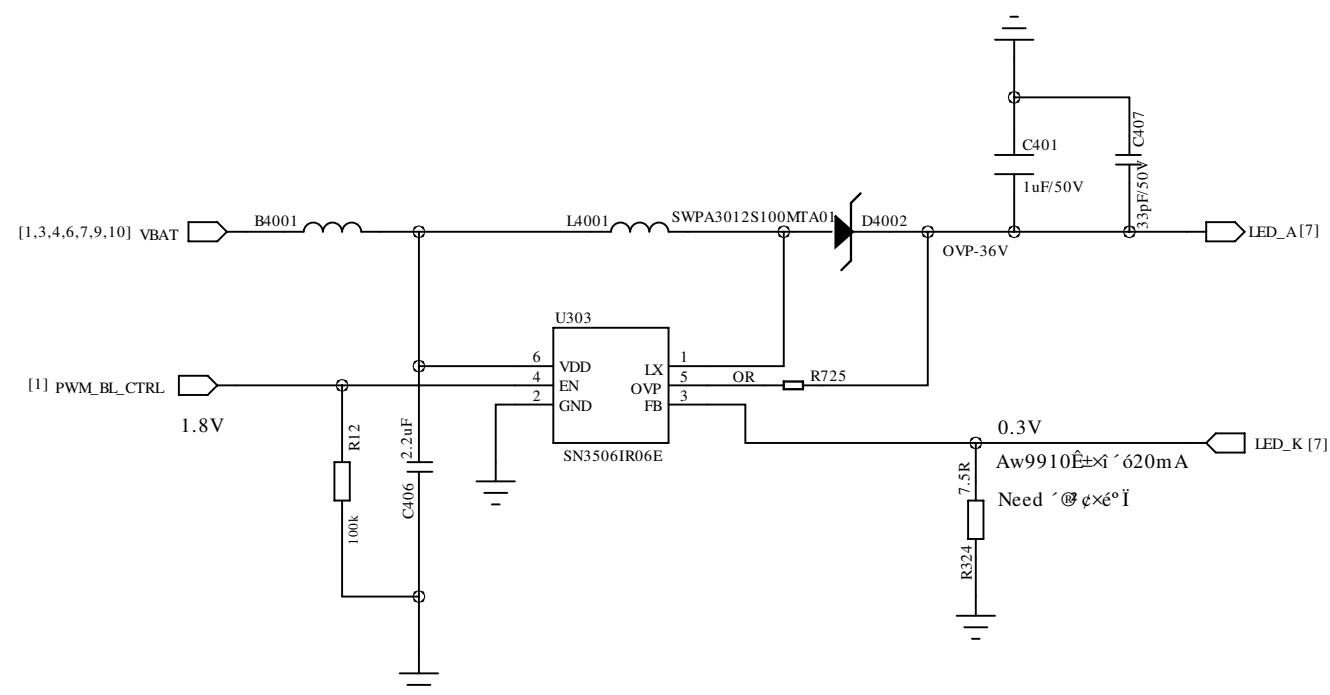
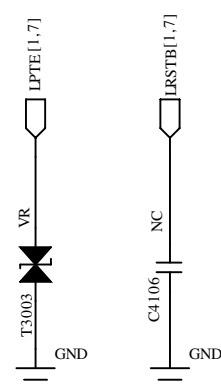
SIM CARD&T CARD



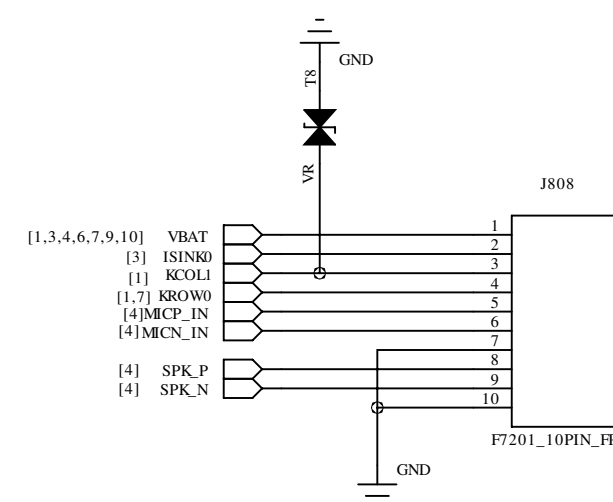
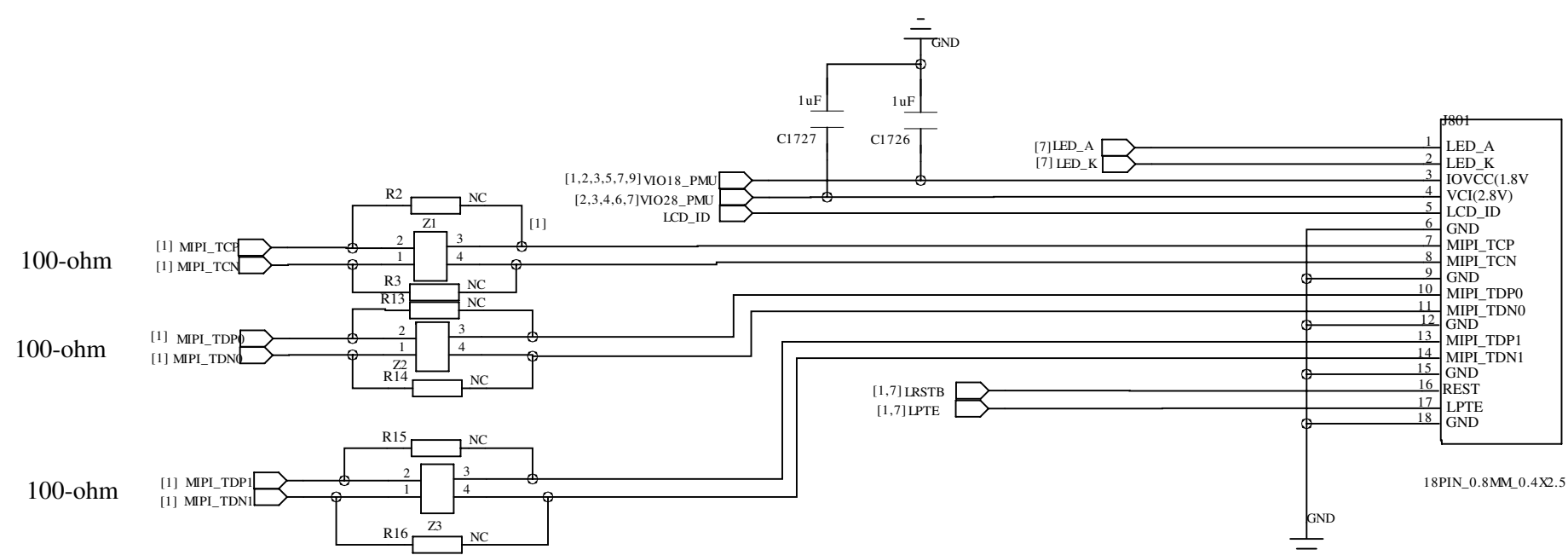
TP



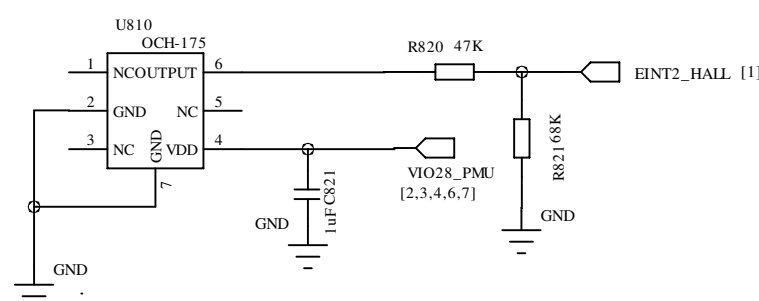
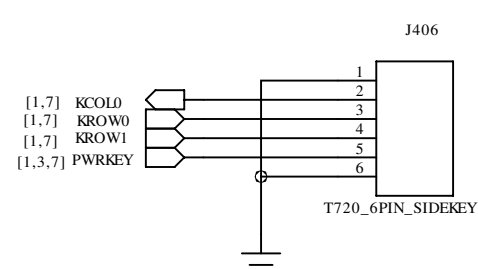
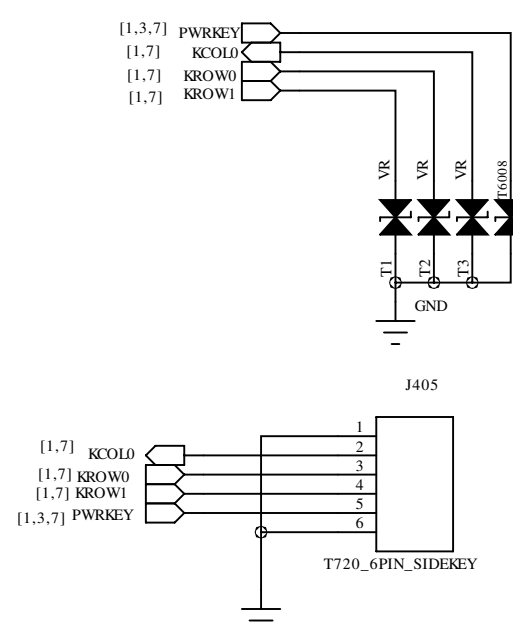
MPI



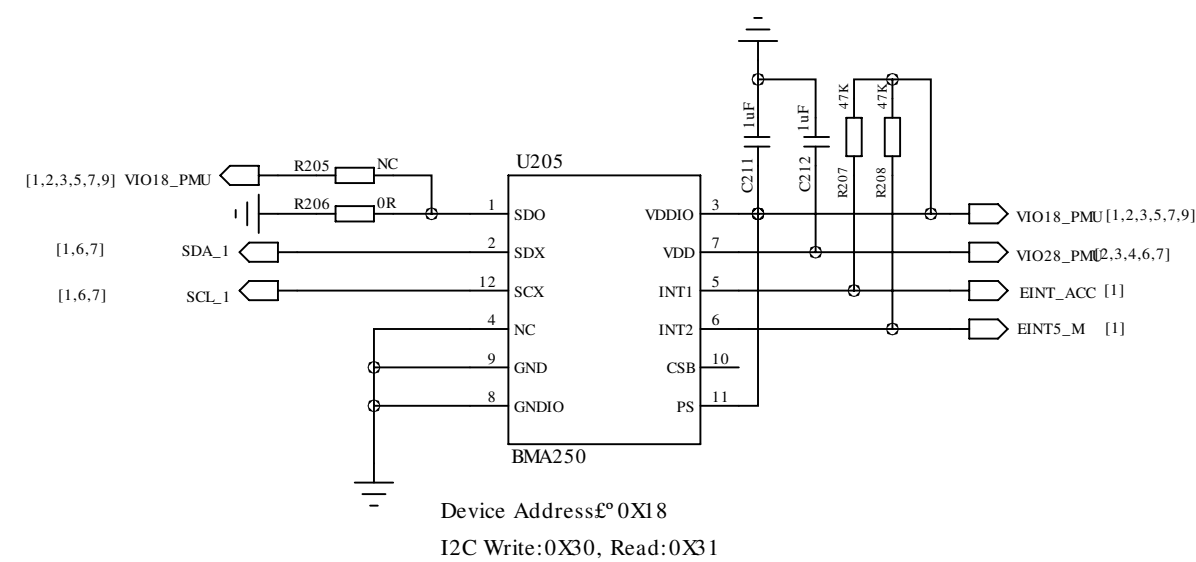
LCM MIPI

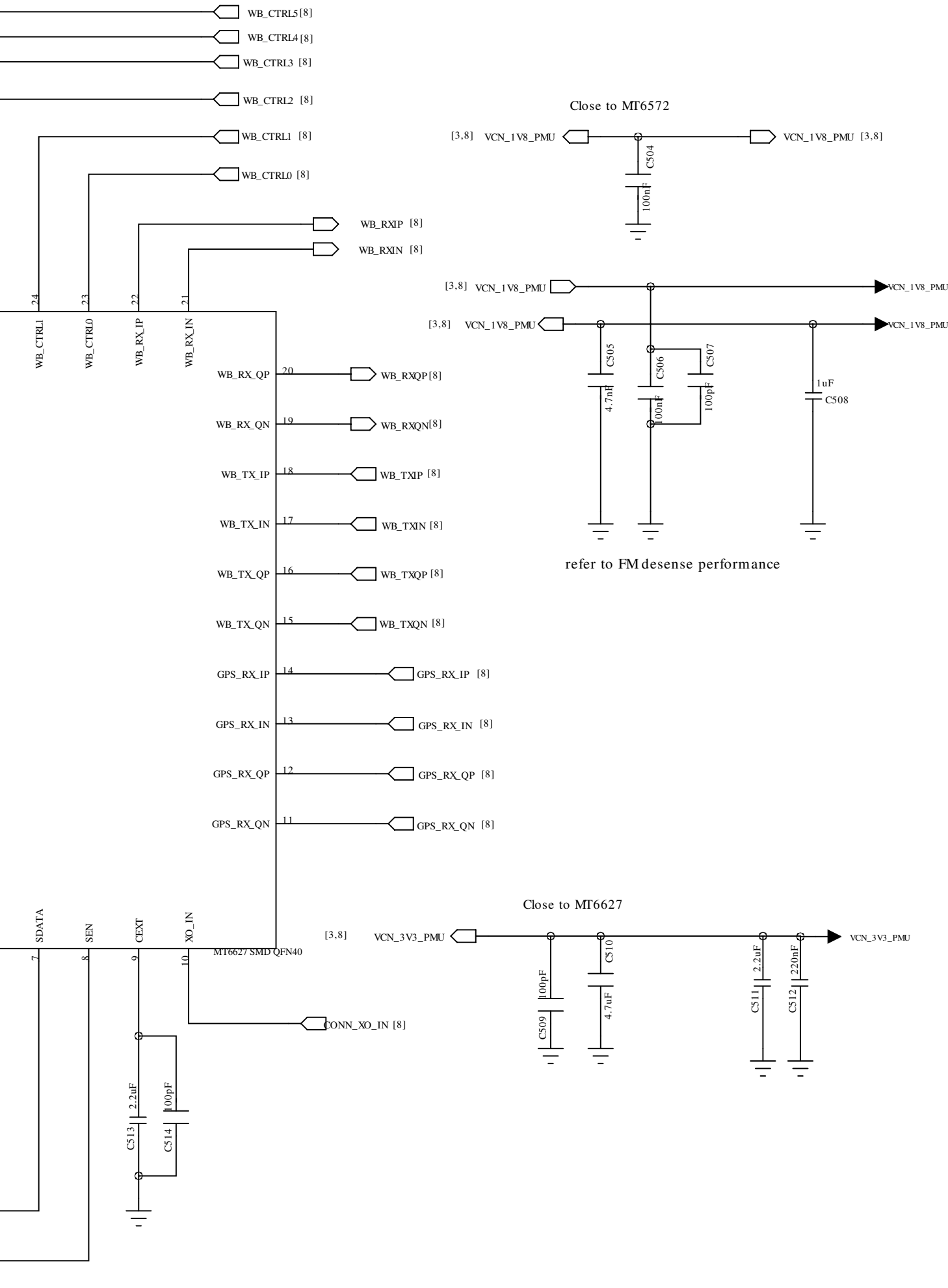
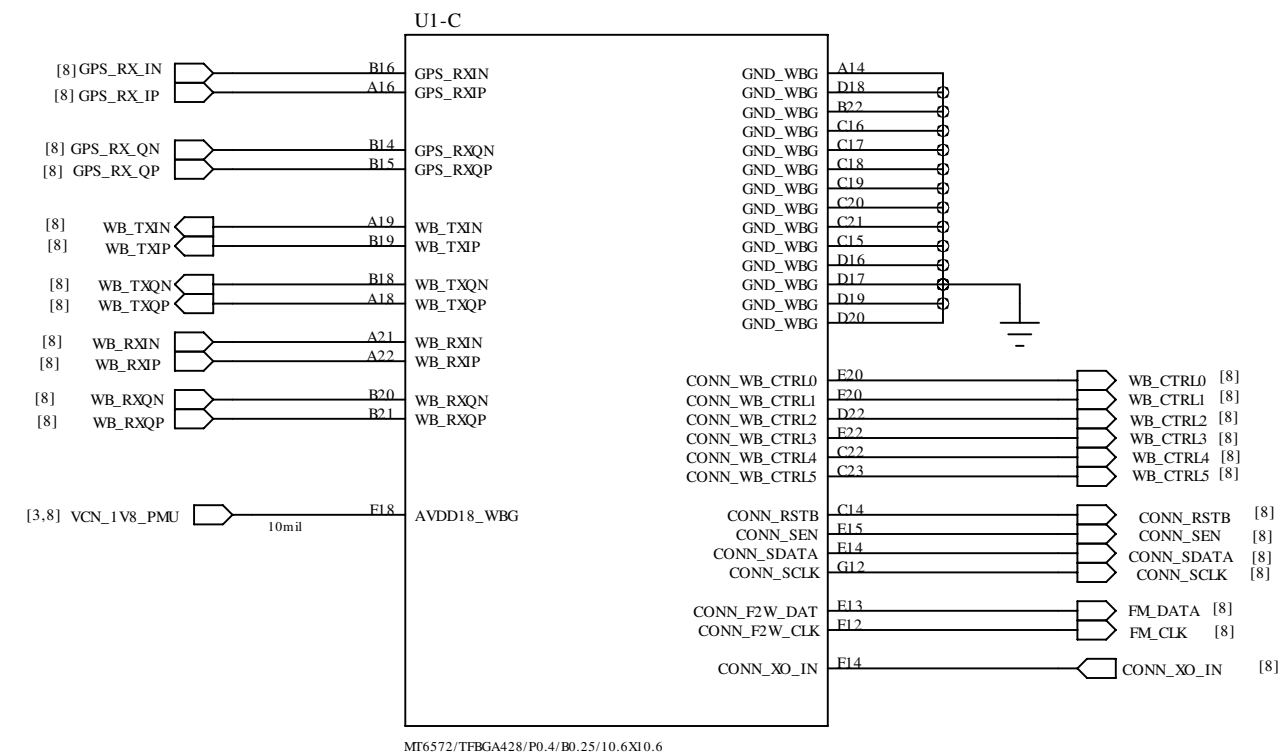


Hall-Sensor

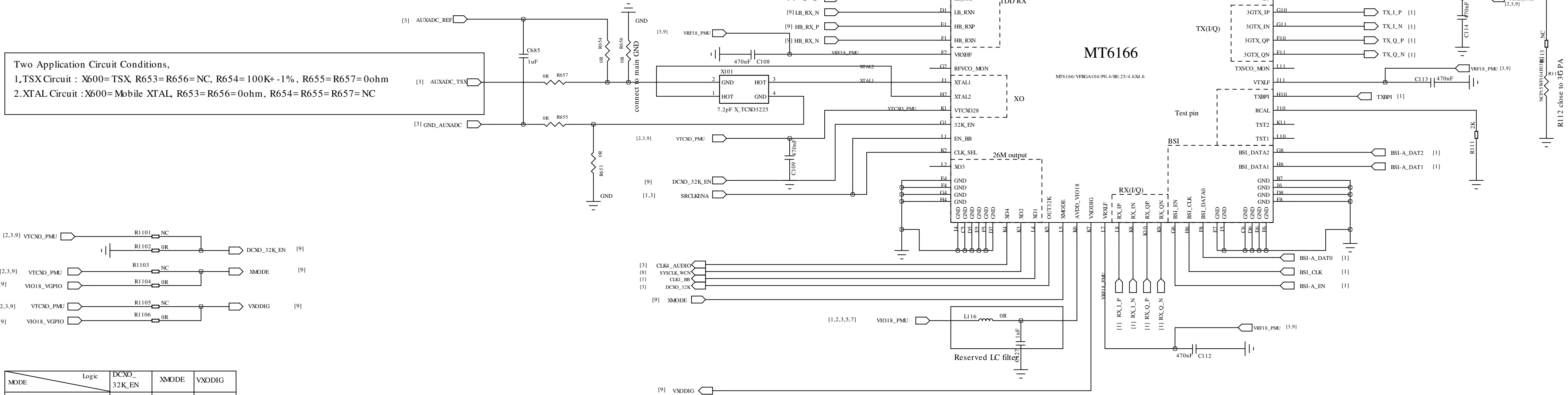
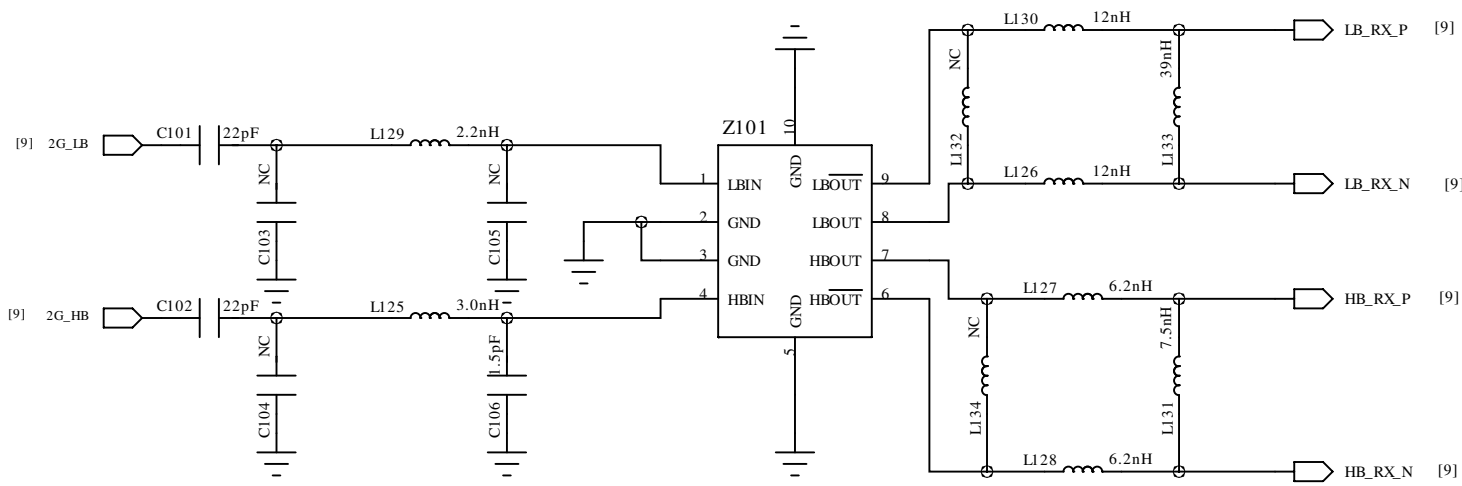
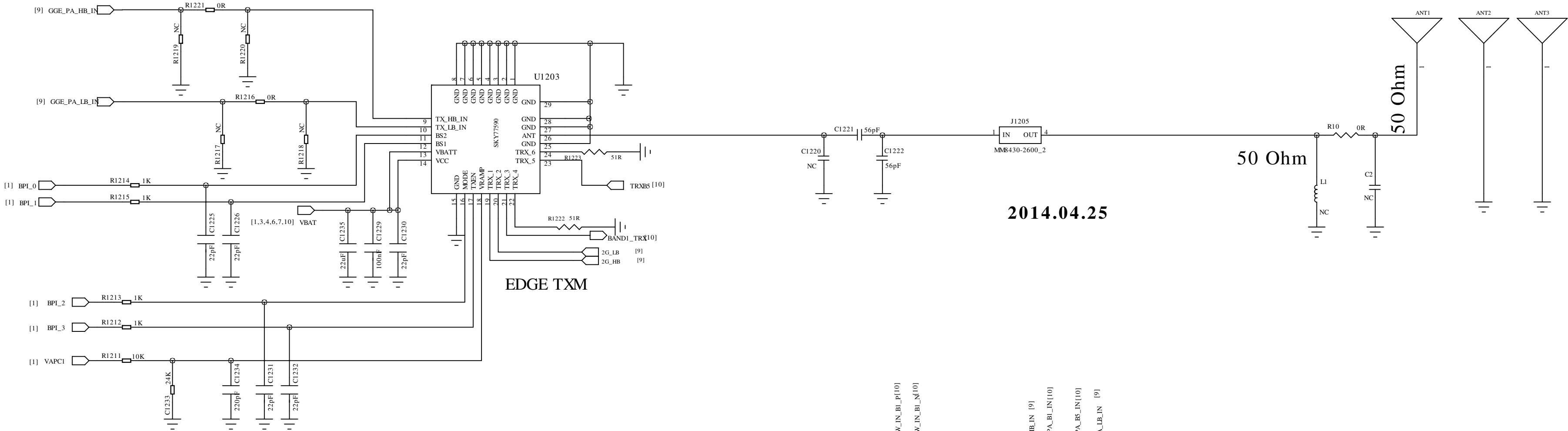


G-SENSOR



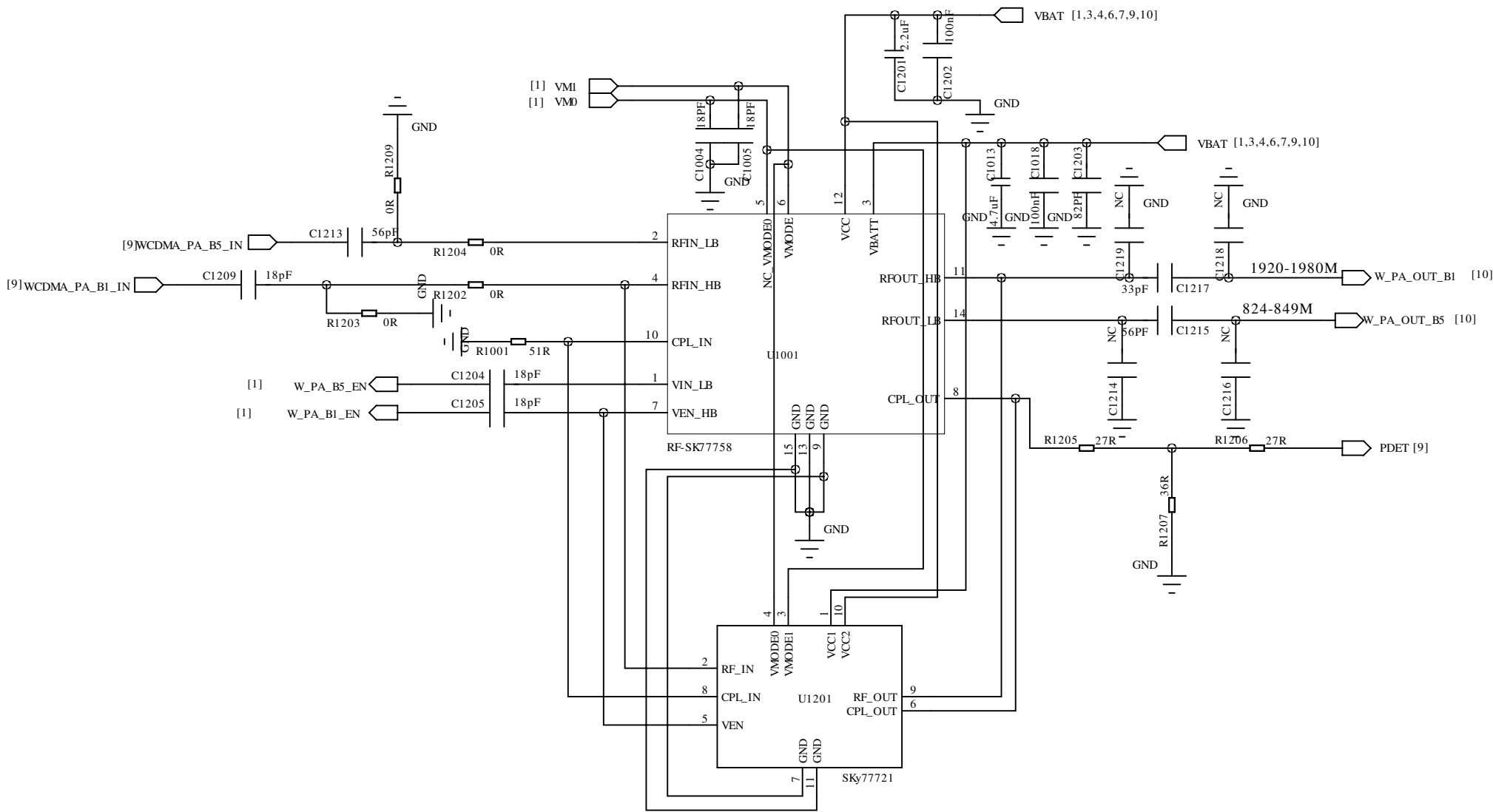
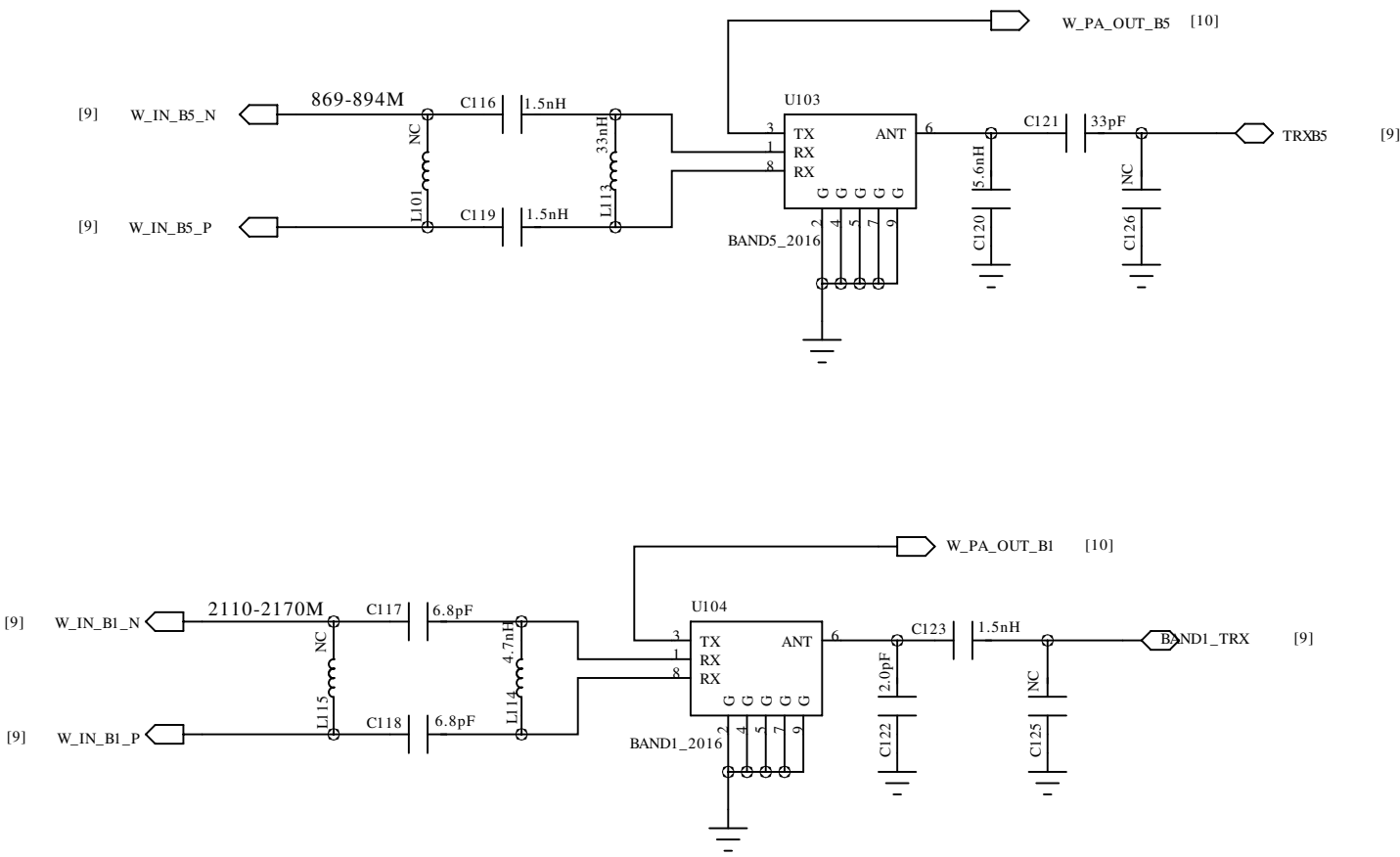


	Enable	VctC	VctB	VctA
LB_GMSK_TX	H	L	L	H
HB_GMSK_TX	H	L	H	H
LB_EDGE_TX	H	H	L	H
HB_EDGE_TX	H	H	H	H
TRX1	L	H	L	L
TRX2	L	H	H	L
TRX3	L	H	L	H
TRX4	L	H	H	H
TRX5	L	L	H	L
TRX6	L	L	L	H

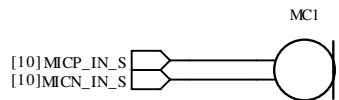


MODE \ Logic	DCXO_32K_EN	XMODE	VXODIG
DCXO + 32K XO	0(GND)	1(VIO18)	1(VIO18)
DCXO + 32K-Less	1(VTXO28)	1(VTXO28)	1(VTXO28)

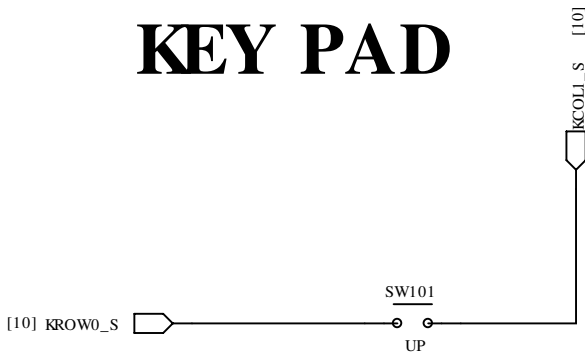
Case1: Band 1
Case2: Band 1+ Band 5
Case2: Band 1+ Band 5+ Band 2



MIC



KEY PAD



SPK

